

.REM_
IDENTIFICATION

PRODUCT ID: AC-T716A-MC
PRODUCT TITLE: CZTSAAO TSU05 DIAG PART 1
DEPARTMENT: COMPUTER SPECIAL SYSTEMS/PPG
DATE: JUNE 03, 1983

THE INFORMATION IN THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE AND SHOULD NOT BE CONSTRUED AS A COMMITMENT BY DIGITAL EQUIPMENT CORPORATION. DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR ANY ERRORS THAT MAY APPEAR IN THIS DOCUMENT.

NO RESPONSIBILITY IS ASSUMED FOR THE USE OR RELIABILITY OF SOFTWARE ON EQUIPMENT THAT IS NOT SUPPLIED BY DIGITAL OR ITS AFFILIATED COMPANIES.

COPYRIGHT (C) 1983 BY DIGITAL EQUIPMENT CORPORATION

THE FOLLOWING ARE TRADEMARKS OF DIGITAL EQUIPMENT CORPORATION:

DIGITAL
DEC

PDP
DECUS

UNIBUS
DECTAPE

MASSBUS

TABLE OF CONTENTS

1.0	GENERAL INFORMATION
1.1	PROGRAM ABSTRACT
1.2	SYSTEM REQUIREMENTS
1.3	RELATED DOCUMENTS AND STANDARDS
1.4	DIAGNOSTIC HIERARCHY PREREQUISITES
1.5	ASSUMPTIONS
2.0	OPERATING INSTRUCTIONS
2.1	COMMANDS
2.2	SWITCHES
2.3	FLAGS
2.4	HARDWARE QUESTIONS
2.5	SOFTWARE QUESTIONS
2.6	EXTENDED P-TABLE DIALOGUE
2.7	QUICK STARTUP PROCEDURE
3.0	ERROR INFORMATION
4.0	PERFORMANCE AND PROGRESS REPORTS
5.0	DEVICE INFORMATION TABLES
6.0	TEST SUMMARIES
7.0	MAINTENANCE HISTORY

C13

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58

SEQ 158

3237	044022			ERRDF	ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT		
	044022	104455					TRAP	C1ERRDF
	044024	002131					.WORD	1113
	044026	003644					.WORD	SFIERR
	044030	011644					.WORD	SFIMSG
3238	044032			3\$:				
3239	044032	012704	044530		MOV	0T11PK2,R4		;WRITE CHARACTERISTICS PACKET
3240	044036	004737	010472		JSR	PC,WRTCHR		;ISSUE WRITE CHARACTERISTICS
3241	044042	103404			BCS	4\$		;BR. IF COMMAND ISSUED OK
3245	044044				ERRHRD	ERRNO,WRTMSG,SFIMSG		;WRITE CHARACTERISTICS FAILED
	044044	104456					TRAP	C1ERRHRD
	044046	002132					.WORD	1114
	044050	005050					.WORD	WRTMSG
	044052	011644					.WORD	SFIMSG
3246	044054			4\$:				
3247	044054	004737	045334		JSR	PC,T11REST		;SET UP PACKET FOR COMMAND
3248	044060	012704	044460		MOV	0T11PACKET,R4		;GET THE ADDRESS OF COMMAND PACKET
3249	044064			5\$:				
3250	044064	005037	002222	10\$:	CLR	INTRECV		;CLEAR INTERRUPT RECEIVED FLAG
3251	044070	010465	000000		MOV	R4,TSD8(R5)		;SET THE PACKET ADDRESS
3252	044074	004737	016146		JSR	PC,CHKTSSR		;WAIT FOR SSR TO SET
3253	044100	103405			BCS	15\$		;BR IF CARRY SET (GOOD RETURN)
3254	044102	010001			MOV	R0,R1		;SAVE CONTENTS OF TSSR
3258	044104				ERRDF	ERRNO,T11SR2,PKTSSR		;DEVICE FATAL SSR FAILED TO SET
	044104	104455					TRAP	C1ERRDF
	044106	002133					.WORD	1115
	044110	045100					.WORD	T11SR2
	044112	011656					.WORD	PKTSSR
3259	044114			15\$:	CKLOOP			;LOOP ON ERROR. IF FLAG SET
	044114	104406					TRAP	C1CLP1
3260	044116				ESCAPE	SEG		;BY-PASS SUBTEST IF FATAL ERROR
	044116	104410					TRAP	C1ESCAPE
	044120	000074					.WORD	10000\$-
3261	044122	005737	002222		IST	INTRECV		;DID AN INTERRUPT OCCUR ?
3262	044126	001004			BNE	22\$		;BRANCH IF YES
3266	044130				ERRHRD	ERRNO,T11NINT,PKTSSR		
	044130	104456					TRAP	C1ERRHRD
	044132	002134					.WORD	1116
	044134	045154					.WORD	T11NINT
	044136	011656					.WORD	PKTSSR
3267	044140	016501	000002	22\$:	MOV	TSSR(R5),R1		;GET THE CONTENTS OF TSSR
3268	044144	012702	000200		MOV	0SSR,R2		;EXPECTED CONTENTS OF TSSR
3269	044150	032701	000100		BIT	0OFFL,R1		;IS OFF-LINE BIT SET ?
3270	044154	001402			BEQ	25\$		;BRANCH IF NOT OFF-LINE
3271	044156	052702	000100		BIS	0OFFL,R2		;SET OFF-LINE IN EXPECTED DATA
3272	044162	020201		25\$:	CMP	R2,R1		;DOES EXPECTED MATCH RECEIVED ?
3273	044164	001404			BEQ	30\$		;OKAY IF MATCH
3277	044166				ERRHRD	ERRNO,1113REJ,PKTSSR		;COMMAND NOT ACCEPTED
	044166	104456					TRAP	C1ERRHRD
	044170	002135					.WORD	1117
	044172	044713					.WORD	T113REJ
	044174	011656					.WORD	PKTSSR
3278	044176			30\$:				
3279	044176	004737	010724	35\$:	JSR	PC,CKRAM		;CHECK RAM TO MEMORY
3280	044202	103405			BCS	59\$		;RAM OK GO ON
3284	044204				ERRHRD	ERRNO,PKTRAM,RAMERR		;THEY DON'T MATCH
	044204	104456					TRAP	C1ERRHRD

AND THE REGISTER MODIFICATION REFUSED (RMR) LOGIC.

TEST 11: NON-TAPE MOTION COMMANDS

THIS TEST VERIFIES PROPER OPERATION OF THE INITIALIZE COMMAND. TWO SUBTESTS ARE USED. THE FIRST VERIFIES THAT THE COMMAND RUNS TO COMPLETION AND STORES A VALID MESSAGE PACKET. THE SECOND VERIFIES THAT NON-ZERO VALUES IN THE COMMAND MODE FIELD CAUSES COMMAND REJECT.

7.0 MAINTENANCE HISTORY

REVISION A - MARCH 1982

```

1
2
3
4          .TITLE   TSV2 - PROGRAM HEADER
5 000000   .SBTTL   PROGRAM HEADER
6          .PSECT   ABS
12
13 000000   .MCALL   SVC
14          SVC                                ; INITIALIZE SUPERVISOR MACROS
15          .ENABLE LC
16          .NLIST   BEX,CND
17          .ENABL   AMA
18          .+2000
19          .+2000
20          BGNMOD   TSV2
21          TSV2::
22          ;
23          ;
24          ;
25          ;
26          ;
27          ; THE PROGRAM HEADER IS THE INTERFACE BETWEEN
28          ; THE DIAGNOSTIC PROGRAM AND THE SUPERVISOR.
29          ;
30          ;
31          ;
32          ;
33          ;
          POINTER BGNSW,BGNSFT,BGNAU,BGNDU,BGNRPT
          HEADER  CZTSA,A,0,655..0
          L$NAME:: ;DIAGNOSTIC NAME
          .ASCII  /C/
          .ASCII  /Z/
          .ASCII  /T/
          .ASCII  /S/
          .ASCII  /A/
          .BYTE   0
          .BYTE   0
          .BYTE   0
          L$REV:: ;REVISION LEVEL
          .ASCII  /A/
          L$DEPO:: ;0
          .ASCII  /O/
          L$UNIT:: ;NUMBER OF UNITS
          .WORD   0
          L$TIML:: ;LONGEST TEST TIME
          .WORD   655.
          L$HPCP:: ;POINTER TO H.W. QUES.
          .WORD   L$HARD
          L$SPCP:: ;POINTER TO S.W. QUES.
          .WORD   L$SOFT
          L$HPTP:: ;PTR. TO DEF. H.W. PTABLE
          .WORD   L$HW
          L$SPTP:: ;PTR. TO S.W. PTABLE
          .WORD   L$SW
          L$LADP:: ;DIAG. END ADDRESS
          .WORD   L$LAST
          L$STA:: ;RESERVED FOR APT STATS
          .WORD   0
          L$CO:: ;
          .WORD   0
          L$DTYP:: ;DIAGNOSTIC TYPE
          .WORD   0

```

H2

TSV2 - PROGRAM HEADER MACRO M1113 07-FEB-84 10:58
PROGRAM HEADER

SEQ 020

002036		L\$APT::		;APT EXPANSION
002036	00C000		.WORD 0	
002040		L\$DTP::		;PTR. TO DISPATCH TABLE
002040	002124'		.WORD L\$DISPATCH	
002042		L\$PRIO::		;DIAGNOSTIC RUN PRIORITY
002042	000000		.WORD 0	
002044		L\$ENVI::		;FLAGS DESCRIBE HOW IT WAS SETUP
002044	000000		.WORD 0	
002046		L\$EXP1::		;EXPANSION WORD
002046	000000		.WORD 0	
002050		L\$MREV::		;SVC. REV AND EDIT #
002050	003		.BYTE C\$REVISION	
002051	003		.BYTE C\$EDIT	
002052		L\$EF::		;DIAG. EVENT FLAGS
002052	000000		.WORD 0	
002054	000000		.WORD 0	
002056		L\$SPL::		
002056	000000		.WORD 0	
002060		L\$DEVP::		; POINTER TO DEVICE TYPE LIST
002060	003400'		.WORD L\$DVTYP	
002062		L\$REPP::		;PTR. TO REPORT CODE
002062	022434'		.WORD L\$RPT	
002064		L\$EXP4::		
002064	000000		.WORD 0	
002066		L\$EXP5::		
002066	000000		.WORD 0	
002070		L\$AUT::		;PTR. TO ADD UNIT CODE
002070	022122'		.WORD L\$AU	
002072		L\$DUT::		;PTR. TO DROP UNIT CODE
002072	022220'		.WORD L\$DU	
002074		L\$LUN::		;LUN FOR EXERCISERS TO FILL
002074	000000		.WORD 0	
002076		L\$DESP::		;POINTER TO DIAG. DESCRIPTION
002076	003406'		.WORD L\$DESC	
002100		L\$LOAD::		;GENERATE SPECIAL AUTOLOAD EMT
002100	104035		EMT E\$LOAD	
002102		L\$ETP::		;POINTER TO ERRTABL
002102	000000		.WORD 0	
002104		L\$ICP::		;PTR. TO INIT CODE
002104	021326'		.WORD L\$INIT	
002106		L\$CCP::		;PTR. TO CLEAN-UP CODE
002106	022406'		.WORD L\$CLEAN	
002110		L\$ACP::		;PTR. TO AUTO CODE
002110	022326'		.WORD L\$AUTO	
002112		L\$PRT::		;PTR. TO PROTECT TABLE
002112	021316'		.WORD L\$PROT	
002114		L\$TEST::		;TEST NUMBER
002114	000000		.WORD 0	
002116		L\$DLY::		;DELAY COUNT
002116	000000		.WORD 0	
002120		L\$HIME::		;PTR. TO HIGH MEM
002120	000000		.WORD 0	

34
35
36
37
38

.SBITL DISPATCH TABLE

H13

TSV6 - PARAMETER CODING MACRO M1113 07-FEB-84 10:58
TEST 11: NON-TAPE MOTION COMMANDS

SEQ 163

```

1          .TITLE   TSV6 - PARAMETER CODING
7
12
18
19 045442          BGNMOD   TSV6
20 045442          TSV6::
21
22          .SBTTL   HARDWARE PARAMETER CODING SECTION
23
24          ;**
25          ; THE HARDWARE PARAMETER CODING SECTION CONTAINS MACROS
26          ; THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES.  THE
27          ; MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
28          ; INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES.  THE
29          ; MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
30          ; WITH THE OPERATOR.
31          ;**
32 045442          BGNHRD
33 045442          .WORD   L10107-L$HARD/2
34 045442          L$HARD::
35 045444          GPRMA   HPM1,0,0,160010,177776,YES          ;GET TSBA/TSDB REGISTER ADDRESS.
36 045444          .WORD   T$CODE
37 045446          .WORD   HPM1
38 045450          .WORD   T$LOLIM
39 045452          .WORD   T$HILIM
40 045454          GPRMA   HPM2,2,0,0,776,YES                  ;GET VECTOR ADDRESS.
41 045454          .WORD   T$CODE
42 045456          .WORD   HPM2
43 045460          .WORD   T$LOLIM
44 045462          .WORD   T$HILIM
45          .GPRMD   HPM3,4,0,340,0,7,YES                      ;GET INTERRUPT PRIORITY.
46          ENDRD
47          .EVEN
48          L10107:
49 045464          104      105      126  HPM1:  .ASCIZ  'DEVICE ADDRESS (TSBA/TSDB) '
50 045520          111      116      124  HPM2:  .ASCIZ  'INTERRUPT VECTOR '
51 045544          111      116      124  HPM3:  .ASCIZ  'INTERRUPT PRIORITY '
52          .EVEN
53          .SBTTL   SOFTWARE PARAMETER CODING SECTION
54          ;**
55          ; THE SOFTWARE PARAMETER CODING SECTION CONTAINS MACROS
56          ; THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES.  THE
57          ; MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
58          ; INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES.  THE
59          ; MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
60          ; WITH THE OPERATOR.
61          ;**
62 045574          BGNSEF
63 045574          .WORD   L10110-L$SOFT/2
64 045576          L$SOFT::
65 045576          GPRMA   SPM1,0,-1,YES                        ; GET TRANSPORT TEST FLAG.
66 045576          GPRMA   SPM4,2,-1,YES                        ; GET ITERATION CONTROL.
67 045576          .WORD   T$CODE

```

NOTICE THAT THE DEFAULT VALUE FOR THE Q-FACTOR CHANGES WHEN A NON-DEFAULT RESPONSE IS GIVEN. BE CAREFUL WHEN SPECIFYING MULTIPLE UNITS!

AS YOU CAN SEE FROM THE ABOVE EXAMPLE, THE HARDWARE PARAMETERS DO NOT VARY SIGNIFICANTLY FROM UNIT TO UNIT. THE PROCEDURE SHOWN IS NOT VERY EFFICIENT.

THE RUNTIME SERVICES CAN TAKE MULTIPLE UNIT SPECIFICATIONS HOWEVER. LET'S BUILD THE SAME TABLE USING THE MULTIPLE SPECIFICATION FEATURE.

♦ UNITS (D) ? 8<CR>

UNIT 1

CSR ADDRESS (O) ? 160000<CR>

SUB-DEVICE # (O) ? 0,1<CR>

Q-FACTOR (O) 0 ? 1,0<CR>

UNIT 3

CSR ADDRESS (O) ? 160000<CR>

SUB-DEVICE # (O) ? 2-5<CR>

Q-FACTOR (O) 0 ? 0<CR>

UNIT 7

CSR ADDRESS (O) ? 160000<CR>

SUB-DEVICE # (O) ? 6,7<CR>

Q-FACTOR (O) 0 ? 1<CR>

AS YOU CAN SEE IN THE ABOVE DIALOGUE, THE RUNTIME SERVICES WILL BUILD AS MANY ENTRIES AS IT CAN WITH THE INFORMATION GIVEN IN ANY ONE PASS THROUGH THE QUESTIONS. IN THE FIRST PASS, TWO ENTRIES ARE BUILT SINCE TWO SUB-DEVICES AND Q-FACTORS WERE SPECIFIED. THE SERVICES ASSUME THAT THE CSR ADDRESS IS 160000 FOR BOTH SINCE IT WAS SPECIFIED ONLY ONCE. IN THE SECOND PASS, FOUR ENTRIES WERE BUILT. THIS IS BECAUSE FOUR SUB-DEVICES WERE SPECIFIED. THE "-" CONSTRUCT TELLS THE RUNTIME SERVICES TO INCREMENT THE DATA FROM THE FIRST NUMBER TO THE SECOND. IN THIS CASE, SUB-DEVICES 2, 3, 4 AND 5 WERE SPECIFIED. (IF THE SUB-DEVICE WERE SPECIFIED BY ADDRESSES, THE INCREMENT WOULD BE BY 2 SINCE ADDRESSES MUST BE ON AN EVEN BOUNDARY.) THE CSR ADDRESSES AND Q-FACTORS FOR THE FOUR ENTRIES ARE ASSUMED TO BE 160000 AND 0 RESPECTIVELY SINCE THEY WERE ONLY SPECIFIED ONCE. THE LAST TWO UNITS ARE SPECIFIED IN THE THIRD PASS.

THE WHOLE PROCESS COULD HAVE BEEN ACCOMPLISHED IN ONE PASS AS SHOWN BELOW.

♦ UNITS (D) ? 8<CR>

UNIT 1

CSR ADDRESS (O) ? 160000<CR>

SUB-DEVICE # (O) ? 0-7<CR>

Q-FACTOR (O) 0 ? 0,1,0,...,1,1<CR>

AS YOU CAN SEE FROM THIS EXAMPLE, NULL REPLIES (COMMAS ENCLOSING

K2

TSV3 - GLOBAL AREAS
SOFTWARE P-TABLE

MACRO M1113 07-FEB-84 10:58

SEQ 023

7
8
13
19
20 002174
002174
21
22
23
24
25
26
27
28
29
33 002174

.TITLE TSV3 - GLOBAL AREAS
.SBTTL GLOBAL EQUATES SECTION

BGNMOD TSV3
TSV3::

.SBTTL GLOBAL EQUATES SECTION

; THE GLOBAL EQUATES SECTION CONTAINS PROGRAM EQUATES THAT
; ARE USED IN MORE THAN ONE TEST.

EQUALS ; GET STANDARD EQUATES.

; BIT DEFINITIONS

100000	BIT15==	100000
040000	BIT14==	40000
020000	BIT13==	20000
010000	BIT12==	10000
004000	BIT11==	4000
002000	BIT10==	2000
001000	BIT09==	1000
000400	BIT08==	400
000200	BIT07==	200
000100	BIT06==	100
000040	BIT05==	40
000020	BIT04==	20
000010	BIT03==	10
000004	BIT02==	4
000002	BIT01==	2
000001	BIT00==	1
001000	BIT9==	BIT09
000400	BIT8==	BIT08
000200	BIT7==	BIT07
000100	BIT6==	BIT06
000040	BIT5==	BIT05
000020	BIT4==	BIT04
000010	BIT3==	BIT03
000004	BIT2==	BIT02
000002	BIT1==	BIT01
000001	BIT0==	BIT00

; EVENT FLAG DEFINITIONS
; EF32:EF17 RESERVED FOR SUPERVISOR TO PROGRAM COMMUNICATION

000040	EF.START==	32.	; START COMMAND WAS ISSUED
000037	EF.RESTART==	31.	; RESTART COMMAND WAS ISSUED
000036	EF.CONTINUE==	30.	; CONTINUE COMMAND WAS ISSUED
000035	EF.NEW==	29.	; A NEW PASS HAS BEEN STARTED
000034	EF.PWR==	28.	; A POWER-FAIL/POWER-UP OCCURRED

;
;

MACRO M1113 07-FEB-84 10:58

SEQ 024

```

; PRIORITY LEVEL DEFINITIONS
;
PRI07-- 340
PRI06-- 300
PRI05-- 240
PRI04-- 200
PRI03-- 140
PRI02-- 100
PRI01-- 40
PRI00-- 0

```

```

; OPERATOR FLAG BITS
;
EVL-- 4
LOT-- 10
ADR-- 20
IDU-- 40
ISR-- 100
UAM-- 200
BOE-- 400
PNT-- 1000
PRI-- 2000
IXE-- 4000
IBE-- 10000
IER-- 20000
LCE-- 40000
HOE-- 100000

```

```

      ..
      KT11
.SBTTL  MEMORY MANAGEMENT DEFINITIONS
;*KT11  VECTOR ADDRESS
MMVEC=  250
;*KT11  STATUS REGISTER ADDRESSES
SR0=    177572
SR1=    177574
SR2=    177576
SR3=    172516
      .IF NB
;*USER  "I" PAGE DESCRIPTOR REGISTERS
UIPDR0= 177600
UIPDR1= 177602
UIPDR2= 177604
UIPDR3= 177606
UIPDR4= 177610
UIPDR5= 177612
UIPDR6= 177614
UIPDR7= 177616
      .IF NB
;*USER  "D" PAGE DESCRIPTOR REGISTERS
UDPDR0= 177620
UDPDR1= 177622
UDPDR2= 177624
UDPDR3= 177626
UDPDR4= 177630
UDPDR5= 177632
UDPDR6= 177634
UDPDR7= 177636

```

```

;DEFINE MEMORY MANAGEMENT REGISTERS

```

M2

TSV3 GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
MEMORY MANAGEMENT DEFINITIONS

SEQ 025

```
.ENDC
;*USER "I" PAGE ADDRESS REGISTERS
UIPAR0= 177640
UIPAR1= 177642
UIPAR2= 177644
UIPAR3= 177646
UIPAR4= 177650
UIPAR5= 177652
UIPAR6= 177654
UIPAR7= 177656
.IF NB
;*USER "D" PAGE ADDRESS REGISTERS
UDPAR0= 177660
UDPAR1= 177662
UDPAR2= 177664
UDPAR3= 177666
UDPAR4= 177670
UDPAR5= 177672
UDPAR6= 177674
UDPAR7= 177676
.ENDC
.ENDC
.IF NB
;*SUPERVISOR "I" PAGE DESCRIPTOR REGISTERS
SIPDR0= 172200
SIPDR1= 172202
SIPDR2= 172204
SIPDR3= 172206
SIPDR4= 172210
SIPDR5= 172212
SIPDR6= 172214
SIPDR7= 172216
.IF NB
;*SUPERVISOR "D" PAGE DESCRIPTOR REGISTERS
SDPDR0= 172220
SDPDR1= 172222
SDPDR2= 172224
SDPDR3= 172226
SDPDR4= 172230
SDPDR5= 172232
SDPDR6= 172234
SDPDR7= 172236
.ENDC
;*SUPERVISOR "I" PAGE ADDRESS REGISTERS
SIPAR0= 172240
SIPAR1= 172242
SIPAR2= 172244
SIPAR3= 172246
SIPAR4= 172250
SIPAR5= 172252
SIPAR6= 172254
SIPAR7= 172256
.IF NB
;*SUPERVISOR "D" PAGE ADDRESS REGISTERS
SDPAR0= 172260
SDPAR1= 172262
SDPAR2= 172264
```

N2

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
MEMORY MANAGEMENT DEFINITIONS

SEQ 026

```

SDPAR3= 172266
SDPAR4= 172270
SDPAR5= 172272
SDPAR6= 172274
SDPAR7= 172276
.ENDC
.ENDC
; *KERNEL "I" PAGE DESCRIPTOR REGISTERS
172300 KIPDR0= 172300
172302 KIPDR1= 172302
172304 KIPDR2= 172304
172306 KIPDR3= 172306
172310 KIPDR4= 172310
172312 KIPDR5= 172312
172314 KIPDR6= 172314
172316 KIPDR7= 172316
; IF NB
; *KERNEL "D" PAGE
DESCRIPTOR REGISTERS
KOPDR0= 172320
KOPDR1= 172322
KOPDR2= 172324
KOPDR3= 172326
KOPDR4= 172330
KOPDR5= 172332
KOPDR6= 172334
KOPDR7= 172336
.ENDC
; *KERNEL "I" PAGE ADDRESS REGISTERS
172340 KIPAR0= 172340
172342 KIPAR1= 172342
172344 KIPAR2= 172344
172346 KIPAR3= 172346
172350 KIPAR4= 172350
172352 KIPAR5= 172352
172354 KIPAR6= 172354
172356 KIPAR7= 172356
; IF NB
; *KERNEL "D" PAGE ADDRESS REGISTERS
KOPAR0= 172360
KOPAR1= 172362
KOPAR2= 172364
KOPAR3= 172366
KOPAR4= 172370
KOPAR5= 172372
KOPAR6= 172374
KOPAR7= 172376
.ENDC

```

39
40
41
42
43
44
45
46
47

000004

.SBTTL TSU05 REGISTER AND PACKET DEFINITIONS

; SOME GENERAL EQUATES.

ERRVEC==

4

; POINTER TO ERROR VECTOR FOR BUG OUT.

FOLLOWS:

CHANGE SW (L) ?

INHIBIT ITERATIONS (L) N ?

6.0 TEST SUMMARIES

TEST 1: BUS RESET TEST

THIS TEST VERIFIES THAT THE M7455 MODULE'S DEVICE REGISTERS ARE ACCESSIBLE ON THE BUS (SUBTEST 1) AND THEN CHECKS THAT THE BUILT-IN INITIALIZATION SELF-TEST MICRODIAGNOSTIC DID NOT FIND ANY BASIC PROBLEMS IN THE MODULE. AREAS OF LOGIC TESTED BY THE SELF-TEST SEQUENCE ARE AS FOLLOWS: ROM AND PIPELINE REGISTER, SEQUENCER, INTERNAL BUSES, 2901 MICROPROCESSOR, AND, RAM. THIS TEST INITIALIZES THE CONTROLLER BY ISSUING THE BUS INTI SIGNAL VIA A RESET INSTRUCTION, OR BY WRITING INTO THE TSSR REGISTER. WAITS A PERIOD OF TIME (TO ALLOW THE CONTROLLER'S INITIALIZATION MICRODIAGNOSTIC SEQUENCE TO BE COMPLETED), AND THEN CHECKS THE CONTENTS OF THE TSSR REGISTER. SUCCESSFUL INITIALIZATION IS INDICATED BY SUBSYSTEM READY (SSR) AND NEED BUFFER ADDRESS (NBA) BITS BEING SET (1) AND ALL OTHER BITS (EXCEPT A17 AND A16 AND OFL, WHICH ARE IGNORED FOR THIS TEST) BEING CLEAR (0). IF THE CONTENTS OF TSSR ARE NOT AS EXPECTED, AN ERROR REPORT IS ISSUED LISTING THE EXPECTED DATA, ACTUAL DATA, AND THE DISCREPANCIES. THE ERROR REPORT ANALYZES THE TSSR CONTENTS AND DISCERNs AND REPORTS ONE OF THREE POSSIBILITIES:

1. TSSR CONTENTS ARE AMBIGUOUS (ANY OF BITS 11-14 ARE SET, OR STATES OF SSR AND SC BITS DO NOT CORRESPOND TO THE APPARENT ERROR CODE IN BITS 0-5); INDICATES THAT THE TSSR CONTENT CANNOT BE TRUSTED. INDICATES A CATASTROPHIC CONTROLLER MALFUNCTION. THIS IS A FATAL ERROR (EXECUTION IS ABORTED). FIELD ACTION WOULD BE TO REPLACE THE M7455. IF THE M7455 ITSELF IS BEING DEBUGGED, THE PROGRAM SHOULD BE RESTARTED WITH LOOP ON ERROR ENABLED IN ORDER TO PROBE FOR THE PROBLEM.
2. SSR = 0, SC = 0 AND THE ERROR CODE IN BITS 0-5 IS IN THE RANGE 17-13: THIS IS A FATAL ERROR. THE ERROR CODE IS DECODED AND THE APPROPRIATE DESCRIPTION GIVEN. INDICATES THAT A SERIOUS PROBLEM EXISTS.

TEST 2: WRAP DATA - HIGH BYTE

THIS TEST VERIFIES OPERATION OF:

1. PART OF THE PDP-11 BUS INTERFACE SECTION OF THE M7455 MODULE: PART OF THE INPUT FILE (TSDR HIGH BYTE), PART

OF THE OUTPUT FILE (TSSR HIGH BYTE AND TSBA, BOTH BYTES), PART OF THE DC005 TRANSCEIVER CIRCUITS (ADDRESS DECODER, BDAL DRIVERS, HIGH BYTE OF INTERNAL DAL BUS DRIVERS), AND BASIC PROGRAMMED I/O CONTROL SEQUENCES AND LOGIC;

2. PART OF 2901 MICROPROCESSOR ELEMENTS (Q-REGISTER, REGISTER O, ROTATE AND NEGATE FUNCTIONS
3. Y AND SOURCE BUSES;
4. BASIC MICROPROGRAM SEQUENCES.

THE PROGRAM WRITES A TEST DATA BYTE INTO THE HIGH BYTE OF TSDB, WAITS FOR THE SSR BIT IN TSSR TO SET, THEN CHECKS THE CONTENTS OF BOTH TSBA AND TSSR. THE MODULE IS FUNCTIONING CORRECTLY IF DATA WRITTEN APPEARS IN BOTH BYTES OF TSBA AND THE FINAL CONTENT OF TSSR IS CORRECT (SAME AS AFTER INITIALIZATION EXCEPT FOR BITS 8 AND 9, WHICH SHOULD CONTAIN BITS 8 AND 9 OF THE DATA PATTERN WRITTEN. AN ERROR IS REPORTED AND A DESCRIPTIVE ANALYSIS GIVEN IF A DISCREPANCY IN TSBA OR TSSR IS DETECTED. THE ANALYSIS LISTS LIKELY FAULTY CANDIDATES FROM THE LOGIC ELEMENTS LISTED ABOVE. THE TEST IS REPEATED FOR ALL COMBINATIONS OF TEST DATA BYTES (0-377 OCTAL).

TEST 3: WRAP DATA - LOW BYTE

THIS TEST FURTHER VERIFIES OPERATION OF MANY OF THE SAME ELEMENTS TESTED IN TEST 2, AND ADDITIONALLY VERIFIES:

1. LOW BYTE OF THE TSDB INPUT FILE REGISTER,
2. LOW BYTE OF INTERNAL DAL BUS DRIVERS ON THE DC005 TRANSCEIVER CIRCUITS,
3. BASIC FUNCTIONING OF PARTS OF THE RAM.

THE PROGRAM WRITES A TEST DATA BYTE INTO THE LOW BYTE OF TSDB, WAITS FOR THE SSR BIT IN TSSR TO SET, THEN CHECKS THE CONTENTS OF BOTH TSBA AND TSSR. THE MODULE IS FUNCTIONING CORRECTLY IF DATA WRITTEN APPEARS IN BOTH BYTES OF TSBA AND THE FINAL CONTENT OF TSSR IS CORRECT (SAME AS AFTER INITIALIZATION EXCEPT FOR BITS 8 AND 9, WHICH SHOULD CONTAIN BITS 8 AND 9 OF THE DATA PATTERN WRITTEN. AN ERROR IS REPORTED AND A DESCRIPTIVE ANALYSIS GIVEN IF A DISCREPANCY IN TSBA OR TSSR IS DETECTED. THE ANALYSIS LISTS LIKELY FAULTY CANDIDATES FROM THE LOGIC ELEMENTS LISTED ABOVE. THE TEST IS REPEATED FOR ALL COMBINATIONS OF TEST DATA BYTES (0-377 OCTAL).

TEST 4: RAM TEST

THIS TEST VERIFIES THAT ALL LOCATIONS OF THE RAM ON THE M7455 CAN PROPERLY STORE AND READ BACK ALL DATA PATTERNS, AND THAT EACH RAM LOCATION IS UNIQUELY ADDRESSFD (I.E., THAT ONE AND ONLY ONE LOCATION IS ACCESSED BY ANY PARTICULAR ADDRESS). THE BYPRODUCT OF THESE TESTS IS A VERIFICATION OF TWO REGISTERS IN THE 2901 AND THE CAPABILITY OF THE 2901 TO CORRECTLY PERFORM AN ADD.

TEST 5: SECOND INITIALIZATION TEST

THIS TEST VERIFIES THE SAME ELEMENTS AS DID INITIALIZATION TEST #1 AND ALSO CHECKS THAT CERTAIN PARTS OF RAM IS CLEARED TO ZERO AND THAT 2901 REGISTERS 10 AND 11 ARE ALSO CLEARED TO ZERO. THIS IS A CONFIDENCE CHECK OF A PART OF THE SELF-TEST SEQUENCE (I.E., THAT IT IS REALLY BEING EXECUTED). FOR EACH OF TWO SUBTESTS (ONE FOR INITIALIZING VIA A BUS INIT, THE OTHER FOR INITIALIZING BY WRITING INTO THE TSSR), THE FOLLOWING SEQUENCE IS PERFORMED:

1. EACH RAM LOCATION AND 2901 REGISTERS 10 AND 11 ARE SET TO ALL 1'S BY USING WRITES INTO THE TSDB REGISTER (LOW BYTE AND MAINTENANCE MODE WORD WRITES).
2. THE CONTROLLER IS INITIALIZED AND THE VARIOUS CHECKS ON THE TSSR DESCRIBED IN INITIALIZATION TEST #1 ARE PERFORMED.
3. #1'S (377 OCTAL) ARE WRITTEN INTO THE LOW BYTE OF TSDB, WHICH SHOULD CAUSE RAM LOCATION 0 TO BE WRITTEN TO ALL 1'S SINCE 2901 REGISTERS 10 AND 11, SPECIFYING THE RAM ADDRESS, SHOULD BE 0. RAM LOCATION 0 IS VERIFIED BY WRITING A WORD OF ZEROS INTO THE TSDB. THE RESULTING LOW BYTE OF TSBA SHOULD CONTAIN ALL 1'S.
4. THE ENTIRE RAM IS SCANNED. LOCATION 0 SHOULD CONTAIN ALL 1'S AND THE REMAINING LOCATIONS, EXCEPT FOR THE MESSAGE BUFFER IMAGE AREA, SHOULD CONTAIN 0. DISCREPANCIES ARE REPORTED. AN ERROR AT THIS POINT IS MOST LIKELY DUE TO A ROM, PIPELINE OR SEQUENCER PROBLEM OR A TIMING PROBLEM.

TEST 6: COMMAND REJECT

THIS TEST VERIFIES THAT ALL COMMANDS OTHER THAN WRITE CHARACTERISTICS ARE REJECTED DUE TO THE NEED BUFFER ADDRESS (NBA) BIT BEING SET IN TSSR, AND THAT THE TSBA AND TSSR REGISTERS ARE LEFT IN THE PROPER STATE AFTER EACH COMMAND IS REJECTED. THIS TEST CHECKS MICROPROCESSOR SEQUENCING, BASIC COMMAND DECODING AND DATA DMA HANDLING. THIS TEST CONTAINS TWO SUBTESTS: SUBTEST 1 SEQUENCES THROUGH ALL COMMAND WORDS (OTHER THAN WRITE CHARACTERISTICS) WITH THE INTERRUPT ENABLE (IE) BIT CLEAR AND VERIFIES THAT AN INTERRUPT IS NOT GENERATED BY THE

F2

USER DOCUMENTATION

MACRO M1113 07-FEB-84 10:58

SEQ 018

AND THE REGISTER MODIFICATION REFUSED (RMR) LOGIC.

TEST 11: NON-TAPE MOTION COMMANDS

THIS TEST VERIFIES PROPER OPERATION OF THE INITIALIZE
COMMAND. TWO SUBTESTS ARE USED. THE FIRST VERIFIES THAT
THE COMMAND RUNS TO COMPLETION AND STORES A VALID
MESSAGE PACKET. THE SECOND VERIFIES THAT NON-ZERO
VALUES IN THE COMMAND MODE FIELD CAUSES COMMAND REJECT.

7.0 MAINTENANCE HISTORY

REVISION A - MARCH 1982


```

1
2
3
4          .TITLE   TSV2 - PROGRAM HEADER
5 000000   .SBTTL   PROGRAM HEADER
6          .PSECT   ABS
12
13 000000   .MCALL   SVC
14          SVC                                ; INITIALIZE SUPERVISOR MACROS
15          .ENABLE  LC
21          .NLIST   BEX,CND
22          .ENABL   AMA
23          .+2000
24 002000   .+2000
          002000   BGNMOD   TSV2
TSV2::
; **
; THE PROGRAM HEADER IS THE INTERFACE BETWEEN
; THE DIAGNOSTIC PROGRAM AND THE SUPERVISOR.
; --
31
32 002000   POINTER BGNSW,BGNSFT,BGNAU,BGNDU,BGNRPT
33 002000   HEADER  CZTSA,A,0,655.,0
          002000   L$NAME::      ;DIAGNOSTIC NAME
          002000       103       .ASCII  /C/
          002001       132       .ASCII  /Z/
          002002       124       .ASCII  /T/
          002003       123       .ASCII  /S/
          002004       101       .ASCII  /A/
          002005       000       .BYTE   0
          002006       000       .BYTE   0
          002007       000       .BYTE   0
          002010       101       L$REV::      ;REVISION LEVEL
          002011       060       L$DEPO::    ;0
          002012       000000    L$UNIT::    ;NUMBER OF UNITS
          002014       001217    L$TIML::    ;LONGEST TEST TIME
          002016       045444'   L$HPCP::    ;PTR. TO H.W. PTABLE
          002020       045576'   L$SPCP::    ;PTR. TO S.W. PTABLE
          002022       002154'   L$HPTP::    ;PTR. TO DEF. H.W. PTABLE
          002024       002164'   L$SPTP::    ;PTR. TO S.W. PTABLE
          002026       045672'   L$LADP::    ;DIAG. END ADDRESS
          002030       000000    L$STA::     ;RESERVED FOR APT STATS
          002032       000000    L$CO::     ;
          002034       000000    L$DTYP::   ;DIAGNOSTIC TYPE
          002034       000000    .WORD   0

```

H2

TSV2 - PROGRAM HEADER MACRO M1113 07-FEB-84 10:58
PROGRAM HEADER

SEQ 020

002036		L\$APT::			;APT EXPANSION
002036	00C000		.WORD	0	
002040		L\$DTP::			;PTR. TO DISPATCH TABLE
002040	002124'		.WORD	L\$DISPATCH	
002042		L\$PRIO::			;DIAGNOSTIC RUN PRIORITY
002042	000000		.WORD	0	
002044		L\$ENVI::			;FLAGS DESCRIBE HOW IT WAS SETUP
002044	000000		.WORD	0	
002046		L\$EXP1::			;EXPANSION WORD
002046	000000		.WORD	0	
002050		L\$MREV::			;SVC REV AND EDIT #
002050	003		.BYTE	C\$REVISION	
002051	003		.BYTE	C\$EDIT	
002052		L\$EF::			;DIAG. EVENT FLAGS
002052	000000		.WORD	0	
002054	000000		.WORD	0	
002056		L\$SPL::			
002056	000000		.WORD	0	
002060		L\$DEVP::			; POINTER TO DEVICE TYPE LIST
002060	003400'		.WORD	L\$DVTYP	
002062		L\$REPP::			;PTR. TO REPORT CODE
002062	022434'		.WORD	L\$RPT	
002064		L\$EXPA::			
002064	000000		.WORD	0	
002066		L\$EXPS::			
002066	000000		.WORD	0	
002070		L\$AUT::			;PTR. TO ADD UNIT CODE
002070	022122'		.WORD	L\$AU	
002072		L\$DUT::			;PTR. TO DROP UNIT CODE
002072	022220'		.WORD	L\$DU	
002074		L\$LUN::			;LUN FOR EXERCISERS TO FILL
002074	000000		.WORD	0	
002076		L\$DESP::			;POINTER TO DIAG. DESCRIPTION
002076	003406'		.WORD	L\$DESC	
002100		L\$LOAD::			;GENERATE SPECIAL AUTOLOAD EMT
002100	104035		EMT	E\$LOAD	
002102		L\$ETP::			;POINTER TO ERRIBL
002102	000000		.WORD	0	
002104		L\$ICP::			;PTR. TO INIT CODE
002104	021326'		.WORD	L\$INIT	
002106		L\$CCP::			;PTR. TO CLEAN-UP CODE
002106	022406'		.WORD	L\$CLEAN	
002110		L\$ACP::			;PTR. TO AUTO CODE
002110	022326'		.WORD	L\$AUTO	
002112		L\$PRT::			;PTR. TO PROTECT TABLE
002112	021316'		.WORD	L\$PROT	
002114		L\$TEST::			;TEST NUMBER
002114	000000		.WORD	0	
002116		L\$DLY::			;DELAY COUNT
002116	000000		.WORD	0	
002120		L\$HIME::			;PTR. TO HIGH MEM
002120	000000		.WORD	0	

34
35
36
37
38

.SBTTL DISPATCH TABLE

J2

TSV2 - PROGRAM HEADER MACRO M1113 07-FEB-84 10:58
SOFTWARE P-TABLE

SEQ 022

76	002172	000310	GERRMAX::	.WORD	200.	; GLOBAL (PER UNIT) ERROR LIMIT
77	002174		ENDSW			
	002174		L10001:			
78						
79	002174		ENDMOD			
80						
81						
84						
85						

K2

TSV3 - GLOBAL AREAS
SOFTWARE P-TABLE

MACRO M1113 07-FEB-84 10:58

SEQ 023

7
8
13
19
20 002174
002174
21
22
23
24
25
26
27
28
29
33 002174

.TITLE TSV3 - GLOBAL AREAS
.SBTTL GLOBAL EQUATES SECTION

BGNMOD TSV3
TSV3::

.SBTTL GLOBAL EQUATES SECTION

;;
; THE GLOBAL EQUATES SECTION CONTAINS PROGRAM EQUATES THAT
; ARE USED IN MORE THAN ONE TEST.
;--

EQUALS ; GET STANDARD EQUATES.

; BIT DEFINITIONS

100000	BIT15==	100000
040000	BIT14==	40000
020000	BIT13==	20000
010000	BIT12==	10000
004000	BIT11==	4000
002000	BIT10==	2000
001000	BIT09==	1000
000400	BIT08==	400
000200	BIT07==	200
000100	BIT06==	100
000040	BIT05==	40
000020	BIT04==	20
000010	BIT03==	10
000004	BIT02==	4
000002	BIT01==	2
000001	BIT00==	1
001000	BIT9==	BIT09
000400	BIT8==	BIT08
000200	BIT7==	BIT07
000100	BIT6==	BIT06
000040	BIT5==	BIT05
000020	BIT4==	BIT04
000010	BIT3==	BIT03
000004	BIT2==	BIT02
000002	BIT1==	BIT01
000001	BIT0==	BIT00

; EVENT FLAG DEFINITIONS

; EF32:EF17 RESERVED FOR SUPERVISOR TO PROGRAM COMMUNICATION

000040	EF.START==	32.	; START COMMAND WAS ISSUED
000037	EF.RESTART==	31.	; RESTART COMMAND WAS ISSUED
000036	EF.CONTINUE==	30.	; CONTINUE COMMAND WAS ISSUED
000035	EF.NEW==	29.	; A NEW PASS HAS BEEN STARTED
000034	EF.PWR==	28.	; A POWER-FAIL/POWER-UP OCCURRED

;

```
                                ; PRIORITY LEVEL DEFINITIONS
                                ;
000340      PRI07== 340
000300      PRI06== 300
000240      PRI05== 240
000200      PRI04== 200
000140      PRI03== 140
000100      PRI02== 100
000040      PRI01== 40
000000      PRI00== 0

                                ; OPERATOR FLAG BITS
                                ;
000004      EVL==      4
000010      LOT==     10
000020      ADR==     20
000040      IDU==     40
000100      ISR==    100
000200      UAM==    200
000400      BOE==    400
001000      PNT==   1000
002000      PRI==   2000
004000      IXE==   4000
010000      IBE==  10000
020000      IER==  20000
040000      LCE==  40000
100000      HOE== 100000

34
35 002174      KT11
                .SBTTL MEMORY MANAGEMENT DEFINITIONS
                ;*KT11 VECTOR ADDRESS
000250      MMVEC= 250
                ;*KT11 STATUS REGISTER ADDRESSES
177572      SR0=   177572
177574      SR1=   177574
177576      SR2=   177576
172516      SR3=   172516
                .IF NB
                ;*USER "I" PAGE DESCRIPTOR REGISTERS
UIPDR0= 177600
UIPDR1= 177602
UIPDR2= 177604
UIPDR3= 177606
UIPDR4= 177610
UIPDR5= 177612
UIPDR6= 177614
UIPDR7= 177616
                .IF NB
                ;*USER "D" PAGE DESCRIPTOR REGISTERS
UDPDR0= 177620
UDPDR1= 177622
UDPDR2= 177624
UDPDR3= 177626
UDPDR4= 177630
UDPDR5= 177632
UDPDR6= 177634
UDPDR7= 177636
```

;DEFINE MEMORY MANAGEMENT REGISTERS

M2

TSV3 GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
MEMORY MANAGEMENT DEFINITIONS

SEQ 025

```
.ENDC
;*USER "I" PAGE ADDRESS REGISTERS
UIPAR0= 177640
UIPAR1= 177642
UIPAR2= 177644
UIPAR3= 177646
UIPAR4= 177650
UIPAR5= 177652
UIPAR6= 177654
UIPAR7= 177656
.IF NB
;*USER "D" PAGE ADDRESS REGISTERS
UDPAR0= 177660
UDPAR1= 177662
UDPAR2= 177664
UDPAR3= 177666
UDPAR4= 177670
UDPAR5= 177672
UDPAR6= 177674
UDPAR7= 177676
.ENDC
.ENDC
.IF NB
;*SUPERVISOR "I" PAGE DESCRIPTOR REGISTERS
SIPDR0= 172200
SIPDR1= 172202
SIPDR2= 172204
SIPDR3= 172206
SIPDR4= 172210
SIPDR5= 172212
SIPDR6= 172214
SIPDR7= 172216
.IF NB
;*SUPERVISOR "D" PAGE DESCRIPTOR REGISTERS
SDPDR0= 172220
SDPDR1= 172222
SDPDR2= 172224
SDPDR3= 172226
SDPDR4= 172230
SDPDR5= 172232
SDPDR6= 172234
SDPDR7= 172236
.ENDC
;*SUPERVISOR "I" PAGE ADDRESS REGISTERS
SIPAR0= 172240
SIPAR1= 172242
SIPAR2= 172244
SIPAR3= 172246
SIPAR4= 172250
SIPAR5= 172252
SIPAR6= 172254
SIPAR7= 172256
.IF NB
;*SUPERVISOR "D" PAGE ADDRESS REGISTERS
SDPAR0= 172260
SDPAR1= 172262
SDPAR2= 172264
```


N2

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
MEMORY MANAGEMENT DEFINITIONS

SEQ 026

```

SDPAR3= 172266
SDPAR4= 172270
SDPAR5= 172272
SDPAR6= 172274
SDPAR7= 172276
.ENDC
.ENDC
; *KERNEL "I" PAGE DESCRIPTOR REGISTERS
172300 KIPDR0= 172300
172302 KIPDR1= 172302
172304 KIPDR2= 172304
172306 KIPDR3= 172306
172310 KIPDR4= 172310
172312 KIPDR5= 172312
172314 KIPDR6= 172314
172316 KIPDR7= 172316
; IF NB
; *KERNEL "D" PAGE
DESCRIPTOR REGISTERS
KDPDR0= 172320
KDPDR1= 172322
KDPDR2= 172324
KDPDR3= 172326
KDPDR4= 172330
KDPDR5= 172332
KDPDR6= 172334
KDPDR7= 172336
.ENDC
; *KERNEL "I" PAGE ADDRESS REGISTERS
172340 KIPAR0= 172340
172342 KIPAR1= 172342
172344 KIPAR2= 172344
172346 KIPAR3= 172346
172350 KIPAR4= 172350
172352 KIPAR5= 172352
172354 KIPAR6= 172354
172356 KIPAR7= 172356
; IF NB
; *KERNEL "D" PAGE ADDRESS REGISTERS
KDPAR0= 172360
KDPAR1= 172362
KDPAR2= 172364
KDPAR3= 172366
KDPAR4= 172370
KDPAR5= 172372
KDPAR6= 172374
KDPAR7= 172376
.ENDC

```

39
40
41
42
43
44
45
46
47

000004

.SBTTL TSUOS REGISTER AND PACKET DEFINITIONS

```

;
; SOME GENERAL EQUATES.
;

```

ERRVEC=

4

; POINTER TO ERROR VECTOR FOR BUG . . . QUI.

TSU05 REGISTER AND PACKET DEFINITIONS

48	000060	TTIVEC--	60	INTERRUPT VECTOR FOR CONSOLE INPUT
49	177560	TTICSR--	177560	BUS ADDRESS OF CONSOLE INPUT
50	177562	TTIBFR--	177562	CONSOLE INPUT DATA BUFFER
51	177520	BDVPCR--	177520	BDV11 PAGE CONTROL REGISTER
52				
53				
54		; BIT DEFINITIONS FOR TSSR REGISTER		
55				
56				
57	100000	SC-	BIT15	SPECIAL CONDITION
58	040000	BIE-	BIT14	BUS INTERFACE ERROR
59	020000	SCE-	BIT13	SANITY CHECK ERROR
60	010000	RMR-	BIT12	MODIFICATION REFUSED
61	004000	NXM-	BIT11	NONEXISTANT MEMORY ERROR
62	002000	NBA-	BIT10	NEED BUFFER ADDRESS
63	001400	NIADDR-	BIT9:BIT8	EXTENDED ADDRESS BITS
64	000200	SSR-	BIT7	SUB SYSTEM READY
65	000100	OFL-	BIT6	OFF LINE BIT
66	000060	FATERR-	BIT4:BIT5	FATAL TERMINATION ERROR CODES
67	000016	TERCLS-	BIT3:BIT2:BIT1	TERMINATION CODES
68				
69				
70				
71				
72		; BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 0		
73		;(XST0)		
74				
75				
76				
77	100000	XSOTM-	BIT15	TAPE MARK DETECTED
78	040000	XSORLS-	BIT14	RECORD LENGTH SHORT
79	020000	XSOLE-	BIT13	LOGICAL END OF TAPE
80	010000	XSORLL-	BIT12	RECORD LENGTH LONG
81	004000	XSOLE-	BIT11	WRITE LOCK ERROR
82	002000	XSONEF-	BIT10	NON EXECUTABLE FUNCTION
83	001000	XSOILC-	BIT9	ILLEGAL COMMAND
84	000400	XSOILA-	BIT8	ILLEGAL ADDRESS
85	000200	XSOHOT-	BIT7	TAPE IN MOTION
86	000100	XSOONL-	BIT6	TRANSPORT ON LINE
87	000040	XSOIE-	BIT5	INTERRUPT ENABLE
88	000020	XSOVCK-	BIT4	VOLUME CHECK BIT
89	000010	XSOPE-	BIT3	PHASE ENCODED DRIVE
90	000004	XSOWLK-	BIT2	WRITE LOCKED
91	000002	XSOBOT-	BIT1	BEGINNING OF TAPE
92	000001	XSOEOT-	BIT0	END OF TAPE
93				
94				
95				
96		; BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 1		
97		;(XST1)		
98				
99	100000	X1.DLT	BIT15	DATA LATE
100	040000	X1.SPARE-	BIT14	NOT USED
101	020000	X1.COR	BIT13	CORRECTABLE DATA ERROR
102	017375	X1.MBZ	BIT12:BIT11:BIT10:BIT9:BIT7:BIT6:BIT5:BIT4:BIT3:BIT2:BIT0	ALWAYS 0
103	000400	X1.RBP	BIT8	READ BUS PARITY ERROR
104	000002	X1.UNC	BIT1	UNCORRECTABLE DATA OR HARD ERROR

C3

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
TSU05 REGISTER AND PACKET DEFINITIONS

SEQ 028

```

105
106
107
108
109
110      100000
111      040000
112      035400
113      002000
114      000200
115      000100
116      000077
117      000007
118
119
120
121
122
123      177400
124      000200
125      000100
126      000040
127      000020
128      000010
129      000006
130      000001
131
132
133
134
135
136      100000
137      040000
138      020000
139      017400
140      000377
141
142
143
144
145
146
147
148
149      000006
150      000006
151
152
153
154
155
156
157
158      000000
159      000000
160      000001
161      000001

;
;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 2
;(XST2)
;-
X2.OPM  = BIT15      ;OPERATION IN PROGRESS (TAPE MOVING)
X2.RCE  = BIT14      ;RAM CHECKSUM ERROR
X2.SPARE= BIT13+BIT12+BIT11+BIT9+BIT8 ;NOT USED BY TSU'S (ALWAYS=0)
X2.WCF  = BIT10      ;WRITE CLOCK FAILURE (FIFO NOT EMPTIED BY TRANSPORT)
X2.EXTF = BIT7       ;IF WRITE CHAR CMD THEN = EXTENDED FEATURES ENABLED
X2.BUFE = BIT6       ;IF WRITE CHAR CMD THEN = BUFFERING ENABLED
X2.REV  = 000077     ;IF WRITE CHAR CMD THEN = MICROCODE REVISION LEVEL
X2.UNIT = BIT2+BIT1+BIT0 ;IF GET STATUS THEN = CURRENTLY SELECTED UNIT NO.

;
;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 3
;(XST3)
;-
X3.MDE  = 177400     ;MICRO-DIAGNOSTIC ERROR CODE
X3.SPARE= BIT7       ;NOT USED BY TSU05
X3.OPI  = BIT6       ;OPERATION INCOMPLETE
X3.REV  = BIT5       ;REVERSE
X3.TRF  = BIT4       ;TRANSPORT RESPONSE FAILURE
X3.DCK  = BIT3       ;DENSITY CHECK
X3.MBZ  = BIT2+BIT1  ;NOT USED ALWAYS 0
X3.RIB  = BIT0       ;REVERSE INTO BOT

;
;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 4
;(XST4)
;-
X4.HSP  = BIT15      ;HIGH SPEED
X4.RCE  = BIT14      ;RETRY COUNT EXCEEDED
X4.TSM  = BIT13      ;TRANSPORT SPECIAL MODE
X4.MBZ  = BIT12+BIT11+BIT10+BIT9+BIT8 ;NOT USED ALWAYS 0
X4.WRC  = 000377     ;WRITE RETRY COUNT FIELD

;
;TSSR TERMINATION CODES (BIT 0-2)
;-
TSREJ= 3+2          ;COMMAND REJECTED
UNREC= 6             ;UNRECOVERABLE ERROR

;
;DEVICE REGISTER OFFSETS
;-
TSBA.. 0
TSDB.. 0             ;TSDB/TSBA REGISTER
TSBAH.. 1
TSDBH.. 1            ;TSDB/TSBA REGISTER HIGH BYTE

```

```

162      000002      TSSR== 2      ;TSSR REGISTER
163      000003      TSSRH== 3      ;TSSR REGISTER HIGH BYTE
164
165      ;+
166      ; TSDB ADDRESS BIT DEFINITIONS
167      ;+
168      000003      A1716 = BIT1+BIT0      ;ADDRESS BITS 17:16 ARE IN 1:0
169
170      ;+
171      ; COMMAND DEFINITIONS
172      ;+
173      000017      P.GETSTAT = 17      ;GET STATUS
174      000013      P.INIT = 13      ;INITIALIZE
175      000012      P.CONTROL = 12      ;CONTROL COMMANDS
176      000011      P.FORMAT = 11      ;FORMAT
177      000010      P.POSITION = 10      ;POSITION
178      000006      P.WRTSUB = 6      ;SUBSYSTEM WRITE
179      000005      P.WRITE = 5      ;WRITE
180      000004      P.WRTCHAR = 4      ;WRITE CHARACTERISTICS
181      000001      P.READ = 1      ;READ
182
183      ;+
184      ; COMMAND PACKET HEADER WORD BIT DEFINITIONS
185      ;+
186      100000      P.ACK = BIT15      ;BUFFER AVAIL FOR CONTROLLER
187      040000      P.CVC = BIT14      ;CLEAR VOLUME CHECK
188      020000      P.OPP = BIT13      ;REVERSE SEQUENCE OF DATA BITS
189      010000      P.SWB = BIT12      ;SWAP BYTES IN MEMORY
190      007400      P.MODE = BIT11:BIT10:BIT9:BIT8 ;EXTENDED COMMAND MODE FIELD
191      000200      P.IE = BIT7      ;INTERRUPT ENABLE
192      000140      P.FMT = BIT6:BIT5      ;PACKET HEADER TYPE (ALWAYS=0)
193      000037      P.CMD = 37      ;MAJOR COMMAND FIELD
194
195      ;+
196      ; CONTROL COMMAND MODE CODES
197      ;+
198      000000      PC.RELEASE = 0*256.      ;RELEASE BUFFER
199      000400      PC.REWIND = 1*256.      ;REWIND
200      001000      PC.NOOP = 2*256.      ;NO-OP
201      002000      PC.IEREW = 4*256.      ;REWIND IMMEDIATE INTERRUPT
202      002400      PC.ERASE = 5*256.      ;SECURITY ERASE
203
204      ;+
205      ; CONTROLLER RAM DEFINITIONS
206      ;+
207      000167      RMCHBEG = 167      ;CHARACTERISTICS IO DATA BEGIN RAM ADDRESS
208      000200      RMCHEND = 200      ;CHARACTERISTICS IO DATA END RAM ADDRESS
209      000201      RMPKTBEG = 201      ;COMMAND PACKET BEGIN RAM ADDRESS
210      000210      RMPKTEND = 210      ;COMMAND PACKET END RAM ADDRESS
211      000215      RMSG8BEG = 215      ;MESSAGE BUFFER BEGIN RAM ADDRESS
212      000234      RMSG8END = 234      ;MESSAGE BUFFER END RAM ADDRESS
213
214      ;+
215      ; REGISTER DEFINITIONS IN THE MESSAGE BUFFER
216      ;+
217
218      000006      XST0== 6      ;EXTENDED STATUS REGISTER 0 (WORD 4)

```

```

276      ;*
277      ;BSEL1 CODES FOR WRITE FORMAT
278      ;*
279      000200      WF.IHISP      = BIT7      ;IHISP - HIGH SPEED
280      000100      WF.IWRT      = BIT6      ;IWRT  - WRITE
281      000040      WF.IREV      = BIT5      ;IREV  - REVERSE
282      000020      WF.IWFM      = BIT4      ;IWFM  - WRITE FILE MARK
283      000010      WF.IEDIT      = BIT3      ;IEDIT - EDIT
284      000004      WF.IERASE      = BIT2      ;IERASE - ERASE
285      000002      WF.I3RESV      = BIT1      ;IRESV3 - RESERVED #3
286      000001      WF.I4RESV      = BIT0      ;IRESV4 - RESERVED #4
287
288
289      ;*
290      ;BSEL1 CODES FOR WRITE MISCELLANEOUS SUBCOMMAND
291      ;*
292      000200      MS.EXT      = BIT7      ;INVERT SENSE OF EXTENDED FEATURES SWITCH
293      000020      MS.RSFIFO      = BIT4      ;RESET FIFO AND INPUT PARITY ERRORR
294      000010      MS.RSTAPE      = BIT3      ;RESET TAPE STATUS IN 2 FLIP-FLOPS
295      000006      MS.ATTN      = BIT2:BIT1      ;ATTENTION TRIGGER FIELD
296      000001      MS.RSD      = BIT0      ;RESET TIMER A,B THEN DELAY TIMES IN SEL2
297
298      ;*
299      ; MS.ATTN SUBCODES
300      ;*
301      000000      MSA.NOP      = 0*2      ;NO-OP (NOTHING TRIGGERED)
302      000002      MSA.VOL      = 1*2      ;SIMULATE ON-LINE/OFF-LINE TRANSITION
303      000004      MSA.NRAM      = 2*2      ;FORCE NON-FATAL RAM ERROR (FORCES ERRCODE 54)
304      000006      MSA.FRAME      = 3*2      ;FORCE FATAL RAM ERROR (CAUSES SCE TO SET)
305
306      ;*
307      ; WRITE SUBSYSTEM WRITE NPR BSEL1 BIT DEFINITIONS
308      ;*
309      000200      NP.IR      = BIT7      ;INTERRUPT REQUEST (0-1 TRANSITION)
310      000100      NP.OUT      = BIT6      ;TAPE DATA DIRECTION OUT (0= IN)
311      000040      NP.LOOP      = BIT5      ;ENABLE TRANSPORT LOOPBACK
312      000020      NP.WRP      = BIT4      ;WRITE CORRECT PARIYY (SET=0 TO WRITE WRONG)
313
314      ;*
315      ; READ STATUS MESSAGE BUFFER BIT DEFINITIONS
316      ;*
317      000200      S2.DIM      = BIT7      ;WORD #9 BYTE 2 DATA IN MISS
318      000100      S2.ILW      = BIT6      ;ILW H
319      000040      S2.OUTRDY      = BIT5      ;OUT RDY H
320      000020      S2.INRDY      = BIT4      ;IN RDY H
321      000010      S2.ATIMR      = BIT3      ;TIMER A FLAG H
322      000004      S2.BTIMR      = BIT2      ;TIMER B FLAG H
323      000003      S2.UNDEF      = BIT1:BIT0      ;(UNDEFINED)
324      100000      S1.PARIN      = BIT15      ;WORD #8 BYTE 1 PARIN H
325      040000      S1.I2RESV      = BIT14      ;IRESV2
326      020000      S1.I1RESV      = BIT13      ;IRESV1
327      010000      S1.IEOT      = BIT12      ;IEOT L
328      004000      S1.IIDENT      = BIT11      ;IIDENT H
329      002000      S1.ICER      = BIT10      ;ICER H
330      001000      S1.IFMK      = BIT9      ;IFMK H
331      000400      S1.IHER      = BIT8      ;IHER H
332      000200      S0.ISPEED      = BIT7      ;WORD #8 BYTE 0 ISPEED H
333      000100      S0.IRDY      = BIT6      ;IRDY L
334      000040      S0.IONL      = BIT5      ;IONL L

```

G3

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
TSU05 REGISTER AND PACKET DEFINITIONS

SEQ 032

```

333      000020      SO.ILDP      ▫ BIT4      ;      ILDP L
334      000010      SO.IDBY      ▫ BIT3      ;      IDBY L
335      C00004      SO.IRWD      ▫ BIT2      ;      IRWD L
336      000002      SO.IF8Y      ▫ BIT1      ;      IF8Y L
337      000001      SO.IFPT      ▫ BIT0      ;      IFPT L
338
339      ;*
340      ;UNIBUS MAP DEFINATIONS
341      ;
342      MMRO= 170200
343
344      .SBTTL SPECIAL MACROS AND OPDEF:
345
346
347      ;*
348      ;SAVE GENERAL REGS 1 TO 5
349      ;
350
351      .MACRO SAVREG
352      JSR R5,REGSAV
353      .ENDM
354
355      ;*
356      ; MACRO TO FORCE AN ERROR
357      ;
358      .MACRO FORCERROR TAG,NOTSSR
359      .NLIST
360      .IF NDF LISTALL, .NLIST
361      .LIST
362      .IF B NOTSSR
363      MOV TSSR(R5),R1 ;READ TSSR
364      .ENDC
365      MOV FORCER,FORCER ;IS FORCER SET? (LEAVE C BIT ALONE)
366      BNE TAG ;BR IF YES
367      .NLIST
368      .IF NDF LISTALL, .LIST
369      .LIST
370      .ENDM
371
372      ;*
373      ; MACRO TO FORCE AN EXIT TO AVOID SECTION ITERATIONS
374      ; WILL EXIT TO A LABEL IF FORCER IS NEGATIVE
375      ; SO TO FORCE ERRORS AND EXIT ON 1 ERROR SET
376      ; FORCER TO 177777
377      ; TO FORCE ERRORS AND ITERATIONS SET FORCER TO 1.
378      ;
379      .MACRO FORCEEXIT TAG
380      .NLIST
381      .IF NDF LISTALL, .NLIST
382      .LIST
383      MOV FORCER,FORCER ;IS FORCER NEGATIVE?
384      BMI TAG ;BR IF YES
385      .NLIST
386      .IF NDF LISTALL, .LIST
387      .LIST
388      .ENDM
389      ;*

```

```

390      ; MACRO TO INCREMENT ERROR COUNTS
391      ;-
392      .MACRO NEXT.ERRNO
393      .NLIST
394      ;;;.IIF NDF LISTALL, .NLIST
395      ERRNO=ERRNO+1
396      ;;;.IIF NDF LISTALL, .LIST
397      .LIST
398      .ENDM
399
400      ;+
401      ;MACRO TO PERFORM XOR
402      ;-
403
404      .MACRO XOR      A,B
405      MOV      A,-(SP)
406      BIC      B,(SP)
407      BIC      A,3
408      BIS      (SP),B
409      .ENDM
410
411      000000      EN=0      ; INITIALIZE ERROR NUMBER
412      .SBTTL  FORCER - FORCE ERROR FLAG
413
414      ;
415      ; THE FOLLOWING LOCATIONS MAY BE PATCHED BY THE USER
416      ; TO OBTAIN THE RESULTS DESCRIBED FOR EACH.
417      ;
418
419      002174  000000  FORCER::      0      ; FORCE TYPE ALL HARD ERRORS (THE ONES CALLED -
420      ; - BY THE MACRO "IFERROR"). AN ERROR NEED NOT -
421      ; - EXIST, JUST ASSUME AND TYPE THE MESSAGE.
422
423
424
425      .SBTTL  GLOBAL DATA SECTION
426
427      ;++
428      ;THE GLOBAL DATA SECTION CONTAINS DATA THAT ARE USED
429      ;IN MORE THAN ONE TEST.
430      ;--
431
432      ;
433      ;THE FOLLOWING DATA ARE SET FOR EACH UNIT AT INIT TIME.
434      ;SINGLE UNIT DEFAULTS (LISTED) ARE IN THE DEFAULT P-TABLE.
435      ;
436      002176  000000  EPRTSW::      .WORD      0      ;PRINT SWITCH
437      002200  000000  UNITN::      .WORD      0      ;UNIT # UNDER TEST.
438      002202  000000  QVP::      .WORD      0      ;QUICK VERIFY FLAG.
439      002204  000000  CSRADDR::      .WORD      0      ;ADDRESS OF CSR FOR CURRENT DEVICE.
440      002206  000224  IVEC::      .WORD      224      ;INTERRUPT VECTOR
441      002210  000200  IPRI::      .WORD      PRI04      ;INTERRUPT PRIORITY.
442      002212  000000  TSTCNT::      .WORD      0      ;NUMBER OF TESTS RUN IN THIS PASS
443      002214  000000  LOOPCNT::      .WORD      0      ;REMAINING ITERATION COUNT FOR TEST
444      002216  000000  DEVCNT::      .WORD      0      ;NUMBER OF DEVICE UNDER TEST
445      002220  000000  FATFLG::      .WORD      0      ;SET IF FATAL ERROR IS DETECTED IN TEST
446      002222  000000  INTRECV::      .WORD      0      ;SET IF TAPE INTERRUPT WAS RECEIVED

```


J3

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
TSTBLK - TEST DATA TABLE

SEQ 035

504	003020	177773	.WORD	+CBIT2	
505	003022	177767	.WORD	+CBIT3	
506	003024	177757	.WORD	+CBIT4	
507	003026	177737	.WORD	+CBIT5	
508	003030	177677	.WORD	+CBIT6	
509	003032	177577	.WORD	+CBIT7	
510	003034	177377	.WORD	+CBIT8	
511	003036	176777	.WORD	+CBIT9	
512	003040	175777	.WORD	+CBIT10	
513	003042	173777	.WORD	+CBIT11	
514	003044	167777	.WORD	+CBIT12	
515	003046	157777	.WORD	+CBIT13	
516	003050	137777	.WORD	+CBIT14	
517	003052	077777	.WORD	+CBIT15	
518	003054	125252	.WORD	125252	;ALTERNATING ONES, ZEROS
519	003056	052525	.WORD	052525	;ALTERNATING ONES, ZERO OPPOSITE FROM ABOVE
520		003060	TBLEND="."		
521					
522					
523					
524			.SBTTL GLOBAL ENVIRONMENT STORAGE		
525			;STORAGE FOR DEVICE REGISTERS		
526					
527	003060	000000 100000 000000	DUMMY:	0,100000,0,0	;DUMMY DEVICE REGISTERS...
528	003070	000000 000000 000000		0,0,0,0,0,0,0,0	
529			;...FOR MULTI-UNIT CHECKOUT.		
530					
531					
532	003110	000000	DUFLG::	.WORD 0	; "DROPPED UNIT" FLAG.
533					;INHIBITS CODE IN "CLEAN-UP".
534	003112	000000	NODEV::	.WORD 0	;FLAG TO SAY NO DEVICE.
535					
536	003114	000000	TEMP1::	.WORD 0	;SOME TEMP LOCATIONS.
537	003116	000000	TEMP2::	.WORD 0	
538	003120	000000	XXCOMM::	.WORD 0	;XXDP+ COMM BLOCK POINTER.
539	003122	000000	FREE::	.WORD 0	;1ST FREE MEMORY ADDRESS...
540	003124	000000	FRESIZ::	.WORD 0	;...AND SIZE (IN WORDS).
541	003126	000000	FREEHI::	.WORD 0	;LAST WORD IN FREE SPACE
542	003130	000000	KTFLG::	.WORD 0	;KT11, MEM AVAIL FLAG -
543					; - .WORD 0 = <24K OR NO KT -
544					; - NZ = >24K AND KT.
545	003132	000000	KTENABLE::	.WORD 0	;SET BY TEST ROUTINES TO FLAG >28K UNDER TEST
546	003134	000000	NXMFLG::	.WORD 0	;SET IF WE CAN TEST CLEARED OTHERWISE
547	003136	000000	NXMLO::	.WORD 0	;NXM LO ADDRESS BITS
548	003140	000000	NXMHI::	.WORD 0	;NXM HI ADDRESS BITS FOR DAL'S 16-21
549	003142	000000	T23A::	.WORD 0	;PROCESSOR TYPE FLAG
550	003144	000000	T23B::	.WORD 0	;PROCESSOR TYPE FLAG B
551	003146	000000	T3BFLG::	.WORD 0	;TEST 3B FLAG +0
552	003150	002000	PST32W::	.WORD 2000	;32W BLOCK ADDRESS FOR 32K START
553	003152	000000	SIFLAG::	.WORD 0	
554	003154	000000	BADDAT::	.WORD 0	;ACTUAL DATA
555	003156	000000	GDDAT::	.WORD 0	;EXPECTED DATA
556	003160	000000	LOOPFL::	.WORD 0	
557	003162		CTAB::		;CONFIGURATION TABLES.
558	003162	000000	CTABM::	.WORD 0	;CONFIG WORK.
559	003164	000000		.WORD 0	
560	003166	000000		.WORD 0	

K3

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
GLOBAL ENVIRONMENT STORAGE

SEQ 036

```

561 003170 000000      .WORD 0
562 003172 177777      .WORD -1      ;END OF MEM TABLE.
563 003174
564      CYTABE::
565      ;ERROR STATISTICS TABLE (1 WORD PER UNIT), 64 UNITS MAX:
566      :
567      : 0      =      UNIT NOT TESTED
568      : 100000 =      UNIT ONLINE, NO ERRORS
569      : 10XXXX =      UNIT ONLINE, ENCOUNTERED XXXX ERRORS
570      : 160000 =      UNIT DROPPED, NON-EXISTENT DEVICE REGISTER
571      : 160001 =      UNIT DROPPED, NOT IDLE AT START
572      : 14XXXX =      UNIT DROPPED, ENCOUNTERED XXXX ERRORS
573 003174
574 003374 000000      ERTAB::      .BLKW 64,
575      ERTABE:      .WORD 0
576 003376 000000      SKIPT: .WORD 0      ;1=SKIP SUBTEST 0=NO SKIP OF SUBTEST
577
578      .SBTTL GLOBAL TEXT MESSAGES
579      ;++
580      ; THE GLOBAL TEXT SECTION CONTAINS FORMAT STATEMENTS,
581      ; MESSAGES, AND ASCII INFORMATION THAT ARE USED IN
582      ; MORE THAN ONE TEST.
583      ;--
584
585
586
587      ;+
588      ;NAMES OF DEVICES SUPPORTED
589      ;-
590
591 003400      DEV TYP <TSU05>
592 003400      L$DVTYP::
593 003400      .ASCIIZ /TSU05/
594      .EVEN
595
596      ;+
597      ;TEST DESCRIPTION
598      ;-
599 003406      DESCRIPT <**** TSU05 DIAG PART 1 - REPLACE M7455 IF ERROR ****>
600 003406      L$DESC::
601 003406      .ASCIIZ /**** TSU05 DIAG PART 1 - REPLACE M7455 IF ERROR ****/
602      .EVEN
603
604
605
606
607      ;+
608      ;BIT TO ASCII CONVERSION FOR TSSR REGISTER
609      ;-
610
611 625 003474 003534' 003537' 003543' TSSRBIT::      .WORD 1$,2$,3$,4$,5$,6$,7$,8$
612 626 003514 003575' 003601' 003605'      .WORD 9$,10$,11$,12$,13$,14$,15$,16$
613 627 003534      123      103      000 1$:      .ASCIIZ 'SC'
614 628 003537      102      111      105 2$:      .ASCIIZ 'BIE'
615 629 003543      123      103      105 3$:      .ASCIIZ 'SCE'
616 630 003547      122      115      122 4$:      .ASCIIZ 'RMR'
617 631 003553      116      130      115 5$:      .ASCIIZ 'NXM'
618 632 003557      116      102      101 6$:      .ASCIIZ 'NBA'

```


L3

TSV3 - GLOBAL AREAS
GLOBAL TEXT MESSAGES

MACRO M1113 07-FEB-84 10:58

SEQ 037

```

633 003563      102      111      124  7$: .ASCIZ 'BIT9'
634 003570      102      111      124  8$: .ASCIZ 'BIT8'
635 003575      123      123      122  9$: .ASCIZ 'SSR'
636 003601      117      106      114 10$: .ASCIZ 'OFL'
637 003605      102      111      124 11$: .ASCIZ 'BIT5'
638 003612      102      111      124 12$: .ASCIZ 'BIT4'
639 003617      102      111      124 13$: .ASCIZ 'BIT3'
640 003624      102      111      124 14$: .ASCIZ 'BIT2'
641 003631      102      111      124 15$: .ASCIZ 'BIT1'
642 003636      102      111      124 16$: .ASCIZ 'BIT0'
643          .EVEN
644 003644      124      123      123 SFIERR: .ASCIZ 'TSSR ERROR AFTER SOFT INIT'
645 003677      124      123      123 SFHERR: .ASCIZ 'TSSR ERROR AFTER BUS RESET'
646 003732      040      040      116 NXR: .ASCIZ / NON-EXISTANT DEVICE REGISTER/
647 003771      045      101      040 NXR$: .ASCIZ /#A ADDRESS: #06/
648 004012      045      101      040 TSSX: .ASCIZ /#A TSBA,TSSR EXP'D: #06#A,#06#N/
649 004052      045      101      040      .ASCIZ /#A TSBA,TSSR REC'D: #06#A,#06/
650 004111      045      116      045 FUSI: .ASCIZ /#N#A/
651 004115      040      040      125 USI: .ASCIZ / UNEXPECTED INTERRUPT/
652 004144      040      040      111 NSI: .ASCIZ / INTERRUPT EXPECTED, NOT RECEIVED/
653 004207      045      116      045 FNCINTR: .ASCIZ /#N#A/
654 004213      040      040      116 NOINTR: .ASCIZ / NO INTERRUPT WAS GENERATED/
655 004250      040      040      111 IFAULT: .ASCIZ / INTERRUPT FAULT/
656 004272      045      101      040 INTX: .ASCIZ /#A CPU PC: #06#A TSBA: #06/
657 004327      040      040      042 NOINIT: .ASCIZ / "BUS-INIT" DIDN'T INITIALIZE CONTROLLER/
658 004401      040      040      042 NSINIT: .ASCIZ / "SOFT-INIT" DIDN'T INITIALIZE THE DPU/
659 004451      040      040      042 BRINIT: .ASCIZ / "BUS-RESET" DIDN'T INITIALIZE THE DPU/
660
661 004521      000          NUL: .ASCIZ //
662 004522      045      116      000 NULCR: .ASCIZ /#N/
663 004525      045      101      040 EXPGOT: .ASCIZ /#A EXP'D: #06#A, REC'D: #06/
664 004561      045      116      045 EXPGT2: .ASCIZ /#N#A EXP'D: #06#A, #06#N#A REC'D: #0#A, #06/
665 004635      045      101      040 DUAD12: .ASCIZ /#A REG(W) WRITTEN TO: #06#A REG(R) READ: EXP'D: #06#A, REC'D: #06/
666 004737      122      101      115 PKTRAM: .ASCIZ 'RAM Contents Do Not Match Packet Sent'
667 005005      040      040      103 SCME: .ASCIZ / CONFIG DOESN'T MATCH MFG. MASTER/
668 005050      127      122      111 WRMSG: .ASCIZ 'WRITE CHARACTERISTICS Failed'
669 005105      124      123      123 WRTERR: .ASCIZ 'TSSR Incorrect After WRITE Command, More Bits Set Than SSR'
670 005200      124      123      123 RDERR: .ASCIZ 'TSSR Incorrect After READ Command, More Bits Set Than SSR'
671 005272      106      101      124 SCHERR: .ASCIZ 'FATAL ERROR IN SUBTEST - CHECK TAPE,CABLES,TRANSPORT etc.'
672 005364      105      122      122 RETERR: .ASCIZ 'ERROR IN SUBTEST - WRITE DATA RETRY FIVE TIMES FAILED'
673 005452      045      116      045 NOMEM: .ASCIZ /#N#A ***** NO NXM ADDRESS--CANNOT TEST NXM TIMEOUT. *****N'
674          .EVEN
675
676          .SBTTL GLOBAL ERROR REPORT SECTION
677
678      ;++
679      ; THE GLOBAL ERROR REPORT SECTION CONTAINS THE PRINTB AND PRINTX
680      ; CALLS THAT ARE USED IN MORE THAN ONE TEST.
681      ; ASCII TEXT STRINGS ARE FOUND IN THE GLOBAL TEXT SECTION.
682      ;--
683
684 005546      BGNMSG  NXRERR          ;NON-EXISTANT DEVICE REGISTER.
        005546
685 005546      PRINTX  #NXRX,NODEV    ;NODEV = NEXM ADDRESS.
        005546      013746 003112'
        005552      012746 003771'
        005556      012746 000002
        MOV          NODEV,-(SP)
        MOV          #NXRX,-(SP)
        MOV          #2,-(SP)

```

M3

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
GLOBAL ERROR REPORT SECTION

SEQ 038

```

005562 010600
005564 104415
005566 062706 000006
686 005572 004737 005600'
687 005576
005576
005576 104423
688
689
690
691
692
693
694 005600 005727
695 005602 000000
696 005604 001402
697 005606 004777 177770
698 005612
005612 012746 004522'
005616 012746 000001
005622 010600
005624 104415
005626 062706 000004
699 005632 000207
700
701
702
703
704
705
706
707
708
709
710
711
712
713
714
715
716
717
718
719 005634
720 005634
721 005640 010104
722 005642
005642 010446
005644 012746 006225'
005650 012746 000002
005654 010600
005656 104414
005660 062706 000006
723 005664 010400
724 005666 004737 015654'
725 005672 103410
726 005674

MOV SP,R0
TRAP C$PNTX
ADD #6,SP
JSR PC,EXTEND ; PRINT EXTENSION IF REQUIRED.
ENDMSG
L10002: TRAP C$MSG

;
; THIS ROUTINE APPENDS A UNIQUE EXTENSION (IF REQUIRED)
; TO ANY OF THE ABOVE ERROR SIGNATURES.
;
EXTEND: TST (PC)+
EXTA: 0 ; 0 = NO EXTENSION.
BEQ 1$
JSR PC,EXTA ; APPEND EXTENSION TEXT.
1$: PRINTX #NULCR ; PRINT A BLANK LINE
MOV #NULCR,-(SP)
MOV #1,-(SP)
MOV SP,R0
TRAP C$PNTX
ADD #4,SP
RTS PC

.SBTTL PRITSSR - PRINT TSSR CONTENTS

;+
;
; ROUTINE TO DISPLAY THE CONTENTS, AND BIT DEFINITIONS, OF
; THE TSSR REGISTER. THIS ROUTINE IS NORMALLY CALLED ONLY
; BY A MESSAGE PRINTING ROUTINE
;
; INPUTS:
;
; R1 CONTENTS OF TSSR
;
; SUBORDINATE ROUTINES:
;
; CHKAMB CHECK FOR AMBIGUOUS CONTENTS
;-

PRITSSR:
SAVREG ; SAVE GENERAL REGISTERS
MOV R1,R4 ; SAVE THE TSSR CONTENTS
PRINTB #TSSRFOR,R4 ; PRINT THE CONTENTS OF TSSR
MOV R4,-(SP)
MOV #TSSRFOR,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C$PNTB
ADD #6,SP
MOV R4,R0 ; GET TSSR BACK FOR CHKAMB
JSR PC,CHKAMB ; ARE CONTENTS AMBIGUOUS ?
BCS 5$ ; BRANCH IF NOT
PRINTX #AMBTSSR ; SHOW CONTENTS ARE AMBIGUOUS

```

N3

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
PRITSSR - PRINT TSSR CONTENTS

SEQ 039

005674	012746	006445'		MOV	#AMBTSSR, -(SP)	
005700	012746	000001		MOV	#1, -(SP)	
005704	010600			MOV	SP, R0	
005706	104415			TRAP	C\$PNTX	
005710	062706	000004		ADD	#4, SP	
727 005714	010403		5\$:	MOV	R4, R3	; CONTENTS OF TSSR
728 005716	042703	001476		BIC	#HIADDR!FATERR!TERCLS, R3	; CLEAR ALL MULTIPLE BIT FIELDS
729 005722	001434			BEQ	20\$	; NO BITS ARE SET
730 005724	012702	002630'		MOV	#TMPBFR, R2	; TEMPORARY ASCII BUFFER
731 005730	012701	003474'		MOV	#TSSRBIT, R1	; ASCII EQUIVALENT OF BITS
732 005734	005703		10\$:	TST	R3	; REMAINING BITS TO CONVERT
733 005736	001413			BEQ	15\$	; BRANCH WHEN ALL ARE DONE
734 005740	000241			CLC		; CLEAR CARRY FOR SHIFT
735 005742	006103			ROL	R3	; SHIFT NEXT BIT TO CARRY
736 005744	103006			BCC	13\$	; BRANCH IF BIT NOT SET
737 005746	011100			MOV	(R1), R0	; POINTER TO BIT DEFINITION
738 005750	112022		11\$:	MOVB	(R0)+, (R2)+	; MOVE ASCII TO BUFFER
739 005752	001376			BNE	11\$	; MOVE ALL BITS
740 005754	112762	000054 177777		MOVB	#', -1(R2)	; INSERT A COMMA TO TERMINATE
741 005762	005721		13\$:	TST	(R1)+	; POINT TO NEXT DESCRIPTION
742 005764	000763			BR	10\$	; GET THE REMAINING BITS
743 005766	105042		15\$:	CLRB	-(R2)	; TERMINATE THE LINE
744 005770				PRINTX	#TSSDEF, #TMPBFR	; PRINT THE BIT DEFINITIONS
005770	012746	002630'		MOV	#TMPBFR, -(SP)	
005774	012746	006416'		MOV	#TSSDEF, -(SP)	
006000	012746	000002		MOV	#2, -(SP)	
006004	010600			MOV	SP, R0	
006006	104415			TRAP	C\$PNTX	
006010	062706	000006		ADD	#6, SP	
745						
746 006014	010403		20\$:	MOV	R4, R3	; GET THE TSSR CONTENTS
747 006016	042703	177761		BIC	#TERCLS, R3	; CLEAR ALL BUT TERMINATION
748 006022	016303	006506'		MOV	TCOCOD(R3), R3	; GET THE TERMINATION CODE MEANING
749 006026				PRINTX	#TCOASC, R3	; PRINT THE TERMINATION CODE
006026	010346			MOV	R3, -(SP)	
006030	012746	006306'		MOV	#TCOASC, -(SP)	
006034	012746	000002		MOV	#2, -(SP)	
006040	010600			MOV	SP, R0	
006042	104415			TRAP	C\$PNTX	
006044	062706	000006		ADD	#6, SP	
750 006050	010403			MOV	R4, R3	; TSSR CONTENTS AGAIN
751 006052	042703	177717		BIC	#CFATERR, R3	; CLEAR ALL BUT FATAL TERMINATION
752 006056	001416			BEQ	25\$	; DON'T PRINT IF ZERO
753 006060	006203			ASR	R3	
754 006062	006203			ASR	R3	
755 006064	006203			ASR	R3	; ALINE TERMINATION CODE FOR INDEX
756 006066	016303	007046'		MOV	TFECOD(R3), R3	; GET THE FATAL TERMINATION CODE
757 006072				PRINTX	#TFEASC, R3	; PRINT THE FATAL TERMINATION CODE
006072	010346			MOV	R3, -(SP)	
006074	012746	006347'		MOV	#TFEASC, -(SP)	
006100	012746	000002		MOV	#2, -(SP)	
006104	010600			MOV	SP, R0	
006106	104415			TRAP	C\$PNTX	
006110	062706	000006		ADD	#6, SP	
758 006114	042704	176377	25\$:	BIC	#CHIADDR, R4	; CLEAR ALL BUT EXTENDED ADDRESS
759 006120	001411			BEQ	30\$	; DON'T PRINT IF ZERO
760 006122				PRINTX	#TEXASC, R4	; PRINT THE EXTENDED ADDRESS BITS

B4

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
PRITSSR - PRINT TSSR CONTENTS

SEQ 040

```

006122 010446      MOV      R4, -(SP)
006124 012746 006245'  MOV      @TEXASC, -(SP)
006130 012746 000002      MOV      @2, -(SP)
006134 010600      MOV      SP, R0
006136 104415      TRAP     C:PNTX
006140 062706 000006      ADD      @6, SP
761 006144 013703 002176' 301:      MOV      EPRTSW, R3          ;PRINT MESSAGE BUFFER ADDRESS
762 006150      PRINTX   R3          ;PRINT PROPER MESSAGE
      006150 010346      MOV      R3, -(SP)
      006152 012746 000001      MOV      @1, -(SP)
      006156 010600      MOV      SP, R0
      006160 104415      TRAP     C:PNTX
      006162 062706 000004      ADD      @4, SP
763 006166 000207      RTS      PC          ;RETURN TO CALLER
764
766 006170      EPRT2:
767 006170      045      116      045  EPRT1: .ASCIZ 'MMA *****REPLACE M7455*****'
782 006225      045      116      045  TSSRFOR: .ASCIZ 'MMA TSSR = #06'
783 006245      045      116      045  TEXASC: .ASCIZ 'MMA Extended Address Bits = #06'
784 006306      045      116      045  TCUASC: .ASCIZ 'MMA Termination Class Code = #T'
785 006347      045      116      045  TFCASC: .ASCIZ 'MMA Fatal Termination Class Code = #T'
786 006416      045      116      045  TSSDEF: .ASCIZ 'MMA TSSR Bits Set; #T'
787 006445      045      116      045  AMBTSSR: .ASCIZ 'MMA TSSR Contents Are Ambiguous'
788
789 006506 006526' 006551' 006577' TCOCOD: .WORD 10,20,30,40,50,60,70,80
790 006526      116      157      162  10: .ASCIZ 'Normal Termination'
791 006551      124      145      162  20: .ASCIZ 'Termination Condition'
792 006577      124      141      160  30: .ASCIZ 'Tape Status Alert'
793 006621      106      165      156  40: .ASCIZ 'Function Reject'
794 006641      122      145      143  50: .ASCIZ 'Recoverable Error - Tape Position One Record Down'
795 006723      122      145      143  60: .ASCIZ 'Recoverable Error - Tape Was Not Moved'
796 006772      125      156      162  70: .ASCIZ 'Unrecoverable Error'
797 007016      106      141      164  80: .ASCIZ 'Fatal Controller Error'
798
799      .EVEN
800 007046 007056' 007112' 007123' TSFCOD: .WORD 10,20,30,40
801 007056      111      156      164  10: .ASCIZ 'Internal Diagnostic Failure'
802 007112      122      145      163  20: .ASCIZ 'Reserved'
803 007123      102      165      163  30: .ASCIZ 'Bus Interface or Sanity Check Error'
804 007167      122      145      163  40: .ASCIZ 'Reserved'
805
806      .EVEN
807
808      .SBTTL  PRIPKT - PRINT THE ADDRESS/CONTENTS OF COMMAND PACKET
809
810      ;
811      ;THIS ROUTINE PRINTS THE ADDRESS AND CONTENTS OF A COMMAND PACKET.
812      ;THIS ROUTINE IS NORMALLY ONLY CALLED FROM A PRINT ROUTINE.
813      ;
814      ;INPUT:
815      ;
816      ;      R0      NUMBER OF WORDS IN PACKET
817      ;      R3      HIGH ORDER COMMAND PACKET ADDRESS
818      ;      R4      ADDRESS OF COMMAND PACKET
819      ;
820      ;      NOTE:  R3 IS IGNORED IF THE KTENABLE FLAG IS CLEAR.
821      ;

```

C4

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 PRIPKT - PRINT THE ADDRESS/CONTENTS OF COMMAND PACKET

SEQ 041

```

822 007200
823 007200
824 007204 010005
825 007206 005737 003132'
826 007212 001001
827 007214 005003
828 007216 010301
829 007220 010400
830 007222 006100
831 007224 006101
832 007226
    007226 010446
    007230 010146
    007232 012746 007364'
    007236 012746 000003
    007242 010600
    007244 104414
    007246 062706 000010
833 007252 010300
834 007254 001404
835 007256 010401
836 007260 004737 017130'
837 007264 010004
838 007266 005001
839 007270 012402
840 007272
    007272 010246
    007274 010146
    007276 012746 007326'
    007302 012746 000003
    007306 010600
    007310 104414
    007312 062706 000010
841 007316 005201
842 007320 020105
843 007322 002762
844 007324 000207
845
846 007326 045 116 045 PKTFRM: .ASCIZ 'NMA Packet Word #01#A - #06'
847 007354 045 116 045 PKTADD: .ASCIZ 'NMA Packet Address - #01#05'
848
849
850
851
852
853
854
855
856
857
858
859
860
861
862
863
864
  
```

PRIPKT:;
 SAVREG
 MOV R0,R5 ;SAVE THE REGISTERS
 IST KTENABLE ;SAVE NO. OF WORDS IN PACKET
 BNE 104 ;ABOVE 28K UNDER TEST?
 CLR R3 ;BR IF YES
 104: MOV R3,R1 ;SET HIGH ORDER ADDRESS TO 0
 MOV R4,R0 ;COPY HIGH ORDER ADDRESS
 ROL R0 ;GET LOWER ADDRESS
 ROL R1 ;SHIFT BIT 15 INTO C.BIT
 PRINTB #PKTADD,R1,R4 ;AND INTO HIGH ORDER.
 ;PRINT PACKET ADDRESS
 MOV R4,-(SP)
 MOV R1,-(SP)
 MOV #PKTADD,-(SP)
 MOV #3,-(SP)
 MOV SP,R0
 TRAP C#PNTB
 ADD #10,SP
 154: MOV R3,R0 ;GET HIGH ORDER ADDRESS
 BEQ 204 ;BR IF NOT ABOVE 28K.
 MOV R4,R1 ;GET LOW ORDER ADDRESS
 JSR PC,SETMAP ;SETUP PAR6 MAPPING FOR 18 BIT ADDRESS
 MOV R0,R4 ;GET RETURNED PAR6 ADDRESS BIAS
 204: CLW R1 ;SAVE WORD NUMBER
 254: MOV (R4),R2 ;GET PACKET CONTENTS
 PRINTB #PKTFRM,R1,R2 ;PRINT THE DATA
 MOV R2,-(SP)
 MOV R1,-(SP)
 MOV #PKTFRM,-(SP)
 MOV #3,-(SP)
 MOV SP,R0
 TRAP C#PNTB
 ADD #10,SP
 INC R1 ;NEXT WORD NUMBER
 CMP R1,R5 ;DONE ALL PACKET WORDS?
 BLT 254 ;LOOP TILL ALL DONE
 RTS PC ;RETURN

.SBTTL PRIBXOR - PRINT EXPD, RECV AND XOR BYTE
 ;
 ;
 ;PRINT EXPECTED DATA, RECEIVED DATA, AND XOR OF THE DATA BYTE
 ;THIS ROUTINE IS NORMALLY CALLED ONLY FOR PRINT ROUTINES.
 ;
 ;INPUTS:
 ;
 ; R1 RECEIVED DATA
 ; R2 EXPECTED DATA
 ;
 ;OUTPUT:
 ;

D4

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
PRIBXOR - PRINT EXPD, RECV AND XOR BYTE

SEQ 042

```

865      ;      RO      XOR OF EXPECTED/RECEIVED DATA
866      ;
867      ;
868      ;
869 007422      PRIBXOR::
870 007422      SAVREG      ;SAVE THE REGISTERS
871 007426 010203      MOV      R2,R3      ;EXPECTED DATA
872 007430      XOR      R1,R3      ;FORM THE EXCLUSIVE OR
873 007440 012700 177400      MOV      #1<377>,R0      ;BYTE MASK
874 007444 040001      BIC      R0,R1      ;SAVE LOW BYTE RECV
875 007446 040002      BIC      R0,R2      ;SAVE LOW BYTE EXPD
876 007450 040003      BIC      R0,R3      ;SAVE LOW BYTE XOR
877 007452      PRINTB      #XORFOR,R2,R1,R3 ;PRINT THE MESSAGE
      007452 010346      MOV      R3,-(SP)
      007454 010146      MOV      R1,-(SP)
      007456 010246      MOV      R2,-(SP)
      007460 012746 007504'      MOV      #XORFOR,-(SP)
      007464 012746 000004      MOV      #4,-(SP)
      007470 010600      MOV      SP,R0
      007472 104414      TRAP      C1PNTB
      007474 062706 000012      ADD      #12,SP
878 007500 010300      MOV      R3,R0      ;RO HAS XOR ON RETURN
879 007502 000207      RTS      PC      ;RETURN TO CALLER
880
881 007504      045      116      045 XORBFOR: .ASCIZ 'NNNA EXPD: #03NA RECV: #03NA XOR: #03'
882      .EVEN
883
884
885      .SBTTL PRIBXOR - PRINT EXPD, RECV AND XOR
886
887
888
889 ;
890 ;PRINT EXPECTED DATA, RECEIVED DATA, AND XOR OF THE TWO
891 ;THIS ROUTINE IS NORMALLY CALLED ONLY FOR PRINT ROUTINES.
892 ;
893 ;INPUTS:
894 ;
895 ;      R1      RECEIVED DATA
896 ;      R2      EXPECTED DATA
897 ;
898 ;OUTPUT:
899 ;
900 ;      RO      XOR OF EXPECTED/RECEIVED DATA
901 ;
902 ;
903 007552      PRIBXOR::
904 007552      SAVREG      ;SAVE THE REGISTERS
905 007556 010203      MOV      R2,R3      ;EXPECTED DATA
906 007560      XOR      R1,R3      ;FORM THE EXCLUSIVE OR
907 007570      PRINTB      #XORFOR,R2,R1,R3 ;PRINT THE MESSAGE
      007570 010346      MOV      R3,-(SP)
      007572 010146      MOV      R1,-(SP)
      007574 010246      MOV      R2,-(SP)
      007576 012746 007622'      MOV      #XORFOR,-(SP)
      007602 012746 000004      MOV      #4,-(SP)
      007606 010600      MOV      SP,R0

```


F4

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 PRIADD - PRINT MEMORY ERROR ADDRESS

SEQ 044

```

957
958
959
960
961
962
963
964
965
966
967 007770
968 007770
969 007774 013700 002234'
970 010000 013701 002236'
971 010004 010102
972 010006 006101
973 010010 006100
974 010012
    010012 010246
    010014 010046
    010016 012746 010040'
    010022 012746 000003
    010026 010600
    010030 104414
    010032 062706 000010
975 010036 000207
976
977 010040 045 116 045
978
979
980
981
982
983
984
985
986
987
988
989
990
991
992
993 010104
994 010104
995 010110 013702 002234'
996 010114 013701 002236'
997
998
999
1000 010120
    010120 010146
    010122 012746 010166'
    010126 012746 000002
    010132 010600
    010134 104414
    010136 062706 000006

;PRINT MEMORY ADDRESS
;THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.
;
; IMPLICIT INPUTS
;
;      ERRHI - HIGH ORDER ADDRESS
;      ERRLO - LOW ORDER ADDRESS
;
; -
PRIADD:
    SAVREG
    MOV     ERRHI,R0
    MOV     ERRLO,R1
    MOV     R1,R2
    ROL     R1
    ROL     R0
    PRINTB  @PRIA0,R0,R2
    MOV     R2,-(SP)
    MOV     R0,-(SP)
    MOV     @PRIA0,-(SP)
    MOV     @3,-(SP)
    MOV     SP,R0
    TRAP    C:PNTB
    ADD     @10,SP
    RTS     PC
;SAVE R1-R5 UNTIL NEXT RETURN
;GET HIGH ADDRESS
;GET LOW ADDRESS
;COPY LOW ADDRESS
;SHIFT BIT 15 TO C BIT
;SHIFT INTO HIGH ORDER
;PRINT MEMORY ADDRESS IN ERROR
;RETURN

.SBTTL PRITADD - PRINT MEMORY TEST ADDRESS
;
;PRINT MEMORY ADDRESS
;THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.
;
; IMPLICIT INPUTS
;
;      ERRHI - HIGH ORDER ADDRESS
;      ERRLO - LOW ORDER ADDRESS
;
; -
PRITADD:
    SAVREG
    MOV     ERRHI,R2
    MOV     ERRLO,R1
    MOV     R1,R2
    ROL     R1
    ROL     R0
    PRINTB  @PRIT0,R1
    MOV     R1,-(SP)
    MOV     @PRIT0,-(SP)
    MOV     @2,-(SP)
    MOV     SP,R0
    TRAP    C:PNTB
    ADD     @6,SP
;SAVE R1-R5 UNTIL NEXT RETURN
;GET HIGH ADDRESS
;GET LOW ADDRESS
;COPY LOW ADDRESS
;SHIFT BIT 15 TO C BIT
;SHIFT INTO HIGH ORDER
;PRINT MEMORY ADDRESS LOW IN ERROR

```

```

1001 010142 010246 PRINTB #PRIT1,R2 ;PRINT MEMORY ADDRESS HIGH IN ERROR
      010142 010246 MOV R2,-(SP)
      010144 012746 010231' MOV #PRIT1,-(SP)
      010150 012716 000002 MOV #2,-(SP)
      010154 010600 MOV SP,R0
      010156 104414 TRAP C#PNTB
      010160 062706 000006 ADD #6,SP
      010164 000207 RTS PC ;RETURN
1002
1003
1004 010166 045 116 045 PRIT0: .ASCIZ 'NWA MEMORY TEST ADDRESS LOW = #05'
1005 010231 045 116 045 PRIT1: .ASCIZ 'NWA MEMORY TEST ADDRESS HIGH = #06'
1006 .EVEN
1007
1008
1009 ;SBTTL SPACE - SPACE RECORDS (FORWARD AND REVERSE) COMMAND
1010
1011 ;+
1012 ;
1013 ;ROUTINE TO ISSUE A SPACE RECORDS
1014 ;COMMAND (FORWARD OR REVERSE)
1015 ;
1016 ;INPUT:
1017 ;
1018 ; R3 NUMBER OF RECORDS TO BE SPACED OVER
1019 ; BIT15 CONTROLS DIRECTION
1020 ; BIT15 = 0 IS FORWARD
1021 ; BIT15 = 1 IS REVERSE
1022 ; R5 FIRST DEVICE UNIBUS ADDRESS
1023 ;
1024 ; REQUIRES A WRITE CHARACTERISTICS DONE PREVIOUSLY
1025 ;
1026 ;OUTPUT:
1027 ;
1028 ; CARRY SET - SPACE RECORDS COMMAND OK
1029 ; CLR - SPACE RECORDS FAILED
1030 ;
1031 ;
1032 ; R0 THE CONTENTS OF R4 IS MOVED TO R0
1033 ;
1034 ;
1035 ;IMPLICIT OUTPUT:
1036 ;
1037 ; TAPE HAS BEEN MOVED
1038 ;
1039 ;SIDE EFFECTS:
1040 ;
1041 ;
1042 ;-
1043
1044 010276 SPACE::
1045 010276 SAVREG ;SAVE THE GENERAL REGISTERS
1046 010302 012737 000764 010470' MOV #500.,SDELAY ;SET UP DELAY
1047 010310 012737 140010 010460' MOV #140010.80$ ;SET UP COMMAND, SPACE FORWARD
1048 010316 005703 TST R3 ;CHECK FOR DIRECTION
1049 010320 100403 BMI 5$ ;BR, IF REVERSE INDICATED
1050 010322 010337 010462' MOV R3,90$ ;LOAD UP NUMBER OF RECORDS TO SPACE
1051 010326 000407 BR 10$ ;GO DO COMMAND

```


TSV3 - GLOBAL AREAS
SPACE - SPACE RECORDS (FORWARD AND REVERSE) COMMAND

SEQ 046

```

1052 010330 042703 100000      5$: BIC      #BIT15,R3      ;CLEAR DIRECTION BIT
1053 010334 010337 010462'    MOV      R3,90$      ;LOAD UP NUMBER OF RECORDS TO SPACE
1054 010340 052737 000400 010460'  BIS      #BIT8,80$      ;SET REVERSE BIT IN COMMAND PACKET
1055 010346 012704 010460'    10$: MOV      #80$,R4      ;SET UP R4 WITH PACKET ADDRESS
1056 010352 010465 000000      MOV      R4,TSD8(R5)    ;SEND OUT COMMAND
1057 010356 004737 016060'    15$: JSR      PC,WAITF      ;WAIT FOR SSR
1058 010362 103420      BCS      20$      ;BR, IF SSR IS SET AND OK
1059 010364      DELAY      250      ;DELAY ABOUT .25 SECONDS
      010364 012727 000250      MOV      #250,(PC)+
      010370 000000      .WORD      0
      010372 013727 002116'    MOV      L$DLY,(PC)+
      010376 000000      .WORD      0
      010400 005367 177772      DEC      -6(PC)
      010404 001375      BNE      -4
      010406 005367 177756      DEC      -22(PC)
      010412 001367      BNE      -20
1060 010414 005337 010470'    DEC      SDELAY      ;BUMP DELAY COUNTER DOWN
1061 010420 001356      BNE      15$      ;BR, IF MORE DELAY
1062 010422 000411      BR      60$      ;BR IF TROUBLE CARRY = CLEAR
1063 010424 016501 000002      20$: MOV      TSSR(R5),R1    ;READ TSSR
1064 010430 012702 000200      MOV      #SSR,R2      ;SET UP EXPECTED
1065 010434 020201      25$: CMP      R2,R1      ;ARE THEY OK
1066 010436 001401      BEQ      40$      ;BR, IF EQUAL = OK
1067 010440 000402      BR      60$      ;TROUBLE EXIT
1068 010442 000261      40$: SEC      ;SET CARRY NO TROUBLE
1069 010444 000401      BR      70$      ;EXIT
1070 010446 000241      60$: CLC      ;CARRY CLEAR = ERROR
1071 010450      70$:      ;
1072 010450 010400      MOV      R4,R0      ;PASS PACKET ADDRESS
1073 010452 000207      RTS      PC      ;RETURN
1074
1075
1076
1077
1078      ;
1079      ;PACKET FOR SPACE COMMAND
1081 010454      ;
      .BLKB      10-<,-TSV2&7>
1083
1084      ;
1085 010460 000000      ;COMMAND WORD
80$: .WORD
1086      ;NUMBER OF RECORDS TO BE SPACED OVER WORD
90$: .WORD
      .WORD
      .WORD
SDELAY: .WORD      0      ;DELAY COUNTER
      .EVEN
1091
1092
1093
1094      .SBTTL WRCHRR - WRITE CHARACTERISTICS COMMAND
1095
1096
1097
1098      ;
1099      ;ROUTINE TO ISSUE A WRITE CHARACTERISTICS
1100      ;COMMAND SO THAT OTHER COMMANDS WILL BE ACCEPTED
1101
1102      ;INPUT:
      ;

```

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 WRTCHR - WRITE CHARACTERISTICS COMMAND

SEQ 047

```

1103      R4      ADDRESS OF PACKET FROM TEST
1104      R5      FIRST DEVICE UNIBUS ADDRESS
1105      REQUIRES A CALL TO SOFINIT BE DONE PREVIOUSLY
1106
1107      OUTPUT:
1108
1109      R0      TSSR CONTENTS
1110      CARRY    SET - WRITE CHARACTERISTICS COMMAND OK
1111              CLR - WRITE CHARACTERISTICS FAILED
1112
1113      IMPLICIT OUTPUT:
1114
1115      MESSAGE BUFFER AND OTHER BUFFERS ALL SET UP
1116      SOFTWARE SWITCHES SET AS FOLLOWS:
1117      EXTFEA = EXTENDED FEATURES PRESENT
1118      BENBSW = BUFFER ENABLE SWITCH ON OR OFF
1119
1120
1121      SIDE EFFECTS:
1122
1123
1124
1125
1126 010472 WRTCHR:
1127 010472      SAVREG
1128 010476 005037 002226'      CLR      BENBSW      ;SAVE THE GENERAL REGISTERS
1129 010502 005037 002224'      CLR      EXTFEA      ;CLEAR BUFFER ENABLE SWITCH
1130 010506 010502 000000'      MOV      R4,TSDB(R5)      ;CLEAR EXTENDED FEATURES SW SWITCH
1131 010512 010502 16146'      JSR      PC,CHKTSSR      ;SEND OUT COMMAND
1132 010516 16146'      BCS      20$      ;WAIT FOR SSR
1133 010520 000000'      BR      60$      ;BR, IF SSR IS SET AND OK
1134 010522 016501 000002'      MOV      TSSR(R5),R1      ;BR IF TROUBLE CARRY = CLEAR
1135 010526 012702 000200'      MOV      0SSR,R2      ;READ TSSR
1136 010532 032701 000100'      BIT      0OFL,R1      ;SET UP EXPECTED
1137 010536 001402'      BEQ      25$      ;WAS OFF LINE SET IN TSSR
1138 010540 052702 000100'      BIS      0OFL,R2      ;BR, IF NO OFL SET
1139 010544 020201'      CMP      R2,R1      ;MAKE THEM LOOK ALIKE
1140 010546 001401'      BEQ      40$      ;ARE THEY OK
1141 010550 000421'      BR      60$      ;BR, IF EQUAL = OK
1142 010552 062704 000010'      ADD      08.,R4      ;TROUBLE EXIT
1143 010556 011403'      MOV      (R4),R3      ;POINT TO WRT CHARA DATA PACKET
1144 010560 032763 000200 000012'      BIT      0X2.EXTF,XST2(R3)      ;GET ADDRESS OF MESSAGE BUFFER
1145 010566 001402'      BEQ      45$      ;EXTENDED FEATURES BIT SET?
1146 010570 005237 002224'      INC      EXTFEA      ;BR IF NO
1147 010574'      45$:      ;SET EXTENDED FEATURES SW SWITCH
1148 010574 032763 000100 000012'      BIT      0X2.BUFE,XST2(R3)      ;BUFFER ENABLE SWITCH SET
1149 010602 001402'      BEQ      50$      ;BR, IF SWITCH NOT SET
1150 010604 005237 002226'      INC      BENBSW      ;SET SOFTWARE SWITCH FOR ENABLED
1151 010610'      50$:
1152 010610 000261'      SEC
1153 010612 000401'      BR      70$      ;SET CARRY NO TROUBLE
1154 010614 000241'      CLC      ;EXIT
1155 010616 016500 000002'      MOV      TSSR(R5),R0      ;CARRY CLEAR = ERROR
1156 010622 000207'      RTS      PC      ;RETURN TSSR CONTENTS
1157
1158
1159      .SBTTL REWIND - POSITION TAPE (REWIND) COMMAND

```

J4

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 REWIND - POSITION TAPE (REWIND) COMMAND

SEQ 048

1160
 1161
 1162
 1163
 1164
 1165
 1166
 1167
 1168
 1169
 1170
 1171
 1172
 1173
 1174
 1175
 1176
 1177
 1178
 1179
 1180
 1181
 1182
 1183
 1184
 1185
 1186
 1187 010624
 1188 010624
 1189 010630 012704 010720'
 1190 010634 010465 000000
 1191 010640 012703 000550
 1192 010644 004737 016060'
 1193 010650 103417
 1194 010652
 010652 012727 000372
 010656 000000
 010660 013727 002116'
 010664 000000
 010666 005367 177772
 010672 001375
 010674 005367 177756
 010700 001367
 1195 010702 005303
 1196 010704 001357
 1197 010706 000241
 1198 010710 010400
 1199 010712 000207
 1200
 1201
 1203 010714
 1205 010720
 1206 010720 102010
 1207 010722 000000
 1208
 1209
 1210

```

;+
; THIS ROUTINE WILL REWIND THE SELECTED TAPE.
;
; CAUTION: THE ROUTINE DOES NOT WAIT FOR BOT
;           TO ARRIVE. ALSO THE CALLER MUST CHECK FOR
;           SSR TO SET IN THE TSSR
;
; CALLING SEQUENCE:
;
; DO A SOFT INIT
; DO A WRITE CHARACTERISTICS
; JSR PC,REWIND
;
; INPUT:
;
; R5      FIRST DEVICE UNIBUS ADDRESS
;
; OUTPUT
;
; R0      THE CONTENTS OF R4 IS PASSED TO R0
;
; -
REWIND::
    SAVREG
    MOV     @RWPACK,R4
    MOV     R4,TSDB(R5)
    MOV     @360,R3
    10$:    JSR     PC,WAITF
    BCS     20$
    DELAY   250
    MOV     @250,.(PC),
    .WORD   0
    MOV     L#0LY,.(PC),
    .WORD   0
    DEC     -6(PC)
    BNE     -4
    DEC     -22(PC)
    BNE     -20
    DEC     R3
    BNE     10$
    CLC
    20$:    MOV     R4,R0
    RTS     PC
; SAVE R1-R5 UNTIL NEXT RETURN
; GET PACKET ADDRESS
; SEND PACKET ADDRESS TO EXECUTE
; ENOUGH TIME FOR 2400' REEL TO REWIND
; WAIT FOR SSR TO SET
; LEAVE WHEN SSR IS SET
; WAIT FOR .25 SECONDS
; BUMP COUNTER DOWN
; KEEP GOING
; CLEAR CARRY TO SET ERROR
; PASS THE PACKET ADDRESS
; RETURN
    .BLKB   10-<.-TSV2&7>
RWPACK:
    .WORD   102010
    .WORD   0
; POSITION COMMAND (REWIND)
; NOT USED
    .SBTTL  CKRAM - COMPARE RAM TO I/O PACKET

```

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
CKRAM - COMPARE RAM TO I/O PACKET

SEQ 049

```

1211
1212
1213
1214 ROUTINE TO READ THE FIRST 8 BYTES FROM RAM
1215 MEMORY AND COMPARE THIS DATA TO A COMMAND PACKET.
1216
1217 INPUT:
1218
1219 R4 ADDRESS OF COMMAND PACKET
1220 R5 FIRST DEVICE I/O ADDRESS
1221
1222 OUTPUT:
1223
1224 CARRY SET - RAM MATCHES PACKET
1225 CLR - RAM DOES NOT MATCH PACKET
1226
1227 IMPLICIT OUTPUT:
1228
1229 THE TABLE RAMDATA IS FILLED WITH THE
1230 DATA HELD IN RAM.
1231 RAMSIZ IS SET TO 8. FOR PRAMPKT ROUTINE
1232
1233 SIDE EFFECTS:
1234
1235 THE SUBSYSTEM IS LEFT IN MAINTENANCE MODE
1236
1237
1238
1239 010724 CKRAM:: SAVREG ;SAVE THE GENERAL REGISTERS
1240 010724 MOV #RAMDATA,R1 ;ADDRESS TO SAVE THE RAM DATA
1241 010730 012701 002240 MOV #RMPKTBEG,R2 ;BYTE ADDRESS OF FIRST RAM DATA
1242 010734 012702 000201 CLR R3 ;CLEAR THE ERROR FLAG
1243 010740 005003 JSR PC,CHKTSSR ;WAIT FOR SSR
1244 010742 004737 016146' 000000 10$: MOVB #0,TSDB(R5) ;SET MAINTENANCE MODE
1245 010746 112765 000000 JSR PC,CHKTSSR ;WAIT FOR SSR TO SET
1246 010754 004737 016146' 10$: MOV R2,TSDB(R5) ;SELECT NEXT RAM ADDRESS
1247 010760 010265 000000 JSR PC,CHKTSSR ;WAIT FOR SSR TO SET
1248 010764 004737 016146' MOVB TSBA(R5),(R1) ;READ THE RAM DATA
1249 010770 116511 000000 CMPB (R1)+,(R4)+ ;COMPARE TO EXPECTED
1250 010774 122124 BEQ 20$ ;BRANCH IF OK
1251 010776 001401 INC R3 ;SET ERROR FLAG
1252 011000 005203 20$: INC R2 ;ADDRESS OF NEXT RAM LOCATION
1253 011002 005202 CMP R2,RMPKTBEG ;REACHED END YET ?
1254 011004 020227 000210 BLE 10$ ;BRANCH TILL ALL READ
1255 011010 003761 TST R3 ;WAS AN ERROR FOUND ?
1256 011012 005703 BEQ 30$ ;BRANCH IF NOT
1257 011014 001402 CLC ;CLEAR CARRY TO SHOW ERROR
1258 011016 000241 BR 50$ ;AND EXIT
1259 011020 000401 SEC ;SHOW GOOD COMPARE
1260 011022 000261 30$: MOV #8,RAMSIZ ;SETUP RAMSIZ FOR PRAMPKT ROUTINE
1261 011024 012737 000010 002300' 50$: RTS PC ;RETURN
1262 011032 000207
1263
1264
1265 ;SBTTL CKRAM2 - COMPARE RAM TO I/O CHARACTERISTICS DATA
1266
1267

```

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
CKRAM2 - COMPARE RAM TO I/O CHARACTERISTICS DATA

SEQ 050

```

1268 ;ROUTINE TO READ THE FIRST 8 OR 10 BYTES FROM RAM
1269 ;MEMORY AND COMPARE THIS DATA TO A CHARACTERISTICS DATA BLOCK.
1270 ;
1271 ;INPUT:
1272 ;
1273 ;      R4      ADDRESS OF THE CHARACTERISTICS DATA
1274 ;      R5      FIRST DEVICE UNIBUS ADDRESS
1275 ;
1276 ;OUTPUT:
1277 ;
1278 ;      CARRY   SET - RAM MATCHES PACKET
1279 ;             CLR - RAM DOES NOT MATCH PACKET
1280 ;
1281 ;IMPLICIT OUTPUT:
1282 ;
1283 ;      THE TABLE RAMDATA IS FILLED WITH THE
1284 ;      DATA HELD IN RAM.
1285 ;      RAMSIZ IS SET TO 8. OR 10. FOR PRAMPKT ROUTINE
1286 ;
1287 ;SIDE EFFECTS:
1288 ;
1289 ;      THE SUBSYSTEM IS LEFT IN MAINTENANCE MODE
1290 ;
1291 ;
1292 ;
1293 011034 CKRAM2:: SAVREG ;SAVE THE GENERAL REGISTERS
1294 011034      MOV  #RAMDATA,R1 ;ADDRESS TO SAVE THE RAM DATA
1295 011040      MOV  #RMCHBEG,R2 ;BYTE ADDRESS OF FIRST RAM DATA
1296 011044      CLR  R3 ;CLEAR THE ERROR FLAG
1297 011050      JSR  PC,CHKTSSR ;WAIT FOR SSR
1298 011052      004737 016146' 000000 000000 10$: MOVE  #0,TSDB(R5) ;SET MAINTENANCE MODE
1299 011056      112765 000000 000000 10$: JSR  PC,CHKTSSR ;WAIT FOR SSR TO SET
1300 011064      004737 016146' 000000 10$: MOV  R2,TSDB(R5) ;SELECT NEXT RAM ADDRESS
1301 011070      010265 000000 000000 10$: JSR  PC,CHKTSSR ;WAIT FOR SSR TO SET
1302 011074      004737 016146' 000000 10$: MOV  TSBA(R5),(R1) ;READ THE RAM DATA
1303 011100      116511 000000 000000 10$: MOV  (R1)+,(R4)+ ;COMPARE TO EXPECTED
1304 011104      122124 000000 000000 10$: CMPB ;BRANCH IF OK
1305 011106      001401 000000 000000 10$: BEQ  20$ ;SET ERROR FLAG
1306 011110      005203 000000 000000 10$: INC  R3 ;ADDRESS OF NEXT RAM LOCATION
1307 011112      005202 000000 000000 10$: INC  R2 ;ASSUME EXTFEA NOT SET
1308 011114      012737 000010 002300' 20$: MOV  #8.,RAMSIZ ;IS THE SOFTWARE EXTENDED FEATURES SET
1309 011122      005737 002224' 000000 20$: TST  EXTFEA ;BR, IF NOT SET
1310 011126      001407 000000 000000 20$: BEQ  25$ ;SET RAMSIZ FOR EXTEND FEATURES
1311 011130      012737 000012 002300' 20$: MOV  #10.,RAMSIZ ;AT END OF EXTENDED BUFFER
1312 011136      020227 000200 000000 20$: CMP  R2,#RMCHEND ;BR, IF NOT AT END YET
1313 011142      003750 000000 000000 20$: BLE  10$ ;AT END BRANCH
1314 011144      000403 000000 000000 20$: BR   27$ ;REACHED END YET ?
1315 011146      020227 000176 000000 25$: CMP  R2,#RMCHEND-2 ;BRANCH TILL ALL READ
1316 011152      003744 000000 000000 25$: BLE  10$ ;WAS AN ERROR FOUND ?
1317 011154      005703 000000 000000 27$: TST  R3 ;BRANCH IF NOT
1318 011156      001402 000000 000000 27$: BEQ  30$ ;CLEAR CARRY TO SHOW ERROR
1319 011160      000241 000000 000000 30$: CLC  ;AND EXIT
1320 011162      000401 000000 000000 30$: BR   50$ ;SHOW GOOD COMPARE
1321 011164      000261 000000 000000 50$: SEC  ;RETURN
1322 011166      000207 000000 000000 50$: RTS  PC
1323
1324

```

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
CKMSG - COMPARE WRITE CHAR. MESSAGE BUFFERS

SEQ 051

```

1325                                     .SBTTL CKMSG - COMPARE WRITE CHAR. MESSAGE BUFFERS
1326                                     ;*
1327                                     ;
1328                                     ;ROUTINE TO COMPARE A WRITE CHARACTERISTICS EXPD AND RECV
1329                                     ;BUFFER. THE EXPECTED AND RECEIVED BUFFERS ARE STORED FOR
1330                                     ;ERROR PRINT ROUTINES.
1331                                     ;
1332                                     ;INPUT:
1333                                     ;
1334                                     ;      R0      RECV MESSAGE BUFFER HIGH ORDER ADDRESS
1335                                     ;      R1      RECV MESSAGE BUFFER LOW ORDER ADDRESS
1336                                     ;      R2      EXPD MESSAGE BUFFER ADDRESS
1337                                     ;OUTPUT:
1338                                     ;
1339                                     ;      CARRY    SET - MESSAGE BUFFERS MATCH
1340                                     ;              CLR -MESSAGE BUFFERS DON'T MATCH
1341                                     ;
1342                                     ;IMPLICIT OUTPUT:
1343                                     ;
1344                                     ;      EXPMSG      BUFFER IS SET TO EXPD DATA
1345                                     ;      RECVMSG      BUFFER IS SET TO RECV DATA
1346                                     ;      RCVHIADD     SET TO HIGH ORDER ADDRESS OF RECV
1347                                     ;      RCVLOAD      SET TO LOW ORDER ADDRESS OF RECV
1348                                     ;
1349                                     ;-
1350 CKMSG::
1351                                     SAVREG
1352                                     MOV R0,RCVHIADD ;SAVE R1-R5 UNTIL NEXT RETURN
1353                                     MOV R1,RCVLOAD ;SAVE RECV HIGH ADDRESS
1354                                     TST KTENABF ;SAVE RECV LOW ADDRESS
1355                                     BEQ 10$ ;TESTING ABOVE 28K?
1356                                     JSR PC,SETMAP ;BR IF NO
1357                                     MOV R0,R1 ;RETURN ADDRESS BIASED TO PAR6 IN R0
1358                                     CLR R4 ;GET RETURNED ADDRESS BIASED TO PAR6
1359                                     CLR R3 ;WORD IN BUFFER
1360                                     MOV R2,R5 ;CLEAR ERROR SEEN FLAG
1361                                     MOV (R2),EXPMSG(R4) ;GET EXPD BUFFER ADDRESS
1362                                     MOV (R1),RECVMSG(R4) ;SAVE EXPD FOR ERROR REPORT
1363                                     CMP (R2)+,(R1)+ ;SAVE RECV FOR ERROR REPORT
1364                                     BEQ 25$ ;EXPD EQUAL RECV?
1365                                     INC R3 ;BR IF YES
1366                                     ADD #2,R4 ;SET ERROR SEEN FLAG
1367                                     CMP R4,#14 ;POINT TO NEXT WORD ADDRESS
1368                                     BLE 15$ ;DONE FIRST 7 WORDS?
1369                                     BIT #X2,EXTF,XST2(R5) ;BR IF NO
1370                                     BEQ 50$ ;IS EXTENDED FEATURES SET IN EXPD?
1371                                     CMP R4,#16 ;BR IF NO
1372                                     BLE 15$ ;DONE EXTENDED FEATURES WORD?
1373                                     TST R3 ;BR IF NO
1374                                     BEQ 55$ ;ANY ERRORS SEEN?
1375                                     CLC ;BR IF NO
1376                                     BR 60$ ;SET FAILURE
1377                                     SEC ;SET SUCCESS
1378                                     RTS PC ;RETURN
1379
1380                                     .SBTTL CKMSG2 - COMPARE EXPD RECV MESSAGE BUFFERS
1381

```


N4

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
CKMSG2 - COMPARE EXPD RECV MESSAGE BUFFERS

SEQ 052

```

1382
1383
1384 ;ROUTINE TO COMPARE AN EXPECTED AND RECEIVED MESSAGE
1385 ;BUFFER. THE EXPECTED AND RECEIVED BUFFERS ARE STORED FOR
1386 ;ERROR PRINT ROUTINES.
1387
1388 ;INPUT:
1389
1390 ;      R0      RECV MESSAGE BUFFER HIGH ORDER ADDRESS
1391 ;      R1      RECV MESSAGE BUFFER LOW ORDER ADDRESS
1392 ;      R2      EXPD MESSAGE BUFFER ADDRESS
1393 ;      R3      NUMBER OF BYTES TO COMPARE
1394
1395 ;OUTPUT:
1396
1397 ;      CARRY    SET - MESSAGE BUFFERS MATCH
1398 ;              CLR - MESSAGE BUFFERS DON'T MATCH
1399
1400 ;IMPLICIT OUTPUT:
1401
1402 ;      EXPMSG    BUFFER IS SET TO EXPD DATA
1403 ;      RECVMSG   BUFFER IS SET TO RECV DATA
1404 ;      RCVHIADD  SET TO HIGH ORDER ADDRESS OF RECV
1405 ;      RCVLOADD  SET TO LOW ORDER ADDRESS OF RECV
1406
1407
1408 011310
1409 011310
1410 011314 020327 000144
1411 011320 003412
1412 011322 012703 000144
1413 011326
      011326 012746 011442'
      011332 012746 000001
      011336 010600
      011340 104417
      011342 062706 000004
1414 011346 010037 002302'
1415 011352 010137 002304'
1416 011356 005737 003132'
1417 011362 001403
1418 011364 004737 017130'
1419 011370 010001
1420 011372 005004
1421 011374 005005
1422 011376 111264 002320'
1423 011402 111164 002464'
1424 011406 122221
1425 011410 001401
1426 011412 005205
1427 011414 062704 000001
1428 011420 020403
1429 011422 002001
1430 011424 000764
1431 011426 005705
1432 011430 001402
1433 011432 000241

```

```

;+
;ROUTINE TO COMPARE AN EXPECTED AND RECEIVED MESSAGE
;BUFFER. THE EXPECTED AND RECEIVED BUFFERS ARE STORED FOR
;ERROR PRINT ROUTINES.
;INPUT:
;      R0      RECV MESSAGE BUFFER HIGH ORDER ADDRESS
;      R1      RECV MESSAGE BUFFER LOW ORDER ADDRESS
;      R2      EXPD MESSAGE BUFFER ADDRESS
;      R3      NUMBER OF BYTES TO COMPARE
;OUTPUT:
;      CARRY    SET - MESSAGE BUFFERS MATCH
;              CLR - MESSAGE BUFFERS DON'T MATCH
;IMPLICIT OUTPUT:
;      EXPMSG    BUFFER IS SET TO EXPD DATA
;      RECVMSG   BUFFER IS SET TO RECV DATA
;      RCVHIADD  SET TO HIGH ORDER ADDRESS OF RECV
;      RCVLOADD  SET TO LOW ORDER ADDRESS OF RECV
;CKMSG2:
      SAVREG                                ;SAVE R1-R5 UNTIL NEXT RETURN
      CMP      R3,RECVMSG-EXPMSG,0000      ;R3 IS COUNT ABOVE MAX ALLOWED?
      BLE      5$,                                ;BR IF NO
      MOV      RECVMSG-EXPMSG,R3,0000
      PRINTF   DEBUGMSG,0000
      MOV      DEBUGMSG,-(SP)
      MOV      #1,-(SP)
      MOV      SP,R0
      TRAP     C$PRINTF
      ADD      #4,SP
5$:      MOV      R0,RCVHIADD                ;SAVE RECV HIGH ADDRESS
      MOV      R1,RCVLOADD                 ;SAVE RECV LOW ADDRESS
      TST      KTENABLE                    ;TESTING ABOVE 28K?
      BEQ      10$                        ;BR IF NO
      JSR      PC,SETMAP                   ;RETURN ADDRESS BIASED TO PAR6 IN R0
      MOV      R0,R1                      ;GET RETURNED ADDRESS BIASED TO PAR6
10$:      CLR      R4                      ;WORD IN BUFFER
      CLR      R5                          ;CLEAR ERROR SEEN FLAG
15$:      MOVB     (R2),EXPMSG(R4)          ;SAVE EXPD FOR ERROR REPORT
      MOVB     (R1),RECVMSG(R4)           ;SAVE RECV FOR ERROR REPORT
      CMPB     (R2)+,(R1)+                ;EXPD EQUAL RECV?
      BEQ      25$                        ;BR IF YES
      INC      R5                          ;SET ERROR SEEN FLAG
25$:      ADD      #1,R4                    ;POINT TO NEXT BYTE
      CMP      R4,R3                      ;DONE ALL BYTES?
      BGE      50$                        ;BR IF YES
      BR       15$                        ;DO NEXT BYTE
50$:      TST      R5                      ;ANY ERRORS SEEN?
      BEQ      55$                        ;BR IF NO
      CLC                                  ;SET FAILURE

```

B5

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 CKMSG2 - COMPARE EXPD RECV MESSAGE BUFFERS

SEQ 053

```

1434 011434 000401          BR      601          ;
1435 011436 000261          551: SEC          ;SET SUCCESS
1436 011440 000207          601: RTS      PC      ;RETURN
1437
1438 011442      120      122      117 DEBUGMSG: .ASCIZ 'PROGRAM INTERNAL ERROR -CKMSG2 MESSAGE BUFFER EXCEEDED-' ;88D
1439 011532      045      116      045 FERCH: .ASCII /NNA ***/
1440 011543      040      040      124 ERCH: .ASCIZ / TSSR ERROR CODE REC'D - /
1441 011576      056      056      056 SIMSG: .ASCIZ /.... AFTER DOING SOFT INIT/
1442 011631      124      105      123 TINERR: .ASCIZ /TEST: .../
1443          .EVEN
1444
1445
1446
1447
1448          ;
1449          ;PRINT ROUTINE TO FATAL SOFT INIT ERRORS
1450          ;INPUT:
1451          ;
1452          ;      R1      CONTENTS OF TSSR AT ERROR
1453          ;
1454          ;SIDE EFFECTS:
1455          ;
1456          ;      EXECUTES DROP UNIT TO CEASE TESTING
1457          ;
1458          ;-
1459
1460 011644          BGNMSG  SFIMSG
1461 011644          SFIMSG: JSR      PC,PRITSSR      ;PRINT CONTENTS OF TSSR REGISTER
1462 011650 004737 005634' JSR      PC,CKDROP      ;DROP UNIT, IF ALLOWED
1463 011654          ENOMSG
1464 011654 104423      L10003: TRAP      CMSG
1465
1466          ;
1467          ;PRINT ROUTINE TO PRINT THE CONTENTS OF
1468          ;TSSR AND A COMMAND PACKET OTHER THAN GET STATUS COMMAND PACKET.
1469          ;INPUTS:
1470          ;
1471          ;      R1      TSSR CONTENTS
1472          ;      R4      ADDRESS OF COMMAND PACKET
1473          ;
1474          ;-
1475
1476 011656          BGNMSG  PKTSSR
1477 011656          PKTSSR: JSR      PC,PRITSSR      ;PRINT THE CONTENTS OF TSSR REGISTER
1478 011662 012700 000004' MOV      #4,R0      ;NO. OF WORDS IN PACKET
1479 011666 004737 007200' JSR      PC,PRIPKT      ;PRINT THE CONTENTS OF COMMAND PACKET
1480 011672          ENOMSG
1481 011672 104423      L10004: TRAP      CMSG
1482
1483          ;
1484          ;PRINT ROUTINE TO PRINT THE CONTENTS OF
          ;TSSR AND A GET STATUS COMMAND PACKET.

```


C5

TSV3 - GLOBAL AREAS MACRO M.113 07-FEB-84 10:50
CKMSG2 - COMPARE EXPD RECV MESSAGE BUFFERS

SEQ 054

```

1485
1486
1487
1488
1489
1490
1491
1492
1493 011674
      011674
1494 011674 004737 005634'
1495 011700 012700 000002
1496 011704 004737 007200'
1497 011710
      011710
      011710 104423
1498
1499
1500
1501
1502
1503
1504
1505
1506
1507
1508
1509 011712
      011712
1510 011712 004737 005634'
1511 011716
      011716
      011716 104423
1512
1513
1514
1515
1516
1517
1518
1519
1520
1521
1522
1523
1524
1525
1526
1527 011720
      011720
1528 011720 004737 005634'
1529 011724 010200
1530 011726 010301
1531 011730 004737 014052'
1532 011734
      011734
      011734 104423

      INPUTS:
      R1      TSSR CONTENTS
      R4      ADDRESS OF COMMAND PACKET

      BGNMSG PKTGETS
PKTGETS:
      JSR      PC,PRITSSR      ;PRINT THE CONTENTS OF TSSR REGISTER
      MOV      #2,R0          ;NO. OF WORDS IN GET STATUS PACKET
      JSR      PC,PRIPKT      ;PRINT THE CONTENTS OF COMMAND PACKET
      ENDMSG
L10005:
      TRAP     C#MSG

      ;PRINT TSSR ERRORS FOR INITIALIZATION TESTS
      INPUTS:
      R1      TSSR CONTENTS
      R4      ADDRESS OF COMMAND PACKET

      BGNMSG SFFMSG
SFFMSG:
      JSR      PC,PRITSSR      ;PRINT CONTENTS OF TSSR REGISTER
      ENDMSG
L10006:
      TRAP     C#MSG

      .SBYTL PKTMES - PRINT TSSR AND MESSAGE BUFFER
      ;PRINT ROUTINE TO PRINT THE CONTENTS OF TSSR AND MESSAGE
      ;BUFFER FOR ERROR REPORTS
      INPUTS:
      R1      CONTENTS OF TSSR
      R2      LOW ORDER MESSAGE BUFFER
      R3      HIGH ORDER MESSAGE BUFFER ADDRESS
      NOTE: R3 IS IGNORED IF KTENABLE FLAG IS CLEAR

      BGNMSG PKTMES
PKTMES:
      JSR      PC,PRITSSR      ;PRINT CONTENTS OF TSSR
      MOV      R2,R0          ;LOW ORDER ADDRESS
      MOV      R3,R1          ;HIGH ORDER ADDRESS
      JSR      PC,PRMESS      ;PRINT THE MESSAGE BUFFER
      ENDMSG
L10007:
      TRAP     C#MSG

```

D5

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
PKTMS - PRINT TSSR AND MESSAGE BUFFER

SEQ 055

```

1533
1534
1535
1536
1537
1538
1539
1540
1541
1542
1543
1544
1545
1546
1547 011736
      011736
1548 011736 004737 010104'
1549 011742 016501 000002'
1550 011746 004737 005634'
1551 011752
      011752
      011752 104423
1552
1553
1554
1555
1556
1557
1558
1559
1560
1561
1562
1563
1564
1565
1566 011754
      011754
1567 011754 012700 000007'
1568 011760 005737 002224'
1569 011764 001402
1570 011766 012700 000010'
1571 011772 004737 014362'
1572 011776
      011776
      011776 104423
1573
1574
1575
1576
1577
1578
1579
1580
1581
1582
1583

```

```

      .SBTTL  ADDSSR - PRINT TEST ADDRESS AND TSSR
      ;
      ;PRINT ROUTINE TO PRINT THE CONTENTS OF
      ;TSSR AND A MEMORY TEST ADDRESS
      ;
      ;INPUTS:
      ;
      ;      R5      FIRST DEVICE UNIBUS ADDRESS
      ;      ERRHI   HIGH ORDER MEMORY TEST ADDRESS
      ;      ERRLO   LOW ORDER MEMORY TEST ADDRESS
      ;
      BGNMSG  ADDSSR
ADDSSR::
      JSR     PC,PRITADD      ;PRINT MEMORY TEST ADDRESS
      MOV     TSSR,R5,P1      ;GET CURRENT TSSR
      JSR     PC,PRITSSR      ;PRINT THE CONTENTS OF TSSR REGISTER
      ENDMMSG
L10010:
      TRAP    C#MSG

      .SBTTL  MSGEXP - PRINT WRITE CHAR. EXPD-RCV MESSAGE BUFFERS
      ;
      ;PRINT ROUTINE TO PRINT WRITE CHARACTERISTIC MESSAGE BUFFER
      ;
      ;IMPLICIT INPUTS:
      ;
      ;      EXPMSG - EXPECTED MESSAGE BUFFER
      ;      RECMMSG - RECEIVED MESSAGE BUFFER
      ;      RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
      ;      RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
      ;
      BGNMSG  MSGEXP
MSGEXP::
      MOV     #7,R0            ;ASSUME NO EXT FEATURES
      TST     EXTFEA           ;EXT FEATURES SET?
      BEQ     5$               ;BR IF NO
      MOV     #8,R0            ;EXT FEATURE BUFFER IS 8 WORDS
      JSR     PC,PRMSGEXP      ;PRINT EXPD/RCV MESSAGE BUFFERS
      ENDMMSG
L10011:
      TRAP    C#MSG

      .SBTTL  FIFEXP - PRINT FIFO EXP/RCV DATA
      ;
      ;PRINT ROUTINE TO PRINT FIFO EXP/RCV DATA
      ;
      ;      R1      - BYTE COUNT
      ;
      ;IMPLICIT INPUTS:
      ;
      ;      EXPMSG - EXPECTED MESSAGE BUFFER (CONTAINS FIFO DATA ONLY

```

F5

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
MSGSTAT - PRINT STATUS HEADER AND MESSAGE BUFFERS

SEQ 057

```

1619 012222 012240' 012302' 012373' STATCOD: .WORD 1$,2$,3$,4$,5$,6$,0
1620 012240 045 116 045 1$:ASCIZ 'NNA Tape Bus Signals in Word 08:'
1621 012302 045 116 045 2$:ASCIZ 'NNA PARERR<15> IEOT <12> IFMK <9> IRDY<6> IRWD<2>'
1622 012373 045 116 045 3$:ASCIZ 'NNA IRESV2<14> IIDENT<11> IHER <8> IONL<5> IFBY<1>'
1623 012464 045 116 045 4$:ASCIZ 'NNA IRESV1<13> ICER <10> ISPEED<7> ILDP<4> IFPT<0>'
1624 012555 045 116 045 5$:ASCIZ 'NNA Tape Bus Signals in Word 09:'
1625 012617 045 116 045 6$:ASCIZ 'NNA DATMIS<7> ILW<6> OUTRDY<5> INRDY<4>'
1626 .EVEN
1627
1628
1629
1630 .SBTTL MSGLOOP - PRINT LOOPBACK HEADER AND MESSAGE BUFFERS
1631 ;
1632 ;
1633 ;PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RCV
1634 ;
1635 ;IMPLICIT INPUTS:
1636 ;
1637 ; EXPMSG - EXPECTED MESSAGE BUFFER
1638 ; RECMMSG - RECEIVED MESSAGE BUFFER
1639 ; RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
1640 ; RCVLOAD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
1641 ;
1642 012674 BGNMSG MSGLOOP
1643 012674 MSGLOOP:
1644 012674 012701 012736' MOV #LOOPCOD,R1 ;ASCII ADDRESS TABLE
1645 012700 012100 10$: MOV (R1)+,R0 ;DONE ALL MSG LINES?
1646 012702 001410 BEQ 20$ ;BR IF YES
1647 012704 PRINTX R0 ;PRINT STATUS BIT NAMES
1648 012704 010046 MOV R0,-(SP)
1649 012706 012746 000001 MOV #1,-(SP)
1650 012712 010600 MOV SP,R0
1651 012714 104415 TRAP C$PNTX
1652 012716 062706 000004 ADD #4,SP
1653 012722 000766 BR 10$ ;DO ANOTHER MSG LINE
1654 012724 012700 000012 20$: MOV #10,R0 ;NUMBER OF WORDS IN A READ STATUS BUFFER
1655 012730 004737 014362' JSR PC,PRMSGEXP ;PRINT EXPD/RCV MESSAGE BUFFERS
1656 012734 ENDMMSG
1657 012734 104423 L10014: TRAP C$MSG
1658
1659 012736 012756' 013031' 013130' LOOPCOD: .WORD 1$,2$,3$,4$,5$,6$,7$,0
1660 012756 045 116 045 1$:ASCIZ 'NNA Tape Bus Loopback Signals in Word 08:'
1661 013031 045 116 045 2$:ASCIZ 'NNA PARERR<15> IRESV2<14> IRESV1<13>'
1662 013130 045 116 045 3$:ASCIZ 'NNA IHISP->IEOT<12> IWRT->IIDENT<11> IREV ->ICER <10>'
1663 013227 045 116 045 4$:ASCIZ 'NNA IWFM ->IFMK<09> IEDIT->IHER <08> IFAD ->ISPEED<07>'
1664 013326 045 116 045 5$:ASCIZ 'NNA ITADO->IRDY<06> ITAD1->IONL <05> IERASE->ILDP <04>'
1665 013425 045 116 045 6$:ASCIZ 'NNA IREW ->IDBY<03> IRWU ->IRWD <02> IFEN ->IFBY <01>'
1666 013524 045 116 045 7$:ASCIZ 'NNA IGO ->IFPT<00>'
1667 .EVEN
1668
1669 .SBTTL MSGSUB - PRINT WRITE SUBSYSTEM MESSAGE BUFFER
1670 ;
1671 ;
1672 ;PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RCV
1673 ;
1674 ;

```

G5

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 MSGSUB - PRINT WRITE SUBSYSTEM MESSAGE BUFFER

SEQ 058

```

1668      ;IMPLICIT INPUTS:
1669      ;
1670      ;     EXPMSG - EXPECTED MESSAGE BUFFER
1671      ;     RECMMSG - RECEIVED MESSAGE BUFFER
1672      ;     RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
1673      ;     RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
1674      ;
1675      ;-
1675      BGNMSG MSGSUB
1676      MSGSUB:
1676      MOV     #10.,R0      ;SIZE OF WRITE SUBSYSTEM BUFFER
1677      JSR     PC,PRMSGEXP  ;PRINT EXPD/RCV MESSAGE BUFFERS
1678      ENDMMSG
1678      L10015:
1678      TRAP    C#MSG
1679
1680
1681
1682
1683
1684      .SBTTL MEMADD - PRINT MEMORY ADDRESS DATA ERROR
1685      ;+
1686      ;
1687      ;PRINT ROUTINE TO PRINT MEMORY ADDRESS DATA COMPARE ERROR
1688      ;
1689      ;IMPLICIT INPUTS:
1690      ;
1691      ;     ERRHI - MEMORY ERROR HIGH ORDER ADDRESS
1692      ;     ERRLO - MEMORY ERROR LOW ORDER ADDRESS
1693      ;     EXP - EXPECTED DATA
1694      ;     RECV - RECEIVED DATA
1695      ;
1696      ;-
1696      BGNMSG MEMADD
1697      MEMADD:
1697      JSR     PC,PRIADD    ;PRINT MEMORY ADDRESS IN ERROR
1698      MOV     EXPD,R1      ;GET EXPD DATA
1699      MOV     RECV,R2      ;GET RECEIVED DATA
1700      JSR     PC,PRI XOR   ;PRINT EXPD/RCV
1701      ENDMMSG
1701      L10016:
1701      TRAP    C#MSG
1702
1703      .SBTTL PRAMPKT - PRINT RAM AND PACKET DATA
1704      ;+
1705      ;
1706      ;PRINT ROUTINE TO DISPLAY RAM/PACKET DATA
1707      ;WHEN THE RAM DATA DOES NOT MATCH.
1708      ;
1709      ;INPUTS:
1710      ;
1711      ;     R4      POINTER TO COMMAND PACKET
1712      ;
1713      ;IMPLICIT INPUTS:
1714      ;
1715      ;     RAMDATA      DATA AS READ FROM THE RAM
1716      ;     RAMSIZ       NUMBER OF BYTES IN PACKET
1717      ;                   IF RAMSIZ=0 THEN DEFAULT TO 8.
1718      ;

```

H5

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 PRAMPKT - PRINT RAM AND PACKET DATA

SEQ 059

```

1719                                     ;IMPLICIT OUTPUTS:
1720                                     ;
1721                                     ;      RAMSIZ  SET TO 0
1722                                     ;
1723
1724 PRAMPKT:
1725     SAVREG
1726     MOV     @RAMDATA,R1               ;SAVE R1-R5 UNTIL NEXT RETURN
1727     CLR     R2                        ;DATA FROM THE RAM
1728     CMQB    (R1), (R4)                ;INIT BYTE NUMBER
1729     BNE     7$                        ;COMPARE EXPECTED, RECEIVED
1730     FORCERROR 7$,NOTSSR              ;BR IF NO MATCH
1731     BR      10$
1732     MOVB    -1(R1),R5                 ;GET RECV RAM DATA
1733     MOVB    -1(R4),R3                 ;GET EXPD PACKET DATA
1734     XOR     R5,R3                     ;XOR EXPD/RECV
1735     BIC     @177400,R3                ;LOW BYTE ONLY
1736     MOVB    -1(R1),RECV              ;GET RECEIVED RAM DATA
1737     MOVB    -1(R4),EXPD              ;GET EXPECTED RAM DATA
1738     PRINTB  @RAMASC,R2,RECV,EXPD,R3
1739     MOV     R3,-(SP)
1740     MOV     EXPD,-(SP)
1741     MOV     RECV,-(SP)
1742     MOV     R2,-(SP)
1743     MOV     @RAMASC,-(SP)
1744     MOV     @5,-(SP)
1745     MOV     SP,R0
1746     THAP    C$PNTB
1747     ADD     @14,SP
1748     INC     R2                        ;UPDATE BYTE COUNT
1749     10$:   1ST     RAMSIZ              ;DEFAULT TO 8.?
1750     BEQ     15$                        ;BR IF YES
1751     CMP     R2,RAMSIZ                  ;DONE ALL BYTES?
1752     BLE     5$                          ;BR IF NO
1753     BR      25$
1754     15$:   CMP     R2,@8.              ;DONE DEFAULT NUMBER OF BYTES?
1755     BLT     5$                          ;BR IF NO
1756     20$:   BLT     5$
1757     25$:   CLR     RAMSIZ              ;SET DEFAULT RAMSIZ
1758     RTS     PC                          ;RETURN
1759
1760 RAMASC: .ASCIZ '4N4A  BYTE: 4D4A RAM: 4034A Packet: 4034A XOR:4034'
1761 .EVEN
1762
1763     .SBTTL  PRMESS - PRINT CONTENTS OF MESSAGE BUFFER
1764
1765     ;
1766     ;THIS ROUTINE PRINTS THE CONTENTS OF
1767     ;THE 7 OR 8 WORD MESSAGE BUFFER RETURNED BY THE
1768     ;TSV-05.
1769     ;
1770     ;INPUT:
1771     ;
1772     ;      R0      LOW ORDER ADDRESS OF MESSAGE BUFFER
1773     ;      R1      HIGH ORDER ADDRESS OF MESSAGE BUFFER
1774     ;      NOTE: R1 IS IGNORED IF KENABLE FLAG IS CLEAR
1775     ;
1776     ;THIS ROUTINE IS NORMALLY CALLED FROM A PRINT ROUTINE

```

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10.12
PRMESS - PRINT CONTENTS OF MESSAGE BUFFER

SEQ 060

```

1767
1768
1769
1770 014052
1771 014052
1772 014056 010005
1773 014060 005737 003132'
1774 014064 001001
1775 014066 005001
1776 014070 010103
1777 014072 006100
1778 014074 006101
1779 014076
    014076 010546
    014100 010146
    014102 012746 014230'
    014106 012746 000003
    014112 010600
    014114 104415
    014116 062706 000010
1780 014122
    014122 012746 014275'
    014126 012746 000001
    014132 010600
    014134 104415
    014136 062706 000004
1781 014142 005004
1782 014144 010501
1783 014146 010300
1784 014150 001405
1785 014152 004737 017130'
1786 014156 010005
1787 014160
    014160 012546
    014162 010446
    014164 012745 014333'
    014170 012746 000003
    014174 010600
    014176 104415
    014200 062706 000010
1788 014204 005204
1789 014206 020427 000007
1790 014212 003005
1791 014214 002761
1792 014216 032763 000200 000012
1793 014224 001355
1794 014226 000207
1795
1796 014230 045 116 045 PROASC: .ASCIZ 'NWA Message Buffer Address - 0105'
1797 014275 045 116 045 PR1ASC: .ASCIZ 'NWA Message Buffer Contents:'
1798 014333 045 116 045 PRASC: .ASCIZ 'NWA Word01WA: 0'
1799
1800
1801
1802
1803
1804

```

PRMESS: SAVREG ;SAVE THE REGISTERS
MOV R0,R5 ;SAVE LOW ORDER ADDRESS
TST KTENAB ;ADDRESS ABOVE 28K?
BNE 10\$;BR IF YES
CLR R1 ;SET HIGH ORDER ADDRESS TO 0
10\$: MOV R1,R3 ;SAVE HIGH ORDER ADDRESS
ROL R0 ;SHIFT BIT15 TO C BIT
ROL R1 ;SHIFT TO HIGH ORDER FOR PRINTOUT
PRINTX @PROASC,R1,R5 ;PRINT MESSAGE BUFFER ADDRESS
MOV R5,-(SP)
MOV R1,-(SP)
MOV @PROASC,-(SP)
MOV @3,-(SP)
MOV SP,R0
TRAP C:PNTX
ADD @10,SP
PRINTX @PR1ASC ;PRINT HEADER FOR CONTENTS
MOV @PR1ASC,-(SP)
MOV @1,-(SP)
MOV SP,R0
TRAP C:PNTX
ADD @4,SP
CLR R4 ;NUMBER OF THE NEXT WORD
MOV R5,R1 ;COPY LOW ORDER ADDRESS
MOV R3,R0 ;COPY HIGH ORDER ADDRESS
BGT 20\$;BR IF NOT ABOVE 28K
JSR PC,SETMAP ;SETUP PAR ADDRESS IN R0
MOV R0,R5 ;GET PAR FORMAT ADDRESS ABOVE 28K
20\$: PRINTX @PRASC,R4,(R5)+ ;PRINT THE CONTENTS OF MEMORY BUFFER
MOV (R5)+,-(SP)
MOV R4,-(SP)
MOV @PRASC,-(SP)
MOV @3,-(SP)
MOV SP,R0
TRAP C:PNTX
ADD @10,SP
INC R4 ;NUMBER OF THE NEXT
CMP R4,#7 ;DONE ALL YET?
BGT 50\$;BRANCH IF ALL DONE
BLT 20\$;PRINT FIRST 7 WORDS
BIT @X2.EXTF,XST2(R3);EXTENDED FEATUTES ON ?
BNE 20\$;PRINT EXTENDED STATUS WORD
50\$: RTS PC ;RETURN

.SBTTL PRMSGEXP - PRINT EXPD/RECV MESSAGE BUFFERS
ROUTINE TO PRINT EXPECTED AND RECEIVED MESSAGE BUFFERS

SET

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:53
PRMSGEXP - PRINT EXPD/RCV MESSAGE BUFFERS

SEQ 061

```

1805
1806
1807
1808
1809
1810
1811
1812
1813
1814
1815 014362
1816 014362
1817 014366 010005 002304'
1818 014370 013700
1819 014374 010004
1820 014376 013701 002302'
1821 014402 006100
1822 014404 006101
1823 014406
      014406 010446
      014410 010146
      014412 012746 014542'
      014416 012746 000003
      014422 010600
      014424 104415
      014426 062706 000010
1824 014432
      014432 012746 014607'
      014436 012746 000001
      014442 010600
      014444 104415
      014446 062706 000004
1825 014452 005004
1826 014454 012701 002320'
1827 014460 012702 002464'
1828 014464 011100
1829 014466 011203
1830 014470
1831 014500
      014500 010746
      014502 012246
      014504 012146
      014506 010446
      014510 012746 014645'
      014514 012746 000005
      014520 010600
      014522 104415
      014524 062706 000014
1832 014530 005204
1833 014532 020405
1834 014534 002001
1835 014536 000752
1836 014540 000207
1837
1838 014542 045 116 045 PRMSG0: .ASCIZ 'NNA Message Buffer Address - #01#05
1839 014607 045 116 045 PRMSG1: .ASCIZ 'NNA Message Buffer Contents: '
1840 014645 045 116 045 PRMSG2: .ASCIZ 'NNA WORD #02#A EXPD: #06#A RECV: #06#A XOR: #06#A

```

;
 ; R0 - NUMBER OF WORDS IN BUFFER
 ; IMPLICIT INPUTS:
 ;
 ; EXPMSG - EXPECTED MESSAGE BUFFER
 ; RECMMSG - RECEIVED MESSAGE BUFFER
 ; RCVHIADD - RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
 ; RCVLOADD - RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
 ;
 PRMSGEXP::
 SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
 MOV R0,R5 ;SAVE NUMBER OF WORDS
 MOV RCVLOADD,R0 ;GET RECV LOW ADDRESS
 MOV R0,R4 ;COPY LOW ADDRESS
 MOV RCVHIADD,R1 ;GET RECV HIGH ADDRESS
 ROL R0 ;SHIFT BIT15 TO C BIT
 ROL R1 ;SHIFT TO HIGH ORDER FOR PRINTOUT
 PRINTX #PRMSG0,R1,R4 ;PRINT MESSAGE BUFFER ADDRESS
 MOV R4,-(SP)
 MOV R1,-(SP)
 MOV #PRMSG0,-(SP)
 MOV #3,-(SP)
 MOV SP,R0
 TRAP C#PNTX
 ADD #10,SP
 PRINTX #PRMSG1 ;PRINT HEADER FOR CONTENTS
 MOV #PRMSG1,-(SP)
 MOV #1,-(SP)
 MOV SP,R0
 TRAP C#PNTX
 ADD #4,SP
 CLR R4 ;NUMBER OF THE CURRENT WORD
 MOV #EXPMSG,R1 ;GET EXPD BUFFER ADDRESS
 MOV #RCMMSG,R2 ;GET RECV BUFFER ADDRESS
 20\$: MOV (R1),R0 ;GET EXPD
 MOV (R2),R3 ;GET RECV
 XOR R0,R3 ;XOR EXPD/RECV
 PRINTX #PRMSG2,R4,(R1)+,(R2)+,R3
 MOV R3,-(SP)
 MOV (R2)+,-(SP)
 MOV (R1)+,-(SP)
 MOV R4,-(SP)
 MOV #PRMSG2,-(SP)
 MOV #5,-(SP)
 MOV SP,R0
 TRAP C#PNTX
 ADD #14,SP
 INC R4 ;NUMBER OF THE NEXT
 CMP R4,R5 ;DONE ALL YET?
 BGE 50\$;BR IF YES
 BR 20\$;DO ANOTHER
 50\$: RTS PC ;RETURN

K5

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 PRMSGEXP - PRINT EXPD/RECV MESSAGE BUFFERS

SEQ 062

```

1841 .EVEN
1842
1843 .SBTTL PRBYTEXP - PRINT ERROR BYTES IN EXP/REC MESSAGE BUFFER
1844
1845 ;*
1846 ;ROUTINE TO PRINT ERROR BYTES IN MESSAGE BUFFERS
1847 ; ONLY THE FIRST 8 ERRORS ENCOUNTERED ARE PRINTED DUE TO SCREEN SPACE
1848
1849 ; RO - NUMBER OF BYTES IN BUFFER
1850
1851 ;IMPLICIT INPUTS:
1852
1853 ; EXPMSG - EXPECTED MESSAGE BUFFER
1854 ; RECMMSG - RECEIVED MESSAGE BUFFER
1855
1856 014732 PRBYTEXP::
1857 014732 SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
1858 014736 010005 MOV RO,R5 ;SAVE NUMBER OF BYTES
1859 014740 005037 002316' CLR PRMNO ;INIT ERROR COUNT
1860 014744 005004 CLR R4 ;NUMBER OF THE CURRENT BYTE
1861 014746 012701 002320' MOV EXPMSG,R1 ;GET EXPD BUFFER ADDRESS
1862 014752 012702 002464' MOV RECMMSG,R2 ;GET RECV BUFFER ADDRESS
1863 014756 111100 20$: MOVB (R1),R0 ;GET EXPD BYTE
1864 014760 042700 177400 BIC #C<377>,R0 ;CLEAR UPPER BYTE
1865 014764 110037 015300' MOVB R0,PRBEXP ;SAVE FOR ERROR REPORT
1866 014770 111203 MOVB (R2),R3 ;GET RECV BYTE
1867 014772 042703 177400 BIC #C<377>,R3 ;CLEAR UPPER BYTE
1868 014776 110337 015302' MOVB R3,PRBREC ;FOR ERROR REPORT
1869 015002 XOR R0,R3 ;XOR EXPD/RECV
1870 015012 122122 CMPB (R1)+,(R2)+ ;EXPD = RECV?
1871 015014 001431 BEQ 30$ ;BR IF YES
1872 015016 005237 002316' INC PRMNO ;UPDATE ERROR COUNT
1873 015022 023727 002316' 000010 CMP PRMNO,#8. ;PRINTED 8?
1874 015030 101023 BHI 30$ ;BR IF YES
1875 015032 27$: PRINTX PRBMSG,R4,PRBEXP,PRBREC,R3
1876 015036 010346 MOV R3,-(SP)
1877 015040 013746 015302' MOV PRBREC,-(SP)
1878 015044 013746 015300' MOV PRBEXP,-(SP)
1879 015048 010446 MOV R4,-(SP)
1880 015052 012746 015146' MOV PRBMSG,-(SP)
1881 015056 012746 000005 MOV #5,-(SP)
1882 015060 010600 MOV SP,R0
1883 015064 104415 TRAP C#PNTX
1884 015068 062706 000014 ADD #14,SP
1885 015072 000404 FORCEEXIT 50$ ;880
1886 015076 000404 BR 35$ ;880
1887 015100 30$:
1888 015104 35$: FORCERROR 27$.NOTSSR ;880
1889 015108 35$:
1890 015112 005204 INC R4 ;NUMBER OF THE NEXT
1891 015116 020405 CMP R4,R5 ;DONE ALL YET?
1892 015120 002001 BGE 50$ ;BR IF YES
1893 015124 000717 BR 20$ ;DO ANOTHER
1894 015128 50$: PRINTX PRBTOT,PRMNO ;PRINT TOTAL ERROR COUNT
1895 015132 013746 002316' MOV PRMNO,-(SP)
1896 015136 012746 015233' MOV PRBTOT,-(SP)
1897 015140 012746 000002 MOV #2,-(SP)

```


L5

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:56
 PRBYTEXP - PRINT ERROR BYTES IN EXP/REC MESSAGE BUFFER

SEQ 063

```

015134 010600      MOV      SP,RO
015136 104415      TRAP     C$PNTX
015140 062706 000006 ADD     @6,SP
1886 015144 000207      RTS      PC              ;RETURN
1887
1888 015146      045      116      045  PR6MSG: .ASCII2 'NNA BYTE D2NA EXPD: 03NA RECV: 03NA XOR: 03'
1889 015233      045      116      045  PRBTOT: .ASCII2 'NNA NUMBER OF BYTES IN ERROR = D2'
1890
1891 015300 000000      PRBEXP: .WORD 0              ;EXPD
1892 015302 000000      PRBREC: .WORD 0              ;RECV
1893
1894                      .SBTTL  EXPREC - PRINT EXPD/RECV WORD DATA
1895
1896                      ;+
1897                      ;PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
1898                      ;
1899                      ;INPUTS:
1900                      ;
1901                      ;      R1      RECEIVED DATA
1902                      ;      R2      EXPECTED DATA
1903                      ;
1904                      ;-
1905
1906 015304      BGNMSG  EXPREC
1907 015304 004737 007552' EXPREC:; JSR      PC,PRIXOR              ;PRINT THE DATA
1908 015310      ENDMMSG
1909 015310 104423      L10017: TRAP     C$MSG
1910
1911
1912
1913                      .SBTTL  EXPBREC - PRINT EXPD/RECV BYTE DATA
1914                      ;+
1915                      ;PRINT ROUTINE TO DISPLAY BYTE EXPD/RECV DATA
1916                      ;
1917                      ;INPUTS:
1918                      ;
1919                      ;      R1      RECEIVED DATA BYTE
1920                      ;      R2      EXPECTED DATA BYTE
1921                      ;
1922                      ;-
1923
1924
1925
1926 015312      BGNMSG  EXPBREC
1927 015312 004737 007422' EXPBREC:; JSR      PC,PRIBXOR              ;PRINT THE DATA
1928 015316      ENDMMSG
1929 015316 104423      L10020: TRAP     C$MSG
1930
1931
1932
1933                      .SBTTL  RAMERR - PRINT RAM AND PACKET DATA

```

M5

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 RAMERR - PRINT RAM AND PACKET DATA

SEQ 064

1934
 1935
 1936
 1937
 1938
 1939
 1940
 1941
 1942
 1943
 1944
 1945
 1946
 1947
 1948
 1949
 1950
 1951
 1952
 1953 015320
 015320
 1954 015320 004737 013606'
 1955 015324
 015324
 015324 104423
 1956
 1957
 1958
 1959
 1960
 1961
 1962
 1963
 1964
 1965
 1966
 1967
 1968
 1969
 1970
 1971
 1972
 1973
 1974
 1975
 1976
 1977
 1978
 1979
 1980 015326
 015325
 1981 015326 004737 010104'
 1982 015332 004737 013606'
 1983 015336
 015336
 015336 104423
 1984

```

;+
;PRINT ROUTINE TO DISPLAY RAM/PACKET DATA
;INPUTS:
;      R4      POINTER TO COMMAND PACKET
;IMPLICIT INPUTS:
;      RAMDATA  DATA AS READ FROM THE RAM
;      RAMSIZ   NUMBER OF BYTES IN PACKET
;              IF RAMSIZ=0 THEN DEFAULT TO 8.
;IMPLICIT OUTPUTS:
;      RAMSIZ   SET TO 0
;-

      BGNMSG  RAMERR
RAMERR: JSR    PC,PRAMPKT      ;PRINT RAM/PACKET DATA
        ENDMSG
L10021: TRAP   C$MSC

      .SBITL  RAMTADD - PRINT TEST ADDRESS, RAM AND PACKET DATA
;+
;PRINT ROUTINE TO DISPLAY RAM/PACKET DATA
;INPUTS:
;      R4      POINTER TO COMMAND PACKET
;IMPLICIT INPUTS:
;      RAMDATA  DATA AS READ FROM THE RAM
;      RAMSIZ   NUMBER OF BYTES IN PACKET
;              IF RAMSIZ=0 THEN DEFAULT TO 8.
;      ERRHI    HIGH ORDER TEST ADDRESS
;      ERRLO    LOW ORDER TEST ADDRESS
;IMPLICIT OUTPUTS:
;      RAMSIZ   SET TO 0
;-

      BGNMSG  RAMTADD
RAMTADD: JSR    PC,PRITADD      ;PRINT TEST ADDRESS
        JSR    PC,PRAMPKT      ;PRINT RAM/PACKET DATA
        ENDMSG
L10022: TRAP   C$MSG

```

N5

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
RAMTADD - PRINT TEST ADDRESS, RAM AND PACKET DATA

SEQ 065

```

1985
1986
1987
1988
1989
1990
1991
1992
1993
1994
1995
1996
1997
1998 015340
      015340
1999 015340 042701 177400
2000 015344 042702 177400
2001 015350 004737 007676'
2002 015354 004737 007552'
2003 015360
      015360
      015360 104423
2004
2005
2006
2007
2008
2009
2010
2011
2012
2013
2014
2015
2016
2017 015362
      015362
2018 015362
      015362 012746 015410'
      015366 012746 000001
      015372 010600
      015374 104415
      015376 062706 000004
2019 015402 004737 007552'
2020 015406
      015406
      015406 104423
2021
2022
2023 015410 045 116 045 TIMSGO: .ASCIZ 'NNA TIMER A STATUS IS IN BIT 3NNA TIMER B STATUS IS IN BIT 2'
2024 .EVEN
2025
2026
2027
2028
2029
2030

```

```

      .SBTTL  RAMEXP  - PRINT RAM EXPD/RECV DATA
      ;+
      ;PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
      ;INPUTS:
      ;
      ;      R1      RECEIVED DATA
      ;      R2      EXPECTED DATA
      ;      R4      CONTROLLER RAM ADDRESS
      ;
      BGNMSG  RAMEXP
RAMEXP::
      BIC     #C<377>,R1          ;SAVE EXPD RAM DATA BYTE
      BIC     #C<377>,R2          ;SAVE EXPD RAM DATA BYTE
      JSR     PC,PRIRAM           ;PRINT THE RAM ADDRESS
      JSR     PC,PRIXOR           ;PRINT THE DATA
      ENDMSG
L10023:
      TRAP    C$MSG

      .SBTTL  TIMEXP  - PRINT TIMER A,B AND EXP/REC
      ;+
      ;PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
      ;AND TIMER A,B HEADER MESSAGE
      ;INPUTS:
      ;
      ;      R1      RECEIVED DATA
      ;      R2      EXPECTED DATA
      ;
      BGNMSG  TIMEXP
TIMEXP::
      PRINTX  #TIMSGO             ;PRINT HEADER
      MOV     #TIMSGO,-(SP)
      MOV     #1,-(SP)
      MOV     SP,R0
      TRAP    C$PNTX
      ADD     #4,SP
      JSR     PC,PRIXOR           ;PRINT THE DATA
      ENDMSG
L10024:
      TRAP    C$MSG

      .SBTTL  BADSSR  - PRINT TSSR ERRORS ON DATA TRANSFERS
      ;+
      ;

```

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 BADSSR - PRINT TSSR ERRORS ON DATA TRANSFERS

SEQ 066

```

2031      PRINT ROUTINE FOR TSSR ERRORS ON DATA TRANSFERS
2032
2033      INPUTS:
2034
2035      R1      CONTENTS OF TSSR
2036      R2      DATA WRITTEN (8 BITS)
2037
2038
2039
2040      BGNMSG  BADSSR
2041      BADSSR:
2042      MOV     R2, -(SP)          ;SAVE DATA TRANSFERRED
2043      BIC     #177400, R2      ;GET JUST ONE BYTE
2044      PRINTB  #XFERASC, R2
2045      MOV     R2, -(SP)
2046      MOV     #XFERASC, -(SP)
2047      MOV     #2, -(SP)
2048      MOV     SP, R0
2049      TRAP     C1PNTB
2050      ADD     #6, SP
2051      MOV     (SP), R2          ;RESTORE R2
2052      JSR     PC, PRITSSR      ;DECODE TSSR CONTENTS
2053      ENDMMSG
2054
2055      L10025:
2056      TRAP     C1MSG
2057      ,ASCIZ  'N/A Data Transferred = #03'
2058
2059      .SBTTL  GLOBAL SUBROUTINES SECTION
2060
2061      ;
2062      ; THE GLOBAL SUBROUTINES SECTION CONTAINS THE SUBROUTINES
2063      ; THAT ARE USED IN MORE THAN ONE TEST.
2064      ;
2065
2066      .SBTTL  SOFINIT - SOFT INITIALIZE OF CONTROLLER
2067
2068      ;
2069      ; ROUTINE TO DO A SOFT INITIALIZE OF THE CONTROLLER
2070      ; BY WRITING INTO THE TSSR REGISTER. AFTER THE INIT,
2071      ; THE TSSR REGISTER IS TESTED FOR ERRORS. ANY ERRORS
2072      ; DETECTED SHOULD BE TREATED AS DEVICE FATAL ERRORS.
2073
2074      INPUTS:
2075
2076      R5      ADDRESS OF FIRST REGISTER
2077
2078      OUTPUTS:
2079
2080      R0      CONTENTS OF TSSR, IF ERROR
2081      CARRY   SET IF INIT WAS OKAY
2082      CLEAR IF FATAL ERROR
2083
2084      CALLING SEQUENCE:
2085
2086      MOV     #ADDRESS, R5

```

C6

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
SOFINIT - SOFT INITIALIZE OF CONTROLLER

SEQ 067

```

2079      JSR      PC,SOFINIT
2080      BCS      CONTINUE
2081      ERDF      ;REPORT FATAL ERROR
2082
2083      ;
2084
2085      SOFINIT:
2086      SAVREG      ; SAVE THE REGISTERS
2087      MOV      #0,TSSR(R5) ; DO THE INIT.
2088      JSR      PC,WAITF      ; WAIT FOR SSR
2089      MOV      TSSR(R5),R0    ; GET THE TSSR REGISTER
2090      MOV      R0,R4          ; TSSR CONTENTS
2091      BIC      #C<HIADDR!OFL>,R4
2092      BIS      #SSR!NBA,R4    ; R4 HAS EXPECTED CONTENTS
2093      CMP      R4,R0          ; ONLY EXPECTED BITS SET ?
2094      BEQ      5$             ; BRANCH IF OKAY
2095      CLC                      ; CLEAR THE CARRY FOR ERROR
2096      BR       10$           ; GO TO EXIT
2097      SEC                      ; SET THE CARRY BIT
2098      RTS      PC            ; RETURN TO CALLER
2099
2100      .SBTTL  CHKAMB - CHECK TSSR FOR AMBIGUITY
2101
2102      ;
2103      ; THIS ROUTINE TESTS THE CONTENTS OF THE TSSR REGISTER
2104      ; FOR AMBIGUITY
2105
2106      ; INPUT:
2107
2108      ;      R0      CONTENTS OF TSSR
2109
2110      ; OUTPUT:
2111
2112      ;      R0      CONTENTS OF TSSR
2113
2114      ;      CARRY   SET - NO AMBIGUITY
2115      ;              CLR - AMBIGUOUS CONTENTS
2116
2117      ;
2118      ;
2119
2120      CHKAMB:
2121      SAVREG      ; SAVE THE GENERAL REGISTERS
2122      MOV      R0,R4          ; CONTENTS OF TSSR
2123      BIT      #SC,R0          ; IS BIT 15 SET ?
2124      BNE      5$             ; BRANCH IF YES
2125      BIT      #C<NBA!OFL!SSR!HIADDR>,R0 ; ANY OTHER BITS SET ?
2126      BNE      40$           ; MUST BE AN ERROR
2127      BR       45$           ; RETURN WITH SUCCESS
2128      BIT      #SSR,R0        ; IS READY BIT SET ?
2129      BNE      10$           ; BRANCH IF READY BIT IS SET.
2130      BIT      #BIT5,R0       ; IS FATAL ERROR BIT SET ?
2131      BEQ      40$           ; ERROR IF NOT
2132      BIC      #C<TERCLS>,R4   ; CLEAR ALL BUT TERMINATION CODE
2133      CMP      R4,#16         ; ALL THREE BITS MUST BE SET
2134      BNE      40$           ; ERROR IF NOT SET
2135      BR       45$           ; OK IF ALL ARE SET

```

D6

TSV3 GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
CHKAMB - CHECK TSSR FOR AMBIGUITY

SEQ 068

```

2136 015730 032700 000040      104:  BIT    #BIT5,R0      ;IS FATAL ERROR BIT SET ?
2137 015734 001405              BEQ    45$          ;ERROR IF BIT IS SET WITH SSR
2138 015736 032700 000006      104:  BIT    #BIT2!BIT1,R0    ;IS THIS A FUNCTION REJECT
2139 015742 001002              BNE    45$          ;BR, IF TSSR IS OK
2140 015744 000241      404:  CLC                    ;AMBIGUOUS CONTENTS
2141 015746 000401              BR     50$
2142 015750 000261      454:  SEC                    ;SHOW SUCCESS - NO AMBIGUITY
2143 015752 000207      504:  RTS     PC              ;RETURN TO CALLER
2144
2145      .SBTTL  ENAINT,DSBINT - ENABLE/DISABLE INTERRUPTS
2146
2147      ; DEFAULT DISPLAY INTERRUPT HANDLERS.
2148      ; IF DISPLAY TIME-OUT, REPORT DEV FATAL, AND ABORT PASS.
2149      ; OTHERWISE, SAVE DPU REGISTERS AND DISMISS.
2150
2151
2152      ; BIT DEFINITIONS FOR "INTMASK" AND "INTFLAG" BYTES:
2153
2154      000200      IOKCKIN=BIT7      ; DON'T CHECK FOR BAD INTERRUPTS -- TEST WILL.
2155      000001      IOKSTP=BIT0      ; EXPECT "STOP" INTERRUPT.
2156
2157      ; INTERRUPT MASK -- SAYS EXPECTING INTERRUPTS
2158 015754      000      INTMASK: .BYTE 0
2159
2160 015755      000      ; INTERRUPT FLAG -- SAYS WE GOT ONE (IF POSITIVE)
2161      INTFLAG: .BYTE 0
2162
2163 015756 000000      ; SAVED INTERRUPT VECTOR:
2164      INTVEC: .WORD 0
2165 015760 000000      ; SAVE CPU PC
2166      INTCPC: .WORD 0
2167
2168 015762 010046      ; SUBROUTINE TO ENABLE INTERRUPTS:
2169 015764 013700 002206'  ENAINT: MOV    RO,-(SP)      ;SAVE RO
2170 015770 012720 016026'      MOV    IVEC,RO      ;GET POINTER TO VECTORS
2171 015774 012720 000340      MOV    #INTR,(RO)+    ;SET UP INTERRUPT VECTOR
2172 016000 012600      MOV    #PRI07,(RO)+
2173 016002 011646      MOV    (SP)+,RO      ;RESTORE RO
2174 016004 012766 000000 000002      MOV    (SP),-(SP)
2175 016012 000002      MOV    #0,2(SP)      ;SET CPU TO LEVEL 0
2176      RTI
2177
2178 016014 011646      ; SUBROUTINE TO DISABLE INTERRUPTS (RAISE PRIORITY TO LEVEL 7)
2179 016016 012766 000340 000002      DSBINT: MOV    (SP),-(SP)
2180 016024 000002      MOV    #PRI07,2(SP)
2181      RTI
2182
2183      .SBTTL  INTR - INTERRUPT HANDLERS
2184 016026
2185 016026      BGNSRV  INTR      ;DEFINE INTERRUPT ENTRY
2186 016034 105037 015755'      INTR:: MOV    #1,INTRECV    ;SET FLAG TO SHOW INTERRUPT RECEIVED
2187 016040 132737 000001 015754'      CLRB   INTFLAG    ;CLEAR FLAG TO SAY WE GOT INTERRUPT
2188 016046 001003      BITB   #IOKSTP,INTMASK    ;EXPECTING STOP INTERRUPT?
2189 016050 152737 000001 015755'      BNE    1$          ;BR IF YES
2190
2191      BISB   #IOKSTP,INTFLAG    ;NO. SET THE ERROR FLAG.
;SAVE REGISTERS, MSG BUFFER, ETC.

```


TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
CHKTSSR - CHECK TSSR FOR READY

SEQ 070

```

2238      ;      RO      CONTENTS OF TSSR
2239      ;      CARRY    SET - OKAY
2240      ;
2241      ;      CLR      - NOT READY AMBIGUOUS, OR SC SET
2242      ;
2243      ;
2244      016146      CHKTSSR:
2245      016146      004737      016060'      JSR      PC, WAITF      ;WAIT FOR READY
2246      016152      103014      BCC      20$      ;BRANCH IF TIME OUT
2247      016154      004737      015654'      JSR      PC, CHXAMB      ;TSSR AMBIGUOUS?
2248      016160      103006      BCC      10$      ;BR IF YES
2249      016162      032700      100000      BIT      0SC, RO      ;SPECIAL CONDITION SET?
2250      016166      001405      BEQ      15$      ;BR IF NO
2251      016170      032700      074000      BIT      0<SCE!BIE!RMR!NXM>, RO      ;ANY ERROR BITS SET?
2252      016174      001402      BEQ      15$      ;BR IF NO
2253      016176      000241      10$:      CLC      ;SET FAILURE
2254      016200      000401      RR      20$      ;
2255      016202      000261      15$:      SEC      ;SET SUCCESS
2256      016204      000207      20$:      RTS      PC      ;RETURN TO CALLER
2257
2258      .SBTTL      NXNM      - CHECK FOR NONEXISTENT MEMORY
2259
2260      ; ROUTINE TO TEST FOR A NEXM IN THE RANGE (R1) THRU (R2).
2261      ; ON RETURN, IF "C" = 1, (R1) = NEXM ADDRESS.
2262      ; "C" = 0, ALL ADDRESSES OK.
2263
2264      ;CALL:      MOV      ADR1, R1
2265      ;           MOV      ADR2, R2
2266      ;           JSR      PC, NXNM
2267      ;           RETURN      ;TEST "C" AND PROCEED.
2268
2269      016206      012737      016242'      000004      NXNM:      MOV      02$, 004      ; SET BUSERR VECTOR.
2270      016214      012737      000200      000006      MOV      0PRI04, 006
2271      016222      005003      CLR      R3      ;FLAG.
2272      016224      000241      CLC      ;CLEAR THE CARRY FOR NO NXM FOUND
2273      016226      005711      1$:      TST      (R1)      ;TEST THE ADDRESS(ES).
2274
2275      016230      020102      CMP      R1, R2      ;IF ANY TRAP, CONTINUE AT 2$.
2276      016232      001407      BEQ      3$      ;OTHERWISE, CONTINUE HERE.
2277      016234      062701      000002      ADD      02, R1      ;BR IF FINISHED (NO NEXM'S).
2278      016240      000772      BR      1$      ;SET NEXT ADDRESS...
2279
2280      016242      005103      2$:      COM      R3      ;...AND CONTINUE.
2281      016244      012716      016252'      MOV      03$, (SP)      ;GOT ONE, SET FLAG...
2282      016250      000002      RTI
2283      016252      012700      000004      3$:      CLRVEC      04      ;...AND DISMISS INTERRUPT...
2284      016256      104436      MOV      04, RO      ;...AND GIVE BACK THE VECTOR.
2285      016260      005703      TRAP      C$CVEC
2286      016262      001401      TST      R3
2287      016264      000261      BEQ      4$      ;DID WE CATCH ONE ??
2288      016266      000207      SEC      ;NO, "C" = 0, SKIP NEXT.
2289
2290      ;YES, "C" = 1, (R1) = NEXM ADDR.
2291      ;
2292      ;

```

.SBTTL TSTLOOP - CHECK ITERATION COUNT

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
TSTLOOP - CHECK ITERATION COUNT

SEQ 071

```

2293
2294
2295
2296
2297
2298
2299
2300 016270
2301 016270 005737 002166'
2302 016274 001006
2303 016276 005737 002202'
2304 016302 100403
2305 016304 005337 002214'
2306 016310 001002
2307 016312 000241
2308 016314 000401
2309 016316 000261
2310 016320 000207
2311
2312
2313
2314
2315
2316
2317
2318
2319
2320
2321
2322
2323
2324
2325
2326
2327
2328
2329
2330
2331
2332
2333
2334
2335
2336
2337
2338 016322
2339 016322 010046
2340 016324 005037 003152'
2341 016330 005037 018570'
2342 016334 005037 005602'
2343 016340 105037 015754'
2344 016344 013700 002200'
2345 016350 006300
2346 016352 005737 003112'
2347 016356 001430
2348 016360 100010
2349 016362 052760 160000 003174'

; SUBROUTINE TO EXECUTE TEST ITERATIONS.
; EXIT WITH "C" SET IF LOOPS ALLOWED AND LOOP COUNT NON-ZERO.
; LOOP COUNTER IS SET BY "BEGIN.TEST" MACRO.
; CALL: LOOPTO ARG
TSTLOOP::
    TST     NOITS          ; ITERATIONS INHIBITED?
    BNE     1$             ; YES.
    TST     QVP            ; NO.
    BMI     1$             ; LOOPS DISALLOWED IN QUICK PASS.
    DEC     LOOPCNT        ; BUMP LOOP COUNTER.
    BNE     2$
1$: CLC                     ; LOOP DISALLOWED, OR DONE.
    BR      3$
2$: SEC                     ; LOOP ENABLED.
3$: RTS     PC

.SBTTL TSTSETUP - PRINT TEST NAME AND INIT ERROR COUNTS
; PRINT THE NUMBER AND NAME OF EACH TEST AS WE GO ALONG.
; INCREMENT "TESTK" TO INDICATE THE NUMBER OF TESTS
; IN THE CURRENT RUN SEQUENCE.
; CLEAR THE ERROR COUNTER AND SIGNATURE EXTENSION FLAGS.
; INPUT:
;     R0      POINTER TO TEST ID ASCIZ STRING
; OUTPUT:
;     R5      ADDRESS OF FIRST DEVICE REGISTER
; IMPLICIT OUTPUTS:
;     TSTCNT  UPDATED TO COUNT TESTS PERFORMED SINCE START OR RESTART
; SIDE EFFECTS:
;     INTERRUPT LEVEL IS RAISED TO LEVEL OF
;     THE DEVICE UNDER TEST
; -
TSTSETUP::
    MOV     R0, -(SP)      ; SAVE THE TEST ID MESSAGE
    CLR     SIFLAG         ; CLEAR "SOFT INIT" FLAG
    CLR     ERRK           ; CLEAR LOCAL ERROR COUNTER.
    CLR     EXTA           ; CLEAR ERROR EXTENSION FLAG.
    CLRB    INTMASK        ; CLEAR INTERRUPT MASK (CHECK ERROR)
    MOV     UNITN, R0       ; GET THE UNIT NUMBER.
    ASL     R0              ; ... AND MAKE IT A WORD OFFSET.
    TST     NODEV          ; DID STARTUP FIND THE DEVICE?
    BEQ     4$             ; BR IF YES
    BPL     3$             ; BR IF NOT IDLE
    BIS     @160000,ERTABL(R0) ; FLAG ERROR IN THE ERROR TABLE

```


H6

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
TSTSETUP - PRINT TEST NAME AND INIT ERROR COUNTS

SEQ 072

```

2350 016370      ERRDF 1,NXR,NXRERR      ; NO DEVICE HERE -- PRINT IT
      016370      104455      TRAP C$ERRDF
      016372      000001      .WORD 1
      016374      003732'     .WORD NXR
      016376      005546'     .WORD NXRERR
2351 016400      000407      BR 2$
2352 016402      052760      160001 003174' 3$: BIS #160001,ERTABL(R0)      ; FLAG ERROR IN THE ERROR TABLE
2353 016410      104455      ERRDF 2,NOINIT      ; DEVICE NOT IDLE
      016412      000002      TRAP C$ERRDF
      016414      004327'     .WORD 2
      016416      000000      .WORD NOINIT
2354 016420      012737      177777 003110' 2$: .WORD 0
2355 016426      013700      002200'     MOV #1,DUFLG      ; DROP THE UNIT
      016432      104451      DODU UNIN,RO
      016434      104444      TRAP C$DODU
2356 016436      000423      DOCLN      ; ABORT THE PASS
      016438      104444      TRAP C$DOCLN
2357 016436      000423      BR 5$
2358
2359 016440      104421      4$: RFLAGS RO      ; GET THE OPERATOR FLAGS.
      016442      032700      001000      TRAP C$RFLA
2360 016442      001412      BIT #PNT,RO      ; PRINT THE TEST NUMBERS?
2361 016446      011600      BEQ 1$      ; BR IF NO
2362 016450      010046      MOV (SP),RO      ; GET THE ID MESSAGE
2363 016452      012746      016516'     PRINTF #TNAM,RO      ; DISPLAY THE TEST ID
      016454      012746      000002      MOV RO,-(SP)
      016460      010600      MOV #TNAM,-(SP)
      016466      062706      000006      MOV #2,-(SP)
      016470      005237      002212'     MOV SP,RO
2364 016474      002212'     TRAP C$PNTF
2365 016500      013700      002210'     ADD #6,SP
      016504      005726      002204'     INC TSTCNT      ; BUMP TEST COUNTER.
      016506      005726      002204'     SETPRI IPRI      ; PRIORITY THAT OF DEVICE
      016510      002207      002204'     MOV IPRI,RO
      016514      002207      002204'     TRAP C$SPRI
2366 016516      045      123      045 5$: TST (SP)      ; FIX UP THE STACK
2367 016516      045      123      045 5$: MOV CSADDR,R5      ; ADDRESS OF TSV REGISTERS ON UNIBUS
2368 016516      045      123      045 5$: RTS PC
2369 016516      045      123      045 5$: .ASCIZ 'TSV Test'
2370
2371
2372      .SBTTL TSTEND - PRINT ERRORS RECEIVED
2373
2374      ; AT END OF EACH TEST, PRINT THE NUMBER OF ERRORS RECEIVED
2375      ; IF NORMAL ERROR REPORTING IS DISABLED (FLA:IER).
2376
2377 016532      104421      TSTEND: RFLAGS RO
      016534      030027      020000      TRAP C$RFLA
2378 016534      001412      BIT RO,IER
2379 016540      001412      BEQ 1$      ; BR IF "IER" NOT SET.
2380 016542      013746      016570'     PRINTF #ESUM,ERRK      ; PRINT ERROR COUNT.
      016546      012746      016572'     MOV ERRK,-(SP)
      016552      012746      000002      MOV #ESUM,-(SP)
      016556      010600      MOV #2,-(SP)
      016560      104417      MOV SP,RO
      016560      104417      TRAP C$PNTF

```

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 TSTEND - PRINT ERRORS RECEIVED

SEQ 073

```

2381 016562 062706 000006      1$: ADD    #6,SP
2382 016566 000207      RTS    PC
2383 016570 000000      ERRK: 0          ; LOCAL ERROR COUNT.
2384 016572 045      101      040 ESUM: .ASCIZ /#A #D#A ERRORS/
2385 016611 105      122      122 EMAXDU: .ASCIZ /ERROR LIMIT REACHED -- DROPPING UNIT/
2386 .EVEN
2387
2388 .SBTTL INCERK - INCREMENT LOCAL ERROR COUNT
2389
2390 ;
2391 ; ROUTINES TO INCREMENT LOCAL ERROR COUNT AND CHECK FOR LIMIT:
2392 INCERK: INC    ERRK          ; INCREMENT LOCAL ERROR COUNT
2393 MOV    RO, -(SP)          ; SAVE RO
2394 MOV    UNITN,RO          ; GET UNIT NUMBER,
2395 ASL    RO                ; ... AND MAKE IT A WORD OFFSET.
2396 ADD    #ERTABL,RO        ; RO GETS ADDRESS OF ERROR TABLE ENTRY.
2397 INC    (RO)              ; INCREMENT THE DEVICE ERROR COUNT
2398 BIT    #7777,(RO)        ; DID WE OVERFLOW THE FIELD?
2399 BNE    1$              ; BR IF NO.
2400 DEC    (RO)              ; YES -- BACK IT UP TO 7777.
2401 MOV    (SP)+,RO          ; RESTORE RO
2402 RTS    PC                ; RETURN TO CALLER.
2403
2404 CKEMAX: MOV    RO, -(SP)    ; SAVE RO
2405 MOV    UNITN,RO          ; GET UNIT NUMBER
2406 ASL    RO                ; ... AND MAKE IT A WORD OFFSET
2407 MOV    #ERTABL(RO),RO    ; GET ERROR TABLE ENTRY
2408 BIC    #170000,PO        ; EXTRACT ERROR COUNT FIELD
2409 CMP    RO,GERRMAX        ; IS GLOBAL LIMIT EXCEEDED FOR THIS UNIT?
2410 BHS    1$              ; BR IF YES
2411 CMP    ERRK,LERRMAX      ; IS LOCAL LIMIT EXCEEDED FOR THIS TEST?
2412 BLO    2$              ; BR IF NO
2413 RFLAGS RO                ; GET OPERATOR FLAGS
2414 TRAP   C#RFLA            ; IS DROPPING INHIBITED?
2415 BNE    2$              ; BR IF YES.
2416 MOV    #-1,DU#LG        ; NO -- DROP THE UNIT
2417 ERDF   4,EMAXDU
2418 TRAP   C#ERDF
2419 .WORD  4
2420 .WORD  EMAXDU
2421 .WORD  0
2422 DODU    UNITN
2423 MOV    UNITN,RO
2424 TRAP   C#DODU
2425 DOCLN
2426 TRAP   C#DCLN
2427 MOV    (SP)+,RO          ; RESTORE RO
2428 RTS    PC                ; RETURN TO CALLER
2429
2430 .SBTTL CKDROP - CHECK IF UNIT SHOULD BE DROPPED
2431
2432 ; CHECK IF UNIT SHOULD BE DROPPED
2433
2434 CKDROP: MOV    RO, -(SP)
2435 FORCERROR 1$,NOTSSR

```

J6

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 CKDROP - CHECK IF UNIT SHOULD BE DROPPED

SEQ 074

```

2429 017026                                RFLAGS R0
      017026 104421                        TRAP  C#RFLA
2430 017030 032700 000040                    BIT  #IDU,R0
2431 017034 001010                        BNE   1$
2432 017036 011600                        MOV   (SP),R0
2433 017040 012737 177777 003110'          MOV   #1,DUFLG
2434 017046                                DODU   UNITN
      017046 013700 002200'                MOV   UNITN,R0
      017052 104451                        TRAP  C#DODU
2435 017054                                DOCLN                                ;ABORT THE PASS
      017054 104444                        TRAP  C#DCLN
2436 017056 012600                        1$: MOV   (SP)+,R0
2437 017060 000207                        RTS   PC
2438
2439                                .SBTTL  CONFIG - DETERMINE CONFIGURATION OF SYSTEM
2440                                ;
2441                                ; SUBROUTINE - DETERMINE CONFIGURATION OF TSUOS SYSTEM.
2442                                ;
2443 017062                                CONFIG:
2444 017062 004737 015604'                  JSR   PC,SOFINIT
2445 017066 000207                        RTS   PC
2446
2447                                .SBTTL  KTON,KTOFF - ENABLE/DISABLE MEMORY MANAGEMENT
2448                                ;
2449                                ; SUBROUTINE - ENABLE MEM MGT.
2450                                ;
2451 017070 005737 003130'                  KTON:  TST   KTFLG                ; GOT KT?
2452 017074 001403                        BEQ   1$                        ; NO.
2453 017076 012737 000001 177572          MOV   #1,SRO                ; YES. ENABLE KT11.
2454 017104 000207                        1$:  RTS   PC
2455
2456
2457
2458                                ;
2459                                ; SUBROUTINE - DISABLE MEM MGT.
2460                                ;
2461 017106 005737 003130'                  KTOFF: TST   KTFLG                ; GOT KT11?
2462 017112 001405                        BEQ   1$                        ; NO.
2463 017114 000240                        NOP
2464 017116 000240                        NOP
2465 017120 012737 000000 177572          MOV   #0,SRO                ; DISABLE KT.
2466 017126 000207                        1$:  RTS   PC
2467
2468                                .SBTTL  SETMAP - SETUP PAR6 MAPPING
2469
2470                                ;
2471                                ;
2472                                ; THIS ROUTINE SETS UP KERNEL PAR6 TP HANDLE
2473                                ; AN 22 BIT ADDRESS. THE OFFSET INTO THE PAGE
2474                                ; IS RETURNED BIASED TO PAR6.
2475                                ;
2476                                ; INPUTS:
2477                                ;
2478                                ; R0      HIGH ORDER ADDRESS BITS
2479                                ; R1      LOW ORDER ADDRESS BITS
2480                                ;
2481                                ; OUTPUTS:

```

```

2482
2483
2484
2485
2486
2487 017130
2488 017130
2489 017134 005737 003130'
2490 017140 001433
2491 017142 010102
2492      000006
2493
2494
2495
2496 017174 042701 000177
2497 017200 020137 003130'
2498 017204 103011
2499 017206 010137 172354
2500 017212 042702 160000
2501 017216 062702 140000
2502 017222 010200
2503 017224 000261
2504 017226 000401
2505 017230 000241
2506 017232 000207
2507
2508
2509
2510
2511
2512
2513
2514
2515
2516
2517
2518
2519
2520
2521
2522
2523
2524 017234
2525 017234
2526 017240 004737 017106'
2527 017244 010003
2528 017246 013701 003122'
2529 017252 013702 003124'
2530 017256 010321
2531 017260 005302
2532 017262 003375
2533 017264 005737 003130'
2534 017270 001502
2535 017272 004737 017070'
2536 017276 005000
2537 017300 013701 003150'
2538      000006

```

```

;
; RO      OFFSET INTO BLOCK WITH PAR6 BIAS (I.E. THE ADDRESS)
; CARRY   SET IF SUCCESS
;          CLR IF ERROR
;
; SETMAP:
; SAVREG          ;SAVE R1-R4 UNTIL NEXT RETURN
; TST             ;SYSTEM HAVE ABOVE 28K?
; BEQ             ;BR IF NO
; MOV             ;SAVE LOW ORDER BITS
; .REPT          6
; ASR             ;CONVERT WORD ADDRESS TO 32W BLOCKS
; ROR             ;MAKE IT DOUBLE PRECISION
; .ENDR
; BIC             ;ALINE FOR LOWER 4K BOUNDARY
; CMP             ;HIGHER THAN EXISTING MEMORY?
; BHIS           10$ ;BR IF YES
; MOV             ;SETUP MAPPING REGISTER PAR6
; BIC             ;SETUP DISPLACEMENT IN PAGE
; AND             ;ADD IN PAR6 BIAS
; MOV             ;RETURN IN R0
; SEC             ;SET SUCCESS
; BR             15$ ;
; CLC             ;SET FAILURE
; RTS            PC  ;RETURN
;
; 10$:
; 15$:

```

```

; SBTTL FILLMEM - FILL MEMORY WITH BACKGROUND PATTERN
;
; FILL MEMORY WITH A BACKGROUND PATTERN
;
; INPUTS:
;
; RO = BACKGROUND PATTERN
; FREE = FIRST LOCATION AVAILABLE TO DIAGNOSTIC
; KTFLG = SET TO HIGHEST MEMORY LOCATION IF > 28K.
;
; OUTPUTS:
;
; NONE
;
; FILLMEM:
; SAVREG          ;SAVE R1-R5 UNTIL NEXT RETURN
; JSR             ;DISABLE KT.
; MOV             ;COPY TEST PATTERN
; MOV             ;GET FIRST FREE LOCATION
; MOV             ;SIZE OF FREE SPACE BELOW 28K.
; MOV             ;STORE A BACKGROUND WORD
; DEC             ;DONE ALL MEMORY IN FREE SPACE?
; BGT             ;BR IF NO
; TST             ;GOT KT?
; BEQ             ;NO. GET OUT.
; JSR             ;YES. ENABLE KT.
; CLR             ;HIGH ORDER ADDRESS START
; MOV             ;GET >28K START ADDRESS (IN 32W BLOCKS)
; .REPT          6
;
; 10$:

```

L6

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
FILLMEM - FILL MEMORY WITH BACKGROUND PATTERN

SEQ 076

```

2539          CLC          ;CLEAR C BIT
2540          ROL R1        ;CONVERT BLOCKS TO WORDS
2541          ROL R0        ;MAKE IT DOUBLE PRECISION
2542          .ENDR
2543 017350 004737 017130'   JSR PC,SETMAP      ;SETUP PAR6 MAPPING REGISTER
2544 017354 010320          30$: MOV R3,(R0)+      ;STORE TEST PATTERN IN >28K ADDRESS
2545 017356 020027 160000   CMP R0,#160000      ;END OF PAR6 MAPPING AREA?
2546 017362 103774          BLO 30$              ;BR IF NO
2547 017364 162700 020000   SUB #20000,R0       ;BACKUP INTO PAR6 MAPPING BEGIN
2548 017370 062737 000200 172354 ADD #200,#KIPAR6 ;POINT TO NEXT 4K BLOCK >28K.
2549 017376 013705 003130'   MOV KTFLG,R5      ;GET VALUE FROM MEMORY SIZER
2550 017402 042705 170000   BIC #170000,R5     ;ONLY 18 BITS PASS
2551 017406 023705 172354   CMP #KIPAR6,R5     ;END OF MEMORY?
2552 017412 001427          BEQ 50$            ;DR IF YES
2553 017414 005737 003142'   TST T23A          ;PROCESSOR TYPE A
2554 017420 001407          BEQ 35$            ;NO KEEP GOING
2555 017422 013704 177572   MOV SR0,R4         ;GET SR0 CONTENTS
2556 017426 042704 177761   BIC #177761,R4    ;CLEAR ALL BUT PAGE NUMBER
2557 017432 022704 000016   CMP #16,R4        ;SEE IF PAGE 7
2558 017436 001415          BEQ 50$            ;EXIT IF THERE
2559 017440 005737 003144'   35$: TST T23B      ;PROCESSOR TYPE B
2560 017444 001410          BEQ 45$            ;NO KEEP GOING
2561 017446 023727 172354 007600 CMP #KIPAR6,#7600 ;REACHED 18 BITS?
2562 017454 103001          BHS 40$            ;YES
2563 017456 000403          BR 45$             ;NO KEEP GOING
2564 017460 012737 000020 172516 40$: MOV #20,SR3 ;SET MMU RELOCATION
2565 017466 000137 017354'   45$: JMP 30$      ;KEEP GOING ON ETC.
2566 017472 004737 017106'   50$: JSR PC,KTOFF ;DISABLE KT.
2567 017476 000207          55$: RTS PC
2568
2569          .SBTTL CMPMEM - COMPARE MEMORY TO BACKGROUND PATTERN
2570
2571          ; COMPARE MEMORY WITH A BACKGROUND PATTERN
2572
2573          ; INPUTS:
2574
2575          ; R0 = BACKGROUND PATTERN
2576          ; FREE = FIRST LOCATION AVAILABLE TO DIAGNOSTIC
2577          ; KTFLG = SET TO HIGHEST MEMORY LOCATION IF > 28K.
2578
2579          ; OUTPUTS:
2580
2581          ; CARRY - SET IF NO ERROR
2582          ; CARRY - CLR IF ERROR
2583
2584          ; IMPLICIT OUTPUTS:
2585
2586          ; ERRHI - ERROR HIGH ADDRESS
2587          ; ERRLO - ERROR LOW ADDRESS
2588          ; EXPD - EXPECTED DATA
2589          ; RECV - RECEIVED DATA
2590
2591 017500          CMPMEM: SAVREG          ;SAVE R1-R5 UNTIL NEXT RETURN
2592 017500          MOV R0,R3          ;COPY TEST PATTERN
2593 017504 010003          JSR PC,KTOFF ;DISABLE KT.
2594 017506 004737 017106'   MOV FREE,R1      ;GET FIRST FREE LOCATION
2595 017512 013701 003122'

```

M6

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 CMPMEM - COMPARE MEMORY TO BACKGROUND PATTERN

SEQ 077

```

2596 017516 013702 003124'      MOV    FRESIZ,R2      ;SIZE OF FREE SPACE BELOW 28K.
2597 017522 020311      10$:    CMP    R3,(R1)      ;FREE SPACE LOCATION EQUAL TO EXPD?
2598 017524 001411      BEQ     15$              ;BR IF YES
2599 017526 010137 002236'      MOV    R1,ERRLO      ;SAVE ADDRESS IN ERROR
2600 017532 005037 002234'      CLR     ERRHI      ;NO HIGH ADDRESS
2601 017536 010337 002230'      MOV    R3,EXPD      ;SAVE EXPD FOR ERROR REPORT
2602 017542 011137 002232'      MOV    (R1),RECV      ;SAVE RECV FOR ERPOR REPORT
2603 017546 000474      BR      50$              ;
2604 017550 005721      15$:    TST    (R1)+        ;POINT TO NEXT ADDRESS
2605 017552 005302      DEC     R2              ;DONE ALL MEMORY IN FREE SPACE?
2606 017554 003362      BGT     10$              ;BR IF NC
2607 017556 005737 003130'      TST    KTFLG      ; GOT KT?
2608 017562 001472      BEQ     55$              ; NO. GET OUT.
2609 017564 004737 017070'      JSR    PC,KTON      ; YES. ENABLE KT.
2610 017570 005000      CLR     R0              ;HIGH ORDER ADDRESS START
2611 017572 013701 003150'      MOV    PST32W,R1      ;GET >28K START ADDRESS (IN 32W BLOCKS)
2612                000006      .REPT    6
2613                ROL     R1              ;CONVERT BLOCKS TO WORDS
2614                ROL     R0              ;MAKE IT DOUBLE PRECISION
2615                .ENDR
2616 017626 042701 000177      BIC     #177,R1      ;ALINE 4K BOUNDARY
2617 017632 00046        MOV    R0,-(SP)      ;SAVE HIGH ORDER
2618 017634 010146      MOV    R1,-(SP)      ;SAVE LOW ORDER
2619 017636 004737 017130'      JSR    PC,SETMAP      ;SETUP PAR6 MAPPING REGISTER
2620 017642 010004      MOV    R0,R4          ;COPY ADDRESS BIASED TO PAR6
2621 017644 012601      MOV    (SP)+,R1      ;RESTORE LOW ORDER IN NON PAR6 FORMAT
2622 017646 012600      MOV    (SP)+,R0      ;RESTORE HIGH ORDER IN NON PAR6 FORMAT
2623 017650 020314      30$:    CMP    R3,(R4)      ;ABOVE 28K LOCATION EQUAL EXPD?
2624 017652 001411      BEQ     32$              ;BR IF YES
2625 017654 010037 002234'      MOV    R0,ERRHI      ;SAVE HIGH ORDER IN ERROR
2626 017660 010137 002236'      MOV    R1,ERRLO      ;SAVE LOW ORDER IN ERROR
2627 017664 010337 002230'      MOV    R3,EXPD      ;SAVE EXPD FOR ERROR REPORT
2628 017670 011437 002232'      MOV    (R4),RECV      ;SAVE RECV FOR ERROR REPORT
2629 017674 000421      BR      50$              ;
2630 017676 062701 000002      32$:    ADD    #2,R1      ;UPDATE NON PAR6 ADDRESS
2631 017702 005500      ADC     R0              ;MAKE IT DOUBLE PRECISION ADD
2632 017704 062704 000002      ADD    #2,R4      ;UPDATE PAR FORMAT ADDRESS
2633 017710 020427 160000      CMP    R4,#160000      ;END OF PAR6 MAPPING AREA?
2634 017714 103755      BLO     30$              ;BR IF NO
2635 017716 162704 020000      SUB    #20000,R4      ;BACKUP INTO PAR6 MAPPING BEGIN
2636 017722 062737 000200 172354      ADD    #200,#KIPAR6      ;POINT TO NEXT 4K BLOCK >28K.
2637 017730 023737 172354 003130'      CMP    #KIPAR6,KTFLG      ;END OF MEMORY?
2638 017736 101744      BLOS    30$              ;BR IF NO
2639 017740 004737 017106'      50$:    JSR    PC,KTOFF      ;TURN OFF MEMORY MAPPING
2640 017744 000241      CLC              ;SET FAILURE
2641 017746 000403      BR      60$              ;
2642 017750 004737 017106'      55$:    JSR    PC,KTOFF      ;TURN OFF MEMORY MAPPING
2643 017754 000261      SEC              ;SET SUCCESS
2644 017756 000207      60$:    RTS     PC
2645
2646                .SBTTL  REGSAV      SAVE R1-R5 ON STACK
2647
2648                ;+
2649                ;ROUTINE TO
2650                ;SAVE R1 THROUGH R5 ON THE STACK
2651                ;
2652                ;CALLING SEQUENCE:

```


N6

T3V3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
REGSAV - SAVE R1-R5 ON STACK

SEQ 078

```

2653
2654
2655
2656
2657
2658
2659
2660
2661
2662
2663
2664
2665
2666 017760
2667 017760 010446
2668 017762 010346
2669 017764 010246
2670 017766 010146
2671 017770 010546
2672 017772 016605 000012
2673 017776 004736
2674 020000 012601
2675 020002 012602
2676 020004 012603
2677 020006 012604
2678 020010 012605
2679 020012 000207
2680
2681
2682
2683
2684
2685
2686
2687
2688
2689
2690
2691
2692
2693
2694
2695
2696
2697
2698
2699
2700 020014
2701 020014
2702 020020
    020020 104443
    020022 000406
    020024 020050
    020026 000022
    020030 020052
    020032 000377
    020034 000000

;
;      JSR      R5,REGSAV
;
; THIS IS A COOROUTINE WHICH TRANSFER CONTROL BACK TO
; THE CALLING ROUTINE. AT THE END OF THE CALLING ROUTINE,
; THE RTS PC RETURNS CONTROL TO THIS ROUTINE TO RESTORE
; REGISTERS.
;
; THIS ROUTINE SHOULD ONLY BE CALLED FROM ROUTINES WHICH ARE
; CALLED VIA A JSR PC INSTRUCTION
;
; -
REGSAV:
    MOV      R4,-(SP)
    MOV      R3,-(SP)
    MOV      R2,-(SP)
    MOV      R1,-(SP)
    MOV      R5,-(SP)
    MOV      10,(SP),R5
    JSR      PC,0(SP)+
    MOV      (SP)+,R1
    MOV      (SP)+,R2
    MOV      (SP)+,R3
    MOV      (SP)+,R4
    MOV      (SP)+,R5
    RTS      PC

;SBTTL  GETPAT  - GET 8 BIT PATTERN FROM OPERATOR
;+
; ROUTINE TO REQUEST AN 8 BIT DATA PATTERN FROM THE OPERATOR
;
; INPUTS:
;
;     NONE.
;
; OUTPUTS:
;
;     R0      OCTAL NUMBER FROM THE OPERATOR
;
; CALLING SEQUENCE:
;
;     JSR      PC,GETPAT
;
; -
GETPAT::
    SAVREG          ;SAVE THE GENERAL REGISTERS
1$:  GMANID  DATASC,PATDAT,0,377,0,377,NO
    TRAP      C$GMAN
    BR        10000$
    .WORD     PATDAT
    .WORD     T$CODE
    .WORD     DATASC
    .WORD     377
    .WORD     T$LOLIM

```

B7

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
GETPAT - GET 8 BIT PATTERN FROM OPERATOR

SEQ 079

```

020036 000377          .WORD  T#HILIM
020040          100001:
2703 020040          BNCOMPLETE 11      ;RETRY IF ERROR
020040 103367          BCC 11
2704 020042 013700 020050'  MOV  PATDAT,R0      ;DATA PATTERN FROM OPERATOR
2705 020046 000207          RTS  PC              ;RETURN TO CALLER
2706
2707          ;
2708          ;LOCAL DATA AREA
2709          ;
2710
2711 020050 000000          PATDAT: .WORD  0      ;TEMPORARY STORAGE FOR DATA
2712 020052 105 116 124 DATASC: .ASCIZ 'ENTER DATA PATTERN'
2713          .EVEN
2714
2715          .SBTTL GETSEL - ISSUE MENU AND GET OPERATOR RESPONSE
2716          ;
2717          ;
2718          ;ROUTINE TO ISSUE A MENU AND GET
2719          ;THE OPERATOR'S RESPONSE.
2720          ;
2721          ;INPUTS:
2722          ;
2723          ; R0 ADDRESS OF ASCIZ STRING OF MENU
2724          ; R1 MAXIMUM ALLOWABLE OPERATOR RESPONSE
2725          ;
2726          ;OUTPUTS:
2727          ;
2728          ; R0 NUMBER OF THE OPERATOR'S SELECTION
2729          ;
2730          ;
2731          ;
2732 020076          GETSEL::
2733 020076          SAVREG          ;SAVE GENERAL REGISTERS
2734 020102 010002          MOV  R0,R2          ;SAVE THE MENU ADDRESS
2735 020104 010203          MOV  R2,R3          ;START OF MENU STRING
2736 020106 005713          TST  (R3)          ;END OF ASCII ?
2737 020110 001412          BEQ  31            ;BRANCH IF ALL LINES DISPLAYED
2738 020112          PRINTF  #SELASC,(R3),      ;DISPLAY THE MENU
020112 012346          MOV  (R3),-(SP)
020116 012746 020262'      MOV  #SELASC,-(SP)
020120 012746 000002      MOV  #2,-(SP)
020124 010600          MOV  SP,R0
020126 104417          TRAP  C#PRINTF
020130 062706 000006      ADD  #6,SP
2739 020134 000764          BR  21
2740 020136          31:  GMANID  MENASC,MENRES,D,-1,0,-1,NO
020136 104443          TRAP  C#GMAN
020140 000406          BR  100011
020142 020316'          .WORD  MENRES
020144 000042          .WORD  T#CODE
020146 020267'          .WORD  MENASC
020150 177777          .WORD  -1
020152 000000          .WORD  T#LOLIM
020154 177777          .WORD  T#HILIM
020156          100011:
2741 020156          BNCOMPLETE 11      ;RETRY IF ERROR

```


C7

TSV3 - GLOBAL AREAS MACRO M1113 07 FEB 84 10:58
GETSEL - ISSUE MENU AND GET OPERATOR RESPONSE

SEQ 080

```

2742 020156 103352      BCC 1#
2743 020160 013700 020316' MOV MENRES,RO      ;GET THE OPERATOR'S REPLY
2744 020166 101411      CMP RO,R1      ;COMPARE TO MAXIMUM ALLOWED
2745 020170      BLOS 5#      ;BRANCH IF OK
      020170 012746 020214' PRINTF #MENERR      ;DISPLAY ERROR MESSAGE
      020174 012746 000001 MOV #MENERR,-(SP)
      020200 010600      MOV #1,-(SP)
      020202 104417      MOV SP,RO
      020204 062706 000004 TRAP C#PNTF
2746 020210 000735      ADD #4,SP
2747 020212 000207      BR 1#      ;RETRY
      020214 045 116 045 5# : RTS PC      ;RETURN TO CALLER
2748 020214 045 116 045 MENERR: .ASCIZ '*** Menu Selection Too Large ***'
2749 020262 045 116 045 SELASC: .ASCIZ '***'
2750 020267 105 156 164 MENASC: .ASCIZ 'Enter Menu Selection: '
2751      .EVEN
2752 020316 000000      MENRES: .WORD 0
2753
2754      .SBTTL CHKMAN - CHECK MANUAL INTERVENTION LEGALITY
2755
2756      ;
2757      ;ROUTINE TO TEST FOR MANUAL INTERVENTION LEGALITY.
2758      ;
2759      ;INPUT:
2760      ;
2761      ; NONE.
2762      ;
2763      ;OUTPUT:
2764      ;
2765      ; CARRY 0 MANUAL INTERVENTION NOT ALLOWED
2766      ; 1 MANUAL INTERVENTION IS OK
2767      ;
2768      ;SIDE EFFECTS:
2769      ;
2770      ; A MESSAGE IS DISPLAYED WARNING THAT TEST IS
2771      ; NOT EXECUTED IF MANUAL INTERVENTION IS NOT
2772      ; ALLOWED.
2773      ;
2774      ;-
2775
2776 020320      CHKMAN:
2777 020320      SAVREG      ;SAVE THE REGISTERS
2778 020324      MANUAL      ;SEE IF MANUAL INTERVENTION OK
2779 020324 104450      TRAP C#MANI
2780 020326 103411      DCOMPLETE 1#      ;BRANCH IF ALLOWED
      020330      BCS 1#
      020330 012746 020354' PRINTF #NOMAN      ;PRINT THE WARNING MESSAGE
      020334 012746 000001 MOV #NOMAN,-(SP)
      020340 010600      MOV #1,-(SP)
      020342 104417      MOV SP,RO
      020344 062706 000004 TRAP C#PNTF
2781 020350 000241      ADD #4,SP
2782 020352 000207      CLC      ;CLEAR CARRY FOR ERROR
2783      1# : RTS PC      ;RETURN
2784 020354 045 116 045 NOMAN: .ASCIZ '*** Manual Intervention not Allowed - Test Aborted ***'
2785      .even

```

F7

TSV3 - GLOBAL AREAS MACRO M1113 07-FEB-84 10:58
 KTINIT - SETUP KT11 MEMORY MANAGEMENT REGISTERS

SEQ 083

```

2901      SUBROUTINE TO CHECK WETHER OR NOT WE'LL TEST NXM
2902
2903
2904      INPUTS:
2905      OUTPUTS:
2906      The NXMFLG is set if we can test.
2907      The NXMLO and NXMHI addresses are setup.
2908
2909
2910      MEMCK::
2911
2912      SAVREG
2913      CLR      NXMFLG
2914      CLR      NXMLO
2915      CLR      NXMHI
2916      BIT      @170000,L$HIME
2917
2918      BNE      14$
2919      TST      T23B
2920      BEQ      1$
2921      CMP      L$HIME,@7777
2922      BLO      2$
2923      JSR      PC,NXMTST
2924      BR       13$
2925      TST      T23A
2926      BEQ      4$
2927      CMP      L$HIME,@5777
2928      BHI      14$
2929      CMP      L$HIME,@3777
2930      BLO      4$
2931      JSR      PC,NXMTST
2932      BR       13$
2933      CMP      L$HIME,@1577
2934      BLO      14$
2935      JSR      PC,NXMTST
2936      ADD      @77,NXMHI
2937      BIT      @177774,NXMHI
2938      BNE      15$
2939      INC      NXMFLG
2940      BR       15$
2941      BR       15$
2942      PRINTF   @NOMEM
2943      MOV      @NOMEM, -(SP)
2944      MOV      @1, -(SP)
2945      MOV      SP,RO
2946      TRAP     CTRAP
2947      ADD      @4,SP
2948      RTS      PC
2949
2950      RETURN
2951
2952
2953      SUBROUTINE TO SETUP THE NXM ADDRESS FOR TESTING
2954
2955      OUTPUTS: NXMLO, NXMHI
2956      SETUP WITH NXM ADDRESS
2957
2958
2959
2960
2961
2962

```

G7

TSV3 - GLOBAL AREAS MACRO MILLI 07-FEB-84 10:58
 KTINIT - SETUP KT11 MEMORY MANAGEMENT REGISTERS

SEQ 084

2953	021222	013701	002120'	NXMTST: MOV	L#HIME,R1	;GET TOP OF MEMORY
2954	021226	062701	000200	ADD	#200,R1	;MAKE IT I/O BLOCK OR OTHER NXM
2955	021232	042701	000177	BIC	#177,R1	
2956	021236	010102		MOV	R1,R2	;RESAVE RESULTS
2957		000006		.REPT	6	
2958				ASL	R1	;PUT IN PLACE FOR XFER
2959				.ENDR		
2960	021254	010137	003136'	MOV	R1,NXMLO	;SAVE TEST ADDRESS LOW
2961		000012		.REPT	10.	
2962				ASR	R2	;PUT IN PLACE FOR XFER
2963				.ENDR		
2964	021304	042702	177700	BIC	#177700,R2	;DON'T WANT ILA!
2965	021310	010237	003140'	MOV	R2,NXMHI	;SAVE TEST ADDRESS HIGH
2966	021314	000207		RTS	PC	;RETURN
2967						
2968						
2969						
2970						
2971	021316			ENDMOD		

H7

TSV4 - MISCELLANEOUS SECTIONS MACRO M1113 07-FEB-84 10:58
 KTINIT - SETUP KT11 MEMORY MANAGEMENT REGISTERS

SEQ 085

```

6          .TITLE  TSV4 - MISCELLANEOUS SECTIONS
7
8 021316    BGNMOD  TSV4
9 021316    TSV4::
10
11
12
13
14          .SBTTL  PROTECTION TABLE
15          BGNPROT
16          L$PROT::
17 021316    .WORD  -1, -1, -1, -1          ;NO DEVICE PROTECTION REQUIRED.
18 021316    177777 177777 177777
19 021326    END:PROT
20
21          .SBTTL  INITIALIZE SECTION
22
23          ;**
24          ;THE INITIALIZE SECTION CONTAINS THE CODING THAT IS PERFORMED
25          ;AT THE BEGINNING OF EACH PASS.
26          ;
27          ;IF "START" OR "RESTART", SET QUICK-PASS FLAG AND BUS-INIT.
28          ;IF "CONTINUE", NOTHING IS REQUIRED.
29          ;
30          ;--
31          ;*
32          ;INSERT TEMPORARY JUMP TO ODT
33          ;-
34 021326    BGNINIT
35 021326    L$INIT::
36 021332    40$: CLR      EXTFEA
37 021332    005037 002224' CLR      NXMFLG
38 021332    005037 003134' MOV      #EPR1,EPR1SW          ;SET UP PRIMARY MESSAGE FOR REPLACEMENT
39 021332    012737 006170' 002176' CLR      SIFLAG          ;CLEAR "SOFT INJT" FLAG
40 021332    005037 003152' CLR      KTENABLE          ;CLEAR TEST ABOVE 28K FLAG
41 021332    005037 002300' CLR      RAMSIZ          ;CLEAR RAM SIZE FOR RAMERR ROUTINE
42 021360    012700 000036 MOV      #EF,CONTINUE
43 021360    104447 TRAP     C$REFG
44 021366    103023 BCC      1$
45 021370    023737 002200' 002012' CMP      UNITN,L$UNIT          ;UNIT IN RANGE?
46 021376    103070 BHS      4$
47 021380    005737 003110' TST      DUFFLG          ;BR IF NO.
48 021404    100472 BMI      NXTU          ;DROPPED UNIT?
49 021406    013701 002200' MOV      UNITN,R1          ;BR IF YES
50 021412    006301 ASL      R1
51 021414    005761 003174' TST      ERTABL(R1)
52 021420    001516 BEQ      SETU
53 021422    0327 040000 003174' BIT      #BIT14,ERTABL(R1)          ;DROPPED?
54 021430    001060 BNE      NXTU
55 021432    104452 EXIT      INIT          ;DO NOTHING IF "CONTINUE".
56 021432    104452 TRAP     C$EXIT
57 021434    000416 .WORD    L10030-.
58 021436    1$: READEF    #EF,NEW
59 021436    012700 000035 MOV      #EF,NEW,RO
60 021442    104447 TRAP     C$REFG
61 021444    BNCOMPLETE NXTU          ;TAKE NEXT UNIT IF NOT NEW PASS.
62 021444    103052 BCC      NXTU
63 021446    READEF    #EF,START

```

TSV4 - MISCELLANEOUS SECTIONS MACRO M1113 07-FEB-84 10:58
INITIALIZE SECTION

SEQ 086

021446	012700	000040		MOV	0EF, START, R0	
021452	104447			TRAP	C\$REFG	
57 021454				BCOMPLETE	2\$	
021454	103404			BCS	2\$	
58 021456				READEF	0EF, RESTART	
021456	012700	000037		MOV	0EF, RESTART, R0	
021462	104447			TRAP	C\$REFG	
59 021464				BNCOMPLETE	31\$	
021464	103031			BCC	31\$	
60 021466			2\$:			;1ST PASS, BUS-INIT...
61 021466				BRESET		;BUS RESET.
021466	104433			TRAP	C\$RESET	
62 021470	005037	002212'		CLR	T\$CNT	;NUMBER OF TESTS RUN IN PASS
63 021474	005037	002220'		CLR	FATFLG	;CLEAR FATAL ERROR COUNT
64 021500	005037	003142'		CLR	T23A	;CLEAR PROCESSOR TYPE A FLAG
65 021504	005037	003144'		CLR	T23B	;CLEAR PROCESSOR TYPE B FLAG
66				MOV	0340, -(SP)	
67				MOV	020\$, -(SP)	
68				JMP	0.0DT	;RETURN TO DEBUGGER
69 021510	005037	003376'		CLR	SKIPT	;ENTER THE DEBUGGER
70 021514			20\$:			;CLEAR THE SUBTEST "SKIPPER"
71 021514	012737	177777	002202'	MOV	0-1, QVP	;...QUICK VERIFY...
72 021522	004737	020450'		JSR	PC, ENVIRN	;SET ENVIRONMENT.
73 021526	004737	020536'		JSR	PC, K\$INIT	;INITIALIZE KT MEMORY MANAGEMENT
74 021532	012700	003174'		MOV	0ERTABL, R0	
75 021536	005020		30\$:	CLR	(R0)	;CLEAR THE ERROR TABLE
76 021540	020027	003374'		CMP	R0, 0ERTABE	
77 021544	103774			BLO	30\$	
78 021546	000404			BR	4\$	
79 021550	005037	002202'	31\$:	CLR	QVP	
80 021554	000137	021624'		JMP	PASRPT	;GO REPORT THE STATUS
81						
82 021560			4\$:			
83 021560	012737	177777	002200'	NEWPAS:	MOV 0-1, UNITN	;INIT UNIT NUMBER...
84 021566	005037	002216'		CLR	DEV\$CNT	;CLEAR COUNT OF DEVICES RUNNING
85 021572				NXTU:	BREAK	
021572	104422			TRAP	C\$BRK	
86 021574	005237	002200'		INC	UNITN	
87 021600	023737	002200'	002012'	CMP	UNITN, L\$UNIT	;...AND SET NEXT UNIT NUMBER.
88 021606	103423			BLO	SETU	
89 021610	012737	177777	003110'	MOV	0-1, DUFLG	
90 021616	000401			BR	11\$	
91 021620				DOCLN		;ABORT, NO MORE UNITS.
021620	104444			TRAP	C\$UCLN	
92 021622	000240		11\$:	NOP		
93 021624			PASRPT:			
94 021624	023727	002012'	000001	CMP	L\$UNIT, 01	;HOW MANY UNITS SELECTED?
95 021632	101752			BLOS	NEWPAS	;BR IF ONLY 1
96 021634	005737	002216'		TST	DEV\$CNT	;ARE ANY STILL RUNNING?
97 021640	001747			BEQ	NEWPAS	;BR IF NO
98 021642				RFLAGS	R0	
021642	104421			TRAP	C\$RFLA	
99 021644	032700	000100		BIT	0ISR, R0	;SHOULD WE PRINT STATISTICS
100 021650	001343			BNE	NEWPAS	;BR IF NO
101						
102 021652				DORPT		
021652	104424			TRAP	C\$DRPT	

J7

TSV4 - MISCELLANEOUS SECTIONS MACRO M1113 07-FEB-84 10:58
INITIALIZE SECTION

SEQ 087

```

103 021654 000741          BR      NEWPAS
104 021656          10$:
105
106 021656          SETU:  GPHARD  UNITN,R0          ;GET UNIT N P-TABLE POINTER.
    021656 013700 002200'  MOV      UNITN,R0
    021662 104442          TRAP     C$GPHRD
107 021664          BNCOMPLETE NXTU          ;BR IF UNIT NOT AVAILABLE.
    021664 103342          BCC      NXTU
108 021666 005037 003110'  CLR      DUFLG          ;CLEAR "DROPPED" FLAG.
109 021672 005237 002216'  INH      DEVCNT
110 021676 012001          MOV      (R0)+,R1          ;GET 1ST REGISTER ADDRESS.
111 021700 010137 002204'  MOV      R1,CSRADDR          ;ADDRESS OF REGISTERS OF UNIT UNDER TEST
112
113 021704 012071          MOV      (R0)+,R1          ;GET VECTOR ADDRESS.
114          ;MOV      (R0),R2          ;GET INTERRUPT PRIORITY
115          ;MOV      R2,IPRI          ;SET INTERRUPT PRIORITY.
116 021706 010137 002206'  MOV      R1,IVEC          ;SET INTERRUPT VECTOR POINTER...
117 021712 012721 016026'  MOV      $INTR,(R1)+          ;...VECTOR...
118 021716 013721 002210'  MOV      IPRI,(R1)+          ;...AND PRIORITY.
119
120 021722          1$:
121          ;      TST      QVP          ;1ST PASS ??
122          ;      BEQ      5$          ;NO, SKIP THE PASS 1 STUFF.
123
124          ;
125          ;1ST PASS, CHECK THAT DEVICE ADDRESSES ARE VALID, AND
126          ;THAT THE DISPLAY STATUS IS PROPERLY INITIALIZED.
127          ;
128 021722 013701 002200'  MOV      UNITN,R1
129 021726 006301          ASL      R1
130 021730 052761 100000 003174'  BIS      $BIT15,ERTABL(R1)          ;SAY DEVICE RUNNING
131 021736 005037 005602'  CLR      EXTA          ;CLEAR ERROR EXTENSION FLAG.
132 021742 023727 002012' 000001  CMP      L$UNIT,#1          ;ARE WE TESTING MULTIPLE UNITS?
133 021750 101416          BLOS     10$          ;BR IF NO.
134 021752          RFLAGS     R0          ;YES -- GET OPERATOR FLAGS.
    021752 104421          TRAP     C$RFLA
135 021754 032700 001000          BIT      $PNT,R0          ;SHOULD WE PRINT UNIT #?
136 021760 001412          BEQ      10$          ;BR IF NOT.
137 021762          PRINTF     $PUNIT,UNITN          ;PRINT THE UNIT #
    021762 013746 002200'  MOV      UNITN,-(SP)
    021766 012746 022054'  MOV      $PUNIT,-(SP)
    021772 012746 000002          MOV      #2,-(SP)
    021776 010600          MOV      SP,R0
    022000 104417          TRAP     C$PNTF
    022002 062706 000006          ADD      #6,SP
138 022006          10$:
139 022006 005037 003112'  CLR      NODEV
140 022012 013701 002204'  MOV      CSRADDR,R1          ;ADDRESS OF FIRST REGISTER
141 022016 010102          MOV      R1,R2          ;START OF REGISTERS
142 022020 062702 000002          ADD      $TSSR,R2          ;ADDRESS OF TSSR REGISTER
143 022024 004737 016206'  JSR      PC,XNXM          ;TEST BOTH CONTROLLER REGISTERS...
144 022030 103005          BCC      2$          ;...AND BR IF ALL OK.
145 022032 010137 003112'  MOV      R1,NODEV          ;FLAG DEVICE AS NON-EXISTENT
146 022036 012737 177777 003110'  MOV      #-1,DUFLG          ;DROP THIS UNIT.
147 022044          2$:
148          ;
149          ;FINALLY, SET CPU PRIORITY AND WE'RE DONE.

```


K7

TSV4 - MISCELLANEOUS SECTIONS MACRO M1113 07-FEB-84 10:58
INITIALIZE SECTION

SEQ 088

```

150
151 022044      5$:  SETPRI 0PRI00      ;ENABLE INTERRUPTS.
      022044 012700 000000      MOV 0PRI00,R0
      022050 104441      TRAP C$SPRI
152 022052      L10030:  ENDINIT
      022052      TRAP C$INIT
      022052 104411
153
154 022054      045      116      045 PUNIT: .ASCIZ /NNNA***** TESTING UNIT %D%A *****/
155      .EVEN
156
157      .SBTTL  ADD AND DROP UNITS SECTIONS
158
159      ;**
160      ; THE ADD-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
161      ; TO BE (A) ADDED TO THE TEST LIST FOR THE FIRST TIME,
162      ; OR (B) RE-INSERTED IF IT HAD BEEN PREVIOUSLY DROPPED.
163      ;--
164 022122      BGNAU
      022122      L$AU::
165 022122 010001      MOV  RO,R1      ; GET UNIT TO BE ADDED (RO)
166 022124 006301      ASL  R1      ; MAKE IT A WORD INDEX
167 022126 052761 100000 003174'  BIS  #100000,ERTABL(R1)  ; SET THE "ACTIVE" BIT
168 022134 042761 040000 003174'  BIC  #40000,ERTABL(R1)  ; CLEAR THE "DROPPED" BIT
169 022142      PRINTF #1$,RO
      022142 010046      MOV  RO,-(SP)
      022144 012746 022170'  MOV  #1$,-(SP)
      022150 012746 000002      MOV  #2,-(SP)
      022154 010600      MOV  SP,RO
      022156 104417      TRAP C$PNTF
      022160 062706 000006      ADD  #6,SP
170 022164      EXIT  AU
      022164 000167      .WORD J$JMP
      022166 000026      .WORD L10031-2
171 022170      045      116      045 1$: .ASCIZ /NNNA UNIT %D%A ADDED/
172      .EVEN
173
174 022216      ENDAU      ; UNUSED.
      022216      L10031:
      022216 104452      TRAP C$AU
175
176      ;**
177      ; THE DROP-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
178      ; TO BE REMOVED FROM THE TEST LIST.
179      ;
180      ; SUPVSR DOES THE "DROPPING". THIS IS JUST TO TELL THE MAN,
181      ; "DROPPED" UNITS ARE RE-SELECTED ON OPERATOR "STA" OR "ADD"
182      ; COMMAND, OTHERWISE REMAIN INACTIVE. THE "DISPLAY" COMMAND
183      ; WILL PRINT ALL DROPPED UNITS, AND THE P-TABLES OF THOSE
184      ; WHICH ARE STILL ACTIVE.
185      ; UPON ENTRY, RO CONTAINS THE UNIT TO BE DROPPED.
186 022220      BGNDU
      022220      L$DU::
187 022220 012737 177777 003110'  MOV  #-1,DUFLG
188 022226 010001      MOV  RO,R1
189 022230 006301      ASL  R1
190 022232 052761 140000 003174'  BIS  #140000,ERTABL(R1)  ; SAY DROPPED

```

L7

TSV4 - MISCELLANEOUS SECTIONS MACRO M1113 07-FEB-84 10:58
ADD AND DROP UNITS SECTIONS

SEQ 089

```

191 022240 000240 000240 000240      240,240,240      ; ??????????
192 022246      PRINTF 01$,RO
      022246 010046      MOV RO,-(SP)
      022250 012746 022274'      MOV 01$,-(SP)
      022254 012746 000002      MOV 02$,-(SP)
      022260 010600      MOV SP,RO
      022262 104417      TRAP C$PNTF
      022264 062706 000006      ADD 06$,SP
193 022270      EXIT DU
      022270 000167      .WORD J$JMP
      022272 000030      .WORD L10032-2-
194 022274      045      116      045 1$: .ASCIZ /NNA UNIT DWA DROPPED/
195      .EVEN
196 022324      ENDDU
      022324      L10032: TRAP C$DU
      022324 104453      ;++
197      ; AUTO-DROP CODE SECTION.
198      ;--
199      BGNAUTO
200 022326      L$AUTO::
      022326      MOV CSRADDR,R5      ;POINT TO DEVICE REGISTER
201 022326 013705 002204'      MOV 0360$,R3      ;ENOUGH TIME FOR 2400' REEL TO REWIND
202 022332 012703 000550      10$: JSR PC,WAITF      ;WAIT FOR SSR TO SET
203 022336 004737 016060'      BCS 20$      ;LEAVE WHEN SSR IS SET
204 022342 103420      DELAY 250.      ;WAIT FOR .25 SECONDS
205 022344      MOV 0250$, (PC)+
      022344 012727 000372      .WORD 0
      022350 000000      MOV L$DLY, (PC)+
      022352 013727 002116'      .WORD 0
      022356 000000      DEC -6(PC)
      022360 005367 177772      BNE -4
      022364 001375      DEC -22(PC)
      022366 005367 177756      BNE -20
      022372 001367      DEC R3      ;BUMP COUNTER DOWN
206 022374 005303      BNE 10$      ;KEEP GOING
207 022376 001357      JSR PC,CKDROP      ;TRY AND DROP UNIT
208 022400 004737 017014'
209 022404      20$: ENDAUTO      ; UNUSED.
210 022404      L10033: TRAP C$AUTO
      022404      .SBTTL CLEAN-UP AND REPORT CODING SECTIONS
      022404 104461      ;++
211      ; THE CLEANUP CODING SECTION CONTAINS THE CODING THAT IS
212      ; EXECUTED AT THE END OF EACH PASS (OR SUB-PASS).
213      ; USE TO RETURN DEVICE UNDER TEST TO A NEUTRAL STATE.
214      ;--
215      BGNCLN
216      L$CLEAN::
217      022406      MOV CSRADDR,R5      ;POINT TO DEVICE REGISTER
218      022406      TST DUFLG      ;"DROPPED" FLAG IS SET ON...
219      022406 013705 002204'      BMI 1$      ;...AND GROSS CONTROLLER FAULT...
220 022412 005737 003110'      ;...DON'T TRY TO XCT CLEANUP CODE.
221 022416 100405
222      MOV 00$,TSSR(R5)      ;DO SOFT INIT
223
224
225 022420 012765 000000 000002

```


M7

TSV4 - MISCELLANEOUS SECTIONS MACRO M1113 07-FEB-84 10:58
CLEAN-UP AND REPORT CODING SECTIONS

SEQ 090

226	022426	004737	016060'		JSR	PC, WAITF	
227	022432			1\$:			
228	022432			2\$:	ENDCLN		
	022432			L10034:			
	022432	104412			TRAP	C\$CLEAN	
229				1\$:			
230							
231							
232							
233	022434				BGNRPT		
	022434			L\$RPT::			
234	022434				PRINTS	\$DEVSUM	
	022440	012746	022676'		MOV	\$DEVSUM, -(SP)	
	022444	010600			MOV	\$1, -(SP)	
	022446	104416			MOV	SP, R0	
	022450	062706	000004		TRAP	C\$PNTS	
235	022454	010246			ADD	\$4, SP	
236	022456	010346			MOV	R2, -(SP)	
237	022460	010446			MOV	R3, -(SP)	
238	022462	012704	003174'		MOV	R4, -(SP)	
239	022466	005003			MOV	\$ERTABL, R4	; GET START OF ERROR TABLE.
240	022470	011402			CLR	R3	; CLEAR UNIT NUMBER
241	022472	001467		1\$:	MOV	(R4), R2	; GET ERROR 1467 ENTRY & TEST IT.
242	022474	100066			BEQ	\$4	; ZERO IF UNIT NOT RUN
243	022476	032702	040000		BPL	\$4	
244	022502	001015			3IT	\$BIT14, R2	; WAS UNIT DROPPED?
245	022504	042702	170000		BNE	\$4	; BR IF YES
246	022510				BIC	\$C7777, R2	; GET ERROR COUNT FIELD
	022510	010246			PRINTS	\$DEVONL, R3, R2	; PRINT
	022512	010346			MOV	R2, -(SP)	
	022514	012746	022733'		MOV	R3, -(SP)	
	022520	012746	000003		MOV	\$DEVONL, -(SP)	
	022524	010600			MOV	\$3, -(SP)	
	022526	104416			MOV	SP, R0	
	022530	062706	000010		TRAP	C\$PNTS	
247	022534	000446			ADD	\$10, SP	
248	022536	020227	160000	2\$:	BR	\$4	
249	022542	001012			CMP	R2, \$160000	; WAS UNIT NON-EXISTENT?
250	022544				BNE	\$4	; BR IF NO
	022544	010346			PRINTS	\$DEVNXR, R3	
	022546	012746	023003'		MOV	R3, -(SP)	
	022552	012746	000002		MOV	\$DEVNXR, -(SP)	
	022556	010600			MOV	\$2, -(SP)	
	022560	104416			MOV	SP, R0	
	022562	062706	000006		TRAP	C\$PNTS	
251	022566	000431			ADD	\$6, SP	
252	022570	020227	160001	3\$:	BR	\$4	
253	022574	001012			CMP	R2, \$160001	; WAS UNIT NOT READY AT STARTUP?
254	022576				BNE	\$3	; BR IF NO.
	022576	010346			PRINTS	\$DEVNRD, R3	
	022600	012746	023065'		MOV	R3, -(SP)	
	022604	012746	000002		MOV	\$DEVNRD, -(SP)	
	022610	010600			MOV	\$2, -(SP)	
	022612	104416			MOV	SP, R0	
	022614	062706	000006		TRAP	C\$PNTS	
255	022620	000414			ADD	\$6, SP	
					BR	\$4	

N7

TSV4 - MISCELLANEOUS SECTIONS MACRO M1113 07-FEB-84 10:58
CLEAN-UP AND REPORT CODING SECTIONS

SEQ 091

```

256 022622 042702 170000      30$: BIC      #C7777,R2
257 022626      010246      PRINTS  #DEVDR0,R3,R2
      022626      010346      MOV      R2,-(SP)
      022630      012746      MOV      R3,-(SP)
      022632      012746 023146'  MOV      #DEVDR0,-(SP)
      022636      012746 000003      MOV      #3,-(SP)
      022642      010600      MOV      SP,R0
      022644      104416      TRAP     C$PNTS
      022646      062706 000010      ADD      #10,SP
258 022652      062704 000002      4$: ADD      #2,R4
259 022656      005203      INC      R3
260 022660      020427 003374'  CMP      R4,#ERTABE
261 022664      103701      BLO      1$
262 022666      012604      MOV      (SP)+,R4
263 022670      012603      MOV      (SP)+,R3
264 022672      012602      MOV      (SP)+,R2
265 022674      ENDRPT      ; UNUSED.
      022674      L10035:
      022674      104425      TRAP     C$RPT
266
267
268 022676      045      116      045 DEVSUM: .ASCIZ  /#N#ALEVICE STATUS SUMMARY:#N/
269 022733      045      101      040 DEVONL: .ASCIZ  /#A UNIT #D3#A ONLINE, ERRORS = #D#N/
270 023003      045      101      040 DEVNLR: .ASCIZ  /#A UNIT #D3#A DROPPED, NON-EXISTENT REGISTER#N/
271 023065      045      101      040 DEVNRD: .ASCIZ  /#A UNIT #D3#A DROPPED, NOT READY AT STARTUP#N/
272 023146      045      101      040 DEVDR0: .ASCIZ  /#A UNIT #D3#A DROPPED, ERRORS = #D#N/
273      .EVEN
274
275 023216      ENDMOD
276
277
278

```

```

1          .TITLE  TSV5A - HARDWARE TESTS
2
9
10 023216      BGNMCD  TSV5
    023216      TSV5::

16
24
25          .SBITL  TEST  1: BUS RESET TEST
26
27
28          THIS TEST VERIFIES THAT THE M7455 MODULE'S DEVICE REGISTERS ARE
29          ACCESSIBLE ON THE BUS (SUBTEST 1) AND THEN CHECKS THAT THE
30          BUILT-IN INITIALIZATION SELF-TEST MICRODIAGNOSTIC DID NOT FIND
31          ANY BASIC PROBLEMS IN THE MODULE.  AREAS OF LOGIC TESTED BY THE
32          SELF-TEST SEQUENCE ARE AS FOLLOWS:  ROM AND PIPELINE REGISTER,
33          SEQUENCER, INTERNAL BUSES, 2901 MICROPROCESSOR, AND, RAM.  THIS
34          TEST INITIALIZES THE CONTROLLER BY ISSUING THE BUS INIT SIGNAL
35          VIA A RESET INSTRUCTION, OR BY WRITING INTO THE TSSR REGISTER,
36          WAITS A PERIOD OF TIME (TO ALLOW THE CONTROLLER'S INITIALIZATION
37          MICRODIAGNOSTIC SEQUENCE TO BE COMPLETED), AND THEN CHECKS THE
38          CONTENTS OF THE TSSR REGISTER.  SUCCESSFUL INITIALIZATION IS
39          INDICATED BY SUBSYSTEM READY (SSR) AND NEED BUFFER ADDRESS (NBA)
40          BITS BEING SET (1) AND ALL OTHER BITS (EXCEPT A17 AND A16 AND
41          OFL, WHICH ARE IGNORED FOR THIS TEST) BEING CLEAR (0).  IF THE
42          CONTENTS OF TSSR ARE NOT AS EXPECTED, AN ERROR REPORT IS ISSUED
43          LISTING THE EXPECTED DATA, ACTUAL DATA, AND THE DISCREPANCIES.
44          THE ERROR REPORT ANALYZES THE TSSR CONTENTS AND DISCERNs AND
45          REPORTS ONE OF THREE POSSIBILITIES:
46
47
48          1.  TSSR CONTENTS ARE AMBIGUOUS (ANY OF BITS 11-14 ARE SET,
49              OR STATES OF SSR AND SC BITS DO NOT CORRESPOND TO THE
50              APPARENT ERROR CODE IN BITS 0-5):  INDICATES THAT THE
51              TSSR CONTENT CANNOT BE TRUSTED.  INDICATES A
52              CATASTROPHIC CONTROLLER MALFUNCTION.  THIS IS A FATAL
53              ERROR (EXECUTION IS ABORTED).  FIELD ACTION WOULD BE TO
54              REPLACE THE M7455.  IF THE M7455 ITSELF IS BEING
55              DEBUGGED, THE PROGRAM SHOULD BE RESTARTED WITH LOOP ON
56              ERROR ENABLED IN ORDER TO PROBE FOR THE PROBLEM.
57
58          2.  SSR = 0, SC = 0 AND THE ERROR CODE IN BITS 0-5 IS IN
59              THE RANGE 17-13:  THIS IS A FATAL ERROR.  THE ERROR
60              CODE IS DECODED AND THE APPROPRIATE DESCRIPTION GIVEN.
61              INDICATES THAT A SERIOUS PROBLEM EXISTS.
62
63
64 023216      BGNTEST
    023216
69 023216      012700  023414'      MOV      #TST1ID,R0      ;ASCII MESSAGE TO IDENTIFY TEST
70 023222      004737  016322'      JSR      PC,TSTSETUP      ;DO INITIAL TEST SETUP
71 023226      012737  000024  002214'  MOV      #20,,LOOPCNT      ;PERFORM 20 ITERATIONS
72 023234
73 023234      005003      T1LOOP:  CLR      R3      ;USE R3 AS FATAL ERROR FLAG
74
75 023236      BGN SUB
    023236      ;//////.//////// BEGIN SUBTEST //////////.
    023236      T1.1:  TRAP      C#BSUB
    104402

```

C8

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 1: BUS RESET TEST

SEQ 093

```

76
77 023240          BRESET          ;ISSUE A BUS RESET
    023240 104433          TRAP      C#RESET
78 023242 004737 016060'    JSR      PC, WAITF    ;WAIT FOR READY
79 023246 016501 000002    MOV      TSSR(R5), R1    ;GET THE CONTENTS OF TSSR
80 023252 010102          MOV      R1, R2          ;CONTENTS OF TSSR
81 023254 042702 176277    BIC      #C<HIADDR!OFL>, R2 ;THESE BITS MAY BE SET
82 023260 052702 002200    BIS      #SSR!NBA, R2    ;READY AND NEW DATA SHOULD BE SET
83 023264 020102          CMP      R1, R2          ;COMPARE EXPECTED TO RECEIVED
84 023266 001405          BEQ      10#             ;BRANCH IF COMPARE
88 023270          ERDIF      ERRNO, SFHERR, SFFMSG ;REPORT A FATAL ERROR
    023270 104455          TRAP      C#ERDIF
    023272 000145          .WORD    101
    023274 003677          .WORD    SFHERR
    023276 011712'        .WORD    SFFMSG
89 023300 005203          INC      R3              ;SET THE FATAL ERROR FLAG
90 023302          10#:          ENDSUB            ;///////////////// END SUBTEST ///////////////////
91 023302          L10037:      TRAP      C#ESUB
    023302 104403
92
93 023304 005703          TST      R3              ;DID WE HAVE FATAL ERROR ?
94 023306 001402          BEQ      20#             ;BRANCH IF NOT
95 023310 004737 017014'    JSR      PC, CKDROP    ;GO DROP THIS UNIT, IF ALLOWED
96 023314 005003          CLR      R3              ;RESET FATAL ERROR FLAG
97
98
99 023316          BGNSUB          ;///////////////// BEGIN SUBTEST ///////////////////
    023316          T1.2:      TRAP      C#BSUB
    023316 104402
100
101 023320 005065 000002    CLR      TSSR(R5)      ;WRITE TO ISSUE A SOFT RESET
102 023324 004737 016060'    JSR      PC, WAITF    ;WAIT FOR READY TO SET
103 023330 016501 000002    MOV      TSSR(R5), R1    ;GET REGISTER TSSR DATA
104 023334 010102          MOV      R1, R2          ;CONTENTS OF TSSR
105 023336 042702 176277    BIC      #C<HIADDR!OFL>, R2 ;THESE BITS MAY BE SET
106 023342 052702 002200    BIS      #SSR!NBA, R2    ;READY AND NEW DATA SHOULD BE SET
107 023346 020102          CMP      R1, R2          ;COMPARE EXPECTED TO RECEIVED
108 023350 001405          BEQ      10#             ;BRANCH IF COMPARE
112 023352          ERDIF      ERRNO, SFIERR, SFFMSG ;REPORT A FATAL ERROR
    023352 104455          TRAP      C#ERDIF
    023354 000146          .WORD    102
    023356 003644'        .WORD    SFIERR
    023360 011712'        .WORD    SFFMSG
113 023362 005203          INC      R3              ;SET THE ERROR FLAG
114 023364          10#:          ENDSUB            ;///////////////// END SUBTEST ///////////////////
115 023364          L10040:      TRAP      C#ESUB
    023364 104403
116
117
118 023366 005703          TST      R3              ;FATAL ERROR DETECTED ?
119 023370 001402          BEQ      20#             ;BRANCH IF NOT
120 023372 004737 017014'    JSR      PC, CKDROP    ;SEE IF TIME TO DROP UNIT
121 023376 004737 016270'    JSR      PC, TSTLOOP   ;SHOULD WE DO ITERATIONS ?
122 023402 103002          BCC      40#             ;BRANCH IF NOT
123 023404 000137 023234'    JMP      T1LOOP        ;LOOP UNTIL COUNT EXPIRED

```

[illegible]

281							LISTS LIKELY FAULTY CANDIDATES FROM THE LOGIC ELEMENTS LISTED
282							ABOVE. THE TEST IS REPEATED FOR ALL COMBINATIONS OF TEST DATA
283							BYTES (0-377 OCTAL).
284							
285	024134				BGNTST		
	024134					T3::	
290	024134	012700	024577'		MOV	#TST3ID,R0	;ASCII MESSAGE TO IDENTIFY TEST
291	024140	004737	016322'		JSR	PC,TSTSETUP	;DO INITIAL TEST SETUP
292	024144	012777	000024' 002214'		MOV	#20,,LOOPCNT	;PERFORM 20 ITERATIONS
293	024152	005004		T3LOOP:	CLR	R4	;STARTING DATA PATTERN
294	024154	012703	000001		MOV	#1,R3	;FORCE SOFT INIT ON FIRST PASS
295	024160	005703		5\$:	TST	R3	;SET IF INIT IS REQUIRED
296	024162	001412			BEQ	10\$	;BRANCH IF NO INIT NEEDED
297	024164	005003			CLR	R3	;ASSUME NO INIT NEEDED ON NEXT LOOP
298	024166	004737	015604'		JSR	PC,SOFINIT	;DO SOFT INIT OF CONTROLLER
299	024172	103406			BCS	10\$	;BR IF SOFT INIT = OK
303	024174	010001			MOV	R0,R1	;SAVE CONTENTS OF TSSR
304	024176				ERRDF	ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT
	024176	101455					TRAP C\$ERDF
	024200	000455					.WORD 301
	024202	003644'					.WORD SFIERR
	024204	011644'					.WORD SFIMSG
305	024206	005203			INC	R3	;FORCE INIT ON NEXT PASS
306	024210	005037	002220'	10\$:	CLR	FATFLG	;CLEAR FATAL ERROR FLAG
307							
308	024214				BGNSEG		;>>>>>>>>> BEGIN SEGMENT >>>>>>>>>
	024214	104404					TRAP C\$BSEG
309							
310	024216	110465	000000		MOVB	R4,TSDB(R5)	;SET MAINT MODE + WRITE DATA
311	024222	004737	016060'		JSR	PC,WAITF	;WAIT FOR SSR TO SET
312	024226	103411			BCS	15\$	;BR IF CARRY SET (GOOD RETURN)
313	024230	010001			MOV	R0,R1	;SAVE CONTENTS OF TSSR
314	024232	010402			MOV	R4,R2	;DATA THAT WAS WRITTEN
318	024234				ERRDF	ERRNO,T3SSR,EXPREC	;DEVICE FATAL SSR FAILED TO SET
	024234	104455					TRAP C\$ERDF
	024236	000456					.WORD 302
	024240	024526'					.WORD T3SSR
	024242	015304'					.WORD EXPREC
319	024244	005203			INC	R3	;FORCE INIT ON NEXT PASS
320	024246	005237	002220'		INC	FATFLG	;SET THE FATAL ERROR FLAG
321	024252			15\$:	CKLOOP		;LOOP ON ERROR, IF FLAG SET
	024252	104406					TRAP C\$CLP1
322	024254	005737	002220'		TST	FATFLG	;DID WE GET FATAL ERROR ?
323	024260	001402			BEQ	20\$	;BRANCH IF NOT
324	024262	004737	017014'		JSR	PC,CKDROP	;DROP UNIT, IF DROP ALLOWED
325	024266	010402		20\$:	MOV	R4,R2	;DATA PATTERN WRITTEN
326	024270	042702	177774		BIC	#C<BIT0!BIT1>,R2	;CLEAR ALL BUT LOW 2 BITS
327	024274	000302			SWAB	R2	;BITS 8 AND 9 HAVE LOW DATA BITS
328	024276	052702	002200		BIS	#SSR!NBA,R2	;THESE BITS MUST BE SET ALSO
329	024302	016501	000002		MOV	TSSR(R5),R1	;GET THE CONTENTS OF TSSR
330	024306	032701	000100		BIT	#OFL,R1	;IS DRIVE OFF-LINE
331	024312	001402			BEQ	25\$	;BRANCH IF NOT OFFLINE
332	024314	052702	000100		BIS	#OFL,R2	;SET OFF-LINE IN EXPECTED
333	024320	020201		25\$:	CMP	R2,R1	;DOES EXPECTED MATCH RECEIVED ?
334	024322	001405			BEQ	30\$	;OKAY IF MATCH
338	024324				ERRHRD	ERRNO,T3TSSR,EXPREC	;TSSR WASN'T CORRECT
	024324	104456					TRAP C\$ERHRD

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 4: RAM TEST

SEQ 099

385
386
387
388
389
390
391
392
393
394
395
396
397
398
399
400
401
402
403
404
405
406
407
408
409
410
411
412
413
414
415
416
417
418
419
420

421 024626
024626

422
423 024626
024626

024626 104402

424

429 024630 012700 026136'

430 024634 004737 016322'

431 024640 012737 000005' 002214'

432 024646

433 024646 004737 015604'

434 024652 103405

438 024654 010701

439 024656

024656 104455

024660 000621

024662 003644'

024664 011644'

440 024666 005004

441 024670 004737 016146'

TEST 4 , SUBTEST 1: -

THIS SUBTEST VERIFIES EACH RAM LOCATION BY FIRST PLACING THE M7455 INTO MAINTENANCE MODE BY WRITING INTO THE LOW BYTE OF TSDB AND THEN PERFORMING THE FOLLOWING SEQUENCE FOR EACH ADDRESS 0-7777 (OCTAL):

1. THE ADDRESS TO BE TESTED IS LOADED INTO THE TSDB (VIA A WORD WRITE).
2. THE ADDRESSED RAM LOCATION IS WRITTEN, THEN READ INTO THE LOW BYTE OF TSBA, BY WRITING A DATA BYTE INTO THE LOW BYTE OF TSDB.
3. THE LOW BYTE OF TSBA IS CHECKED TO SEE IF IT CONTAINS THE DATA PATTERN ORIGINALLY WRITTEN; A DISCREPANCY IS REPORTED AS AN ERROR.
4. THE ADDRESS OF THE LOCATION BEING TESTED IS AGAIN WRITTEN INTO TSDB (WORD WRITE), TO CAUSE THE LOCATION UNDER TEST TO AGAIN BE READ INTO THE LOW BYTE OF TSBA. THE LOW BYTE OF TSBA IS AGAIN CHECKED AND DISCREPANCIES REPORTED.
5. THE HIGH BYTE OF TSBA IS CHECKED; IT SHOULD CONTAIN THE SUM OF THE HIGH AND LOW BYTES LAST WRITTEN INTO TSDB AS A WORD. A DISCREPANCY IS REPORTED AS A 2901 PROBLEM.
6. THE CONTENT OF TSSR IS CHECKED; SETTING OF THE SC BIT IS IGNORED. OTHER DISCREPANCIES IN TSSR ARE REPORTED.

BGNTST

T4::

BGNSUB

////////// BEGIN SUBTEST //////////

T4.1:

TRAP C#BSUB

MOV #TST4ID,R0
JSR PC,TSTSETUP
MOV #5,LOOPCNT

;ASCII MESSAGE TO IDENTIFY TEST
;DO INITIAL TEST SETUP
;PERFORM 5 ITERATIONS

T4LOOP:

JSF PC,SOFINIT
BCS 20\$
MOV R0,R1
ERRDF EPRNO,SFIERR,SFIMSG

;DO INITIALIZE ON CONTROLLER
;BR IF INIT WAS OK
;CONTENTS OF TSSR REGISTER
;FATAL ERROR TSSR WAS NOT OK

TRAP C#ERDF
.WORD 401
.WORD SFIERR
.WORD SFIMSG

20\$:

CLR R4
JSR PC,CHKTSSR

;SET RAM ADDRESS AT ZERO
;WAIT FOR READY, NON-AMBIGUOUS

Line	Address	Offset	Label	Operation	Comments
442	024674	105035	000000	CLR B	TSDB(R5)
443	024700		25\$:		;SET INTO MAINTENANCE MODE
444	024700			BGNSEG	
	024700	104404			;BEGIN SEGMENT
445					TRAP C\$BSEG
446	024702	110402		MOVB R4,R2	;EXPECTED DATA FROM WRAP-AROUND
447	024704	004737	016146'	JSR PC,CHK1SSR	;WAIT FOR READY, NON-AMBIGUOUS
448	024710	010465	000000	MOV R4,TSDB(R5)	;LOAD ADDRESS INTO TSDB
449	024714	004737	016146'	JSR PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS
450	024720	110265	000000	MOVB R2,TSDB(R5)	;LOADS DATA INTO RAM LOCATION
451	024724	004737	016146'	JSR PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS
452	024730	016501	000000	MOV TSBA(R5),R1	;READS WRAP DATA
453	024734	120102		CMPB R1,R2	;DOES WRITTEN(WRAP) = READ
454	024736	001404		BEQ 30\$	;BR IF OK, THEY ARE EQUAL
458	024740			ERRHRD	ERRNO,TSBAM2,EXPREC
	024740	104456			
	024742	000622			TRAP C\$ERHRD
	024744	025774'			.WORD 402
	024746	015304'			.WORD TSBAM2
					.WORD EXPREC
459	024750		30\$:		
460	024750			ENDSEG	
	024750				;END SEGMENT
	024750	104405			10000\$:
					TRAP C\$ESEG
461	024752	005204		INC R4	;NEXT ADDRESS
463	024754	020427	010000	CMR R4,#10000	;END OF RAM MEMORY CHECK
464	024760	001347		BNE 25\$	;LOOP TILL ALL RAM WRITTEN
465	024762			ESCAPE SUB	;DON'T CONTINUE IF ERROR ON WRITE
	024762	104410			TRAP C\$ESCAPE
	024764	000102			.WORD L10044-
466	024766	005002		CLR R2	;CLEAR OUT R2 HIGH BITS
468	024770	005304		DEC R4	;SET BACK TO 7777
469	024772	110402		MOVB R4,R2	;GET DATA PATTERN BACK IN SHAPE
470	024774	004737	016146'	JSR PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS
471	025000	010465	000000	MOV R4,TSDB(R5)	;LOAD UP RAM ADDRESS POINTER
472	025004	004737	016146'	JSR PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS
473	025010	016501	000000	MOV TSBA(R5),R1	;READ RAM CONTENTS BACK
474	025014	120102		CMPB R1,R2	;CHECK WITH DATA WRITTEN
475	025016	001404		BEQ 45\$	;BR IF OK, DATA IN = DATA OUT
479	025020			ERRHRD	ERRNO,TSBAM2,EXPREC
	025020	104456			
	025022	000623			TRAP C\$ERHRD
	025024	025774'			.WORD 403
	025026	015304'			.WORD TSBAM2
					.WORD EXPREC
430	025030		45\$:	CKLOOP	
	025030	104406			;SCOPE LOOP
					TRAP C\$CLP1
481	025032	116501	000001	MOVB TSBAH(R5),R1	;HIGH BYTE READ OF TSBA
482	025036	010402		MOV R4,R2	;DATA PATTERN WRITTEN
483	025040	000302		SWAB R2	;HIGH TO LOW
484	025042	060402		ADD R4,R2	;TOTAL OF BYTES IN LOW BYTE
485	025044	120102		CMPB R1,R2	;SUM OF BYTES WRITTEN TO TSDB = TSBAH
486	025046	001404		BEQ 50\$	;BR IF OK, THEY SHOULD BE
490	025050			ERRHRD	ERRNO,M2901,EXPREC
	025050	104456			
	025052	000624			TRAP C\$ERHRD
	025054	025704'			.WORD 404
					.WORD M2901

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 4: RAM TEST

SEQ 102

```

537
538 025206 005304          35$: DEC    R4          ;SET BACK TO 7777
539 025210 005002          CLR    R2          ;SET TO ALL ZEROS
540 025212 004737 016146'  40$: JSR    PC,CHKTSSR      ;WAIT FOR READY (SSR) TO SET
541 025216 010465 000000    MOV    R4,TSDB(R5)    ;LOAD UP THE ADDRESS FOR RAM
542 025222 004737 016146'  JSR    PC,CHKTSSR      ;WAIT FOR READY (SSR) TO SET
543 025226 016501 000000    MOV    TSBA(R5),R1    ;READ THE RAM CONTENTS BACK
544 025232 005002          CLR    R2          ;LOOKING FOR 000000 (EXPECTED)
545 025234 120102          CMPB   R1,R2          ;BOTH SHOULD BE 00000000 BINARY
546 025236 001404          BEQ    43$          ;BR, IF DATA IS GOOD
550 025240          ERRHRD   ERRNO,TSBAM3,EXPREC ;CHARACTERISTICS DATA NOT CORRECT
      025240 104456          TRAP    C$ERHRD
      025242 000627          .WORD   407
      025244 026056'        .WORD   TSBAM3
      025246 015304'        .WORD   EXPREC
551 025250 012702 000377  43$: MOV    #000377,R2    ;SET ALL ONES WORD
552 025254 010465 000000    MOV    R4,TSDB(R5)    ;LOAD UP RAM ADDRESS POINTER
553 025260 004737 016146'  JSR    PC,CHKTSSR      ;WAIT FOR READY, NON-AMBIGUOUS
554 025264 110265 000000    MOVB   R2,TSDB(R5)    ;WRITE DATA INTO RAM
555 025270 004737 016146'  JSR    PC,CHKTSSR      ;WAIT FOR READY, NON-AMBIGUOUS
556 025274 016501 000000    MOV    TSBA(R5),R1    ;READ RAM CONTENTS BACK
557 025300 120102          CMPB   R1,R2          ;CHECK WITH DATA WRITTEN
558 025302 001404          BEQ    45$          ;BR IF OK, DATA IN = DATA OUT
562 025304          ERRHRD   ERRNO,TSBAM2,EXPREC ;WRITTEN DATA NOT = TO READ
      025304 104456          TRAP    C$ERHRD
      025306 000630          .WORD   408
      025310 025774'        .WORD   TSBAM2
      025312 015304'        .WORD   EXPREC
563 025314          45$:  CKLOOP          ;SCOPE LOOP
      025314 104406          .RAPH   C$CLP1
564 025316 004737 016146'  JSR    PC,CHKTSSR      ;WAIT FOR READY, NON-AMBIGUOUS
565 025322 010465 000000    MOV    R4,TSDB(R5)    ;WORD WRITE TO SET UP ADDRESS
566 025326 004737 016146'  JSR    PC,CHKTSSR      ;WAIT FOR READY, NON-AMBIGUOUS
567 025332 116501 000001    MOVB   TSBAH(R5),R1    ;HIGH BYTE READ OF TSBA
568 025336 010403          MOV    R4,R3          ;DATA PATTERN WRITTEN
569 025340 000303          SWAB   R3          ;HIGH TO LOW
570 025342 060403          ADD    R4,R3          ;TOTAL OF BYTES IN LOW BYTE
571 025344 120103          CMPB   R1,R3          ;SUM OF BYTES WRITTEN TO TSDB = TSBAH
572 025346 001404          BEQ    50$          ;BR IF OK, THEY SHOULD BE
576 025350          ERRHRD   ERRNO,M2901,EXPREC ;12901 PROBLEM ADDER
      025350 104456          TRAP    C$ERHRD
      025352 000631          .WORD   409
      025354 025704'        .WORD   M2901
      025356 015304'        .WORD   EXPREC
577 025360          50$:  CKLOOP          ;SCOPE LOOP
      025360 104405          .RAPH   C$CLP1
578 025362 005304          DEC    R4          ;DROP RAM ADDRESS POINTER
579 025364 002312          BGE    40$          ;NOT AT LOC. ZERO YET
580
581 025366          ENDSUB          ;///////////////// END SUBTEST ///////////////////
      025366          L10045:
      025366 104403          TRAP    C$ESUB
582
583
584 025370          BGNSUB          ;///////////////// BEGIN SUBTEST ///////////////////
      025370          T4,3:
      025370 104402          TRAP    C$BSUB

```


	025550	104456							TRAP	C\$ERHRD
	025552	000634							.WORD	412
	025554	026056'							.WORD	TSBAM3
	025556	015304'							.WORD	EXPREC
638	025560	005002		43\$:	CLR	R2		;SET UP NEW EXPECTED		
639	025562	010465	000000		MOV	R4,TSDB(R5)		;LOAD UP RAM ADDRESS POINTER		
640	025566	004737	016146'		JSR	PC,CHKTSSR		;WAIT FOR READY, NON-AMBIGUOUS		
641	025572	110265	000000		MOVB	R2,TSDB(R5)		;WRITE DATA INTO RAM		
642	025576	004737	016146'		JSR	PC,CHKTSSR		;WAIT FOR READY, NON-AMBIGUOUS		
643	025602	016501	000000		MOV	TSBA(R5),R1		;READ RAM CONTENTS BACK		
644	025606	120102			CMPB	R1,R2		;CHECK WITH DATA WRITTEN		
645	025610	001404			BEQ	45\$		;BR IF OK, DATA IN = DATA OUT		
649	025612				ERRHRD	ERRNO,TSBAM2,EXPREC		;WRITTEN DATA NOT = TO READ		
	025612	104456						TRAP	C\$ERHRD	
	025614	000635						.WORD	413	
	025616	025704'						.WORD	TSBAM2	
	025620	015304'						.WORD	EXPREC	
650	025622			45\$:	CKLOOP			;SCOPE LOOP		
	025622	104406						TRAP	C\$CLP1	
651	025624	004737	016146'		JSR	PC,CHKTSSR		;WAIT FOR READY, NON-AMBIGUOUS		
652	025630	116501	000001		MOVB	TSBAH(R5),R1		;HIGH BYTE READ OF TSBA		
653	025634	010203			MOV	R2,R3		;DATA PATTERN WRITTEN		
654	025636	000303			SWAB	R3		;HIGH TO LOW		
655	025640	060203			ADD	R2,R3		;TOTAL OF BYTES IN LOW BYTE		
656	025642	120103			CMPB	R1,R3		;SUM OF BYTES WRITTEN TO TSDB = TSBAH		
657	025644	001404			BEQ	50\$		;BR IF OK, THEY SHOULD BE		
661	025646				ERRHRD	ERRNO,M2901,EXPREC		;2901 PROBLEM ADDER		
	025646	104456						TRAP	C\$ERHRD	
	025650	000636						.WORD	414	
	025652	025704'						.WORD	M2901	
	025654	015304'						.WORD	EXPREC	
662	025656			50\$:	CKLOOP			;SCOPE LOOP		
	025656	104406						TRAP	C\$CLP1	
663	025660	005304			DEC	R4		;DROP RAM ADDRESS POINTER		
664	025662	001315			BNE	40\$		;NOT AT LOC. ZERO YET		
665										
666	025664				ENDSUB			;\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\ END SUBTEST \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		
	025664							L10046:		
	025664	104403						TRAP	C\$ESUB	
667										
668	025666	004737	016270'		JSR	PC,TSTLOOP		;DO WE NEED TO ITERATE TEST ?		
669	025672	103002			BCC	63\$		;BRANCH IF NOT		
670	025674	000137	024646'		JMP	T4LOOP		;EXECUTE AGAIN		
671	025700			63\$:	EXIT	TST		;ALL DONE THIS TEST		
	025700	104432						TRAP	C\$EXIT	
	025702	000256						.WORD	L10043-	
672				::						
673				::LOCAL TEXT MESSAGES FOR TEST						
674				::						
675				::						
676	025704	040	124	123	M2901:	.ASCIZ	' TSBA High Byte Not Sum of Last TSDB Write (2901 Error)'			
677	025774	040	127	162	TSBAM2:	.ASCIZ	' Write to TSDB Not Equal to Read of TSBA Low Byte'			
678	026056	127	162	151	TSBAM3:	.ASCIZ	'Write To RAM Location Modified Another Location'			
679	026136	122	101	115	TST4ID:	.ASCIZ				

L10043:

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 4: RAM TEST

SEQ 105

026160 104401

TRAP C1ETST

682
683
684
685
686
687
688
689
690
691
692
693
694
695
696
697
698
699
700
701
702
703
704
705
706
707
708
709
710
711
712
713
714
715
716
717
718
719

.SBTTL TEST 5: SECOND INITIALIZATION TEST

THIS TEST VERIFIES THE SAME ELEMENTS AS DID INITIALIZATION TEST #1 AND ALSO CHECKS THAT CERTAIN PARTS OF RAM IS CLEARED TO ZERO AND THAT 2901 REGISTERS 10 AND 11 ARE ALSO CLEARED TO ZERO. THIS IS A CONFIDENCE CHECK OF A PART OF THE SELF-TEST SEQUENCE (I.E., THAT IT IS REALLY BEING EXECUTED). FOR EACH OF TWO SUBTESTS (ONE FOR INITIALIZING VIA A BUS INIT, THE OTHER FOR INITIALIZING BY WRITING INTO THE TSSR), THE FOLLOWING SEQUENCE IS PERFORMED:

1. EACH RAM LOCATION AND 2901 REGISTERS 10 AND 11 ARE SET TO ALL 1'S BY USING WRITES INTO THE TSDB REGISTER (LOW BYTE AND MAINTENANCE MODE WORD WRITES).
2. THE CONTROLLER IS INITIALIZED AND THE VARIOUS CHECKS ON THE TSSR DESCRIBED IN INITIALIZATION TEST #1 ARE PERFORMED.
3. #1'S (377 OCTAL) ARE WRITTEN INTO THE LOW BYTE OF TSDB, WHICH SHOULD CAUSE RAM LOCATION 0 TO BE WRITTEN TO ALL 1'S SINCE 2901 REGISTERS 10 AND 11, SPECIFYING THE RAM ADDRESS, SHOULD BE 0. RAM LOCATION 0 IS VERIFIED BY WRITING A WORD OF ZEROS INTO THE TSDB. THE RESULTING LOW BYTE OF TSBA SHOULD CONTAIN ALL 1'S.
4. THE ENTIRE RAM IS SCANNED. LOCATION 0 SHOULD CONTAIN ALL 1'S AND THE REMAINING LOCATIONS, EXCEPT FOR THE MESSAGE BUFFER IMAGE AREA, SHOULD CONTAIN 0. DISCREPANCIES ARE REPORTED. AN ERROR AT THIS POINT IS MOST LIKELY DUE TO A ROM, PIPELINE OR SEQUENCER PROBLEM OR A TIMING PROBLEM.

720 026162

BGNTST

026162

725 026162 012700 027134'

MOV #TSTSID,RO

TS::

726 026166 004737 016322'

JSR PC,TSTSETUP

;ASCII MESSAGE TO IDENTIFY TEST

727 026172 012757 000024 002214'

MOV #20,,LOOPCNT

;DO INITIAL TEST SETUP

728 026200

CLR FATFLG

;PERFORM 20 ITERATIONS

729 026200 005037 002220'

TSLOOP:

;CLEAR THE FATAL ERROR FLAG

730

BCNSUB

;////////// BEGIN SUBTEST //////////

731 026204

TS.1:

026204

026204 104402

TRAP C1BSUB

732

733 026206 004737 015604'

JSR PC,SOFINIT

;DO A SOFT TO START

734 026212 103404

BCS 101

;BRANCH IF O.K.

738 026214

ERRDF ERRNO,SFIERR,SFIMSG

;REPORT ERROR AND DROP DRIVE

026214 104455

TRAP C1ERDF

026216 000765

,WORD

501

026220 003644'

,WORD

SFIERR

Address	Hex	Dec	Label	Mnemonic	Operands	Description	Trap	Word
739	026222	011644	10:	MOV	#-1,R2	ALL ONE DATA PATTERN		SFIMSG
740	026230	005004		CLR	R4	STARTING RAM ADDRESS		
741	026232	004737		JSR	PC,CHKTSSR	WAIT FOR READY, NON-AMBIGUOUS		
742	026236	105065	15:	CLRB	TSD8(R5)	SET MAINTENANCE MODE		
743	026242	004737		JSR	PC,CHKTSSR	WAIT FOR READY, NON-AMBIGUOUS		
744	026246	010465		MOV	R4,TSD8(R5)	SET THE NEXT RAM ADDRESS		
745	026252	004737		JSR	PC,CHKTSSR	WAIT FOR READY, NON-AMBIGUOUS		
746	026256	110265		MOVB	R2,TSD8(R5)	LOAD TEST DATA		
747	026262	005204		INC	R4	NEXT ADDRESS TO TEST		
748	026264	020427		CMP	R4,#7777	COMPARE TO LAST ADDRESS		
749	026270	003762		BLE	15	BRANCH TILL ALL DATA WRITTEN		
750	026272			BRESET		ISSUE A BUS RESET		
751	026274	004737		JSR	PC,CHKTSSR	WAIT FOR READY, NON-AMBIGUOUS	TRAP	C#RESET
752	026300	016501		MOV	TSSR(R5),R1	GET THE CONTENTS OF TSSR		
753	026304	010102		MOV	R1,R2	CONTENTS OF TSSR		
754	026306	042702		BIC	#YC<HIADDR OFL>,R2	THESE BITS MAY BE SET		
755	026312	052702		BIS	#SSR!NBA,R2	READY AND NEW DATA SHOULD BE SET		
756	026316	020102		CMP	R1,R2	COMPARE EXPECTED TO RECEIVED		
757	026320	001406		BEQ	20	BRANCH IF COMPARE		
761	026322			ERRDF	ERRNO,SFHERR,SFFMSG	REPORT A FATAL ERROR		
	026322	104455					TRAP	C#ERDF
	026324	000766					.WORD	502
	026326	003677					.WORD	SFHERR
	026330	011712					.WORD	SFFMSG
762	026332	005237	20:	INC	FATFLG	SET FATAL ERROR FLAG		
763	026336			CKLOOP		LOOP ON ERROR IF FLAG SET		
764	026340	104406		ESCAPE	SUB	EXIT IF FATAL ERROR DETECTED	TRAP	C#CLP1
	026340	104410					TRAP	C#ESCAPE
	026342	000170					.WORD	L10050-
765	026344	004737		JSR	PC,CHKTSSR	WAIT FOR SSR TO SET		
766	026350	105065		CLRB	TSD8(R5)	PUT BACK INTO MAINTENANCE MODE		
767	026354	004737		JSR	PC,CHKTSSR	WAIT FOR READY, NON-AMBIGUOUS		
768	026360	005065		CLR	TSD8(R5)	SET ADDRESS BACK TO 0000		
769	026364	012702		MOV	#377,R2			
770	026370	004737		JSR	PC,CHKTSSR	WAIT FOR READY, NON-AMBIGUOUS		
771	026374	110265		MOVB	R2,TSD8(R5)	SHOULD POINT TO RAM 0		
772	026400	004737		JSR	PC,CHKTSSR	WAIT FOR READY, NON-AMBIGUOUS		
773	026404	005065		CLR	TSD8(R5)	SELECT LOCATION 0		
774	026410	004737		JSR	PC,CHKTSSR	WAIT FOR READY, NON-AMBIGUOUS		
775	026414	116501		MOVB	TSDA(R5),R1	READ RAM LOCATION SPECIFIED		
776	026420	120102		CMPB	R1,R2	LOCATION SHOULD BE 377 OCTAL		
777	026422	001406		BEQ	25	BR IF OK		
778	026424			ERRDF	ERRNO,TSADDR,EXPREC	WASN'T POINTING TO CORRECT LOC.		
	026424	104455					TRAP	C#ERDF
	026426	000766					.WORD	502
	026430	027222					.WORD	TSADDR
	026432	015304					.WORD	EXPREC
779	026434	005237	25:	INC	FATFLG	SET THE FATAL ERROR FLAG		
780	026440			CKLOOP		SCOPE LOOP		
781	026442	104406		ESCAPE	SUB	NO MORE CHECKS IF FATAL ERROR	TRAP	C#CLP1
	026442	104410					TRAP	C#ESCAPE
	026444	000066					.WORD	L10050-
782	026446	012704		MOV	#310,R4	START WITH LOC 310		

F9

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 5: SECOND INITIALIZATION TEST

SEQ 109

```

      027066 104410
      027070 000012
869 027072 005204
870 027074 020427 000400
871 027100 001353
872
873 027102
      027102
      027102 104403
874
875 027104 005737 002220'
876 027110 001402
877 027112 004737 017014'
878 027116 004737 016270'
879 027122 103002
880 027124 000137 026200'
881 027130
      027130 104432
      027132 000132
882
883
884
885
886
887 027134 105 170 164 TST5ID: .ASCIZ 'Extended Initialization'
888 027164 111 156 143 T5MEM: .ASCIZ 'Incorrect RAM Data After Init'
889 027222 111 156 143 T5ADDR: .ASCIZ 'Incorrect RAM Address After Init'
890
891 027264
      027264
      027264 104401
892
893
894
895
896
897
898
899
900
901
902
903
904
905
906
907
908
909
910
911
912
913
914
915
916
917

      TRAP C$ESCAPE
      .WORD L10051-.

      INC R4
      CMP R4, #400
      BNE 30$
      ;LOOK AT NEXT RAM LOC.
      ;AT TOP OF RAM ADDRESS SPACE
      ;BRANCH TILL ALL MEMORY TESTED
      ;///////////////// END SUBTEST ///////////////////
      L10051:
      TRAP C$ESUB

      TST FATFLG
      BEQ 50$
      JSR PC,CKDROP
      JSR PC,TSTLOOP
      BCC 60$
      JMP T5LOOP
      EXIT TST
      ;IS FATAL ERROR FLAG SET ?
      ;BRANCH IF NOT
      ;NO LOOP, TRY TO DROP DEVICE
      ;SHOULD WE DO ITERATIONS ?
      ;BRANCH IF NOT
      ;LCOP UNTIL COUNT EXPIRED
      ;ALL DONE THIS TEST
      TRAP C$EXIT
      .WORD L10047-.

      ;+
      ;LOCAL TEXT MESSAGES FOR TEST
      ;-
      164 TST5ID: .ASCIZ 'Extended Initialization'
      143 T5MEM: .ASCIZ 'Incorrect RAM Data After Init'
      143 T5ADDR: .ASCIZ 'Incorrect RAM Address After Init'
      .EVEN
      ENDTST

      L10047:
      TRAP C$ETST

      .SBTTL TEST 6: COMMAND REJECT

      ;
      ; THIS TEST VERIFIES THAT ALL COMMANDS OTHER THAN WRITE
      ; CHARACTERISTICS ARE REJECTED DUE TO THE NEED BUFFER ADDRESS
      ; (NBA) BIT BEING SET IN TSSR, AND THAT THE TSBA AND TSSR
      ; REGISTERS ARE LEFT IN THE PROPER STATE AFTER EACH COMMAND IS
      ; REJECTED. THIS TEST CHECKS MICROPROCESSOR SEQUENCING, BASIC
      ; COMMAND DECODING AND DATI DMA HANDLING. THIS TEST CONTAINS TWO
      ; SUBTESTS: SUBTEST 1 SEQUENCES THROUGH ALL COMMAND WORDS (OTHER
      ; THAN WRITE CHARACTERISTICS) WITH THE INTERRUPT ENABLE (IE) BIT
      ; CLEAR AND VERIFIES THAT AN INTERRUPT IS NOT GENERATED BY THE
      ; REJECTED COMMAND, SUBTEST 2 PERFORMS SIMILARLY TO SUBTEST 1 BUT
      ; SETS THE IE BIT IN EACH COMMAND WORD AND VERIFIES THAT AN
      ; INTERRUPT IS GENERATED WHEN THE COMMAND IS REJECTED. SUBTEST 1
      ; SETS UP THE INTERRUPT SERVICE ROUTINE TO FLAG UNEXPECTED
      ; INTERRUPTS. THE COMMAND WORD IN THE COMMAND BUFFER IS
      ; INITIALIZED TO 100000 (OCTAL) AND THE REMAINING THREE WORDS IN
      ; THE COMMAND BUFFER ARE SET TO KNOWN UNIQUE PATTERNS. THEN THE
      ; FOLLOWING SEQUENCE IS PERFORMED:

      ;
      ; 1. INITIALIZE THE CONTROLLER BY WRITING INTO THE TSSR;
      ; PROPER INITIAL CONDITIONS ARE VERIFIED.

```


918				
919				
920				
921				
922				
923				
924				
925				
926				
927				
928				
929				
930				
931				
932				
933				
934				
935				
936				
937				
938				
939				
940				
941				
942				
943				
944				
945				
946				
947				
948				
949				
950				
951				
952				
953				
954	027266			
	027266			
959	027266	012700	030523'	
960	027272	004737	016322'	
961	027276	012737	000024	002214'
962	027304			
963	027304			
	027304			
	027304	104402		
964				
965	027306			
	027306	012700	000000	
	027312	104441		
966	027314	012704	030200'	
967	027320	012703	002750'	
968	027324	012314		
969	027326			
	027326	104404		
970	027330	004737	015604'	
971	027334	103405		
975	027336	010001		

2. TSSD IS WRITTEN WITH ADDRESS OF THE COMMAND BUFFER TO START PROCESSING.
3. THE PROGRAM WAITS FOR SSR TO SET; IF SSR DOES NOT SET, AN ERROR REPORT IS ISSUED AND THE TEST IS ABORTED.
4. THE CONTENTS OF TSSR ARE CHECKED. TSSR IS CORRECT IF IT CONTAINS EITHER OCTAL 102206 OR 102306 (BIT 6 DEPENDS UPON THE STATE OF THE TAPE TRANSPORT).
5. THE CONTENTS OF TSBA ARE CHECKED. TSBA SHOULD CONTAIN THE INITIAL COMMAND BUFFER ADDRESS (LOADED IN STEP 2) PLUS 10 (OCTAL); I.E., TSBA SHOULD POINT TO THE WORD JUST AFTER THE COMMAND PACKET (NOTE THAT 4 COMMAND PACKET WORDS ARE ALWAYS FETCHED).
6. USING THE MAINTENANCE MODE WRAPAROUND FUNCTIONS, THE COMMAND IMAGE BLOCK IN THE M7455'S RAM (LOCATIONS 201-210 (OCTAL)) ARE CHECKED; THE IMAGE SHOULD CONTAIN A COPY OF THE FOUR COMMAND PACKET WORDS AS SET UP IN CPU MEMORY.
7. THE COMMAND WORD IN THE COMMAND BUFFER IS INCREMENTED TO THE NEXT PATTERN NOT CONTAINING WRITE CHARACTERISTICS OR IE. THE REMAINING THREE WORDS OF THE COMMAND BUFFER ARE SEQUENCED WITH PSEUDO-RANDOM DATA. IF THE COMMAND WORD HAS NOT REACHED ITS MAXIMUM VALUE (177777+1), THE TEST SEQUENCE IS REPEATED.

SUBTEST 2 IS IDENTICAL TO SUBTEST 1, EXCEPT THAT THE PROGRAM CAUSES THE IE BIT TO BE SET IN EACH COMMAND WORD AND THEN VERIFIES THAT AN INTERRUPT OCCURS.

BGNTST

```
MOV    @TST6ID,R0
JSR    PC,TSTSETUP
MOV    @20,LOOPCNT
```

```
T6::
;ASCII MESSAGE TO IDENTIFY TEST
;DC INITIAL TEST SETUP
;PERFORM 20 ITERATIONS
```

CGNSUB

```

;//////////////// BEGIN SUBTEST //////////////////
                T6.1:

```

TRAP C\$R SUB

SETPRI 0PRI00

```

:LOWER PRIORITY TO ALLOW INTERRUPTS

```

```

MOV     #PRIORITY,RO
TRAP    C$SPRI

```

```
MOV      ♦T6PACKET,R4
MOV      ♦TSTBLK,R3
MOV      (R3)+,(R4)
BGNSEG
```

[illegible]

```
JSR      PC,SOFINIT
BCS      10$
MOV      R0,R1
```

```

;DO SOFT INIT OF CONTROLLER
;BR IF SOFT INIT = OK
;SAVE CONTENTS OF ISSR

```

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 6: COMMAND REJECT

SEQ 111

976	027340		ERRDF	ERRNO,SFIERR,SFIMSG	DEVICE FATAL ERROR DURING INIT		
	027340	104455				TRAP	C\$ERDF
	027342	001131				.WORD	601
	027344	003644'				.WORD	SFIERR
	027346	011644'				.WORD	SFIMSG
977	027350	005037	002220'	10\$: CLR	FATFLG	;	CLEAR FATAL ERROR FLAG
978	027354	005037	002222'	CLR	INTRECV	;	CLEAR INTERRUPT RECEIVED FLAG
979	027360	004737	016146'	JSR	PC,CHKTSSR	;	WAIT FOR READY, NON-AMBIGUOUS
980	027364	042714	000200	BIC	#BIT7,(R4)	;	DISABLE INTERRUPTS
981	027370	010465	000000	MOV	R4,TSDB(R5)	;	SET THE PACKET ADDRESS
982	027374	004737	016060'	JSR	PC,WAITF	;	WAIT FOR SSR TO SET
983	027400	103407		BCS	15\$	;	BR IF CARRY SET (GOOD RETURN)
984	027402	010001		MOV	R0,R1	;	SAVE CONTENTS OF TSSR
988	027404			ERRDF	ERRNO,T6SSR,PKTSSR	;	DEVICE FATAL SSR FAILED TO SET
	027404	104455				TRAP	C\$ERDF
	027406	001132				.WORD	602
	027410	030235'				.WORD	T6SSR
	027412	011656'				.WORD	PKTSSR
989	027414	005237	002220'	15\$: INC	FATFLG	;	SET FATAL ERROR FLAG
990	027420			CKLOOP		;	LOOP ON ERROR, IF FLAG SET
991	027422	104406		ESCAPE	SUB	;	BY-PASS SUBTEST IF FATAL ERROR
	027422	104410				TRAP	C\$CLP1
	027424	000170				TRAP	C\$ESCAPE
992	027426	005737	002222'	TSI	INTRECV	;	DID AN INTERRUPT OCCUR ?
993	027432	001404		BEQ	22\$	;	BRANCH IF NOT
997	027434			ERRHRD	ERRNO,T6INT,PKTSSR		
	027434	104456				TRAP	C\$ERHRD
	027436	001133				.WORD	603
	027440	030313'				.WORD	T6INT
	027442	011656'				.WORD	PKTSSR
998	027444	012702	102206	22\$: MOV	#SC!NBA!SSR!TSREJ,R2	;	EXPECTED CONTENTS OF TSSR
999	027450	004737	016146'	JSR	PC,CHKTSSR	;	WAIT FOR READY, NON-AMBIGUOUS
1000	027454	016501	000002	MOV	TSSR(R5),R1	;	GET THE CONTENTS OF TSSR
1001	027460	032701	000100	BIT	#OFF,R1	;	IS OFF-LINE BIT SET ?
1002	027464	001402		BEQ	25\$	;	BRANCH IF NOT OFF-LINE
1003	027466	052702	000100	BIS	#OFF,R2	;	SET OFF-LINE IN EXPECTED DATA
1004	027472	020201		25\$: CMP	R2,R1	;	DOES EXPECTED MATCH RECEIVED ?
1005	027474	001404		BEQ	30\$	;	OKAY IF MATCH
1009	027476			ERRHRD	ERRNO,T6NBA,PKTSSR	;	NBA NOT SET TO REJECT
	027476	104456				TRAP	C\$ERHRD
	027500	001134				.WORD	604
	027502	030210				.WORD	T6NBA
	027504	011656'				.WORD	PKTSSR
1010	027506			30\$: CKLOOP		;	LOOP ON ERROR ?
	027506	104406				TRAP	C\$CLP1
1011	027510	004737	016146'	JSR	PC,CHKTSSR	;	WAIT FOR READY, NON-AMBIGUOUS
1012	027514	016501	000000	MOV	TSBA(R5),R1	;	GET TSBA REGISTER CONTENTS
1013	027520	010402		MOV	R4,R2	;	START OF THE PACKET
1014	027522	062702	000010	ADD	#10,R2	;	EXPECT TSBA TO PACKET + 10
1015	027526	020102		CMP	R1,R2	;	COMPARE EXPECTED TO RECEIVED
1016	027530	001404		BEQ	35\$	;	ERROR IF NOT EQUAL
1020	027532			ERRHRD	ERRNO,T6TSBA,EXPREC	;	PRINT THE ERROR & EXPD/RECV
	027532	104456				TRAP	C\$ERHRD
	027534	001135				.WORD	605
	027536	030451'				.WORD	T6TSBA
	027540	015304'				.WORD	EXPREC

Line	Address	Offset	Label	Instruction	Comment
1021					
1022					
1023	027542	004737	010724'	35\$: JSR PC,CKRAM	;SEE IF DATA IN RAM IS CORRECT
1024	027546	103404		BCS 40\$	;BRANCH IF PACKET IN RAM IS CORRECT
1028	027550			ERRHRD ERRNO,PKTRAM,RAMERR	;REPORT THE RAM ERROR(S)
	027550	104456			TRAP C\$ERHRD
	027552	001136			.WORD 606
	027554	004737'			.WORD PKTRAM
	027556	015320'			.WORD RAMERR
1029	027560			40\$: ENDSEG	;***** END SEGMENT *****
	027560				10000\$:
	027560	104405			TRAP C\$ESEG
1030	027562	011300		MOV (R3),R0	;PACKET COMMAND WORD
1031	027564	042700	177740	BIC #177740,R0	;GET BITS 0-4
1032	027570	020027	000000'	CMP R0,#4	;DON'T TEST WRITE CHARACTERISTICS
1033	027574	001002		BNE 45\$	;BRANCH IF OK
1034	027576	062703	000002	ADD #2,R3	;GET NEXT WORD FROM DATA TABLE
1035	027602	020327	003060'	45\$: CMP R3,#TBLEND	;REACHED END OF TABLE ?
1036	027606	103002		BHIS 50\$	;BRANCH IF END OF TABLE
1037	027610	000137	027324'	JMP 5\$	;CONTINUE TEST WITH NEW DATA
1038					
1039	027614			50\$: ENDSUB	;***** END SUBTEST *****
	027614				L10053:
	027614	104403			TRAP C\$ESUB
1040					
1041	027616	005737	002220'	TST FATFLG	;ANY FATAL ERRORS ?
1042	027622	001402		BEQ 60\$	;BRANCH IF NOT
1043	027624	004737	017014'	JSR PC,CKDROP	;TRY TO DROP THE UNIT
1044					
1045	027630			60\$: BGNSUB	;***** BEGIN SUBTEST *****
	027630				T6.2:
	027630	104402			TRAP C\$BSUB
1046					
1047	027632			SETPRI #PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS
	027632	012700	060000		MOV #PRI00,R0
	027636	104441			TRAP C\$SPRI
1048	027640	012704	030200	MOV #T6PACKET,R4	;GET THE ADDRESS OF COMMAND PACKET
1049	027644	012703	002750'	MOV #T6BLK,R3	;START OF TEST DATA
1050	027650	012314		5\$: MOV (R3)+,(R4)	;PLACE NEXT DATA WORD IN PACKET
1051	027652			BGNSEG	;***** BEGIN SEGMENT *****
	027652	104404			TRAP C\$BSEG
1052	027654	004737	015604'	JSR PC,SOFINIT	;DO SOFT INIT OF CONTROLLER
1053	027660	103105		BCS 10\$	;BR IF SOFT INIT = OK
1057	027662	010001		MOV R0,R1	;SAVE CONTENTS OF TSSR
1058	027664			ERRDF ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT
	027664	104455			TRAP C\$ERDF
	027666	001137			.WORD 607
	027670	003644'			.WORD SFIERR
	027672	011644'			.WORD SFIMSG
1059	027674	005037	002220'	10\$: CLR FATFLG	;CLEAR FATAL ERROR FLAG
1060	027700	005037	002222'	CLR INTRECV	;CLEAR INTERRUPT RECEIVED FLAG
1061	027704	004737	016146'	JSR PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS
1062	027710	052714	000200	BIS #BIT7,(R4)	;ENABLE INTERRUPTS
1063	027714	010465	000000	MOV R4,TSD0(R5)	;SET THE PACKET ADDRESS
1064	027720	004737	016060'	JSR PC,WAITF	;WAIT FOR SSR TO SET
1065	027724	103407		BCS 15\$	;BR IF CARRY SET (GOOD RETURN)
1066	027726	010001		MOV R0,R1	;SAVE CONTENTS OF TSSR

Address	Offset	Instruction	Label	Comment	Trap	Word
1070	027730	ERRDF	ERRNO,T6SSR,PKTSSR	DEVICE FATAL SSR FAILED TO SET		
	027730				TRAP	C\$ERDF
	027732				.WORD	608
	027734				.WORD	T6SSR
	027736				.WORD	PKTSSR
1071	027740	005237	002220'			
1072	027744	15\$:	INC CKLOOP	SET FATAL ERROR FLAG		
	027744			LOOP ON ERROR, IF FLAG SET		
1073	027746		ESCAPE SUB		TRAP	C\$CLP1
	027746			BY-PASS SUBTEST IF FATAL ERROR		
	027750				TRAP	C\$ESCAPE
	027750				.WORD	L10054-
1074	027752	005737	002222'			
1075	027756	001004				
1079	027760					
	027760					
	027762				TRAP	C\$ERHRD
	027762				.WORD	609
	027764				.WORD	T6NINT
	027766				.WORD	PKTSSR
1080	027770	012702	102206			
1081	027774	004737	016146'			
1082	030000	016501	000002			
1083	030004	032701	000100			
1084	030010	001402				
1085	030012	052702	000100			
1086	030016	020201				
1087	030020	001404				
1091	030022					
	030022					
	030024				TRAP	C\$ERHRD
	030024				.WORD	610
	030026				.WORD	T6NBA
	030030				.WORD	PKTSSR
1092	030032	30\$:	CKLOOP	LOOP ON ERROR ?		
	030032				TRAP	C\$CLP1
1093	030034	004737	016146'			
1094	030040	016501	000000			
1095	030044	010402				
1096	030046	062702	000010			
1097	030052	020102				
1098	030054	001404				
1102	030056					
	030056					
	030060				TRAP	C\$ERHRD
	030060				.WORD	611
	030062				.WORD	T6TSBA
	030064				.WORD	EXPREC
1103						
1104						
1105	030066	004737	010724'			
1106	030072	103404				
1110	030074					
	030074					
	030076				TRAP	C\$ERHRD
	030100				.WORD	612
	030102				.WORD	PKTRAM
	030102				.WORD	RAMERR
1111	030104	40\$:	ENDSEG	END SEGMENT		
	030104					
	030104					
1112	030106					
	030106				TRAP	C\$ESEG
	030106					

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58

SEQ 114

```

1113 030110 042700 177740      BIC      0177740,R0      ;GET BITS 0-4
1114 030114 020027 000004      CMP      R0,#4        ;DON'T TEST WRITE CHARACTERISTICS
1115 030120 001002              BNE      45$          ;BRANCH IF NOT WRITE CHARACTERISTICS
1116 030122 062703 000002      ADD      02,R3        ;BY-PASS WRITE CHARACTERISTICS
1117 030126 020327 003060'     45$:  CMP      R3,#TBLEND    ;HAVE WE COMPLETED DATA TABLE ?
1118 030132 103002              BHS      50$          ;BRANCH IF ALL TESTED
1119 030134 000137 027650'     JMP      5$          ;TEST WITH NEXT DATA
1120
1121 030140              50$:  ENDSUB                ;\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\ END SUBTEST \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
1122 030142 005737 002220'     TST      FATFLG        ;ANY FATAL ERRORS ?
1123 030146 001402              BEQ      60$          ;BRANCH IF NOT
1124 030150 004737 017014'     JSR      PC,CKDROP    ;TRY TO DROP THE UNIT
1125 030154 004737 016270'     60$:  JSR      PC,TSTLOOP    ;SHOULD WE DO ITERATIONS ?
1126 030160 103002              BCC      62$          ;BRANCH IF NOT
1127 030162 000137 027304'     JMP      T6LOOP      ;LOOP UNTIL COUNT EXPIRED
1128 030166              62$:  EXIT      TST          ;ALL DONE THIS TEST
1129 030166 104432              TRAP      C$EXIT      TRAP      C$EXIT
1130 030170 000352              .WORD      L10052-..  .WORD      L10052-..
1131
1132      ;*
1133      ;LOCAL STORAGE FOR THIS TEST
1134      ;*
1135 030172              .BLKB      10-<.-TSV267>
1137 030200      T6PACKET:  .WORD      0              ;COMMAND PACKET FOR TEST
1138 030200 000000              .WORD      052525      ;WILL CONTAIN VARIABLE COMMANDS
1139 030202 052525              .WORD      125252
1140 030204 125252              .WORD      052525
1141 030206 052525              .WORD      052525
1142
1143      ;*
1144      ;LOCAL TEXT MESSAGES FOR TEST
1145      ;*
1146
1147
1148 030210 103 157 155 T6NBA:  .ASCIZ  'Command Not Rejected'
1149 030235 103 157 156 T6SSR:  .ASCIZ  'Contents of TSSR Incorrect After Write Packet'
1150 030313 125 156 145 T6INT:  .ASCIZ  'Unexpected Interrupt Received On Write Packet'
1151 030371 105 170 160 T6NJNT: .ASCIZ  'Expected Interrupt Not Received On Write Packet'
1152 030451 111 156 143 T6TSBA: .ASCIZ  'Incorrect TSBA Address After Packet Write'
1153 030523 103 157 155 T6TID:  .ASCIZ  'Command Reject'
1154
1155 030542              .EVEN
1156 030542 104401              ENDTST
1157
1158      L10052:  TRAP      C$ETST
1159
1160      .SBTTL  TEST  7:  WRITE CHARACTERISTICS
1161
1162      ;
1163      ; THIS TEST VERIFIES BASIC OPERATION OF THE WRITE CHARACTERISTICS
1164      ; COMMAND. IT VERIFIES THAT THE COMMAND BLOCK AND CHARACTERISTICS
1165      ; DATA BLOCK ARE FETCHED PROPERLY FROM CPU MEMORY, THE NEED BUFFER
1166      ; ADDRESS (NBA) BIT IN TSSR IS HANDLED PROPERLY, AND THAT A PROPER
1167      ; MESSAGE PACKET IS STORED, WHERE APPROPRIATE. THIS TEST DOES NOT
1168      ; CHECK THAT THE VARIOUS FUNCTIONS ENABLED BY CHARACTERISTIC MODE

```


Address	OpCode	OpHex	OpDec	OpHex2	OpDec2	OpHex3	OpDec3	OpHex4	OpDec4	OpHex5	OpDec5	OpHex6	OpDec6	OpHex7	OpDec7	OpHex8	OpDec8	OpHex9	OpDec9	OpHex10	OpDec10	OpHex11	OpDec11	OpHex12	OpDec12	OpHex13	OpDec13	OpHex14	OpDec14	OpHex15	OpDec15	OpHex16	OpDec16	OpHex17	OpDec17	OpHex18	OpDec18	OpHex19	OpDec19	OpHex20	OpDec20	OpHex21	OpDec21	OpHex22	OpDec22	OpHex23	OpDec23	OpHex24	OpDec24	OpHex25	OpDec25	OpHex26	OpDec26	OpHex27	OpDec27	OpHex28	OpDec28	OpHex29	OpDec29	OpHex30	OpDec30	OpHex31	OpDec31	OpHex32	OpDec32	OpHex33	OpDec33	OpHex34	OpDec34	OpHex35	OpDec35	OpHex36	OpDec36	OpHex37	OpDec37	OpHex38	OpDec38	OpHex39	OpDec39	OpHex40	OpDec40	OpHex41	OpDec41	OpHex42	OpDec42	OpHex43	OpDec43	OpHex44	OpDec44	OpHex45	OpDec45	OpHex46	OpDec46	OpHex47	OpDec47	OpHex48	OpDec48	OpHex49	OpDec49	OpHex50	OpDec50	OpHex51	OpDec51	OpHex52	OpDec52	OpHex53	OpDec53	OpHex54	OpDec54	OpHex55	OpDec55	OpHex56	OpDec56	OpHex57	OpDec57	OpHex58	OpDec58	OpHex59	OpDec59	OpHex60	OpDec60	OpHex61	OpDec61	OpHex62	OpDec62	OpHex63	OpDec63	OpHex64	OpDec64	OpHex65	OpDec65	OpHex66	OpDec66	OpHex67	OpDec67	OpHex68	OpDec68	OpHex69	OpDec69	OpHex70	OpDec70	OpHex71	OpDec71	OpHex72	OpDec72	OpHex73	OpDec73	OpHex74	OpDec74	OpHex75	OpDec75	OpHex76	OpDec76	OpHex77	OpDec77	OpHex78	OpDec78	OpHex79	OpDec79	OpHex80	OpDec80	OpHex81	OpDec81	OpHex82	OpDec82	OpHex83	OpDec83	OpHex84	OpDec84	OpHex85	OpDec85	OpHex86	OpDec86	OpHex87	OpDec87	OpHex88	OpDec88	OpHex89	OpDec89	OpHex90	OpDec90	OpHex91	OpDec91	OpHex92	OpDec92	OpHex93	OpDec93	OpHex94	OpDec94	OpHex95	OpDec95	OpHex96	OpDec96	OpHex97	OpDec97	OpHex98	OpDec98	OpHex99	OpDec99	OpHex100	OpDec100	OpHex101	OpDec101	OpHex102	OpDec102	OpHex103	OpDec103	OpHex104	OpDec104	OpHex105	OpDec105	OpHex106	OpDec106	OpHex107	OpDec107	OpHex108	OpDec108	OpHex109	OpDec109	OpHex110	OpDec110	OpHex111	OpDec111	OpHex112	OpDec112	OpHex113	OpDec113	OpHex114	OpDec114	OpHex115	OpDec115	OpHex116	OpDec116	OpHex117	OpDec117	OpHex118	OpDec118	OpHex119	OpDec119	OpHex120	OpDec120	OpHex121	OpDec121	OpHex122	OpDec122	OpHex123	OpDec123	OpHex124	OpDec124	OpHex125	OpDec125	OpHex126	OpDec126	OpHex127	OpDec127	OpHex128	OpDec128	OpHex129	OpDec129	OpHex130	OpDec130	OpHex131	OpDec131	OpHex132	OpDec132	OpHex133	OpDec133	OpHex134	OpDec134	OpHex135	OpDec135	OpHex136	OpDec136	OpHex137	OpDec137	OpHex138	OpDec138	OpHex139	OpDec139	OpHex140	OpDec140	OpHex141	OpDec141	OpHex142	OpDec142	OpHex143	OpDec143	OpHex144	OpDec144	OpHex145	OpDec145	OpHex146	OpDec146	OpHex147	OpDec147	OpHex148	OpDec148	OpHex149	OpDec149	OpHex150	OpDec150	OpHex151	OpDec151	OpHex152	OpDec152	OpHex153	OpDec153	OpHex154	OpDec154	OpHex155	OpDec155	OpHex156	OpDec156	OpHex157	OpDec157	OpHex158	OpDec158	OpHex159	OpDec159	OpHex160	OpDec160	OpHex161	OpDec161	OpHex162	OpDec162	OpHex163	OpDec163	OpHex164	OpDec164	OpHex165	OpDec165	OpHex166	OpDec166	OpHex167	OpDec167	OpHex168	OpDec168	OpHex169	OpDec169	OpHex170	OpDec170	OpHex171	OpDec171	OpHex172	OpDec172	OpHex173	OpDec173	OpHex174	OpDec174	OpHex175	OpDec175	OpHex176	OpDec176	OpHex177	OpDec177	OpHex178	OpDec178	OpHex179	OpDec179	OpHex180	OpDec180	OpHex181	OpDec181	OpHex182	OpDec182	OpHex183	OpDec183	OpHex184	OpDec184	OpHex185	OpDec185	OpHex186	OpDec186	OpHex187	OpDec187	OpHex188	OpDec188	OpHex189	OpDec189	OpHex190	OpDec190	OpHex191	OpDec191	OpHex192	OpDec192	OpHex193	OpDec193	OpHex194	OpDec194	OpHex195	OpDec195</
---------	--------	-------	-------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	------------

TSVSA - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 7: WRITE CHARACTERISTICS

SEQ 118

PC	PC+4	PC+8	PC+12	PC+16	PC+20	PC+24	PC+28	PC+32	PC+36	PC+40	PC+44	PC+48	PC+52	PC+56	PC+60	PC+64	PC+68	PC+72	PC+76	PC+80	PC+84	PC+88	PC+92	PC+96	PC+100	PC+104	PC+108	PC+112	PC+116	PC+120	PC+124	PC+128	PC+132	PC+136	PC+140	PC+144	PC+148	PC+152	PC+156	PC+160	PC+164	PC+168	PC+172	PC+176	PC+180	PC+184	PC+188	PC+192	PC+196	PC+200	PC+204	PC+208	PC+212	PC+216	PC+220	PC+224	PC+228	PC+232	PC+236	PC+240	PC+244	PC+248	PC+252	PC+256	PC+260	PC+264	PC+268	PC+272	PC+276	PC+280	PC+284	PC+288	PC+292	PC+296	PC+300	PC+304	PC+308	PC+312	PC+316	PC+320	PC+324	PC+328	PC+332	PC+336	PC+340	PC+344	PC+348	PC+352	PC+356	PC+360	PC+364	PC+368	PC+372	PC+376	PC+380	PC+384	PC+388	PC+392	PC+396	PC+400	PC+404	PC+408	PC+412	PC+416	PC+420	PC+424	PC+428	PC+432	PC+436	PC+440	PC+444	PC+448	PC+452	PC+456	PC+460	PC+464	PC+468	PC+472	PC+476	PC+480	PC+484	PC+488	PC+492	PC+496	PC+500	PC+504	PC+508	PC+512	PC+516	PC+520	PC+524	PC+528	PC+532	PC+536	PC+540	PC+544	PC+548	PC+552	PC+556	PC+560	PC+564	PC+568	PC+572	PC+576	PC+580	PC+584	PC+588	PC+592	PC+596	PC+600	PC+604	PC+608	PC+612	PC+616	PC+620	PC+624	PC+628	PC+632	PC+636	PC+640	PC+644	PC+648	PC+652	PC+656	PC+660	PC+664	PC+668	PC+672	PC+676	PC+680	PC+684	PC+688	PC+692	PC+696	PC+700	PC+704	PC+708	PC+712	PC+716	PC+720	PC+724	PC+728	PC+732	PC+736	PC+740	PC+744	PC+748	PC+752	PC+756	PC+760	PC+764	PC+768	PC+772	PC+776	PC+780	PC+784	PC+788	PC+792	PC+796	PC+800	PC+804	PC+808	PC+812	PC+816	PC+820	PC+824	PC+828	PC+832	PC+836	PC+840	PC+844	PC+848	PC+852	PC+856	PC+860	PC+864	PC+868	PC+872	PC+876	PC+880	PC+884	PC+888	PC+892	PC+896	PC+900	PC+904	PC+908	PC+912	PC+916	PC+920	PC+924	PC+928	PC+932	PC+936	PC+940	PC+944	PC+948	PC+952	PC+956	PC+960	PC+964	PC+968	PC+972	PC+976	PC+980	PC+984	PC+988	PC+992	PC+996	PC+1000	PC+1004	PC+1008	PC+1012	PC+1016	PC+1020	PC+1024	PC+1028	PC+1032	PC+1036	PC+1040	PC+1044	PC+1048	PC+1052	PC+1056	PC+1060	PC+1064	PC+1068	PC+1072	PC+1076	PC+1080	PC+1084	PC+1088	PC+1092	PC+1096	PC+1100	PC+1104	PC+1108	PC+1112	PC+1116	PC+1120	PC+1124	PC+1128	PC+1132	PC+1136	PC+1140	PC+1144	PC+1148	PC+1152	PC+1156	PC+1160	PC+1164	PC+1168	PC+1172	PC+1176	PC+1180	PC+1184	PC+1188	PC+1192	PC+1196	PC+1200	PC+1204	PC+1208	PC+1212	PC+1216	PC+1220	PC+1224	PC+1228	PC+1232	PC+1236	PC+1240	PC+1244	PC+1248	PC+1252	PC+1256	PC+1260	PC+1264	PC+1268	PC+1272	PC+1276	PC+1280	PC+1284	PC+1288	PC+1292	PC+1296	PC+1300	PC+1304	PC+1308	PC+1312	PC+1316	PC+1320	PC+1324	PC+1328	PC+1332	PC+1336	PC+1340	PC+1344	PC+1348	PC+1352	PC+1356	PC+1360	PC+1364	PC+1368	PC+1372	PC+1376	PC+1380	PC+1384	PC+1388	PC+1392	PC+1396	PC+1400	PC+1404	PC+1408	PC+1412	PC+1416	PC+1420	PC+1424	PC+1428	PC+1432	PC+1436	PC+1440	PC+1444	PC+1448	PC+1452
----	------	------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------

G10

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 7: WRITE CHARACTERISTICS

SEQ 123

Address	Offset	Hex	Label	Operation	Comments	Trap	Value
1544	032216	001402		BEQ	25\$		
1545	032220	052702	000100	BIS	00FL,R2		
1546	032224	020201		CMP	R2,R1		
1547	032226	001414		BEQ	30\$		
1548	032230	010100		MOV	R1,R0		
1549	032232			XOR	R2,R0		
1550	032242	020027	002000	CMP	R0,0NBA		
1551	032246	001404		BEQ	30\$		
1555	032250			ERRHRD	ERRNO,T75REJ,PKTSSR		
	032250	104456				TRAP	C\$ERHRD
	032252	001325				.WORD	725
	032254	033403				.WORD	T75REJ
	032256	011656				.WORD	PKTSSR
1556	032260			30\$:	CKLOOP		
	032260	104406					
1557	032262	032701	002000	BIT	0NBA,R1		
1558	032266	001004		BNE	35\$		
1562	032270			ERRHRD	ERRNO,T72NBA,PKTSSR		
	032270	104456				TRAP	C\$ERHRD
	032272	001326				.WORD	726
	032274	032762				.WORD	T72NBA
	032276	011656				.WORD	PKTSSR
1563	032300			35\$:			
1564	032300			ENDSEG			
	032300						
	032300	104405					
1565							
1566	032302	005203		INC	R3		
1567	032304	020327	000016	CMP	R3,014.		
1568	032310	002002		BGE	57\$		
1569	032312	000137	032070	JMP	5\$		
1570							
1571	032316			57\$:	ENDSUB		
	032316						
	032316	104403					
1572							
1573							
1574							
1575							
1576							
1577							
1578							
1579							
1580							
1581							
1582							
1583							
1584							
1585	032320						
	032320						
	032320	104402					
1586	032322	005737	002224	TST	EXTFEA		
1587	032326	001002		BNE	4\$		
1588	032330			EXIT	TST		
	032330	104432					
	032332	001576					
1589	032334	004737	034062	4\$:	JSR	PC,T7REST	

H10

[illegible]

	032536	104406						TRAP	C\$CLP1
1637	032540	004737	016146'		JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS		
1638	032544	016501	000000		MOV	TSBA(R5),P1	;GET TSBA REGISTER CONTENTS		
1639	032550	012702	032728'		MOV	#T7BFR,R2	;START OF THE DATA BUFFER		
1640	032554	032762	000200 000012		BIT	#BIT7,XST2(R2)	;IS EXTENDED FEATURES BIT SET ?		
1641	032562	001402			BEQ	32\$	;BRANCH IF EXTENDED FEATURES OFF		
1642	032564	062702	000002		ADD	#2,R2	;EXTRA WORD IF SPECIAL FEATURES		
1643	032570	062702	000016	32\$;	ADD	#14.,R2	;EXPECTED CONTENTS OF TSBA		
1644	032574	020102			CMP	R1,R2	;COMPARE EXPECTED TO RECEIVED		
1645	032576	001404			BEQ	35\$	;ERROR IF NOT EQUAL		
1649	032600				ERRHRD	ERRNO,T7TSBA,EXPREC	;PRINT THE ERROR & EXPD/RECV		
	032600	104456					TRAP	C\$ERHRD	
	032602	001333					.WORD	731	
	032604	033750'					.WORD	T7TSBA	
	032606	015304'					.WORD	EXPREC	
1650									
1651									
1652	032610	012704	032710'	35\$;	MOV	#T7DATA,R4	;SET POINTER FOR CHECKER		
1653	032614	004737	011034'		JSR	PC,CKRAM2	;SEE IF DATA IN RAM IS CORRECT		
1654	032620	103404			BCS	40\$	;BRANCH IF PACKET IN RAM IS CORRECT		
1658	032622				ERRHRD	ERRNO,PKTRAM,RAMERR	;REPORT THE RAM ERROR(S)		
	032622	104456					TRAP	C\$ERHRD	
	032624	001334					.WORD	732	
	032626	004737'					.WORD	PKTRAM	
	032630	015320'					.WORD	RAMERR	
1659									
1660	032632	012704	032700'	40\$;	MOV	#T7PACKET,R4	;RESET PACKET POINTER		
1661	032636				ENDSEG		;***** END SEGMENT *****		
	032636						10000\$;		
	032636	104405					TRAP	C\$ESEG	
1662									
1663	032640	012364	000006		MOV	(R3)+,PKBCNT(R4)	;SET THE TEST WORD		
1664	032644	020327	003060'		CMP	R3,#TBLEND	;HAS ALL DATA BEEN TESTED ?		
1665	032650	103002			BHIS	55\$	;BRANCH IF ALL DATA DONE		
1666	032652	000137	032372'		JMP	5\$	;BRANCH TILL BACK TO ZERO		
1667									
1668	032656			55\$;	ENDSUB		;////////// END SUBTEST //////////		
	032656						L10063;		
	032656	104403					TRAP	C\$ESUB	
1669									
1670	032660	005737	002220'		TST	FATFLG	;ANY FATAL ERRORS ?		
1671	032664	001402			BEQ	60\$	;BRANCH IF NOT		
1672	032666	004737	017014'		JSR	PC,CKDROP	;TRY TO DROP THE UNIT		
1673	032672			60\$;					
1674	032672				EXIT	TST	;ALL DONE THIS TEST		
1675	032672	104432					TRAP	C\$EXIT	
1676	032674	001234					.WORD	L10055-	
1677									
1678									
1679									
1680	032676				.BLKB	10-<. -TSV2&7>			
1681	032700				T7PACKET:		;COMMAND PACKET FOR TEST		
1682	032700	100004			.WORD	100004	;WRITE CHARACTERISTICS COMMAND, WITH ACK		
1683	032702	032710'			.WORD	T7DATA	;ADDRESS OF CHARACTERISTICS BLOCK		
1684	032704	000000			.WORD	0			

J10

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 7: WRITE CHARACTERISTICS

SEQ 126

```

1687 032706 000010          .WORD 8.          ;STARTING VALUE OF BLOCK SIZE
1688
1689 032710          T7DATA:          ;CHARACTERISTICS DATA BLOCK
1690 032710 032726'          .WORD T7BFR          ;ADDRESS OF MESSAGE BUFFER
1691 032712 000000          .WORD 0
1692 032714 000016          .WORD 14.          ;LENGTH OF MESSAGE BUFFER
1693 032716 000000          .WORD 0
1694 032720 000000          T7SP: .WORD 0          ;EXTFEA EXTRA WORD
1695
1696 032722 000000 000000          .WORD 0,0          ;SPACE
1697 032726          T7BFR: .BLKW 8.          ;MESSAGE BUFFER
1698
1699
1700
1701          ;+
1702          ;TEST DATA FOR SUBTEST TWO
1703          ;
1704          ;DATA HAS FORMAT:
1705          ;
1706          ;          1ST WORD          OFFSET TO TEST WORD IN PACKET
1707          ;          2ND WORD          BITS TO SET FOR TEST
1708          ;
1709          ;-
1710 032746          T72DATA:
1711 032746 000000 037140          .WORD 0,BIT5!BIT6!BIT9!BIT10!BIT11!BIT12!BIT13
1712 032752 000002 000001          .WORD 2,BIT0
1713 032756 000004 100100          .WORD 4,BIT6!BIT15
1714          T72DONE=.
1715
1716
1717          ;+
1718          ;LOCAL TEXT MESSAGES FOR TEST
1719          ;
1720          ;-
1721 032762          116          102          101 T72NBA: .ASCIZ 'NBA Not Set On Rejected WRITE CHARACTERISTICS'
1722 033040          127          122          111 T7NBA: .ASCIZ 'WRITE CHARACTERISTICS Command Not Accepted'
1723 033113          127          122          111 T72REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Non-Zero Unused Fields'
1724 033212          127          122          111 T73REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Data Count'
1725 033305          127          122          111 T74REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Block Address'
1726 033403          127          122          111 T75REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Buffer Length'
1727 033501          103          157          156 T7SSR: .ASCIZ 'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
1728 033570          105          170          160 T7NINT: .ASCIZ 'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
1729 033661          125          156          145 T7INT: .ASCIZ 'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
1730 033750          111          156          143 T7TSBA: .ASCIZ 'Incorrect TSBA Address After WRITE CHARACTERISTICS'
1731 034033          127          162          151 TST7ID: .ASCIZ 'Write Characteristics'
1732          .EVEN
1733
1734
1735
1736          ;+
1737          ;ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
1738          ;
1739          ;-
1740
1741 034062          T7REST:
1742 034062          SAVREG          ;SAVE THE REGISTERS
1743 034066 012701 032700'          MOV #T7PACKET,R1          ;START OF THE PACKET

```

K10

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 7: WRITE CHARACTERISTICS

SEQ 127

1744 034072 012721 100004
 1745 034076 012721 032710'
 1746 034102 005021
 1747 034104 012721 000010
 1748 034110 012721 032726'
 1749 034114 005021
 1750 034116 012721 000020
 1751 034122 005021
 1752 034124 005011
 1753 034126 000207
 1754 034130
 034130
 034130 104401

MOV #100004,(R1)+ ;WRITE CHARACTERISTICS WITH ACK
 MOV #T7DATA,(R1)+ ;ADDRESS OF CHAR DATA BLOCK
 CLR (R1)+ ;EXTENDED ADDRESS
 MOV #8,(R1)+ ;SIZE OF DATA BLOCK IN BYTES
 MOV #T7BFR,(R1)+ ;ADDRESS OF MESSAGE BUFFER
 CLR (R1)+
 MOV #16,(R1)+ ;LENGTH OF MESSAGE BUFFER
 CLR (R1)+
 CLR (R1)
 RTS PC ;RETURN
 ENDTST

L10055: TRAP C\$ETST

1755
 1756
 1757
 1758
 1759
 1760
 1761
 1762
 1763
 1764
 1765
 1766
 1767
 1768
 1769
 1770
 1771
 1772
 1773
 1774
 1775
 1776
 1777
 1778
 1779
 1780
 1781
 1782
 1783
 1784
 1785
 1786
 1787
 1788
 1789
 1790

.SBTTL TEST 8: VOLUME CHECK

THIS TEST VERIFIES THAT THE VOLUME CHECK (VCK) BIT, A FLAG HELD WITHIN THE M7455 AND APPEARING IN XST0, IS SET BY INITIALIZE AND CLEARED BY EXECUTING A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT SET. IT IS ALSO VERIFIED THAT A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT CLEAR DOES NOT AFFECT THE STATE OF THE VOLUME CHECK BIT. THE ACTUAL FUNCTION OF VOLUME CHECK, THAT OF PREVENTING OR ALLOWING A TAPE MOTION COMMAND DEPENDING UPON WHETHER VOLUME CHECK IS SET OR CLEAR, IS NOT CHECKED BY THIS TEST; THIS FUNCTIONALITY IS CHECKED IN THE INDIVIDUAL TESTS OF TAPE MOTION COMMANDS.

THE TEST PROCEEDS AS FOLLOWS:

1. THE CONTROLLER IS INITIALIZED BY WRITING INTO THE TSSR.
2. A WRITE CHARACTERISTICS COMMAND IS ISSUED (WITH CVC=0) AND XST0 IN THE RETURNED MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD BE CLEAR (0).
3. THE PREVIOUS STEP IS REPEATED TO VERIFY THAT VCK DOES NOT CHANGE (REMAINS AT 0).
4. A WRITE CHARACTERISTICS COMMAND IS ISSUED WITH CVC=1 AND THE VCK BIT IN XST0 IN THE MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD BE CLEAR (0).
5. A WRITE CHARACTERISTICS COMMAND IS ISSUED WITH CVC=0 AND THE VCK BIT IN XST0 IN THE MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD REMAIN CLEAR (0).

1791 034132
 034132
 1796 034132 012700 035017'
 1797 034136 004737 010322'
 1798 034142 012737 000024 002214'
 1799 034150
 1800
 1801 034150 012704 034540'

T8LOOP:

BGNTST
 MOV #T8ID,R0 ;ASCII MESSAGE TO IDENTIFY TEST
 JSR PC,T8SETUP ;DO INITIAL TEST SETUP
 MOV #20,LOOPCNT ;PERFORM 20 ITERATIONS
 MOV #T8PACKET,R4 ;PACKET FOR WRITE CHARACTERISTICS

L10

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 8: VOLUME CHECK

SEQ 128

1802	034154	004737	015604'	5\$:	JSR	PC,SOFINIT	;DO SOFT INIT OF CONTROLLER	
1803	034160	103405			BCS	10\$	;BR IF SOFT INIT = OK	
1807	034162	010001			MOV	R0,R1	;SAVE CONTENTS OF TSSR	
1808	034164				ERRDF	ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT	
	034164	104455					TRAP	C\$ERDF
	034166	001441					.WORD	801
	034170	003644'					.WORD	SFIERR
	034172	011644'					.WORD	SFIMSG
1809	034174	042714	040000	10\$:	BIC	#BIT14,(R4)	;CLEAR THE CVC BIT	
1810	034200	010465	000000		MOV	R4,TSDB(R5)	;SET THE PACKET ADDRESS FOR WRITE CHAR	
1811	034204	004737	016146'		JSR	PC,CHKTSSR	;WAIT FOR SSR TO SET	
1812	034210	103405			BCS	15\$	;BR IF CARRY SET (GOOD RETURN)	
1813	034212	010001			MOV	R0,R1	;SAVE CONTENTS OF TSSR	
1817	034214				ERRDF	ERRNO,T8SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET	
	034214	104455					TRAP	C\$E7F
	034216	001442					.WORD	802
	034220	034730'					.WORD	T8SSR
	034222	011656'					.WORD	PKTSSR
1818	034224			15\$:	CKLOOP		;LOOP ON ERROR, IF FLAG SET	
	034224	104406					TRAP	C\$CLP1
1819	034226				ESCAPE	TST	;EXIT IF FATAL ERROR	
	034226	104410					TRAP	C\$ESCAPE
	034230	000604					.WORD	L10064-
1820	034232	012702	034562'		MOV	#T8BFR,R2	;ADDRESS OF THE MESSAGE BUFFER	
1821	034236	032762	000020 000006		BIT	#XSOVCK,XSTO(R2)	;IS VOLUME CHECK SET IN XSTO ?	
1822	034244	001406			BEG	20\$	;OKAY IF VOLUME CHECK IS CLEAR	
1826	034246	016501	000002		MOV	TSSR(R5),R1	;CONTENTS OF TSSR FOR ERROR REPORT	
1827	034252				ERRHRD	ERRNO,T8NVCK,PKTMES	;VOLUME CHECK NOT CLEAR	
	034252	104456					TRAP	C\$ERHRD
	034254	001443					.WORD	803
	034256	034637'					.WORD	T8NVCK
	034260	011720'					.WORD	PKTMES
1828	034262			20\$:	CKLOOP		;LOOP ON ERROR ?	
	034262	104406					TRAP	C\$CLP1
1829	034264	010465	000000		MOV	R4,TSDB(R5)	;SET THE PACKET ADDRESS FOR WRITE CHAR	
1830	034270	004737	016146'		JSR	PC,CHKTSSR	;WAIT FOR SSR TO SET	
1831	034274	103405			BCS	25\$	;BR IF CARRY SET (GOOD RETURN)	
1832	034276	010001			MOV	R0,R1	;SAVE CONTENTS OF TSSR	
1836	034300				ERRDF	ERRNO,T8SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET	
	034300	104455					TRAP	C\$ERDF
	034302	001444					.WORD	804
	034304	034730'					.WORD	T8SSR
	034306	011656'					.WORD	PKTSSR
1837	034310			25\$:	CKLOOP		;LOOP ON ERROR, IF FLAG SET	
	034310	104406					TRAP	C\$CLP1
1838	034312				ESCAPE	TST	;EXIT IF FATAL ERROR	
	034312	104410					TRAP	C\$ESCAPE
	034314	000520					.WORD	L10064-
1839	034316	032762	000020 000006		BIT	#XSOVCK,XSTO(R2)	;IS VOLUME CHECK SET IN XSTO ?	
1840	034324	001406			BEG	30\$	;OKAY IF VOLUME CHECK IS SET	
1844	034326	016501	000002		MOV	TSSR(R5),R1	;CONTENTS OF TSSR FOR ERROR REPORT	
1845	034332				ERRHRD	ERRNO,T8NVCK,PKTMES	;VOLUME CHECK NOT SET	
	034332	104456					TRAP	C\$ERHRD
	034334	001445					.WORD	805
	034336	034637'					.WORD	T8NVCK
	034340	011720'					.WORD	PKTMES
1846	034342			30\$:	CKLOOP		;LOOP ON ERROR ?	

M10

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 8: VOLUME CHECK

SEQ 129

Address	OpCode	OpHex	OpDec	OpHex2	OpDec2	OpHex3	OpDec3	OpHex4	OpDec4	OpHex5	OpDec5	OpHex6	OpDec6	OpHex7	OpDec7	OpHex8	OpDec8	OpHex9	OpDec9	OpHex10	OpDec10	OpHex11	OpDec11	OpHex12	OpDec12	OpHex13	OpDec13	OpHex14	OpDec14	OpHex15	OpDec15	OpHex16	OpDec16	OpHex17	OpDec17	OpHex18	OpDec18	OpHex19	OpDec19	OpHex20	OpDec20	OpHex21	OpDec21	OpHex22	OpDec22	OpHex23	OpDec23	OpHex24	OpDec24	OpHex25	OpDec25	OpHex26	OpDec26	OpHex27	OpDec27	OpHex28	OpDec28	OpHex29	OpDec29	OpHex30	OpDec30	OpHex31	OpDec31	OpHex32	OpDec32	OpHex33	OpDec33	OpHex34	OpDec34	OpHex35	OpDec35	OpHex36	OpDec36	OpHex37	OpDec37	OpHex38	OpDec38	OpHex39	OpDec39	OpHex40	OpDec40	OpHex41	OpDec41	OpHex42	OpDec42	OpHex43	OpDec43	OpHex44	OpDec44	OpHex45	OpDec45	OpHex46	OpDec46	OpHex47	OpDec47	OpHex48	OpDec48	OpHex49	OpDec49	OpHex50	OpDec50	OpHex51	OpDec51	OpHex52	OpDec52	OpHex53	OpDec53	OpHex54	OpDec54	OpHex55	OpDec55	OpHex56	OpDec56	OpHex57	OpDec57	OpHex58	OpDec58	OpHex59	OpDec59	OpHex60	OpDec60	OpHex61	OpDec61	OpHex62	OpDec62	OpHex63	OpDec63	OpHex64	OpDec64	OpHex65	OpDec65	OpHex66	OpDec66	OpHex67	OpDec67	OpHex68	OpDec68	OpHex69	OpDec69	OpHex70	OpDec70	OpHex71	OpDec71	OpHex72	OpDec72	OpHex73	OpDec73	OpHex74	OpDec74	OpHex75	OpDec75	OpHex76	OpDec76	OpHex77	OpDec77	OpHex78	OpDec78	OpHex79	OpDec79	OpHex80	OpDec80	OpHex81	OpDec81	OpHex82	OpDec82	OpHex83	OpDec83	OpHex84	OpDec84	OpHex85	OpDec85	OpHex86	OpDec86	OpHex87	OpDec87	OpHex88	OpDec88	OpHex89	OpDec89	OpHex90	OpDec90	OpHex91	OpDec91	OpHex92	OpDec92	OpHex93	OpDec93	OpHex94	OpDec94	OpHex95	OpDec95	OpHex96	OpDec96	OpHex97	OpDec97	OpHex98	OpDec98	OpHex99	OpDec99	OpHex100	OpDec100	OpHex101	OpDec101	OpHex102	OpDec102	OpHex103	OpDec103	OpHex104	OpDec104	OpHex105	OpDec105	OpHex106	OpDec106	OpHex107	OpDec107	OpHex108	OpDec108	OpHex109	OpDec109	OpHex110	OpDec110	OpHex111	OpDec111	OpHex112	OpDec112	OpHex113	OpDec113	OpHex114	OpDec114	OpHex115	OpDec115	OpHex116	OpDec116	OpHex117	OpDec117	OpHex118	OpDec118	OpHex119	OpDec119	OpHex120	OpDec120	OpHex121	OpDec121	OpHex122	OpDec122	OpHex123	OpDec123	OpHex124	OpDec124	OpHex125	OpDec125	OpHex126	OpDec126	OpHex127	OpDec127	OpHex128	OpDec128	OpHex129	OpDec129	OpHex130	OpDec130	OpHex131	OpDec131	OpHex132	OpDec132	OpHex133	OpDec133	OpHex134	OpDec134	OpHex135	OpDec135	OpHex136	OpDec136	OpHex137	OpDec137	OpHex138	OpDec138	OpHex139	OpDec139	OpHex140	OpDec140	OpHex141	OpDec141	OpHex142	OpDec142	OpHex143	OpDec143	OpHex144	OpDec144	OpHex145	OpDec145	OpHex146	OpDec146	OpHex147	OpDec147	OpHex148	OpDec148	OpHex149	OpDec149	OpHex150	OpDec150	OpHex151	OpDec151	OpHex152	OpDec152	OpHex153	OpDec153	OpHex154	OpDec154	OpHex155	OpDec155	OpHex156	OpDec156	OpHex157	OpDec157	OpHex158	OpDec158	OpHex159	OpDec159	OpHex160	OpDec160	OpHex161	OpDec161	OpHex162	OpDec162	OpHex163	OpDec163	OpHex164	OpDec164	OpHex165	OpDec165	OpHex166	OpDec166	OpHex167	OpDec167	OpHex168	OpDec168	OpHex169	OpDec169	OpHex170	OpDec170	OpHex171	OpDec171	OpHex172	OpDec172	OpHex173	OpDec173	OpHex174	OpDec174	OpHex175	OpDec175	OpHex176	OpDec176	OpHex177	OpDec177	OpHex178	OpDec178	OpHex179	OpDec179	OpHex180	OpDec180	OpHex181	OpDec181	OpHex182	OpDec182	OpHex183	OpDec183	OpHex184	OpDec184	OpHex185	OpDec185	OpHex186	OpDec186	OpHex187	OpDec187	OpHex188	OpDec188	OpHex189	OpDec189	OpHex190	OpDec190	OpHex191	OpDec191	OpHex192	OpDec192	OpHex193	OpDec193	OpHex194	OpDec194	OpHex195	OpDec195</
---------	--------	-------	-------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	------------

N10

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 8: VOLUME CHECK

SEQ 130

```

1889
1890
1891          ;+
1892          ;LOCAL STORAGE FOR THIS TEST
1893          ;+
1894
1895          .BLKB 10-<.-TSV267>
1896          T8PACKET:
1897          .WORD 100004          ;COMMAND PACKET FOR TEST
1898          .WORD T8DATA          ;WRITE CHARACTERISTICS COMMAND
1899          .WORD 0          ;ADDRESS OF CHARACTERISTICS BLOCK
1900          .WORD 10          ;STARTING VALUE OF COUNTER
1901          T8DATA:
1902          .WORD T8BFR          ;CHARACTERISTICS DATA BLOCK
1903          .WORD 0          ;ADDRESS OF MESSAGE BUFFER
1904          .WORD 16          ;LENGTH OF MESSAGE BUFFER
1905          .WORD 0.0
1906          T8BFR: .BLKW 8          ;MESSAGE BUFFER
1907
1908          ;+
1909          ;LOCAL TEXT MESSAGES FOR TEST
1910          ;+
1911
1912          126      157      154 T8VCK: .ASCIZ 'Volume Check Bit Not Cleared'
1913          126      157      154 T8NVCK: .ASCIZ 'Volume Check Bit (VCK) Not Clear After Initialize (XST0)'
1914          103      157      156 T8SSR: .ASCIZ 'Contents of TSSR Incorrect After Write Characteristics'
1915          126      157      154 T8TID: .ASCIZ 'Volume Check'
1916          .EVEN
1917          ENDTST
1918
1919          L10064:          TRAP      C$ETST
1920
1921          .SBTTL TEST 9: COMPLETION INTERRUPT
1922
1923          ;
1924          ; THIS TEST VERIFIES THAT AN INTERRUPT IS GENERATED AT THE
1925          ; COMPLETION OF THE WRITE CHARACTERISTICS COMMAND IF THE INTERRUPT
1926          ; ENABLE (IE) BIT IN THE COMMAND HEADER WORD IS SET. THIS TEST
1927          ; CHECKS THE FUNCTIONING OF THE INTERRUPT LOGIC AND BASIC
1928          ; PROCESSING OF THE IE BIT.
1929
1930          ;
1931          ; THE SEQUENCES OF TEST 7 ARE REPEATED, EXCEPT THAT THE INTERRUPT
1932          ; SERVICE ROUTINE IS SET UP TO EXPECT INTERRUPTS AND EACH WRITE
1933          ; CHARACTERISTICS COMMAND IS ISSUED WITH THE IE BIT SET (1). IT
1934          ; IS VERIFIED, WHERE APPROPRIATE, THAT THE IE STATUS BIT IN XST0
1935          ; OF ANY MESSAGE PACKET IS SET AND THAT A COMPLETION INTERRUPT IS
1936          ; GENERATED. FINALLY, A SEQUENCE OF TWO COMMANDS ARE ISSUED, THE
1937          ; FIRST WITH IE=1 AND THE SECOND WITH IE=0. IT IS VERIFIED THAT
1938          ; NO INTERRUPT IS GENERATED AFTER THE SECOND COMMAND AND THAT THE
1939          ; IE BIT IN XST0 IS 0.
1940
1941          ;
1942          BGNTS1
1943
1944          CLR      EXTFEA          ;CLEAR EXTENDED FEATURES SWITCH
1945          MOV      #TST9ID,R0          ;ASCII MESSAGE TO IDENTIFY TEST
1946
1947          005037 002224'
1948          012700 040061'

```

B11

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 9: COMPLETION INTERRUPT

SEQ 131

1949	035046	004777	016322'		JSR	PC,TSTSETUP	DO INITIAL TEST SETUP
1950	035052	012737	000024	002214'	MOV	#20,,LCOPCNT	PERFORM 20 ITERATIONS
1951	035060						
1952	035060				BGN SUB		////////// BEGIN SUBTEST //////////
	035060						T9.1:
	035060	104402					TRAP C1BSUB
1953							
1954	035062	004737	040106'		JSR	PC,T9REST	SET PACKET TO INITIAL VALUES
1955	035066				SETPRI	#PRI00	LOWER PRIORITY TO ALLOW INTERRUPTS
	035066	012700	000000				MOV #PRI00,R0
	035072	104441					TRAP C1SPRI
1956	035074	012703	002762'		MOV	#TSTBLK+10,,R3	START OF TEST DATA
1957	035100	012704	037010'		MOV	#T9PACKET,R4	GET THE ADDRESS OF COMMAND PACKET
1958	035104	012764	000010	000006	MOV	#8,,PKBCNT(R4)	START WITH MINIMUM ALLOWABLE VALUE
1959	035112						
1960	035112						
	035112	104404			BGN SEG		***** BEGIN SEGMENT *****
1961							TRAP C1BSEG
1962	035114	004737	015604'		JSR	PC,SOFINIT	DO SOFT INIT OF CONTROLLER
1963	035120	103405			BCS	101	BR IF SOFT INIT = OK
1967	035122	010001			MOV	R0,R1	SAVE CONTENTS OF TSSR
1968	035124				ERRDF	ERRNO,SFIERR,SFIMSG	DEVICE FATAL ERROR DURING INIT
	035124	104455					TRAP C1ERDF
	035126	001605					.WORD 901
	035130	003644'					.WORD SFIERR
	035132	011644'					.WORD SFIMSG
1969	035134	005037	002220'		CLR	FATFLG	CLEAR FATAL ERROR FLAG
1970	035140	005037	002222'		CLR	INTRECV	CLEAR INTERRUPT RECEIVED FLAG
1971	035144	010465	000000		MOV	R4,TSD8(R5)	SET THE PACKET ADDRESS
1972	035150	004737	016146'		JSR	PC,CHKTSSR	WAIT FOR SSR TO SET
1973	035154	103407			BCS	151	BR IF CARRY SET (GOOD RETURN)
1974	035156	010001			MOV	R0,R1	SAVE CONTENTS OF TSSR
1978	035160				ERRDF	ERRNO,T9SSR,PKTSSR	DEVICE FATAL SSR FAILED TO SET
	035160	104455					TRAP C1ERDF
	035162	001606					.WORD 902
	035164	037527'					.WORD T9SSR
	035166	011656'					.WORD PKTSSR
1979	035170	005237	002220'		INC	FATFLG	SET FATAL ERROR FLAG
1980	035174				CKLOOP		LOOP ON ERROR, IF FLAG SET
	035174	104406					TRAP C1CLP1
1981	035176				ESCAPE	SEG	BY-PASS SUBTEST IF FATAL ERROR
	035176	104410					TRAP C1ESCAPE
	035200	000056					.WORD 100001-
1982	035202	005737	002222'		TST	INTRECV	DID AN INTERRUPT OCCUR ?
1983	035206	001004			BNE	221	BRANCH IF YES
1987	035210				ERRHRD	ERRNO,T9NINT,PKTSSR	
	035210	104456					TRAP C1ERHRD
	035212	001607					.WORD 903
	035214	037616'					.WORD T9NINT
	035216	011656'					.WORD PKTSSR
1988	035220	016501	000002		MOV	TSSR(R5),R1	GET THE CONTENTS OF TSSR
1969	035224	012702	000200		MOV	#SSR,R2	EXPECTED CONTENTS OF TSSR
1990	035230	032701	000100		BIT	#OFL,R1	IS OFF-LINE BIT SET ?
1991	035234	001402			BEQ	251	BRANCH IF NOT OFF LINE
1992	035236	052702	000100		BIS	#OFL,R2	SET OFF LINE IN EXPECTED DATA
1993	035242	020201			CMP	R2,R1	DOES EXPECTED MATCH RECEIVED ?
1994	035244	001404			BEQ	301	OKAY IF MATCH

Year	Line	Address	Offset	Label	Instruction	Comment	Trap	Code
1998	035246	104456		ERRHRD	ERRNO,T9NBA,PKTSSH	INBA NOT ZERO		
	035250	001610					TRAP	C#ERHRD
	035252	037066'					.WORD	904
	035254	011656'					.WORD	T9NBA
1999	035256						.WORD	PKTSSH
2000	035256			ENDSEG		END SEGMENT		
	035256	104405				10000\$:	TRAP	C#ESEG
2001	035260	012364	000006	MOV	(R3)+,PKBCNT(R4)	SET THE TEST WORD		
2002	035264	020327	003060'	CMP	R3,#TBLEND	HAS ALL DATA BEEN TESTED ?		
2003	035270	103002		BHIS	55\$	BRANCH IF ALL DATA DONE		
2004	035272	000137	035112'	JMP	5\$	BRANCH TILL BACK TO ZERO		
2005	035276			ENDSUB		END SUBTEST		
2006	035276					L10066:		
2007	035276	104403					TRAP	C#ESUB
2008	035300	005737	002220'	TST	FATFLG	ANY FATAL ERRORS ?		
2009	035304	001402		BEQ	60\$	BRANCH IF NOT		
2010	035306	004737	017014'	JSR	PC,CKDROP	TRY TO DROP THE UNIT		
2011	035312	033727	037044'	BIT	T9BFR,12,#BIT7	EXTENDED FEATURES SET?		
2012	035320	001402		BEQ	70\$	BR IF NO		
2013	035322	005237	002224'	INC	EXTFEA	SET EXT FEATURE FLAG		
2014	035326							
2015								
2016								
2017								
2018								
2019								
2020								
2021								
2022								
2023								
2024								
2025								
2026	035326			BGN SUB		BEGIN SUBTEST		
	035326					T9.2:		
2027	035326	104402					TRAP	C#BSUB
2028	035330			SETPRI	#PRI00	LOWER PRIORITY TO ALLOW INTERRUPTS		
	035330	012700	000000				MOV	APRICO,R0
	035334	104441					TRAP	C#BPT
2029	035336	012703	037052'	MOV	#T92DATA,R3	START OF TEST DATA FOR SUBTEST		
2030	035342	012704	037010'	MOV	#T9PACKET,R4	GET THE ADDRESS OF COMMAND PACKET		
2031	035346	004737	040106'	JSR	PC,T9REST	RESTORE PACKET TO STARTING VALUES		
2032	035352			BGN SEG		BEGIN SEGMENT		
2033	035352	104404					TRAP	C#BSEG
2034	035354	004737	015604'	JSR	PC,SOFINIT	DO SOFT INIT OF CONTROLLER		
2035	035360	103405		BCS	10\$	BR IF SOFT INIT = OK		
2036	035362	010001		MOV	R0,R1	SAVE CONTENTS OF ISSR		
2037	035364			ERRDF	ERRNO,SFIERR,SFIMSG	DEVICE FATAL ERROR DURING INIT		
	035364	104455					TRAP	C#ERDF
	035366	001611					.WORD	905
	035370	003644'					.WORD	SFIERR

C7

TSV3 GLOBAL AREAS MAC80 M1113 07-FEB-84 10:58
GETSEL - ISSUE MENU AND GET OPERATOR RESPONSE

SEQ 080

```

2742 020156 103352          BCC 11
2743 020160 013700 020316' MOV MENRES,RO      ;GET THE OPERATOR'S REPLY
2744 020166 020001          CMP RO,R1      ;COMPARE TO MAXIMUM ALLOWED
2745 020170 101411          BLOS 51        ;BRANCH IF OK
2746 020170 012746 020314' PRINTF 0MENERR ;DISPLAY ERROR MESSAGE
2747 020174 012746 000001 MOV 0MENERR,-(SP)
2748 020200 010600          MOV 01,-(SP)
2749 020202 104417          MOV SP,RO
2750 020204 062706 000004 TRAP C1PNTF
2751 020210 000735          ADD 04,SP
2752 020212 000207          BR 11          ;RETRY
2753 020214 045 116 045 51: RTS PC        ;RETURN TO CALLER
2754 020214 045 116 045 MENERR: .ASCIZ 'NMA *** Menu Selection Too Large ***'
2755 020262 045 116 045 SELASC: .ASCIZ 'NMT'
2756 020267 105 156 164 MENASC: .ASCIZ 'Enter Menu Selection: '
2757 020316 000000          MENRES: .WORD 0
2758 020316 000000          .SBTTL CHKMAN - CHECK MANUAL INTERVENTION LEGALITY
2759 020316 000000          ;ROUTINE TO TEST FOR MANUAL INTERVENTION LEGALITY.
2760 020316 000000          ;INPUT:
2761 020316 000000          ;NONE.
2762 020316 000000          ;OUTPUT:
2763 020316 000000          ;CARRY 0 MANUAL INTERVENTION NOT ALLOWED
2764 020316 000000          ;1 MANUAL INTERVENTION IS OK
2765 020316 000000          ;SIDE EFFECTS:
2766 020316 000000          ;A MESSAGE IS DISPLAYED WARNING THAT TEST IS
2767 020316 000000          ;NOT EXECUTED IF MANUAL INTERVENTION IS NOT
2768 020316 000000          ;ALLOWED.
2769 020316 000000          ;
2770 020316 000000          ;
2771 020316 000000          ;
2772 020316 000000          ;
2773 020316 000000          ;
2774 020316 000000          ;
2775 020316 000000          ;
2776 020320 000000          CHKMAN:;
2777 020320 000000          SAVREG          ;SAVE THE REGISTERS
2778 020324 104450          MANUAL          ;SEE IF MANUAL INTERVENTION OK
2779 020326 103411          TRAP C1MANI
2780 020330 012746 020354' DCOMPLETE 11 ;BRANCH IF ALLOWED
2781 020334 012746 000001 BCS 11
2782 020340 010600          PRINTF 0NOMAN ;PRINT THE WARNING MESSAGE
2783 020342 104417          MOV 0NOMAN,-(SP)
2784 020344 062706 000004 MOV 01,-(SP)
2785 020350 000241          MOV SP,RO
2786 020352 000207          TRAP C1PNTF
2787 020354 045 116 045 11: CLC          ;CLEAR CARRY FOR ERROR
2788 020354 045 116 045 RTS PC          ;RETURN
2789 020354 045 116 045 NOMAN: .ASCIZ 'NMA *** Manual Intervention not Allowed - Test Aborted ***'
2790 020354 045 116 045 .even

```


PC	PC+4	PC+8	PC+12	PC+16	PC+20	PC+24	PC+28	PC+32	PC+36	PC+40	PC+44	PC+48	PC+52	PC+56	PC+60	PC+64	PC+68	PC+72	PC+76	PC+80	PC+84	PC+88	PC+92	PC+96	PC+100	PC+104	PC+108	PC+112	PC+116	PC+120	PC+124	PC+128	PC+132	PC+136	PC+140	PC+144	PC+148	PC+152	PC+156	PC+160	PC+164	PC+168	PC+172	PC+176	PC+180	PC+184	PC+188	PC+192	PC+196	PC+200	PC+204	PC+208	PC+212	PC+216	PC+220	PC+224	PC+228	PC+232	PC+236	PC+240	PC+244	PC+248	PC+252	PC+256	PC+260	PC+264	PC+268	PC+272	PC+276	PC+280	PC+284	PC+288	PC+292	PC+296	PC+300	PC+304	PC+308	PC+312	PC+316	PC+320	PC+324	PC+328	PC+332	PC+336	PC+340	PC+344	PC+348	PC+352	PC+356	PC+360	PC+364	PC+368	PC+372	PC+376	PC+380	PC+384	PC+388	PC+392	PC+396	PC+400	PC+404	PC+408	PC+412	PC+416	PC+420	PC+424	PC+428	PC+432	PC+436	PC+440	PC+444	PC+448	PC+452	PC+456	PC+460	PC+464	PC+468	PC+472	PC+476	PC+480	PC+484	PC+488	PC+492	PC+496	PC+500	PC+504	PC+508	PC+512	PC+516	PC+520	PC+524	PC+528	PC+532	PC+536	PC+540	PC+544	PC+548	PC+552	PC+556	PC+560	PC+564	PC+568	PC+572	PC+576	PC+580	PC+584	PC+588	PC+592	PC+596	PC+600	PC+604	PC+608	PC+612	PC+616	PC+620	PC+624	PC+628	PC+632	PC+636	PC+640	PC+644	PC+648	PC+652	PC+656	PC+660	PC+664	PC+668	PC+672	PC+676	PC+680	PC+684	PC+688	PC+692	PC+696	PC+700	PC+704	PC+708	PC+712	PC+716	PC+720	PC+724	PC+728	PC+732	PC+736	PC+740	PC+744	PC+748	PC+752	PC+756	PC+760	PC+764	PC+768	PC+772	PC+776	PC+780	PC+784	PC+788	PC+792	PC+796	PC+800	PC+804	PC+808	PC+812	PC+816	PC+820	PC+824	PC+828	PC+832	PC+836	PC+840	PC+844	PC+848	PC+852	PC+856	PC+860	PC+864	PC+868	PC+872	PC+876	PC+880	PC+884	PC+888	PC+892	PC+896	PC+900	PC+904	PC+908	PC+912	PC+916	PC+920	PC+924	PC+928	PC+932	PC+936	PC+940	PC+944	PC+948	PC+952	PC+956	PC+960	PC+964	PC+968	PC+972	PC+976	PC+980	PC+984	PC+988	PC+992	PC+996	PC+1000	PC+1004	PC+1008	PC+1012	PC+1016	PC+1020	PC+1024	PC+1028	PC+1032	PC+1036	PC+1040	PC+1044	PC+1048	PC+1052	PC+1056	PC+1060	PC+1064	PC+1068	PC+1072	PC+1076	PC+1080	PC+1084	PC+1088	PC+1092	PC+1096	PC+1100	PC+1104	PC+1108	PC+1112	PC+1116	PC+1120	PC+1124	PC+1128	PC+1132	PC+1136	PC+1140	PC+1144	PC+1148	PC+1152	PC+1156	PC+1160	PC+1164	PC+1168	PC+1172	PC+1176	PC+1180	PC+1184	PC+1188	PC+1192	PC+1196	PC+1200	PC+1204	PC+1208	PC+1212	PC+1216	PC+1220	PC+1224	PC+1228	PC+1232	PC+1236	PC+1240	PC+1244	PC+1248	PC+1252	PC+1256	PC+1260	PC+1264	PC+1268	PC+1272	PC+1276	PC+1280	PC+1284	PC+1288	PC+1292	PC+1296	PC+1300	PC+1304	PC+1308	PC+1312	PC+1316	PC+1320	PC+1324	PC+1328	PC+1332	PC+1336	PC+1340	PC+1344	PC+1348	PC+1352	PC+1356	PC+1360	PC+1364	PC+1368	PC+1372	PC+1376	PC+1380	PC+1384	PC+1388	PC+1392	PC+1396	PC+1400	PC+1404	PC+1408	PC+1412	PC+1416	PC+1420	PC+1424	PC+1428	PC+1432	PC+1436	PC+1440	PC+1444	PC+1448	PC+1452
----	------	------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------

Address	Offset	Hex	Label	Instruction	Comment	Trap	Code
2228	036166	010001		MOV R0,R1	;SAVE CONTENTS OF TSSR		
2229	036170			ERRDF ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT		
	036170	104455				TRAP	C\$ERDF
	036172	001625				.WORD	917
	036174	003644'				.WORD	SFIERR
	036176	011644'				.WORD	SFIMSG
2230	036200	005037	002222'	10\$: CLR INTRECV	;CLEAR INTERRUPT RECEIVED FLAG		
2231	036204	010337	037024'	MOV R3,T9DATA+4	;INSERT THE BAD MESSAGE LENGTH		
2232	036210	010465	000000	MOV R4,TSD8(R5)	;SET THE PACKET ADDRESS		
2233	036214	004737	016060'	JSR PC,WAITF	;WAIT FOR SSR TO SET		
2234	036220	103405		BCS 15\$	;BR IF CARRY SET (GOOD RETURN)		
2235	036222	010001		MOV R0,R1	;SAVE CONTENTS OF TSSR		
2239	036224			ERRDF ERRNO,T9SSR,PKTSSR	;DEVICE FATAL SSF. FAILED TO SET		
	036224	104455				TRAP	C\$ERDF
	036226	001626				.WORD	918
	036230	037527'				.WORD	T9SSR
	036232	011656'				.WORD	PKTSSR
2240	036234			15\$: CKLOOP	;LOOP ON ERROR, IF FLAG SET		
	036234	104406				TRAP	C\$CLP1
2241	036236			ESCAPE SEG	;BY-PASS SUBTEST IF FATAL ERROR		
	036236	104410				TRAP	C\$ESCAPE
	036240	000056				.WORD	10000\$-
2242	036242	005737	002222'	TST INTRECV	;DID AN INTERRUPT OCCUR ?		
2243	036246	001004		BNE 22\$	;BRANCH IF YES		
2247	036250			ERRHRD ERRNO,T9NINT,PKTSSR			
	036250	104456				TRAP	C\$ERHRD
	036252	001627				.WORD	919
	036254	037616'				.WORD	T9NINT
	036256	011656'				.WORD	PKTSSR
2248	036260	016501	000002	22\$: MOV TSSR(R5),R1	;GET THE CONTENTS OF TSSR		
2249	036264	012702	102206	MOV #SC!SSR!TSREJ!NBA,R2	;EXPECTED CONTENTS OF TSSR		
2250	036270	032701	000100	BIT #OFL,R1	;IS OFF-LINE BIT SET ?		
2251	036274	001402		BEQ 25\$	;BRANCH IF NOT OFF-LINE		
2252	036276	052702	000100	BIS #OFL,R2	;SET OFF-LINE IN EXPECTED DATA		
2253	036302	020201		25\$: CMP R2,R1	;DOES EXPECTED MATCH RECEIVED ?		
2254	036304	001404		BEQ 30\$	;OKAY IF MATCH		
2258	036306			ERRHRD ERRNO,T95REJ,PKTSSR	;COMMAND NOT REJECTED		
	036306	104456				TRAP	C\$ERHRD
	036310	001630				.WORD	920
	036312	037431'				.WORD	T95REJ
	036314	011656'				.WORD	PKTSSR
2259	036316			30\$:			
2260	036316			ENDSEG	;<<<<<<<<<<<<<<<< END SEGMENT <<<<<<<<<<<<<<<<<<		
	036316	104405			10000\$:	TRAP	C\$ESEG
2261	036320	005203					
2262	036322	020327	000014	INC R3	;NEXT BUFFER LENGTH		
2263	036326	002002		CMP R3,#14	;HAVE ALL BAD VALUES BEEN TESTED ?		
2264	036330	000137	036146'	BGE 59\$	;BRANCH IF ALL TESTED		
2265	036334			JMP 5\$	;BRANCH TILL BACK TO ZERO		
2266	036334						
2267	036334			59\$: ENDSUB	;\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\ END SUBTEST \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		
	036334	104403			L10072:	TRAP	C\$ESUB
2268							
2269							

	036504	037616'				.WORD	T9NINT
	036506	011656'				.WORD	PKTSSR
2319	036510	016501	000002	22\$:	MOV TSSR(R5),R1	;GET THE CONTENTS OF TSSR	
2320	036514	012702	000200		MOV #SSR,R2	;EXPECTED CONTENTS OF TSSR	
2321	036520	032701	000100		BIT #OFL,R1	;IS OFF-LINE BIT SET ?	
2322	036524	001402			BEQ 25\$	;BRANCH IF NOT OFF-LINE	
2323	036526	052702	000100		BIS #OFL,R2	;SET OFF-LINE IN EXPECTED DATA	
2324	036532	020201		25\$:	CMP R2,R1	;DOES EXPECTED MATCH RECEIVED ?	
2325	036534	001404			BEQ 30\$	;OKAY IF MATCH	
2329	036536				ERRHRD ERRNO,T9NBA,PKTSSR	;NBA NOT ZERO	
	036536	104456				TRAP	C\$ERHRD
	036540	001634				.WORD	924
	036542	037066'				.WORD	T9NBA
	036544	011656'				.WORD	PKTSSR
2330	036546			30\$:			
2331	036546				ENDSEG	;<<<<<<<<<<<< END SEGMENT <<<<<<<<<<	
	036546					10000\$:	
	036546	104405				TRAP	C\$ESEG
2332							
2333	036550	012364	000006		MOV (R3)+,PKBCNT(R4)	;SET THE TEST WORD	
2334	036554	020327	003060'		CMP R3,#TBLEND	;HAS ALL DATA BEEN TESTED ?	
2335	036560	103002			BHIS 55\$	;BRANCH IF ALL DATA DONE	
2336	036562	000137	036402'		JMP 5\$	;BRANCH TILL BACK TO ZERO	
2337							
2338	036566			55\$:	ENDSUB	;////////// END SUBTEST \\\\\\\	
	036566					L10073:	
	036566	104403				TRAP	C\$ESUB
2339							
2340							
2341				:			
2342				:			
2343				:	TEST 9, SUBTEST 7		
2344				:			
2345				:	TEST WRITE CHARACTERISTICS WITH/WITHOUT INTERRUPTS ENABLED		
2346				:			
2347				:			
2348				:			
2349	036570				BGNBUB	;////////// BEGIN SUBTEST \\\\\\\	
	036570					T9.7:	
	036570	104402				TRAP	C\$BSUB
2350							
2351	036572				SETPRI #PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS	
	036572	012700	000000			MOV #PRI00,R0	
	036576	104441				TRAP	C\$SPRI
2352	036600	012704	037010'		MOV #T9PACKET,R4	;GET THE ADDRESS OF COMMAND PACKET	
2353	036604	004737	040106'		JSR PC,T9RESI	;SET UP A VALID PACKET	
2354	036610	004737	015604'		JSR PC,SOFINIT	;DO SOFT INIT OF CONTROLLER	
2355	036614	103405			BCS 10\$	;BR IF SOFT INIT = OK	
2359	036616	010001			MOV R0,R1	;SAVE CONTENTS OF TSSR	
2360	036620				ERRDF ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT	
	036620	104455				TRAP	C\$ERDF
	036622	001635				.WORD	925
	036624	003644'				.WORD	SFIERR
	036626	011644'				.WORD	SFIMSG
2361	036630	005037	002222'	10\$:	CLR INTRECV	;CLEAR INTERRUPT RECEIVED FLAG	
2362	036634	052714	000200		BIS #BIT7,(R4)	;ENABLE INTERRUPTS	
2363	036640	010465	000000		MOV R4,TSD8(R5)	;SET THE PACKET ADDRESS	

K11

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 9: COMPLETION INTERRUPT

SEQ 140

2364	036644	004737	016146'	JSR	PC,CHKTSSR	;	WAIT FOR SSR TO SET		
2365	036650	103405		BCS	15\$	;	BR IF CARRY SET (GOOD RETURN)		
2366	036652	010001		MOV	R0,R1	;	SAVE CONTENTS OF TSSR		
2370	036654			ERRDF	ERRNO,T9SSR,PKTSSR	;	DEVICE FATAL SSR FAILED TO SET		
	036654	104455						TRAP	C\$ERDF
	036656	001636						.WORD	926
	036660	037527'						.WORD	T9SSR
	036662	011656'						.WORD	PKTSSR
2371	036664			15\$:	CKLOOP	;	LOOP ON ERROR, IF FLAG SET		
	036664	104406						TRAP	C\$CLP1
2372	036666			ESCAPE	SUB	;	BY-PASS SUBTEST IF FATAL ERROR		
	036666	104410						TRAP	C\$ESCAPE
	036670	000102						.WORD	L10074--
2373	036672	005737	002222'	TST	INTRECV	;	DID AN INTERRUPT OCCUR ?		
2374	036676	001004		BNE	22\$	;	BRANCH IF YES		
2378	036700			ERRHRD	ERRNO,T9NINT,PKTSSR				
	036700	104456						TRAP	C\$ERHRD
	036702	001637						.WORD	927
	036704	037616'						.WORD	T9NINT
	036706	011656'						.WORD	PKTSSR
2379	036710			22\$:	CKLOOP	;	LOOP ON ERROR ?		
	036710	104406						TRAP	C\$CLP1
2380									
2381	036712	005037	002222'	CLR	INTRECV	;	CLEAR INTERRUPT RECEIVED FLAG		
2382	036716	042714	000200	BIC	#BIT7,(R4)	;	DISABLE INTERRUPTS		
2383	036722	010465	000000	MOV	R4,TSD9(R5)	;	SET THE PACKET ADDRESS		
2384	036726	004737	016146'	JSR	PC,CHKTSSR	;	WAIT FOR SSR TO SET		
2385	036732	103405		BCS	25\$	;	BR IF CARRY SET (GOOD RETURN)		
2386	036734	010001		MOV	R0,R1	;	SAVE CONTENTS OF TSSR		
2390	036736			ERRDF	ERRNO,T9SSR,PKTSSR	;	DEVICE FATAL SSR FAILED TO SET		
	036736	104455						TRAP	C\$ERDF
	036740	001640						.WORD	928
	036742	037527'						.WORD	T9SSR
	036744	011656'						.WORD	PKTSSR
2391	036746			25\$:	CKLOOP	;	LOOP ON ERROR, IF FLAG SET		
	036746	104406						TRAP	C\$CLP1
2392	036750			ESCAPE	SUB	;	BY-PASS SUBTEST IF FATAL ERROR		
	036750	104410						TRAP	C\$ESCAPE
	036752	000020						.WORD	L10074--
2393	036754	005737	002222'	TST	INTRECV	;	DID AN INTERRUPT OCCUR ?		
2394	036760	001404		BEQ	30\$	;	BRANCH IF NOT		
2398	036762			ERRHRD	ERRNO,T9INT,PKTSSR				
	036762	104456						TRAP	C\$ERHRD
	036764	001641						.WORD	929
	036766	037707'						.WORD	T9INT
	036770	011656'						.WORD	PKTSSR
2399	036772			30\$:					
2400	036772			ENDSUB		;	////////// END SUBTEST //////////		
	036772	104403						L10074:	
								TRAP	C\$ESUB
2401									
2402	036774			EXIT	TST	;	ALL DONE THIS TEST		
	036774	104432						TRAP	C\$EXIT
	036776	001162						.WORD	L10065--
2403									
2404									
2405									

; LOCAL STORAGE FOR THIS TEST

```

; -
;
; BLKB 10-<.-TSV2&7>
T9PACKET: ; COMMAND PACKET FOR TEST
; WORD 100204 ; WRITE CHAR COMMAND, WITH IE, ACK
; WORD T9DATA ; ADDRESS OF CHARACTERISTICS BLOCK
; WORD 0
; WORD 8. ; STARTING VALUE OF BLOCK SIZE
T9DATA: ; CHARACTERISTICS DATA BLOCK
; WORD T9BFR ; ADDRESS OF MESSAGE BUFFER
; WORD 0
; WORD 14. ; LENGTH OF MESSAGE BUFFER
; WORD 0,0
T9BFR: .BLKW 8. ; MESSAGE BUFFER
; +
;
; TEST DATA FOR SUBTEST TWO
;
; DATA HAS FORMAT:
;
; 1ST WORD OFFSET TO TEST WORD IN PACKET
; 2ND WORD BITS TO SET FOR TEST
; -
;
T92DATA:
; WORD 0,BIT5!BIT6!BIT9!BIT10!BIT11!BIT12!BIT13
; WORD 2,BIT0
; WORD 4,BIT6!BIT15
T92DONE=.
; +
;
; LOCAL TEXT MESSAGES FOR TEST
; -
;
T9N9A: .ASCIZ 'WRITE CHARACTERISTICS Command Not Accepted'
T92REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Non-Zero Unused Fields'
T93REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Data Count'
T94REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Block Address'
T95REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Buffer Length'
T9SSR: .ASCIZ 'Contents of ISSR Incorrect After WRITE CHARACTERISTICS'
T9NINT: .ASCIZ 'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
T9INT: .ASCIZ 'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
T9TSBA: .ASCIZ 'Incorrect TSBA Address After WRITE CHARACTERISTICS'
T9T9ID: .ASCIZ 'Completion Interrupt'
; EVEN
; +
;
; ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
;
; -

```

Address	Hex	Dec	Label	Instruction	Comment
2465					
2466	040106				
2467	040106				
2468	040112	012701	037010'		
2469	040116	012721	100204		
2470	040122	012721	037020'		
2471	040126	005021			
2472	040130	012721	000010		
2473	040134	012721	037032'		
2474	040140	005021			
2475	040142	012721	000016		
2476	040146	005021			
2477	040150	005011			
2478	040152	005037	037032'		
2479	040156	000207			
2480	040160				
	040160				
	040160	104401			
2481					
2482					
2483					
2484					
2485					
2486					
2487					
2488					
2489					
2490					
2491					
2492					
2493					
2494					
2495					
2496					
2497	040162				
	040162				
2502	040162	012700	043117'		
2503	040166	004737	016322'		
2504	040172	012737	000024 002214'		
2505	040200				
2506					
2507	040200				
	040200				
	040200	104402			
2508					
2509	040202	004737	043146'		
2510	040206				
	040206	012700	000000		
	040212		104441		
2511	040214	012704	042310'		
2512	040220	012764	000010 000006		
2513	040226				
2514	040226				
	040226	104404			
2515					
2516	040230	004737	015604'		
2517	040234	103405			

N11

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 10: BASIC PACKET PROTOCOL

SEQ 143

Address	Offset	Hex	Label	Op	Op2	Op3	Op4	Op5	Op6	Op7	Op8	Op9	Op10	Op11	Op12	Op13	Op14	Op15	Op16	Op17	Op18	Op19	Op20	Op21	Op22	Op23	Op24	Op25	Op26	Op27	Op28	Op29	Op30	Op31	Op32	Op33	Op34	Op35	Op36	Op37	Op38	Op39	Op40	Op41	Op42	Op43	Op44	Op45	Op46	Op47	Op48	Op49	Op50	Op51	Op52	Op53	Op54	Op55	Op56	Op57	Op58	Op59	Op60	Op61	Op62	Op63	Op64	Op65	Op66	Op67	Op68	Op69	Op70	Op71	Op72	Op73	Op74	Op75	Op76	Op77	Op78	Op79	Op80	Op81	Op82	Op83	Op84	Op85	Op86	Op87	Op88	Op89	Op90	Op91	Op92	Op93	Op94	Op95	Op96	Op97	Op98	Op99	Op100	Op101	Op102	Op103	Op104	Op105	Op106	Op107	Op108	Op109	Op110	Op111	Op112	Op113	Op114	Op115	Op116	Op117	Op118	Op119	Op120	Op121	Op122	Op123	Op124	Op125	Op126	Op127	Op128	Op129	Op130	Op131	Op132	Op133	Op134	Op135	Op136	Op137	Op138	Op139	Op140	Op141	Op142	Op143	Op144	Op145	Op146	Op147	Op148	Op149	Op150	Op151	Op152	Op153	Op154	Op155	Op156	Op157	Op158	Op159	Op160	Op161	Op162	Op163	Op164	Op165	Op166	Op167	Op168	Op169	Op170	Op171	Op172	Op173	Op174	Op175	Op176	Op177	Op178	Op179	Op180	Op181	Op182	Op183	Op184	Op185	Op186	Op187	Op188	Op189	Op190	Op191	Op192	Op193	Op194	Op195	Op196	Op197	Op198	Op199	Op200	Op201	Op202	Op203	Op204	Op205	Op206	Op207	Op208	Op209	Op210	Op211	Op212	Op213	Op214	Op215	Op216	Op217	Op218	Op219	Op220	Op221	Op222	Op223	Op224	Op225	Op226	Op227	Op228	Op229	Op230	Op231	Op232	Op233	Op234	Op235	Op236	Op237	Op238	Op239	Op240	Op241	Op242	Op243	Op244	Op245	Op246	Op247	Op248	Op249	Op250	Op251	Op252	Op253	Op254	Op255	Op256	Op257	Op258	Op259	Op260	Op261	Op262	Op263	Op264	Op265	Op266	Op267	Op268	Op269	Op270	Op271	Op272	Op273	Op274	Op275	Op276	Op277	Op278	Op279	Op280	Op281	Op282	Op283	Op284	Op285	Op286	Op287	Op288	Op289	Op290	Op291	Op292	Op293	Op294	Op295	Op296	Op297	Op298	Op299	Op300	Op301	Op302	Op303	Op304	Op305	Op306	Op307	Op308	Op309	Op310	Op311	Op312	Op313	Op314	Op315	Op316	Op317	Op318	Op319	Op320	Op321	Op322	Op323	Op324	Op325	Op326	Op327	Op328	Op329	Op330	Op331	Op332	Op333	Op334	Op335	Op336	Op337	Op338	Op339	Op340	Op341	Op342	Op343	Op344	Op345	Op346	Op347	Op348	Op349	Op350	Op351	Op352	Op353	Op354	Op355	Op356	Op357	Op358	Op359	Op360	Op361	Op362	Op363	Op364	Op365	Op366	Op367	Op368	Op369	Op370	Op371	Op372	Op373	Op374	Op375	Op376	Op377	Op378	Op379	Op380	Op381	Op382	Op383	Op384	Op385	Op386	Op387	Op388	Op389	Op390	Op391	Op392	Op393	Op394	Op395	Op396	Op397	Op398	Op399	Op400	Op401	Op402	Op403	Op404	Op405	Op406	Op407	Op408	Op409	Op410	Op411	Op412	Op413	Op414	Op415	Op416	Op41
---------	--------	-----	-------	----	-----	-----	-----	-----	-----	-----	-----	-----	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	------

B12

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 10: BASIC PACKET PROTOCOL

SEQ 144

040430	104455					TRAP	C1ERDF
040432	001755					.WORD	1005
040434	042650					.WORD	T10SSR
040436	011656					.WORD	PKTSSR
2568	040440	005237	002220	45:	INC	FATFLG	SET FATAL ERROR FLAG
2569	040444				CKLOOP		LOOP ON ERROR, IF FLAG SET
	040444	104406					TRAP
2570	040446	005737	002222		TST	INTRECV	DID AN INTERRUPT OCCUR ?
2571	040452	001404			BEQ	52:	BRANCH IF NO
2575	040454				ERRHRD	ERRNO,T10INT,PKTSSR	
	040454	104456					TRAP
	040456	001756					.WORD
	040460	043030					.WORD
	040462	011656					.WORD
2576	040464	016501	000002	52:	MOV	TSSR(R5),R1	GET THE CONTENTS OF TSSR
2577	040470	012702	000200		MOV	#SSR,R2	EXPECTED CONTENTS OF TSSR
2578	040474	032701	000100		BIT	#OFL,R1	IS OFF-LINE BIT SET ?
2579	040500	001402			BEQ	55:	BRANCH IF NOT OFF-LINE
2580	040502	052702	000100		BIS	#OFL,R2	SET OFF-LINE IN EXPECTED DATA
2581	040506	020201		55:	CMP	R2,R1	DOES EXPECTED MATCH RECEIVED ?
2582	040510	001404			BEQ	60:	OKAY IF MATCH
2586	040512				ERRHRD	ERRNO,T10MBA,PKTSSR	MBA SET
	040512	104456					TRAP
	040514	001757					.WORD
	040516	042573					.WORD
	040520	011656					.WORD
2587	040522			60:			
2588	040522	013701	042532		MOV	T10BFR,R1	PICK UP THE 1ST WORD OF MESSAGE BUFFER
2589	040526	012702	025252		MOV	#025252,R2	SET UP EXPECTED DATA
2590	040532	020102			CMP	R1,R2	WAS ANY MESSAGE REC'D
2591	040534	001404			BEQ	70:	IF OK (EQUAL)
2595	040536				ERRHRD	ERRNO,T10MBF,EXPREC	MESSAGE BUFFER WAS MODIFIED
	040536	104456					TRAP
	040540	001760					.WORD
	040542	042414					.WORD
	040544	015304					.WORD
2596	040546			70:			
2597	040546	005737	002220		TST	FATFLG	ANY FATAL ERRORS
2598	040552	001403			BEQ	80:	IF NO FATAL ERRORS
2599	040554	004737	017014		JSR	PC,CKDROP	TRY TO DROP THE UNIT
2600	040560				ENDSEG		END SEGMENT
	040560						10001:
	040560	104403					TRAP
2601	040562			80:			C1ESEG
2602	040562				ENDSUB		END SUBTEST
	040562						L10076:
	040562	104403					TRAP
2603							C1ESUB
2604							
2605							
2606							
2607							
2608							
2609							
2610							
2611							
2612							

TEST 10 SUBTEST 2

CHECKS THAT THE MESSAGE BUFFER RELEASE COMMAND WORKS PROPERLY AND THAT THERE IS AN INTERRUPT IF THE "IE" BIT IS SET IN THE COMMAND PACKET AND THE "ERI" BIT IS SET IN THE CHARACTERISTICS DATA PACKET

	2613	040564 040564 040564			BGN SUB	/ / / / / / / / BEGIN SUBTEST / / / / / / / / TIO.2:
			104402			TRAP C#BSUB
	2614	040566	004737	043146'	JSR PC,TIORST	ISET PACKET TO INITIAL VALUES
	2615	040572			SETPRI #PRI00	ILOWER PRIORITY TO ALLOW INTERRUPTS
		040572	012700	000000		MOV #PRI00,R0
		040576	104441			TRAP C#SPRI
	2617	040600	012704	042310'	MOV #TIOPACKET,R4	IGET THE ADDRESS OF COMMAND PACKET
	2618	040604	012764	000010 000006	MOV #B,,PKBCNT(R4)	ISTART WITH MINIMUM ALLOWABLE VALUE.
	2619	040612		5% :	BGN SEG	I>>>>>>> BEGIN SEGMET >>>>>>>
	2620	040612				TRAP C#BSEG
		040612	104404			
	2621	040614	004737	015604'	JSR PC,SOFINIT	IDO SOFT INIT OF CONTROLLER
	2622	040620	103405		BCS 10%	I BR IF SOFT INIT = OK
	2627	040622	010001		MOV R0,R1	ISAVE CONTENTS OF TSSR
	2628	040624			ERRDF ERRNO,SFIERR,SFIMSG	IDEVICE FATAL ERROR DURING INIY
		040624	104455			TRAP C#ERDF
		040628	001761			.WORD 1009
		040630	003644'			.WORD SFIERR
		040632	011644'			.WORD SFIMSG
	2629	040634	005037	002220' 10% :	CLR FATFLG	ICLEAR FATAL ERROR FLAG
	2630	040640	005037	002222' 042326'	CLR INTRECV	ICLEAR INTERRUPT RECEIVED FLAG
	2631	040644	012737	000020	MOV #000020,TIODETA+6	ISET ERI IN CHARACTERISTICS DATA
	2632	040652	010465	000000	MOV R4,TSD8(R5)	ISET THE PACKET ADDRESS
	2633	040656	004737	016146'	JSR PC,CHKTSSR	IWAIT FOR SSR TO SET
	2634	040662	103407		BCS 15%	I BR IF CARRY SET (GOOD RETURN)
	2635	040664	010001		MOV R0,R1	ISAVE CONTENTS OF TSSR
	2639	040666			ERRDF ERRNO,TIOSSR,PKTSSR	IDEVICE FATAL SSR FAILED TO SET
		040666	104455			TRAP C#ERDF
		040670	001762			.WORD 1010
		040672	042650'			.WORD TIOSNR
		040674	011656'			.WORD PKTSSR
	2640	040676	005237	002220'	INC FATFLG	ISET FATAL ERROR FLAG
	2641	040702		15% :	CKLOOP	ILoop ON ERROR, IF FLAG SET
		040702	104406			TRAP C#CLP1
	2642	040704			ESCAPE SEG	IBY-PASS SUBTEST IF FATAL ERROR
		040704	104410			TRAP C#ESCAP
		040706	000016			.WORD 100001-
	2643	040710	005737	002222'	TST INTRECV	IDID AN INTERRUPT OCCUR ?
	2644	040714	001004		BNE 22%	IBRANCH IF YES
	2648	040716			ERRHRD ERRNO,TIONINT,PJTSSR	
		040716	104456			TRAP C#ERRHD
		040720	001763			.WORD 1011
		040722	042737'			.WORD TIONINT
		040724	011656'			.WORD PJTSSR
	2649	040726	016501	000002	MOV TSSR(R5),R1	I GET THE CONTENTS OF TSSR
	2650	040732	012702	000200	Mov #SSR,R2	IEXPECTED CONTENTS CF TSSR
	2651	040736	032701	000100	BIT #OFFL,R1	II OFF-LINE BIT SET ?
	2652	040742	001402		BEQ 25%	IBRANCH IF NOT OFF-LINE
	2653	040744	052702	000100	BIS #OFFL,R2	ISET OFF-LINE IN EXPECTED DATA
	2654	040750	020201		CMP R2,R1	IDoes EXPECTED MATCH RECEIVED ?
	2655	040752	001404	25% :	BEQ 30%	IOKAY IF MATCH
	2659	040754			ERRHRD ERRNO,TIONBA,PJTSSR	INBA NOT ZERO
		040754	104456			TRAP C#ERRHD
		040756	001764			.WORD 1012

PC	PC+4	PC+8	PC+12	PC+16	PC+20	PC+24	PC+28	PC+32	PC+36	PC+40	PC+44	PC+48	PC+52	PC+56	PC+60	PC+64	PC+68	PC+72	PC+76	PC+80	PC+84	PC+88	PC+92	PC+96	PC+100	PC+104	PC+108	PC+112	PC+116	PC+120	PC+124	PC+128	PC+132	PC+136	PC+140	PC+144	PC+148	PC+152	PC+156	PC+160	PC+164	PC+168	PC+172	PC+176	PC+180	PC+184	PC+188	PC+192	PC+196	PC+200	PC+204	PC+208	PC+212	PC+216	PC+220	PC+224	PC+228	PC+232	PC+236	PC+240	PC+244	PC+248	PC+252	PC+256	PC+260	PC+264	PC+268	PC+272	PC+276	PC+280	PC+284	PC+288	PC+292	PC+296	PC+300	PC+304	PC+308	PC+312	PC+316	PC+320	PC+324	PC+328	PC+332	PC+336	PC+340	PC+344	PC+348	PC+352	PC+356	PC+360	PC+364	PC+368	PC+372	PC+376	PC+380	PC+384	PC+388	PC+392	PC+396	PC+400	PC+404	PC+408	PC+412	PC+416	PC+420	PC+424	PC+428	PC+432	PC+436	PC+440	PC+444	PC+448	PC+452	PC+456	PC+460	PC+464	PC+468	PC+472	PC+476	PC+480	PC+484	PC+488	PC+492	PC+496	PC+500	PC+504	PC+508	PC+512	PC+516	PC+520	PC+524	PC+528	PC+532	PC+536	PC+540	PC+544	PC+548	PC+552	PC+556	PC+560	PC+564	PC+568	PC+572	PC+576	PC+580	PC+584	PC+588	PC+592	PC+596	PC+600	PC+604	PC+608	PC+612	PC+616	PC+620	PC+624	PC+628	PC+632	PC+636	PC+640	PC+644	PC+648	PC+652	PC+656	PC+660	PC+664	PC+668	PC+672	PC+676	PC+680	PC+684	PC+688	PC+692	PC+696	PC+700	PC+704	PC+708	PC+712	PC+716	PC+720	PC+724	PC+728	PC+732	PC+736	PC+740	PC+744	PC+748	PC+752	PC+756	PC+760	PC+764	PC+768	PC+772	PC+776	PC+780	PC+784	PC+788	PC+792	PC+796	PC+800	PC+804	PC+808	PC+812	PC+816	PC+820	PC+824	PC+828	PC+832	PC+836	PC+840	PC+844	PC+848	PC+852	PC+856	PC+860	PC+864	PC+868	PC+872	PC+876	PC+880	PC+884	PC+888	PC+892	PC+896	PC+900	PC+904	PC+908	PC+912	PC+916	PC+920	PC+924	PC+928	PC+932	PC+936	PC+940	PC+944	PC+948	PC+952	PC+956	PC+960	PC+964	PC+968	PC+972	PC+976	PC+980	PC+984	PC+988	PC+992	PC+996	PC+1000	PC+1004	PC+1008	PC+1012	PC+1016	PC+1020	PC+1024	PC+1028	PC+1032	PC+1036	PC+1040	PC+1044	PC+1048	PC+1052	PC+1056	PC+1060	PC+1064	PC+1068	PC+1072	PC+1076	PC+1080	PC+1084	PC+1088	PC+1092	PC+1096	PC+1100	PC+1104	PC+1108	PC+1112	PC+1116	PC+1120	PC+1124	PC+1128	PC+1132	PC+1136	PC+1140	PC+1144	PC+1148	PC+1152	PC+1156	PC+1160	PC+1164	PC+1168	PC+1172	PC+1176	PC+1180	PC+1184	PC+1188	PC+1192	PC+1196	PC+1200	PC+1204	PC+1208	PC+1212	PC+1216	PC+1220	PC+1224	PC+1228	PC+1232	PC+1236	PC+1240	PC+1244	PC+1248	PC+1252	PC+1256	PC+1260	PC+1264	PC+1268	PC+1272	PC+1276	PC+1280	PC+1284	PC+1288	PC+1292	PC+1296	PC+1300	PC+1304	PC+1308	PC+1312	PC+1316	PC+1320	PC+1324	PC+1328	PC+1332	PC+1336	PC+1340	PC+1344	PC+1348	PC+1352	PC+1356	PC+1360	PC+1364	PC+1368	PC+1372	PC+1376	PC+1380	PC+1384	PC+1388	PC+1392	PC+1396	PC+1400	PC+1404	PC+1408	PC+1412	PC+1416	PC+1420	PC+1424	PC+1428	PC+1432	PC+1436	PC+1440	PC+1444	PC+1448	PC+1452
----	------	------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------	---------

Address	Offset	Instruction	Comment	Trap	Word
041500	042573				T10NNBA
041502	011656				PKTSSR
2800	041504	10\$:			
2801	041504	013701	042332	MOV T10BFR,R1	;PICK UP THE 1ST WORD OF MESSAGE BUFFER
2802	041510	012702	025252	MOV #025252,R2	;SET UP EXPECTED DATA
2803	041514	020102		CMP R1,R2	;WAS ANY MESSAGE REC'D
2804	041516	001404		BEQ 70\$	;BR, IF OK (EQUAL)
2808	041520			ERRHRD ERRNO,T10MBF,EXPREC	;MESSAGE BUFFER WAS MODIFIED
	041520	104456			TRAP C\$ERRHRD
	041522	002000			.WORD 1024
	041524	042414			.WORD T10MBF
	041526	015304			.WORD EXPREC
2809					
2810	041530	70\$:	CKLOOP		;LOOP ON ERROR IF FLAG SET
	041530	104406			TRAP C\$CLP1
2811					
2812	041532	005037	002222	CLR INTRECV	;CLEAR INTERRUPT RECEIVED FLAG
2813	041536	004737	043146	JSR PC,T10RST	;RESET THE PACKETS AND COMMANDS
2814	041542	042714	100000	BIC #100000,(R4)	;CLEAR THE ACK BIT
2815	041546	010465	000000	MOV R4,TSD8(R5)	;SET THE PACKET ADDRESS
2816	041552	003737	016146	JSR PC,CHKTSSR	;WAIT FOR SSR TO SET
2817	041556	108407		BCS 75\$	;BR IF CARRY SET (GOOD RETURN)
2818	041560	010001		MOV R0,R1	;SAVE CONTENTS OF TSSR
2822	041562			ERRDF ERRNO,T10SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET
	041562	104455			TRAP C\$ERDF
	041564	002001			.WORD 1025
	041566	042650			.WORD T10SSR
	041570	011656			.WORD PKTSSR
2823	041572	005237	002220	INC FATFLG	;SET FATAL ERROR FLAG
2824	041576			CKLOOP	;LOOP ON ERROR, IF FLAG SET
	041576	104406			TRAP C\$CLP1
2825	041600			ESCAPE SEG	;BY-PASS SUBTEST IF FATAL ERROR
	041600	104410			TRAP C\$ESCAPE
	041602	000062			.WORD 10001\$-
2826	041604	005737	002222	TST INTRECV	;DID AN INTERRUPT OCCUR ?
2827	041610	001006		BNE 82\$	;BRANCH IF YES
2831	041612	016500	000002	MOV TSSR(R5),R0	;GET TSSR FOR ERROR REPORT
2832	041616			ERRHRD ERRNO,T10NINT,PKTSSR	
	041616	104456			TRAP C\$ERRHRD
	041620	002002			.WORD 1026
	041622	042737			.WORD T10NINT
	041624	011656			.WORD PKTSSR
2833	041626	016501	000002	82\$:	MOV TSSR(R5),R1
2834	041632	012702	000200		MOV #SSR,R2
2835	041636	032701	000100		BIT #OFL,R1
2836	041642	001402			BEQ 85\$
2837	041644	052702	000100		BIS #OFL,R2
2838	041650	020201		85\$:	CMP R2,R1
2839	041652	001404			BEQ 90\$
2843	041654			ERRHRD ERRNO,T10SSR,PKTSSR	;NBA NOT ZERO
	041654	104456			TRAP C\$ERRHRD
	041656	002003			.WORD 1027
	041660	042650			.WORD T10SSR
	041662	011656			.WORD PKTSSR
2844	041664			90\$:	
2845	041664			ENDSEG	;***** END SEGMENT *****
	041664				10001\$:

Address	Offset	Hex	Label	Instruction	Comment	Trap	Code
2846	041664	005737	002220'	TST	FATFLG	ANY FATAL EPRORS	C\$ESEG
2847	041672	001403		BEQ	95\$	BR, IF NO FATAL ERRORS	
2848	041674	004737	017014'	JSR	PC,CKDROP	TRY TO DROP THE UNIT	
2849							
2850	041700			BGNSEG		BBBBBBBBBBBB BGN SEGMENT BBBBBBBBBBBB	
2851	041700	104404					C\$BSEG
2852	041702	005037	002222'	95\$: CLR	INTRECV	CLEAR INTERRUPT RECEIVED FLAG	
2853	041706	004737	043146'	JSR	PC,T10RST	RESET THE PACKETS AND COMMANDS	
2854	041712	010465	000000	MOV	R4,TSD8(R5)	SET THE PACKET ADDRESS	
2855	041716	004737	016146'	JSR	PC,CHKTSSR	WAIT FOR SSR TO SET	
2856	041722	103407		BCS	100\$	BR IF CARRY SET (GOOD RETURN)	
2857	041724	010001		MOV	R0,R1	SAVE CONTENTS OF TSSR	
2858	041726			ERRDF	ERRNO,T10SSR,PKTSSR	DEVICE FATAL SSR FAILED TO SET	
2859	041726	104455					C\$ERDF
2860	041730	002004					1028
2861	041732	042650'					T10SSR
2862	041734	011656'					PKTSSR
2863	041736	005237	002220'	100\$: INC	FATFLG	SET FATAL ERROR FLAG	
2864	041742			CKLOOP		LOOP ON ERROR, IF FLAG SET	
2865	041742	104406					C\$CLP1
2866	041744			ESCAPE	SEG	BY-PASS SUBTEST IF FATAL ERROR	
2867	041744	104410					C\$ESCAPE
2868	041746	000062					10002\$-
2869	041750	005737	002222'	TST	INTRECV	DID AN INTERRUPT OCCUR ?	
2870	041754	001006		BNE	112\$	BRANCH IF YES	
2871	041756	016500	000002	MOV	TSSR(R5),R0	GET TSSR FOR ERROR REPORT	
2872	041762			ERRHRD	ERRNO,T10INT,PKTSSR		
2873	041762	104456					C\$ERHRD
2874	041764	002005					1029
2875	041766	042737'					T10INT
2876	041770	011656'					PKTSSR
2877	041772	016501	000002	112\$: MOV	TSSR(R5),R1	GET THE CONTENTS OF TSSR	
2878	041776	012702	000200	MOV	SSR,R2	EXPECTED CONTENTS OF TSSR	
2879	042002	032701	000100	BIT	OFFL,R1	IS OFF-LINE BIT SET ?	
2880	042006	001402		BEQ	125\$	BRANCH IF NOT OFF-LINE	
2881	042010	052702	000100	BIS	OFFL,R2	SET OFF-LINE IN EXPECTED DATA	
2882	042014	020201		125\$: CMP	R2,R1	DOES EXPECTED MATCH RECEIVED ?	
2883	042016	001405		BEQ	130\$	OKAY IF MATCH	
2884	042020			ERRHRD	ERRNO,T10SSR,PKTSSR	NBA NOT ZERO	
2885	042020	104456					C\$ERHRD
2886	042022	002006					1030
2887	042024	042650'					T10SSR
2888	042026	011656'					PKTSSR
2889	042030			ENDSEG		BBBBBBBBBBBB END SEGMENT BBBBBBBBBBBB	
2890	042030	104405					10002\$:
2891	042032	005737	002220'	130\$: TST	FATFLG	CHECK FATAL FLAG FOR SET	C\$ESEG
2892	042036	001402		BEQ	140\$	BR, IF NOT SET (OK)	
2893	042040	004737	017014'	JSR	PC,CKDROP	DROP DEVICE IF INDICATED	
2894	042044			140\$:			
2895	042044			ENDSUB		////////// END SUBTEST //////////	
2896	042044	104403					L10100:
2897	042046	</					

2891					;TEST 10 SUBTEST 4	
2892					;	
2893					;CHECKS THAT THE REGISTER MODIFICATION REFUSED (RMR) BIT IN	
2894					;THE TSSR WILL BE SET IF A WRITE CHARACTERISTICS COMMAND	
2895					;BEING EXECUTED AND ANOTHER "WC" COMMAND IS ATTEMPTED	
2896					;	
2897					;-	
2898						
2899	042046			BGNSUB	;/***** BEGIN SUBTEST *****/	
	042046				110.4:	
	042046	104402			TRAP C\$BSUB	
2900						
2901	042050	004737	043220'	JSR PC,T1OR12	;SET SECOND PACKET UP	
2902	042054	004737	043146'	JSR PC,T1ORST	;SET PACKET TO INITIAL VALUES	
2903	042060			SETPRI \$PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS	
	042060	012700	000000		MOV \$PRI00,R0	
	042064	104441			TRAP C\$SPRI	
2904	042066	012704	042310'	MOV \$T1OPACKET,R4	;GET THE ADDRESS OF COMMAND PACKET	
2905	042072	012703	042352'	MOV \$T1OPKT,R3	;GET THE ADDRESS OF 2ND CMD PACKET	
2906	042076	012764	000010 000006	MOV \$B.,PKSCNT(R4)	;START WITH MINIMUM ALLOWABLE VALUE	
2907	042104	012763	000010 000006	MOV \$B.,PKBCNT(R3)	;START WITH MINIMUM ALLOWABLE VALUE	
2908	042112			5\$:		
2909	042112			BGNSEG	;***** BEGIN SEGMENT *****	
	042112	104404			TRAP C\$BSEG	
2910	042114	004737	015604'	JSR PC,SOFINIT	;DO SOFT INIT OF CONTROLLER	
2911	042120	103405		BCS 10\$	;BR IF SOFT INIT = OK	
2915	042122	010001		MOV R0,R1	;SAVE CONTENTS OF TSSR	
2916	042124			ERRDF ERRNO,\$FIERR,\$FIMSG	;DEVICE FATAL ERROR DURING INIT	
	042124	104455			TRAP C\$ERDF	
	042126	002007			.WORD 1031	
	042130	003644'			.WORD \$FIERR	
	042132	011644'			.WORD \$FIMSG	
2917	042134	005037	002220'	10\$: CLR FATFLG	;CLEAR FATAL ERROR FLAG	
2918	042140	005037	002222'	CLR INTRECV	;CLEAR INTERRUPT RECEIVED FLAG	
2919	042144	010465	000000	MOV R4,\$SD8(R5)	;SET THE PACKET ADDRESS	
2920	042150	010365	000000	MOV R3,\$SD8(R5)	;SECOND COMMAND PACKET	
2921	042154	004737	016060'	JSR PC,WAITF	;WAIT FOR SSR TO SET	
2922	042160	016501	000002	MOV TSSR(R5),R1	;GET CONTENTS OF TSSR REGISTER	
2923	042164	032701	000200	BIT \$SSR,R1	;CHECK FOR SSR (TSSR) SET	
2924	042170	001006		BNE 15\$	;BR, IF SSR SET (GOOD)	
2928	042172			ERRDF ERRNO,T1OSSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET	
	042172	104455			TRAP C\$ERDF	
	042174	002010			.WORD 1032	
	042176	042650'			.WORD T1OSSR	
	042200	011656'			.WORD PKTSSR	
2929	042202	005237	002220'	15\$: INC FATFLG	;SET FATAL ERROR FLAG	
2930	042206			CKLOOP	;LOOP ON ERROR, IF FLAG SET	
	042206	104406			TRAP C\$CLP1	
2931	042210			ESCAPE SEG	;BY-PASS SUBTEST IF FATAL ERROR	
	042210	104410			TRAP C\$ESCAPE	
	042212	000056			.WORD 10000\$-	
2932	042214	005737	002222'	TST INTRECV	;DID AN INTERRUPT OCCUR ?	
2933	042220	001004		BNE 22\$	;BRANCH IF YES	
2934						
2935						
2939	042222			ERRHRD ERRNO,T1ONINT,PKTSSR		
	042222	104456			TRAP C\$ERHRD	


```

                                .WORD    1033
                                .WORD    T10NINT
                                .WORD    PKTSSR
;GET THE CONTENTS OF TSSR
;EXPECTED CONTENTS OF TSSR
;IS OFF-LINE BIT SET ?
;BRANCH IF NOT OFF-LINE
;SET OFF-LINE IN EXPECTED DATA
;DOES EXPECTED MATCH RECEIVED ?
;OKAY IF MATCH
;NBA NOT ZERO

                                TRAP      C$ERFRD
                                .WORD    1034
                                .WORD    T10SSR
                                .WORD    PKTSSR

;<<<<<<<<<<<<<<<< END SEGMENT 10000$:
                                TRAP      C$ESEG
;///////////////// END SUBTEST L10101:
                                TRAP      C$ESUB
;ALL DONE WITH THIS TEST
                                TRAP      C$EXIT
                                .WORD    L10075-

;COMMAND PACKET FOR TEST
;WRITE CHAT COMMAND, WITH IE, ACK
;ADDRESS OF CHARACTERISTICS BLOCK
;STARTING VALUE OF BLOCK SIZE
;CHARACTERISTICS DATA BLOCK
;ADDRESS OF MESSAGE BUFFER
;LENGTH OF MESSAGE BUFFER
;MESSAGE BUFFER

;COMMAND PACKET FOR TEST
;WRITE CHAT COMMAND, WITH IE, ACK
;ADDRESS OF CHARACTERISTICS BLOCK
;STARTING VALUE OF BLOCK SIZE
;CHARACTERISTICS DATA BLOCK
;ADDRESS OF MESSAGE BUFFER

```

K12

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 10: BASIC PACKET PROTOCOL

SEQ 153

```

2989 042364 000000          .WORD 0
2990 042366 000016          .WORD 14.          ;LENGTH OF MESSAGE BUFFER
2991 042370 000000 000000    .WORD 0,0
2992
2993 042374          T10BUFR: .BLKW 8.          ;MESSAGE BUFFER
2994
2995          ;+
2996          ;LOCAL TEXT MESSAGES FOR TEST
2997          ;-
2998
2999
3000 042414          115      145      163 T10MBF: .ASCIIZ 'Message Buffer Modified after MESSAGE BUFFER RELEASE Command'
3001 042511          116      102      101 T10NBA: .ASCIIZ 'NBA Not Clear After WRITE CHARACTERISTICS Command'
3002 042573          116      102      101 T10NNBA: .ASCIIZ 'NBA Set After MESSAGE BUFFER RELEASE Command'
3003 042650          103      157      156 T10SSR: .ASCIIZ 'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
3004 042737          105      170      160 T10NINT: .ASCIIZ 'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
3005 043030          125      156      145 T10INT: .ASCIIZ 'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
3006 043117          102      141      163 TST10ID: .ASCIIZ 'Basic Packet Protocol'
3007
3008          .EVEN
3009
3010
3011          ;+
3012          ;ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
3013          ;
3014          ;-
3015
3016
3017 043146          T10RST:
3018 043146          SAVREG          ;SAVE THE REGISTERS
3019 043152 012701 042310' MOV      *T10PACKET,R1          ;START OF THE PACKET
3020 043156 012721 100204' MOV      *100204,(R1)+          ;WRITE CHARACTERISTICS WITH ACK, IE
3021 043162 012721 042320' MOV      *T10DATA,(R1)+          ;ADDRESS OF CHAR DATA BLOCK
3022 043166 005021          CLR      (R1)+          ;EXTENDED ADDRESS
3023 043170 012721 000010' MOV      *8.,(R1)+          ;SIZE OF DATA BLOCK IN BYTES
3024 043174 012721 042332' MOV      *T10BFR,(R1)+          ;ADDRESS OF MESSAGE BUFFER
3025 043200 005021          CLR      (R1)+
3026 043202 012721 000016' MOV      *14.,(R1)+          ;LENGTH OF MESSAGE BUFFER
3027 043206 005021          CLR      (R1)+
3028 043210 005011          CLR      (R1)
3029 043212 005037 042332' CLR      T10BFR          ;CLEAR 1ST LOC IN MESSAGE BUFFER
3030 043216 000207          RTS      PC          ;RETURN
3031
3032          ;+
3033          ;ROUTINE TO RESTORE COMMAND PACKET #2 TO START-UP (DEFAULT) VALUES
3034          ;
3035          ;-
3036
3037 043220          T10RT2:
3038 043220          SAVREG          ;SAVE THE REGISTERS
3039 043224 012701 042352' MOV      *T10PKT,R1          ;START OF THE PACKET
3040 043230 012721 100204' MOV      *100204,(R1)+          ;WRITE CHARACTERISTICS WITH ACK, IE
3041 043234 012721 042362' MOV      *T10DTA,(R1)+          ;ADDRESS OF CHAR DATA BLOCK
3042 043240 005021          CLR      (R1)+          ;EXTENDED ADDRESS
3043 043242 012721 000010' MOV      *8.,(R1)+          ;SIZE OF DATA BLOCK IN BYTES
3044 043246 012721 042374' MOV      *T10BUFR,(R1)+          ;ADDRESS OF MESSAGE BUFFER
3045 043252 005021          CLR      (R1)+

```


L12

TSV5A - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 10: BASIC PACKET PROTOCOL.

SEQ 154

[illegible]

M12

TSVSA - HARDWARE TESTS MACRO M1113 07-FEB-84 10:58
TEST 11: NON-TAPE MOTION COMMANDS

SEQ 155

Address	Hex	Dec	Label	Op	Op	Comment	Trap	Code
3097	043376	005037	002220'	10\$:	CLR	FATFLG		
3098	043376	005037	002220'		CLR	INTRECV		
3099	043402	005037	002222'		MOV	R4,TSDB(R5)		
3100	043406	010465	000000		JSR	PC,CHKTSSR		
3101	043412	004737	016146'		BCS	15\$		
3102	043416	103407			MOV	R0,R1		
3103	043420	010001			ERRDF	ERRNO,T11SSR,PKTSSR		
3107	043422							
	043422	104455					TRAP	C\$ERDF
	043424	002117					.WORD	1103
	043426	045024'					.WORD	T11SSR
	043430	011656'					.WORD	PKTSSR
3108	043432	005237	002220'	15\$:	INC	FATFLG		
3109	043436				CKLOOP			
3110	043436	104406			ESCAPE	SEG		
	043440						TRAP	C\$CLP1
	043440	104410					TRAP	C\$ESCAPE
	043442	000074					.WORD	10000\$-
3111	043444	005737	002222'		TST	INTRECV		
3112	043450	001004			BNE	22\$		
3116	043452				ERRHRD	ERRNO,T11NINT,PKTSSR		
	043452	104456					TRAP	C\$ERHRD
	043454	002120					.WORD	1104
	043456	045154'					.WORD	T11NINT
	043460	011656'					.WORD	PKTSSR
3117	043462	016501	000002	22\$:	MOV	TSSR(R5),R1		
3118	043466	012702	000200		MOV	SSR,R2		
3119	043472	032701	000100		BIT	OFFL,R1		
3120	043476	001402			BEQ	25\$		
3121	043500	052702	000100		BIS	OFFL,R2		
3122	043504	020201		25\$:	CMP	R2,R1		
3123	043506	001404			BEQ	30\$		
3127	043510				ERRHRD	ERRNO,T11NBA,PKTSSR		
	043510	104456					TRAP	C\$ERHRD
	043512	002121					.WORD	1105
	043514	044572'					.WORD	T11NBA
	043516	011656'					.WORD	PKTSSR
3128	043520			30\$:				
3129	043520	004737	010724'	35\$:	JSR	PC,CKRAM		
3130	043524	103405			BCS	59\$		
3134	043526				ERRHRD	ERRNO,PKTRAM,RAMERR		
	043526	104456					TRAP	C\$ERHRD
	043530	002122					.WORD	1106
	043532	004737'					.WORD	PKTRAM
	043534	015320'					.WORD	RAMERR
3135	043536				ENDSEG			
	043536							
	043536	104405					TRAP	C\$ESEG
3136								
3137								
3138	043540			59\$:	ENDSUB			
	043540							
	043540	104403					TRAP	C\$ESUB

PC	Instruction	Comments	Trap Word	Control
043676				C#ESCAPE
043700				100001-
3191 043702	005737	002.222'		
3192 043706	001004			
3196 043710				
043710	104456			C#ERRRD
043712	002126			1110
043714	045154'			T11NINT
043716	011656'			PKTSSR
3197 043720	016501	000002		
3198 043724	012702	100206		
3199 043730	032701	000100		
3200 043734	001402			
3201 043736	052702	000100		
3202 043742	020201			
3203 043744	001404			
3207 043746				
043746	104456			C#ERRRD
043750	002127			1111
043752	044632'			T112REJ
043754	011656'			PKTSSR
3208 043756				
3209 043756	004737	010724'		
3210 043762	103405			
3214 043764				
043764	104456			C#ERRRD
043766	002130			1112
043770	004737'			PKTRAM
043772	015320'			RAMERR
3215 043774				
043774				
043774	104405			
3216				
3217 043776				
043776				
043776	104403			
3218				
3219				
3220				
3221				
3222				
3223				
3224				
3225				
3226				
3227 044000				
044000				
044000	104402			
3228				
3229 044002				
044002	012700	000000		
044006	104441			
3230 044010				
044010	104404			
3231 044012	004737	015604'		
3232 044016	103405			
3236 044020	010001			

Line	Address	Hex	Label	Op	Op2	Op3	Op4	Op5	Op6	Op7	Op8	Op9	Op10	Op11	Op12	Op13	Op14	Op15	Op16	Op17	Op18	Op19	Op20	Op21	Op22	Op23	Op24	Op25	Op26	Op27	Op28	Op29	Op30	Op31	Op32	Op33	Op34	Op35	Op36	Op37	Op38	Op39	Op40	Op41	Op42	Op43	Op44	Op45	Op46	Op47	Op48	Op49	Op50	Op51	Op52	Op53	Op54	Op55	Op56	Op57	Op58	Op59	Op60	Op61	Op62	Op63	Op64	Op65	Op66	Op67	Op68	Op69	Op70	Op71	Op72	Op73	Op74	Op75	Op76	Op77	Op78	Op79	Op80	Op81	Op82	Op83	Op84	Op85	Op86	Op87	Op88	Op89	Op90	Op91	Op92	Op93	Op94	Op95	Op96	Op97	Op98	Op99	Op100	Op101	Op102	Op103	Op104	Op105	Op106	Op107	Op108	Op109	Op110	Op111	Op112	Op113	Op114	Op115	Op116	Op117	Op118	Op119	Op120	Op121	Op122	Op123	Op124	Op125	Op126	Op127	Op128	Op129	Op130	Op131	Op132	Op133	Op134	Op135	Op136	Op137	Op138	Op139	Op140	Op141	Op142	Op143	Op144	Op145	Op146	Op147	Op148	Op149	Op150	Op151	Op152	Op153	Op154	Op155	Op156	Op157	Op158	Op159	Op160	Op161	Op162	Op163	Op164	Op165	Op166	Op167	Op168	Op169	Op170	Op171	Op172	Op173	Op174	Op175	Op176	Op177	Op178	Op179	Op180	Op181	Op182	Op183	Op184	Op185	Op186	Op187	Op188	Op189	Op190	Op191	Op192	Op193	Op194	Op195	Op196	Op197	Op198	Op199	Op200	Op201	Op202	Op203	Op204	Op205	Op206	Op207	Op208	Op209	Op210	Op211	Op212	Op213	Op214	Op215	Op216	Op217	Op218	Op219	Op220	Op221	Op222	Op223	Op224	Op225	Op226	Op227	Op228	Op229	Op230	Op231	Op232	Op233	Op234	Op235	Op236	Op237	Op238	Op239	Op240	Op241	Op242	Op243	Op244	Op245	Op246	Op247	Op248	Op249	Op250	Op251	Op252	Op253	Op254	Op255	Op256	Op257	Op258	Op259	Op260	Op261	Op262	Op263	Op264	Op265	Op266	Op267	Op268	Op269	Op270	Op271	Op272	Op273	Op274	Op275	Op276	Op277	Op278	Op279	Op280	Op281	Op282	Op283	Op284	Op285	Op286	Op287	Op288	Op289	Op290	Op291	Op292	Op293	Op294	Op295	Op296	Op297	Op298	Op299	Op300	Op301	Op302	Op303	Op304	Op305	Op306	Op307	Op308	Op309	Op310	Op311	Op312	Op313	Op314	Op315	Op316	Op317	Op318	Op319	Op320	Op321	Op322	Op323	Op324	Op325	Op326	Op327	Op328	Op329	Op330	Op331	Op332	Op333	Op334	Op335	Op336	Op337	Op338	Op339	Op340	Op341	Op342	Op343	Op344	Op345	Op346	Op347	Op348	Op349	Op350	Op351	Op352	Op353	Op354	Op355	Op356	Op357	Op358	Op359	Op360	Op361	Op362	Op363	Op364	Op365	Op366	Op367	Op368	Op369	Op370	Op371	Op372	Op373	Op374	Op375	Op376	Op377	Op378	Op379	Op380	Op381	Op382	Op383	Op384	Op385	Op386	Op387	Op388	Op389	Op390	Op391	Op392	Op393	Op394	Op395	Op396	Op397	Op398	Op399	Op400	Op401	Op402	Op403	Op404	Op405	Op406	Op407	Op408	Op409	Op410	Op411	Op412	Op413	Op414	Op415	Op416	Op417	Op418	Op419	Op420	Op421	Op422	Op423	Op424	Op425	Op426	Op427	Op428	Op429	Op430	Op431	Op432	Op433	Op434	Op435	Op436	Op437	Op438	Op439	Op440	Op441	Op442	Op443	Op444	Op445	Op446	Op447	Op448	Op449	Op450	Op451	Op452	Op453	Op454	Op455	Op456	Op457	Op458	Op459	Op460	Op461	Op462	Op463	
------	---------	-----	-------	----	-----	-----	-----	-----	-----	-----	-----	-----	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	--

H13

TSV6 - PARAMETER CODING MACRO H1113 07-FEB-84 10:58
TEST 11: NON-TAPE MOTION COMMANDS

SEQ 163

```

1          .TITLE  TSV6 - PARAMETER CODING
7
12
18
19 045442          BGNMOD  TSV6
    045442          TSV6::
20
21
22          .SBTTL  HARDWARE PARAMETER CODING SECTION
23
24          ;++
25          ; THE HARDWARE PARAMETER CODING SECTION CONTAINS MACROS
26          ; THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES.  THE
27          ; MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
28          ; INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES.  THE
29          ; MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
30          ; WITH THE OPERATOR.
31          ;--
32 045442          BGNHRD
    045442          .WORD  L10107-L$HARD/2
    045444          L$HARD::
33
34 045444          GPRMA   HPM1,0,0,160010,177776,YES      ;GET TSBA/TSDB REGISTER ADDRESS.
    045444          .WORD  T$CODE
    045446          .WORD  HPM1
    045450          .WORD  T$LOLIM
    045452          .WORD  T$HILIM
35 045454          GPRMA   HPM2,2,0,0,776,YES              ;GET VECTOR ADDRESS.
    045454          .WORD  T$CODE
    045456          .WORD  HPM2
    045460          .WORD  T$LOLIM
    045462          .WORD  T$HILIM
36          .GPRMD   HPM3,4,0,340,0,7,YES                ;GET INTERRUPT PRIORITY.
37 045464          ENDRD
    045464          .EVEN
    045464          L10107:
38 045464          104      105      126  HPM1:  .ASCIZ  'DEVICE ADDRESS (TSBA/TSDB) '
39 045520          111      116      124  HPM2:  .ASCIZ  'INTERRUPT VECTOR '
40 045544          111      116      124  HPM3:  .ASCIZ  'INTERRUPT PRIORITY '
41          .EVEN
42
43          .SBTTL  SOFTWARE PARAMETER CODING SECTION
44
45          ;++
46          ; THE SOFTWARE PARAMETER CODING SECTION CONTAINS MACROS
47          ; THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES.  THE
48          ; MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
49          ; INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES.  THE
50          ; MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
51          ; WITH THE OPERATOR.
52          ;--
53 045574          BGNSFT
    045574          .WORD  L10110-L$SOFT/2
    045576          L$SOFT::
54          .GPRMD   SPM1,0,-1,YES                        ; GET TRANSPORT TEST FLAG.
55 045576          .GPRMD   SPM4,2,-1,YES                  ; GET ITERATION CONTROL.
    045576          .WORD  T$CODE

```



```

      045600 045634'
      045602 177777
56      ; .WORD SPM4
57      ; .WORD -1
58 045604      ; GPRMD SPM6,4,D,7777,0,7777,YES ; GET LOCAL ERROR LIMIT
      ; GPRMD SPM7,6,D,7777,0,7777,YES ; GET GLOBAL ERROR LIMIT
      ENDSFT
      .EVEN
      045604      L10110:
59
60
61 045604      105      116      101 SPM1: .ASCIZ 'ENABLE TRANSPORT TESTS '
62 045634      111      116      110 SPM4: .ASCIZ 'INHIBIT ITERATIONS '
63      ; SPM6: .ASCIZ 'PER TEST ERROR LIMIT '
64      ; SPM7: .ASCIZ 'PER UNIT ERROR LIMIT '
65      .SBTTL PATCH AREA
66
67      ;
68      ; FINALLY A GENEROUS PATCH AREA.
69      ;
70      ; AND AN ADJUSTMENT TO ACCOUNT FOR THE "LASTAD BIT7" HACK
71      ; DESCRIBED IN "SUPPRG.MEM" (FOR REV C).
72      ;
73
74 045664      PATCH:
75
76      ; .BLKW 32.
77 045664      ; .BLKW 1.
78
79      ; .IF NZ,.E377
80      ; .,.E377+1
81      ; .ENDC
82 045666      LASTAD ;SET LAST USED ADDRESS.
      .EVEN
      .WORD 0
      .WORD 0
      L$LAST:
83 045672      ENDMOD
84      .SBTTL HARD CODED P-TABLE
85
86      ;++
87      ;
88      ;--
88 045672      BGNSETUP 1
89 045672      BGNPTAB
      .WORD 0
      .WORD L10113-1/2-1
      L10111:
90 045676      172522
91 045700      000224
92 045702      000240
93 045704      ENDPTAB
      L10113:
94 045704      ENDSETUP
95
96      000001
      .END

```

M13

TSV6 - PARAMETER CODING MACRO M1113 07-FEB-84 10:58
SYMBOL TABLE

SEQ 168

T8NVCK	034637R	002	T92DAT	037052R	002	WF.IRE	000040	XSOILC	001000	X1.UNC	000002		
T8PACK	034540R	002	T92DON	037066R	002	WF.IWF	000020	XSOLET	020000	X2.BUF	000100		
T8SSR	034730R	002	T92REJ	037141R	002	WF.IWR	000100	XSOMOT	000200	X2.EXT	000200		
T8VCK	034602R	002	T93REJ	037240R	002	WF.I3R	000002	XSONEF	002000	X2.OPM	100000		
T9	035036RG	002	T94REJ	037333R	002	WF.I4R	000001	XSOONL	000100	X2.RCE	040000		
T9BFR	037032R	002	T95REJ	037431R	002	WRICHR	010472RG	002	XSOPEL	000010	X2.REV	000077	
T9DATA	037020R	002	UAM	000200 G		WRIFRR	005105R	002	XSORLL	010000	X2.SPA	035400	
T9INT	037707R	002	UNXTN	002200RG	002	WRIMSG	005050R	002	XSORLS	040000	X2.UNI	000007	
T9LOOP	035060R	002	UNREC	000006		WSMBK	021020RG	002	XSOIMK	100000	X2.WCF	002000	
T9NBA	037066R	002	US	004115R	002	XFERAS	015550R	002	XSOVCK	000020	X3.DCK	000010	
T9NINT	037616R	002	WAJTF	016060RG	002	XNXM	016206R	002	XSOWLE	004000	X3.MBZ	000006	
T9PACK	037010R	002	WC.IFA	000200		XORBF0	007504R	002	XSOWLK	000004	X3.MDE	177400	
T9REST	040106R	002	WC.IFE	000002		XORFOR	007622R	002	XXCOMM	003120RG	002	X3.OPI	000100
T9SSR	037527R	002	WC.IG0	000001		XST0	000006 G		X\$ALWA	000000		X3.REV	000040
T9TSBA	037776R	002	WC.IRE	000010		XST1	000010 G		X\$FALS	000040		X3.RIB	000001
T9.1	035060R	002	WC.IRW	000004		XST2	000012 G		X\$OFFS	000400		X3.SPA	000200
T9.2	035326R	002	WC.IOT	000100		XST3	000014 G		X\$TRUE	000020		X3.TRF	000020
T9.3	035540R	002	WC.IIT	000040		XST4	000016 G		X1.COR	020000		X4.HSP	100000
T9.4	035744R	002	WC.I5R	000020		XSOBOT	000002		X1.DLT	100000		X4.MBZ	017400
T9.5	036132R	002	WF.IED	000010		XSOEOT	000001		X1.MBZ	017375		X4.RCE	040000
T9.6	036336R	002	WF.IER	000004		XSOIE	000040		X1.RBP	000400		X4.TSM	020000
T9.7	036570R	002	WF.IHI	000200		XSOILA	000400		X1.SPA	040000		X4.WRC	000377

. ABS. 000000 000

000000 000

ABS 045704 002

ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 28264 WORDS (111 PAGES)

DYNAMIC MEMORY: 20614 WORDS (79 PAGES)

ELAPSED TIME: 00:36:10

CZTSAA,CZTSAA,SEQ/-SP=SVC/ML,TSV1A,TSV22A,TSV3B,TSV4,TSV5A,TSV6

.....B1	CKMSG2 - COMPARE EX....B5	TEST 4: RAM TESTB9	TEST 11: NON-TAPE M....B13
.....C1	CKMSG2 - COMPARE EX....C5	TEST 5: SECOND INIT....C9	TEST 11: NON-TAPE M....C13
.....D1	PKTMES - PRINT TSSR....D5	TEST 5: SECOND INIT....D9	TEST 11: NON-TAPE M....D13
.....E1	FIFEXP - PRINT FIFO....E5	TEST 5: SECOND INIT....E9	TEST 11: NON-TAPE M....E13
.....F1	MSGSTAT - PRINT STAT....F5	TEST 5: SECOND INIT....F9	TEST 11: NON-TAPE M....F13
.....G1	MSGSUB - PRINT WRITE....G5	TEST 6: COMMAND REJ....G9	TEST 11: NON-TAPE M....G13
.....H1	PRAMPKT - PRINT RAMH5	TEST 6: COMMAND REJ....H9	TEST 11: NON-TAPE M....H13
.....I1	PRMESS - PRINT CONT....I5	TEST 6: COMMAND REJ....I9	SOFTWARE PARAMETER C....I13
.....J1	PRMSGEXP - PRINT EXP....J5	TEST 6: COMMAND REJ....J9	SYMBOL TABLEJ13
.....K1	PRMSGEXP - PRINT EXP....K5	TEST 6: COMMAND REJ....K9	SYMBOL TABLEK13
.....L1	PRBYTEXP - PRINT ERR....L5	TEST 7: WRITE CHARA....L9	SYMBOL TABLEL13
.....M1	RAMERR - PRINT RAMM5	TEST 7: WRITE CHARA....M9	SYMBOL TABLEM13
.....N1	RAMTADD - PRINT TEST....N5	TEST 7: WRITE CHARA....N9	
.....B2	BADSSR - PRINT TSSRB6	TEST 7: WRITE CHARA....B10	
.....C2	SOFINIT - SOFT INITI....C6	TEST 7: WRITE CHARA....C10	
.....D2	CHKAMB - CHECK TSSR....D6	TEST 7: WRITE CHARA....D10	
.....E2	INTR - INTERRUPTE6	TEST 7: WRITE CHARA....E10	
.....F2	CHKTSSR - CHECK TSSR....F6	TEST 7: WRITE CHARA....F10	
.....G2	TSTLOOP - CHECK ITER....G6	TEST 7: WRITE CHARA....G10	
PROGRAM HEADER	TSTSETUP - PRINT TES....H6	TEST 7: WRITE CHARA....H10	
DISPATCH TABLE	TSTEND - PRINT ERRO....I6	TEST 7: WRITE CHARA....I10	
SOFTWARE P-TABLE	CKDROP - CHECK IF U....J6	TEST 7: WRITE CHARA....J10	
SOFTWARE P-TABLE	SETMAP - SETUP PAR6....K6	TEST 7: WRITE CHARA....K10	
GLOBAL EQUATES SECTI....L2	FILLMEM - FILL MEMOR....L6	TEST 8: VOLUME CHEC....L10	
MEMORY MANAGEMENT DE....M2	CHPMEM - COMPARE ME....M6	TEST 8: VOLUME CHEC....M10	
MEMORY MANAGEMENT DE....N2	REGSAV - SAVE R1-R5....N6	TEST 8: VOLUME CHEC....N10	
.....B3	GETPAT - GET 8 BITB7	TEST 9: COMPLETIONB11	
TSU05 REGISTER AND P....C3	GETSEL - ISSUE MENU....C7	TEST 9: COMPLETIONC11	
TSU05 REGISTER AND P....D3	CHKMAN - CHECK MANU....D7	TEST 9: COMPLETIOND11	
TSU05 REGISTER AND P....E3	KTINIT - SETUP KT11....E7	TEST 9: COMPLETIONE11	
TSU05 REGISTER AND P....F3	KTINIT - SETUP KT11....F7	TEST 9: COMPLETIONF11	
TSU05 REGISTER AND P....G3	KTINIT - SETUP KT11....G7	TEST 9: COMPLETIONG11	
SPECIAL MACROS AND O....H3	KTINIT - SETUP KT11....H7	TEST 9: COMPLETIONH11	
GLOBAL DATA SECTIONI3	INITIALIZE SECTIONI7	TEST 9: COMPLETIONI11	
TSTBLK - TEST DATAJ3	INITIALIZE SECTIONJ7	TEST 9: COMPLETIONJ11	
GLOBAL ENVIRONMENT S....K3	INITIALIZE SECTIONK7	TEST 9: COMPLETIONK11	
GLOBAL TEXT MESSAGES....L3	ADD AND DROP UNITS S....L7	TEST 9: COMPLETIONL11	
GLOBAL ERROR REPORTM3	CLEAN-UP AND REPORTM7	TEST 9: COMPLETIONM11	
PRITSSR - PRINT TSSR....N3	CLEAN-UP AND REPORTN7	TEST 10: BASIC PACK....N11	
PRITSSR - PRINT TSSR....B4	CLEAN-UP AND REPORTB8	TEST 10: BASIC PACK....B12	
PRIPKT - PRINT THEC4	TEST 1: BUS RESET T....C8	TEST 10: BASIC PACK....C12	
PRIBXOR - PRINT EXPO....D4	TEST 1: BUS RESET T....D8	TEST 10: BASIC PACK....D12	
PRI XOR - PRINT EXPO....E4	TEST 2: WRAP DATAE8	TEST 10: BASIC PACK....E12	
PRIADD - PRINT MEMO....F4	TEST 2: WRAP DATAF8	TEST 10: BASIC PACK....F12	
PRITADD - PRINT MEMO....G4	TEST 3: WRAP DATAG8	TEST 10: BASIC PACK....G12	
SPACE - SPACE RECO....H4	TEST 3: WRAP DATAH8	TEST 10: BASIC PACK....H12	
WRTCHR - WRITE CHAR....I4	TEST 4: RAM TESTI8	TEST 10: BASIC PACK....I12	
REWIND - POSITION T....J4	TEST 4: RAM TESTJ8	TEST 10: BASIC PACK....J12	
CKRAM - COMPARE RA....K4	TEST 4: RAM TESTK8	TEST 10: BASIC PACK....K12	
CKRAM2 - COMPARE RA....L4	TEST 4: RAM TESTL8	TEST 11: NON-TAPE M....N12	
CKMSG - COMPARE WR....M4	TEST 4: RAM TESTM8		
CKMSG2 - COMPARE EX....N4	TEST 4: RAM TESTN8		