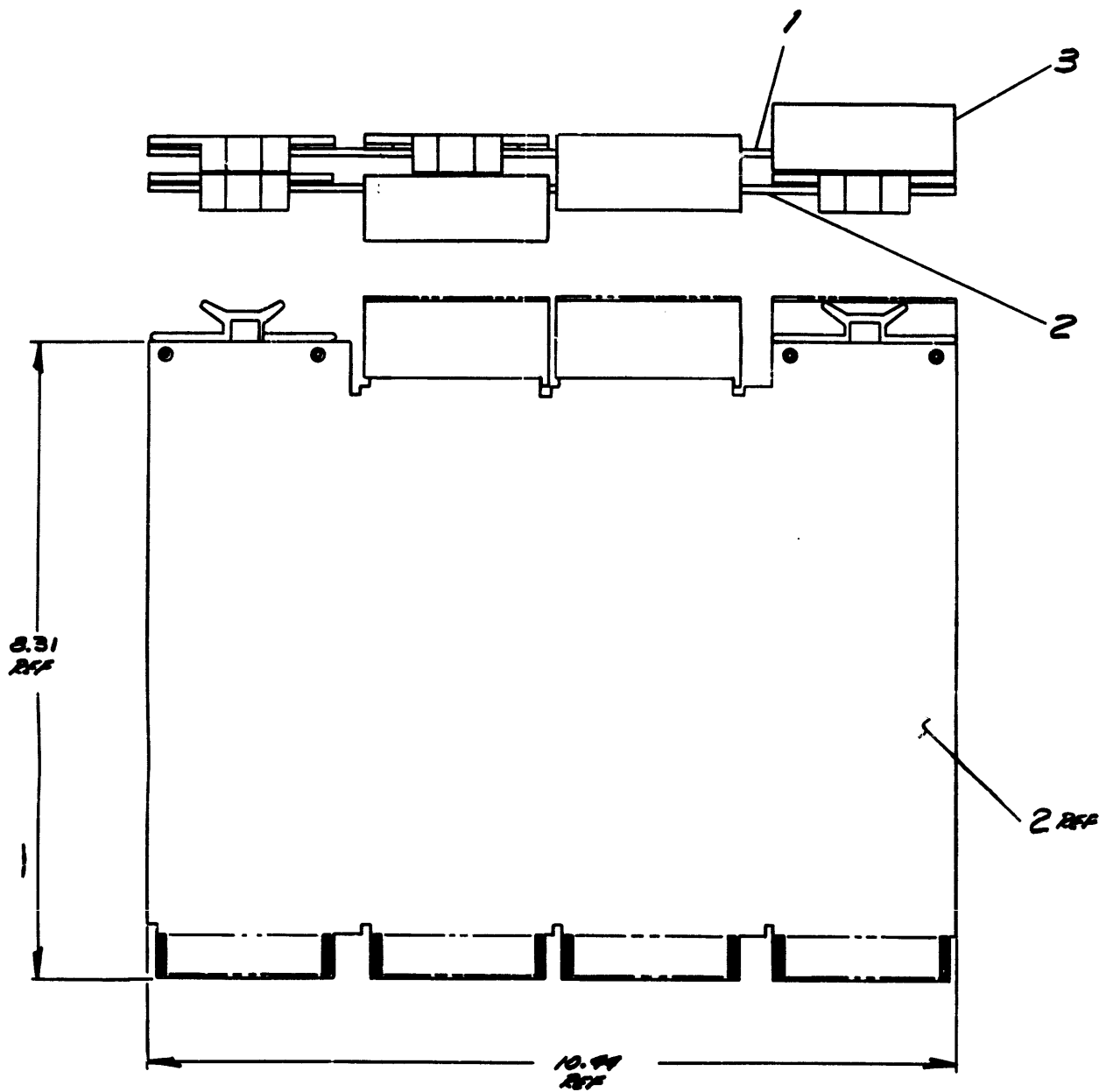


**KE8-E extended
arithmetic element
engineering drawings**

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2 DUA KE8-E-0
REV. 0000 3278



FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP 8-E					
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		PARTS LIST			
DECIMALS	ANGLES	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS			
.XXX = .005	± 0° 30'	TITLE EXTENDED ARITH ELEMENT			
.XX = .02		SIZE CODE DUA KE8-E-0			
.X = .1		NUMBER REV.			
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		SCALE SHEET 1 OF 1			
MATERIAL		NEXT HIGHER ASSY.			
FINISH		DIST. 1			

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

PARTS LIST

MADE BY	KEN GULICK	CHECKED	KEN GULICK	SECTION
DATE	7/29/71	DATE	7/29/71	1
ENG	<i>Radamel H. Chirine</i>	PROD	<i>R. Peters</i>	ISSUED SECT.
DATE	8/18/71	DATE	8/18/71	1

QUANTITY / VARIATION

[illegible]

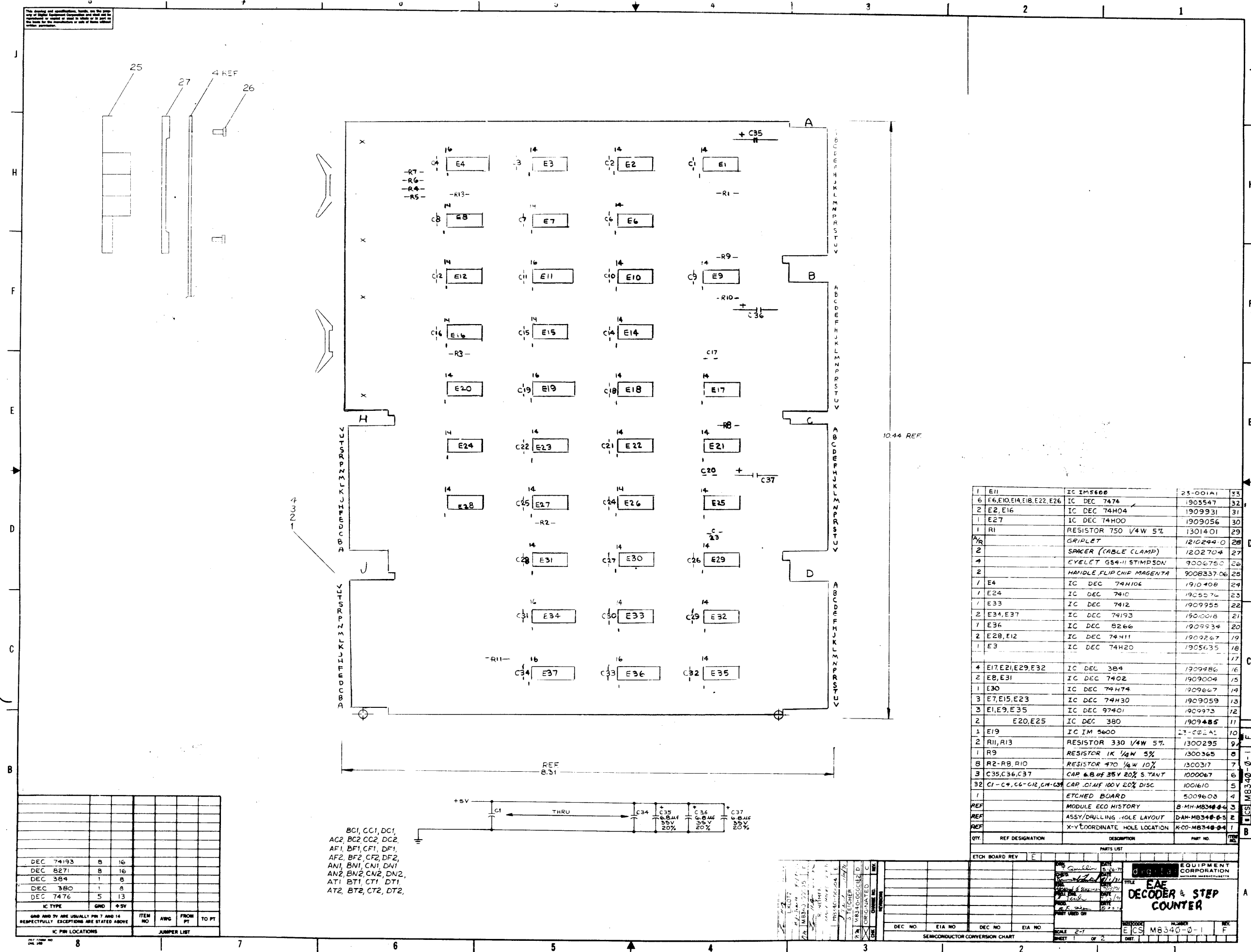
KE-1

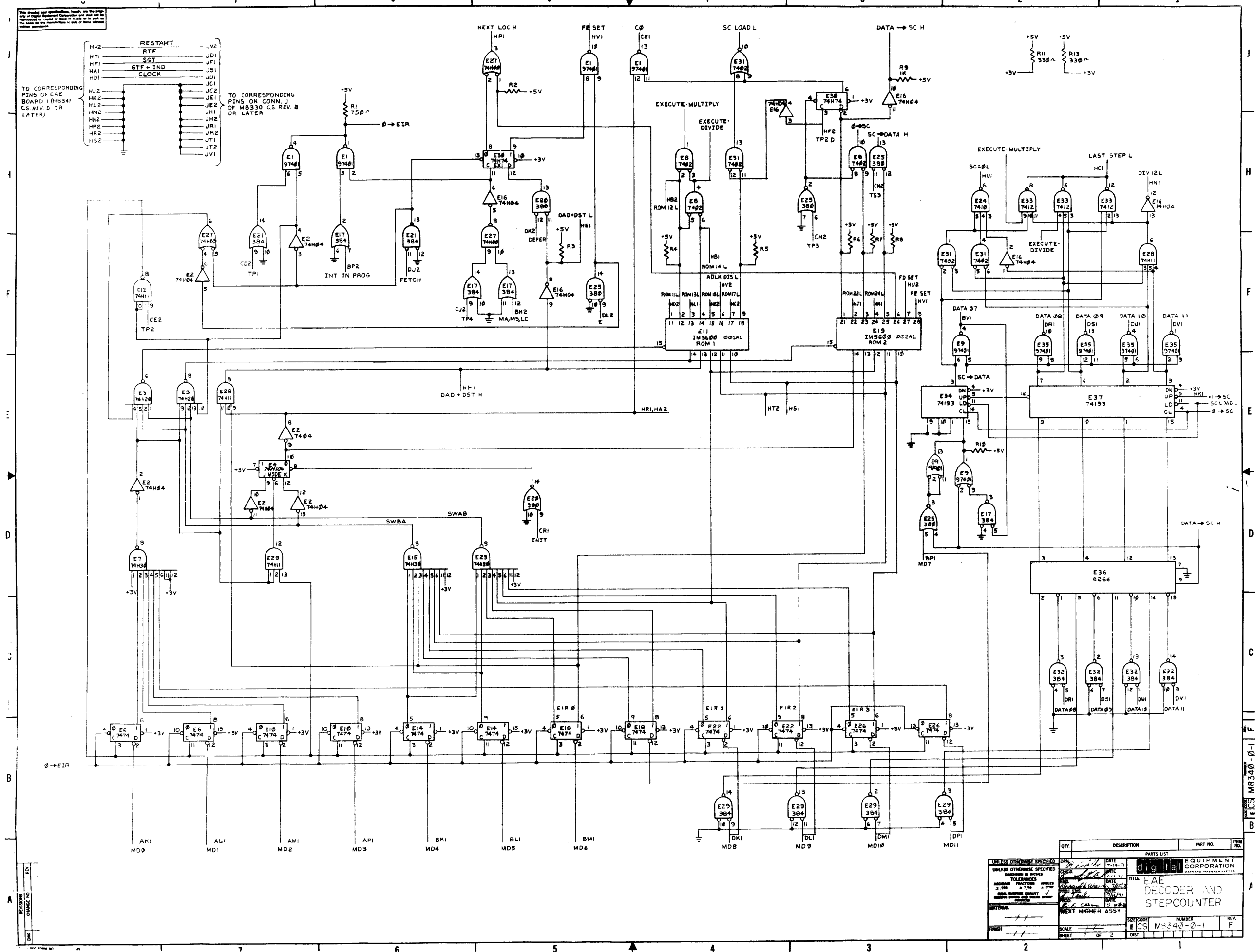
1

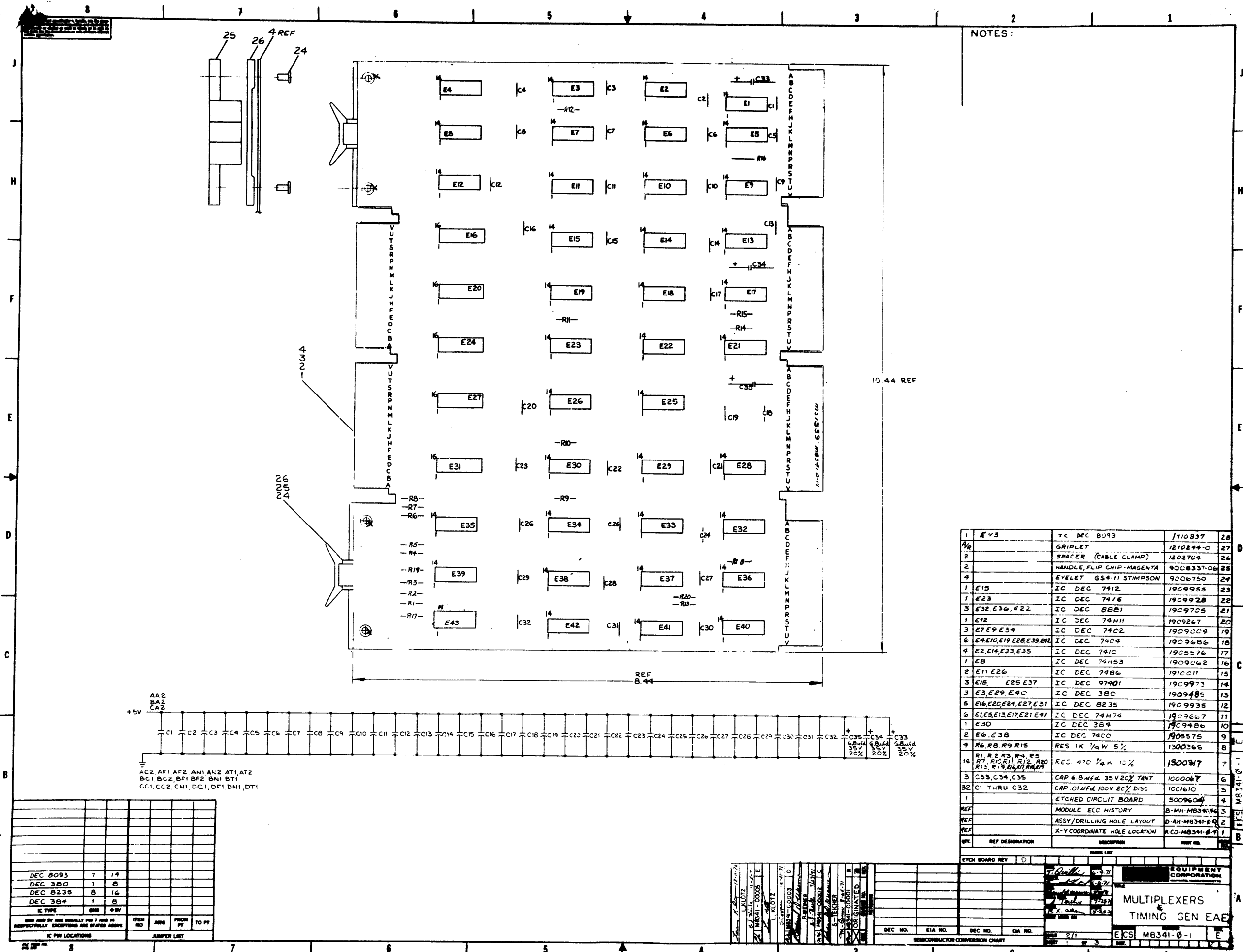
1

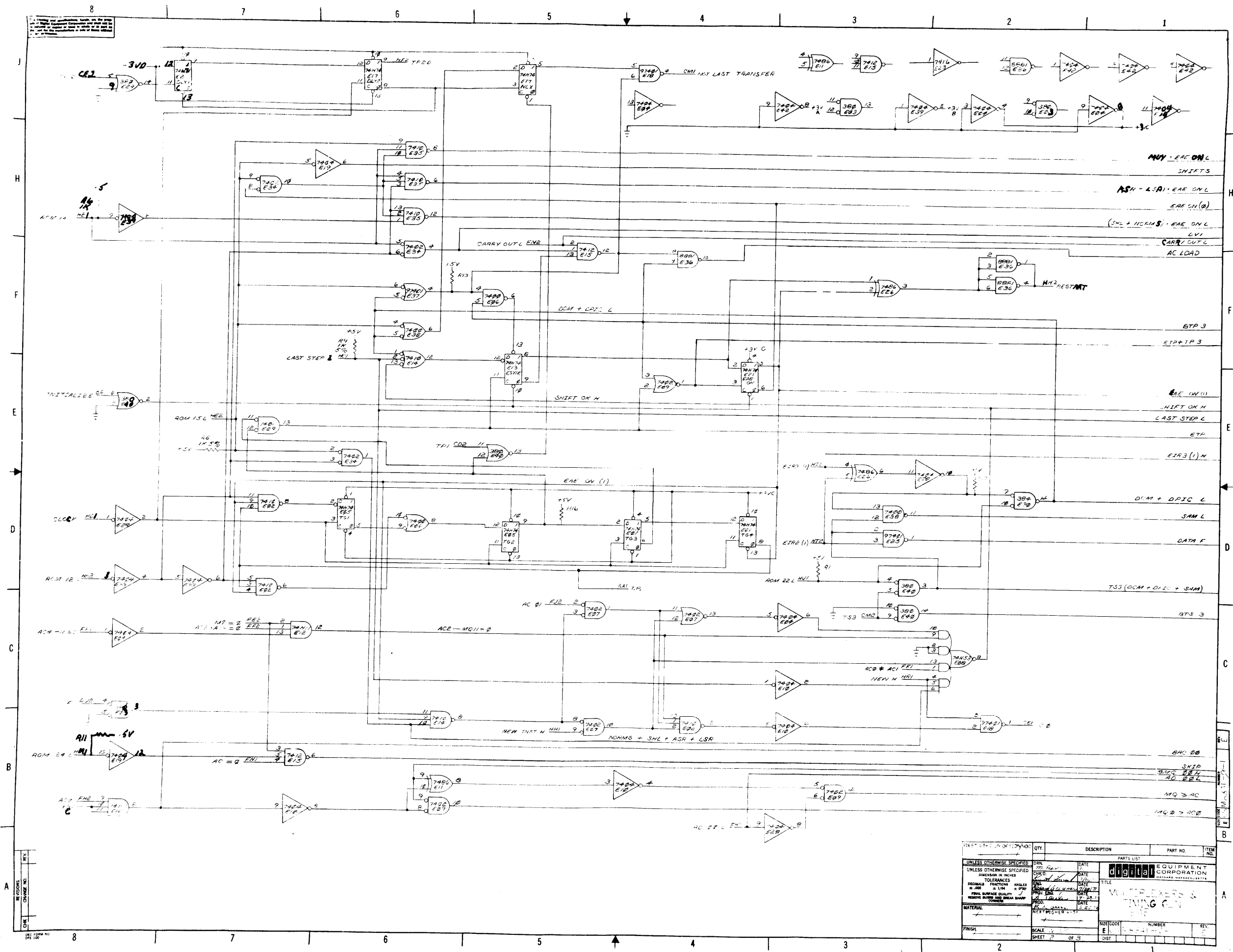
3

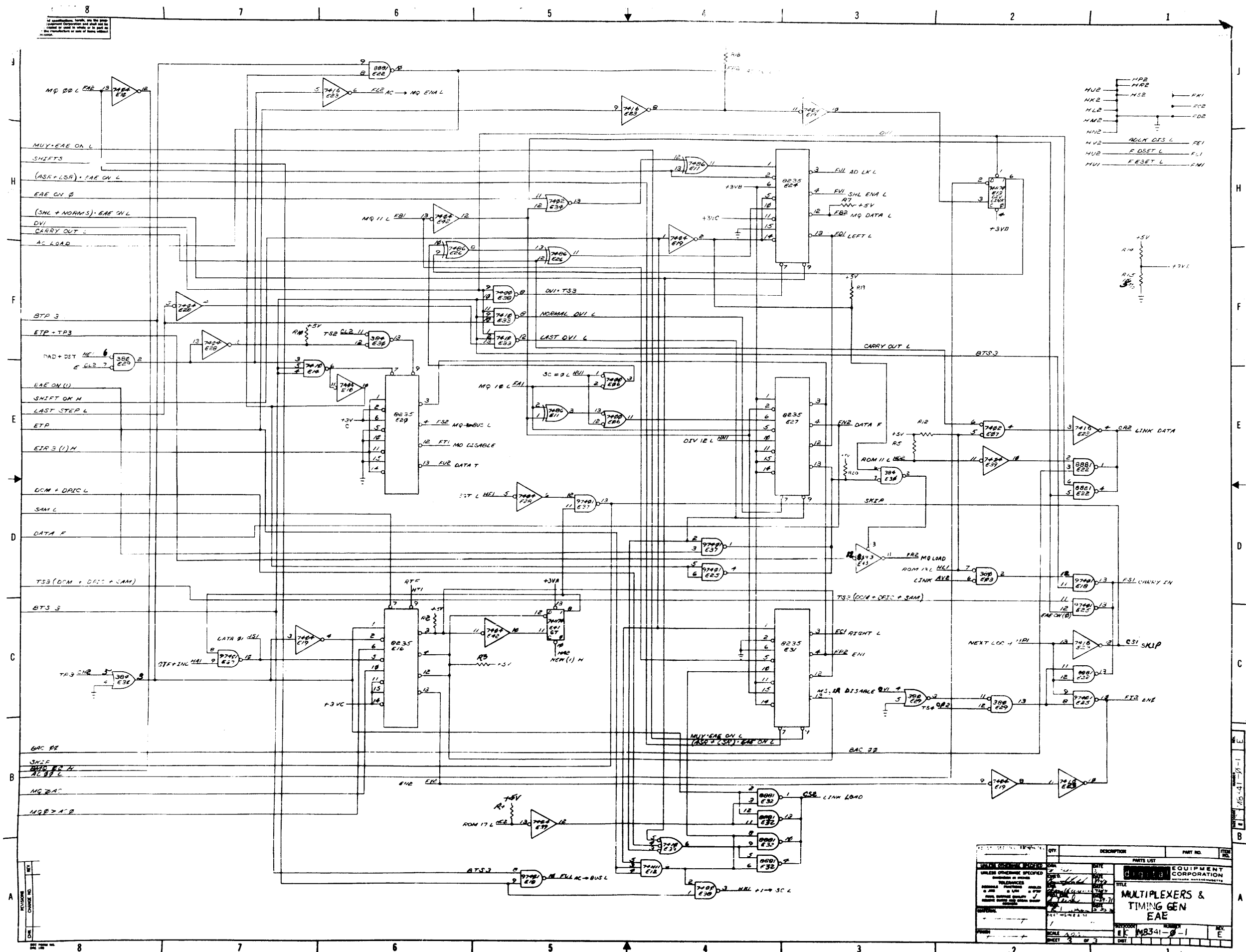
TITLE EXTENDED ARITH ELEMENT	ASSY NO. D-UA-KE8-E-Ø	SIZE A	CODE PL	NUMBER KE8-E-Ø				REV.	ECO NO.
	SHEET 1 OF 1	DIST.							









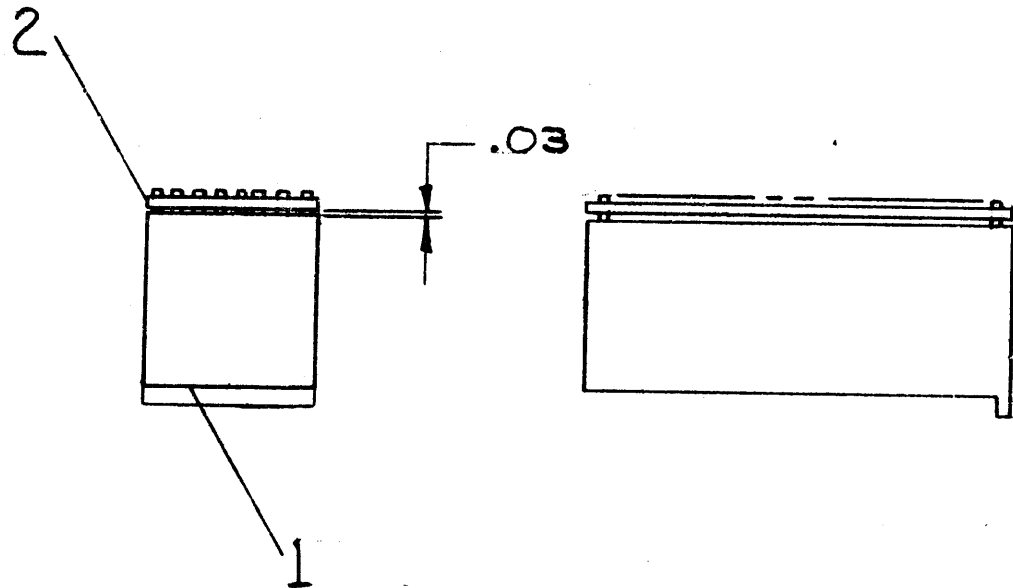


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REV. B
NUMBER 0-0-198HVA
SIZE CODE 0-0-198HVA

2

1



REF	MODULE ECO HISTORY	B-MH-H851-0-6	5
REF	ASSY HOLE LAYOUT	C-AH-H851-0-5	4
REF	CIRCUIT SCHEMATIC	B-CS-H851-0-1	3
1	ETCH BOARD	D-IA-50089030-0	2
1	CONN BLOCK, 72 PIN	1210152	1
QTY.	DESCRIPTION	PART NO.	ITEM NO.

FIRST USED ON OPTION/MODEL
H851

UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
ANGLES
± 0°30'
FINAL SURFACE QUALITY
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL
H

FINISH
H

DRN. H. Flandin
CHK'D. R. Pines
ENG. Jack Prime
PROJ. ENG. Jack Prime
PROD. W. Miller
DATE 7/28/70
DATE 8/13/70
DATE 8-13-70
DATE 8-13-70

NEXT HIGHER ASSY
H

SCALE H
SHEET 1 OF 1

PARTS LIST

digital EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

TITLE
H851
EDGE CONNECTOR

SIZE CODE B UA
NUMBER H851-0-0
REV. B
DIST. G

REVISIONS	CHANGE NO.	REV.
1	H851-00001	A
2	H851-00002	B
3	H851-00003	C
4	H851-00004	D
5	H851-00005	E
6	H851-00006	F
7	H851-00007	G
8	H851-00008	H
9	H851-00009	I
10	H851-00010	J
11	H851-00011	K
12	H851-00012	L
13	H851-00013	M
14	H851-00014	N
15	H851-00015	O
16	H851-00016	P
17	H851-00017	Q
18	H851-00018	R
19	H851-00019	S
20	H851-00020	T
21	H851-00021	U
22	H851-00022	V
23	H851-00023	W
24	H851-00024	X
25	H851-00025	Y
26	H851-00026	Z
27	H851-00027	AA
28	H851-00028	AB
29	H851-00029	AC
30	H851-00030	AD
31	H851-00031	AE
32	H851-00032	AF
33	H851-00033	AG
34	H851-00034	AH
35	H851-00035	AI
36	H851-00036	AJ
37	H851-00037	AK
38	H851-00038	AL
39	H851-00039	AM
40	H851-00040	AN
41	H851-00041	AO
42	H851-00042	AP
43	H851-00043	AQ
44	H851-00044	AR
45	H851-00045	AS
46	H851-00046	AT
47	H851-00047	AU
48	H851-00048	AV
49	H851-00049	AW
50	H851-00050	AX
51	H851-00051	AY
52	H851-00052	AZ
53	H851-00053	BA
54	H851-00054	BB
55	H851-00055	BC
56	H851-00056	BD
57	H851-00057	BE
58	H851-00058	BF
59	H851-00059	BG
60	H851-00060	BH
61	H851-00061	BI
62	H851-00062	BJ
63	H851-00063	BK
64	H851-00064	BL
65	H851-00065	BM
66	H851-00066	BN
67	H851-00067	BO
68	H851-00068	BP
69	H851-00069	BQ
70	H851-00070	BR
71	H851-00071	BS
72	H851-00072	BT
73	H851-00073	BU
74	H851-00074	BV
75	H851-00075	BW
76	H851-00076	BX
77	H851-00077	BY
78	H851-00078	BZ
79	H851-00079	CA
80	H851-00080	CB
81	H851-00081	CC
82	H851-00082	CD
83	H851-00083	CE
84	H851-00084	CF
85	H851-00085	CG
86	H851-00086	CH
87	H851-00087	CI
88	H851-00088	CJ
89	H851-00089	CK
90	H851-00090	CL
91	H851-00091	CM
92	H851-00092	CN
93	H851-00093	CO
94	H851-00094	CP
95	H851-00095	CQ
96	H851-00096	CR
97	H851-00097	CS
98	H851-00098	CT
99	H851-00099	CU
100	H851-00100	CV

[illegible]

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20-8-E-02

NOTES:

1. IF ANY CHANGES ARE TO BE MADE TO THIS PRINT, FIRST CHECK WITH PURCHASE SPEC'S A-PS-23-001A1 AND A-SP-23 002A1.

ROM 1
ENABLE IF MAJOR STATE = F OR E
EIR 3 → A
EIR 2 → B
EIR 1 → C
EIR 0 → NEW → D
EXECUTE → E

FUNCTION	Y1	Y2	Y3	Y4	Y5	Y6	Y7	Y8	OCTAL
F - NOP	1	1	1	1	1	1	1	1	377
F - SCL	1	1	1	1	1	1	1	1	377
F - MUY	1	1	1	1	1	0	0	1	371
F - DVI	1	1	1	1	1	1	1	1	377
F - SHL	0	1	1	0	0	0	1	1	143
F - ASR	1	1	1	1	1	0	0	1	171
F - LSR	1	1	1	1	1	0	0	1	371
F - SCA	1	1	1	1	1	1	1	1	377
F - DAD	1	1	1	1	1	0	0	1	371
F - DST	1	1	1	1	1	1	1	1	377
F - NOP	1	1	1	1	1	1	1	1	377
F - DPSZ	1	1	1	1	1	1	1	1	377
F - DPIC	1	0	0	1	1	0	0	1	231
F - DCM	1	0	0	1	1	0	0	1	231
F - SAM	1	1	0	1	1	0	0	1	331
E - NOP	1	1	1	1	1	1	1	1	377
E - SCL	1	1	1	1	1	1	1	0	376
E - MUY	1	0	1	1	0	1	1	1	267
E - DVI	1	0	1	0	0	0	0	1	241
NOT USED									X
E - SHL	0	1	1	0	0	0	1	0	142
E - ASR	1	1	1	1	0	1	1	0	366
E - LSR	1	1	1	1	0	1	1	0	366
NOT USED									X
E - DAD	1	1	0	1	1	0	0	1	331
E - DST	1	1	1	1	1	1	1	1	377
NOT USED									X
NOT USED									X
NOT USED									X
NOT USED									X

0 INDICATES ACB → LINK DATA AT TS3
0 INDICATES TG SLOW → ADD (NOT MERELY SHIFT)
0 INDICATES CARRY COUPLE AT TS3 (L → CARRY IN, CARRY OUT → DATA)
0 INDICATES LEFT SHIFT
0 INDICATES A SHIFT OPERATION
0 DISABLES CPU ADDER LINK GATING
0 INDICATES LINK LOAD AT TP3
0 INDICATES LOAD SC AT TP2

ROM 2
ENABLE IF MAJOR STATE = F
EIR 3 → A
EIR 2 → B
EIR 1 → C
EIR 0 → D
OLD → E

FUNCTION	Y1	Y2	Y3	Y4	Y5	Y6	Y7	Y8	OCTAL
NOP	1	1	1	1	1	1	1	1	377
ACS	0	1	1	1	1	1	1	1	177
NEW MUY	1	1	0	1	1	0	0	1	331
NEW DVI	1	1	0	1	1	0	0	1	331
NMI	1	1	0	1	1	1	1	1	337
SHL	1	1	1	1	1	0	1	0	372
ASR	1	1	1	1	1	0	1	0	372
LSR	1	1	1	1	1	0	1	0	372
SCA	1	1	1	1	0	1	1	1	367
DAD	1	1	1	1	1	0	0	1	371
DST	1	1	1	1	1	0	0	1	371
NOP	1	1	1	1	1	1	1	1	377
DPSZ	1	1	1	0	1	1	1	1	357
DPIC	1	0	1	1	1	1	1	1	277
DCM	1	0	1	1	1	1	1	1	277
SAM	1	0	1	1	1	1	1	1	277
NOP	1	1	1	1	1	1	1	1	377
SCL	1	1	1	1	1	0	1	0	372
OLD MUY	1	1	0	1	1	0	1	0	332
OLD DVI	1	1	0	1	1	0	1	0	332
NMI	1	1	0	1	1	1	1	1	337
SHL	1	1	1	1	1	0	1	0	372
ASR	1	1	1	1	1	0	1	0	372
LSR	1	1	1	1	1	0	1	0	372
SCA	1	1	1	1	0	1	1	1	367
SCA - SCL	1	1	1	1	0	0	1	0	362
SCA - OLD MUY	1	1	0	1	0	0	1	0	322
SCA - OLD DVI	1	1	0	1	0	0	1	0	322
SCA - NMI	1	1	0	1	0	1	1	1	327
SCA - SHL	1	1	1	1	0	0	1	0	362
SCA - ASR	1	1	1	1	0	0	1	0	362
SCA - LSR	1	1	1	1	0	0	1	0	362

0 INDICATES ACS
0 INDICATES DCM+SAM+DPIC
0 INDICATES 0 → SC AT F • TP3
0 INDICATES DPSZ
0 INDICATES SCA
0 INDICATES MA → 1 → MA, 1 → SKIP
0 INDICATES DSET
0 INDICATES ESET

FIRST USED ON OPTION/MODEL KE8-E	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES	DRN CHK'D DATE 2/1/71	DATE 2/1/71	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
DECIMALS XXX = .005 XX = .02 X = .1	ANGLES +0° 30'	ENG. PROJ. ENG. DATE 2/1/71	TITLE ROM ENCODING	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	PROD. DATE 2/1/71			
MATERIAL	NEXT HIGHER ASSY.			
FINISH	A-ML-KE8-E	SIZE CODE D	NUMBER FD KE8-E-02	REV A
SHEET 1 OF 1		DIST		

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DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE 2/15/72

TITLE KE8E ACCEPTANCE PROCEDURE

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE KE8E ACCEPTANCE PROCEDURE

- Equipment Required
 - PDP-8E or PDP-8M
 - M8340
 - M8341
 - 3-H851's
 - Teletype
 - Maindec-8E-DØLB-D-PB KE8E Instruction Test 1
 - Maindec-8E-DØMB-D-PB KE8E Instruction Test 2
 - Maindec-8E-DØRA-D PB KE8E Extended Memory Exerciser
- Check that the M8340 and M8341 have:
 - Proper circuit revision.
 - Day code.
- Load and run the following diagnostics, consulting the Diagnostic Document for proper operating procedure.
 - Maindec-8E-DØLB for two complete program passes which will be indicated by "KE81" being typed cut twice.
 - Maindec-8E-DØMB for two complete program passes which will be indicated by "KE8E" being typed out twice.
 - Maindec-8E-DØRA for five complete program passes which will be indicated by "KE8EME" being typed out five times. At the beginning of the program, be sure to type the correct value defining the maximum amount of memory.
- Shipping Hardware
 - M8340
 - M8341
 - 3-H8351's
- Shipping Software
 - Libkit-8E-KE8E
 - KE8E Print Set
 - KE8E Maintenance Manual

ENG <i>Louis H. G.</i>	APPD <i>John McQuinn</i>	SIZE A	CODE SP	NUMBER KE8-E-0-4	REV
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DEC 16-(327)-1071-N971
DRA 107

SIZE A	CODE SP	NUMBER KE8-E-0-4	REV
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