

MAINTENANCE CARD



DDT COMMANDS

Format	Description
(CR)	Close period (no action except next command).
(L)N	Open cursor, located open (or seq. cont.) location, or L/N/L.
(N)N	Open cursor location or GPR.
(M)N	Take contents of cursor location, index by contents of PC, and store at location.
(S)	Take contents of cursor location to store one address and store at location.
(T)	Open PC word at location N.
(Z)	Escape the last option.
\$N)O (R)N	Open general register N (R=1 or 2, R/N register).
(R)N (G)	Go to location N and start program.
(H)	Load hardware loader using main device GSP. Command system will be 1.7/2/3/1.
(P)N (P)	Proceed with program execution.
REBUGUI/ DELETE	Enter previous main keyboard character. Response is a backspace.
(N)	Print internal register contents. Take information from CPU address DDT.
XXXXXXXX	where:
N = 0 or 4	Full instruction set (3-40000).
1 or 5	Sequence while getting device info and word.
2 or 6	Sequence during memory refresh.
3	Double bus error (data transfer inconsistent, ignored).
4	Reserved instruction trap.
7	Conditional 1, 2, 3 command.
* Register value data to be displayed full only; 20000 condition, this command must be executed immediately.	

TABLE OF CONTENTS

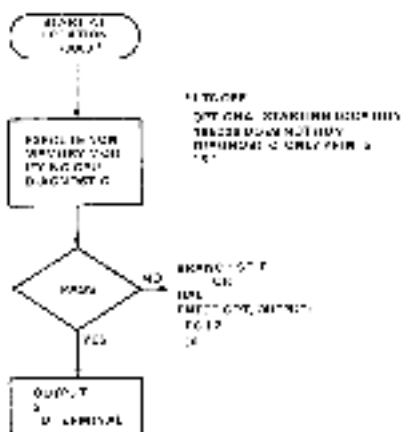
OUT COMMANDS	1
SWITCH REGISTER TO DECIMAL CONVERSION	2
INPUT COMMANDS	3
Multiple Values and Inputs	7
BACKPLANS	8
KID	10
WRITE	12
WRITE LD	17
WRITEA	17
READ	19
READ	17
READ	21
READ	23
READ INSTRUCT. ON GET	25
RESERVED TRAIL AND INTERRUPT VALUES	29
GET ADDRESS CODE	30
PROCESSOR STATUS WORD	30
FILE PLANNING	3

SWITCH REGISTER TO DECIMAL CONVERSION

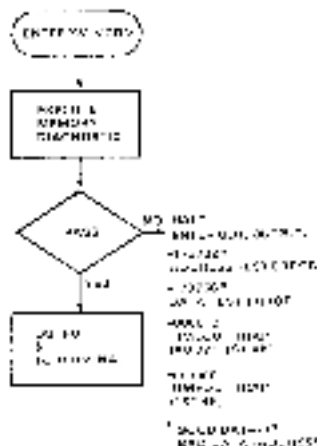


SWITCH X: 7	VALUE
0	0
1	1
2	10
3	100
4	1000
5	10000
6	100000
7	1000000
8	10000000
9	100000000
10	1000000000
11	10000000000
12	100000000000
13	1000000000000
14	10000000000000
15	100000000000000

RFV-1 STARTUP FLOWCHART

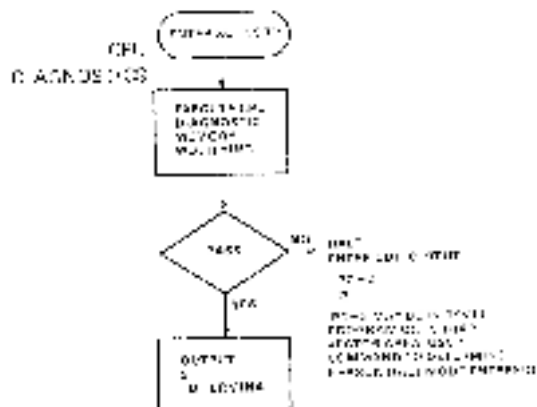


MEMORY DIAGNOSTIC

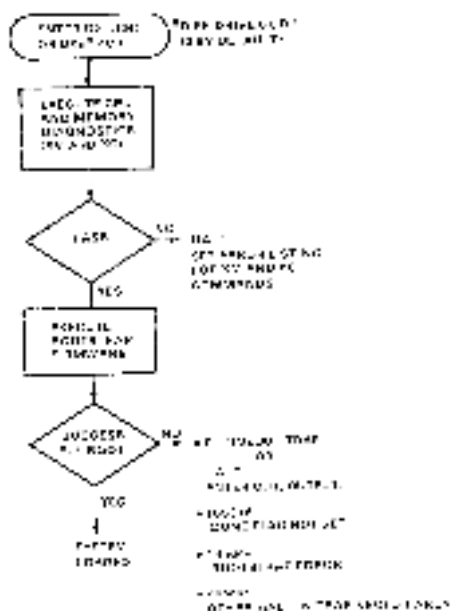


ODT

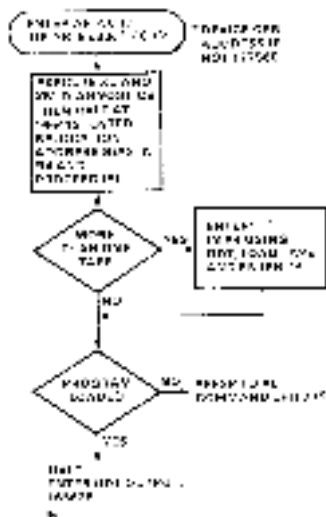




RXV1: 300151E2H



ABSOLUTE LOADER (RELOCATED)



MODULE VERSIONS AND JUMPERS

WITH JFET MODULE	Van - 10000	200 - 10000	300 - 10000	400 - 10000	500 - 10000	600 - 10000	700 - 10000	800 - 10000	900 - 10000	OPTION
W1										OPTION 1
W2										OPTION 2
W3										OPTION 3
W4										OPTION 4
W5										OPTION 5
W6										OPTION 6
W7										OPTION 7
W8										OPTION 8
W9										OPTION 9
W10										OPTION 10

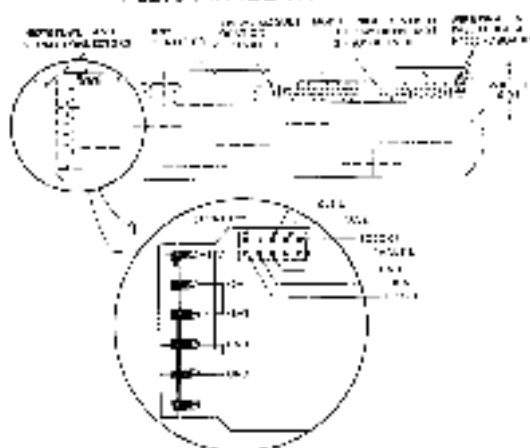
TABLE 10000 (OPTIONAL) JUMPERS TO - 10000

OPTION - 10000 JUMPERS TO - 10000

* FACTORY SET FROM 10000 TO 10000 * - 10000 JUMPERS



FB270 PIN IDENTIFICATION



FB270	
PROCESSOR MODEL	
OPTION 1	OPTION 2
OPTION 3	OPTION 4
OPTION 5	OPTION 6

NOTE: The model and pin numbers are subject to change without notice.

BACKPLANEFS (Cont.)

DDW 1-3

A	B	C	D	E	
← PROCESSOR →					1
OPTION 2		OPTION 1			2
OPTION 3		OPTION 4			3
OPTION 5		OPTION 5			4
OPTION 7		OPTION 8			5
OPTION 10		OPTION 9			6
OPTION 11		OPTION 12			7
OPTION 14		OPTION 13			8
OPTION 15		OPTION 16			9
USER DEFINED SLOTS					

NOTE 1. J1 and J2 require backplane support for
HANGOFF
SDXGICOL

NOTE 2. A processor is required in the adjacent slot for
1 to 15 slots and a limited I/O module and
utilized.

MULTIPLE BACKPLANE CONFIGURATION

NUMBER OF HANGOFF BACKPLANES	BACKPLANE OPTION		
	1st to 2nd	2nd to 3rd	3rd
2	M0400 YE to M0401	to M0400 YE or M0400 YE	
3	M0400 YE to M0401	M0400 YE to M0401	to M0400 YE or M0400 YE

NOTE: XX denotes sublength. 0040b XX and 0041a XX refers
to 1000 and 1000 mm difference between four feet.

KD17: PROCESSOR (M7264)



M251 EICHREV 1 (AND AFF)



M254 PTO: 125 2, 2-INT (1) NO - SED:

DATA IDENTIFICATION

	VC E/G/F/B	EIS/FIS	D-BOL/FIS
NI WOS	KD11 H	KD11-A	KD11 G
WOS	KD11 F	KD11-I	KD11 P

K111 PROCESSOR (Cont.)

ALL FUNCTIONAL UNITS

REGISTER MEMORY ADDRESSING

FUNCTIONAL

INSTRUMENT

bank	Addr	W2
0	0	1
1	1	0
Not addressed	2	0

Pin	W2
Pin 1	0
Pin 2	1

PROCESSOR RESET

OVERVIEW

	W2
Enable	0
Disable	1

W2	W2	W2
0 to 24	0	0
25 to 31	0	0
32 to 39	0	0
40 to 47	0	0

Reset Mode = Factory Set

FUNCTIONS AND ABOVE ONLY UNITS

	W2	Enable	Disable
FUNCTIONS	W2	FACTORY	FACTORY
ON BOARD PROCESSOR			
MEMORY			
REFRESH	W2	0	1
REFRESH	W2	0	1
MEMORY SELECT	W2	1	0

WIND AND SEA KNOCKY STARTING AFTER 0800 GMT 15 AND 16 APRIL

METS (3)			T2D (10)			METS (14)				CVD	CVD RISK FACTORS
METS			T2D			METS					
MI	FA	NO	DI	DI	DI	MI	MI	MI	MI		
1		1	0	0	0		0	0	0	1	1
		1	1	0	0	1	0	0	0	1	1
1	1		0	1	0		0	0	0	1	1
1	1	1	1	1	0		0	0	0	1	1
0			0	0	1		1	0	0	1	1
0			1	0	1		1	0	0	1	1
0	1		0	1	1	1	1	1	0	0	1
0	1	1	0	1	1	1	1	1	0	0	1
0	1	1	1	1	1	1	1	1	0	0	1

$$\frac{d}{dt} \left(\frac{\partial L}{\partial \dot{x}} \right) = \frac{\partial L}{\partial x}$$

1	1 2 3 4 5 6 7 8 9 10 11 12	11	12 13 14 15
12	1 2 3 4 5 6 7 8 9 10 11 12	2	12 13 14 15

5.21 4.0 MEKOPY 1.01

5.71	CM	AD	6.72	2016
10k	1	1		1
2X	1			1
5X		1	2	0
7K	1	2		0

W1, 2, 3, and Reference (see p. 100).
 100-101: Microcomputer
 102-103: (See also W1, 2, 3, 100-101)
 104-105: (See also W1, 2, 3, 100-101)

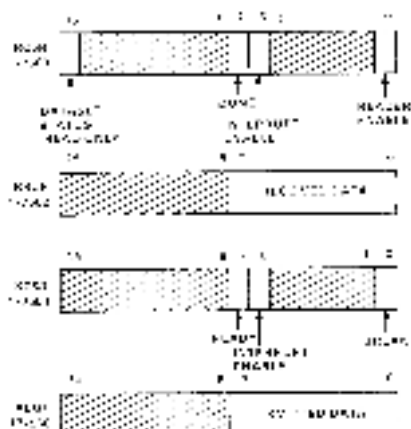
POLYMER LETTERS

[illegible][illegible]

Figure 1 is a schematic diagram of the experimental setup. It shows a horizontal tube with several components. From left to right, there is an 'INLET' section with a valve, a 'HEATER' section with a heating coil, a 'METER' section with a flow meter, and an 'OUTLET' section with a valve. A pressure tap is located between the heater and meter sections. A dashed line indicates a cross-section of the tube.

and $\epsilon = 10^{-6}$ (HSLA = 10000, 177775, 1.444444, 1.444444).

6. 1.12.2018. 08:40:18. 1. 12.2018. 08:40:18





XMTR/RECEIVE MODE

CURRENT LOGS Active C_1,2,3 / ALL INSERTED
By who

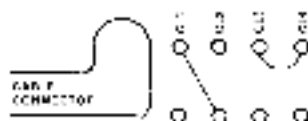


TABLE 12.11: SUMMARY OF RESULTS

LAMBDA RATE	Fb				HARDY RATE	FR			
	0	1	2	3		C	1	2	3
50	1	F		1	600	1	0	0	1
75		1		1	1000	0	0	1	0
110	1	0	0	1	1800	1	0	1	0
134.5		1	0	1	2400	0	0	0	1
150		0	0	0		1		0	0
200	0	1	0	1	4000	0	1	1	0
374	0		0	0	5600	1	1	1	0
					1200	-	1	-	0
					1900, 3010				

DLV11 (Cont.)

PARITY		BRK		
	OFF	ON		
ENABLE	1		ENABLE	1
OFF	0		DISABLE	0
DISABLE		0		

DATA FORMAT

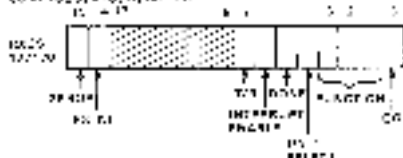
NO. OF DATA BITS	MSB	LSB	NO. OF STOP BITS	STOP
5	1	1	1	1
6	1	1	2	1
7	1	1		
8	1	1		

- NOTE: * FOR LT33 (ANALOG) or LT33 (DIGITAL), monitoring of signals between TP1-TP2.
 * VIA CABLE - BONDING
 * 70mA CURRENT LOOP CABLE - BONDING

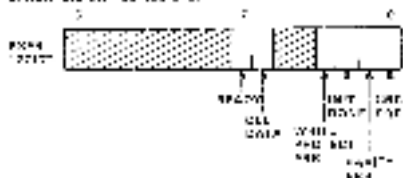
DCL

COMMAND	FUNCTIONS	COMMAND PROTOCOL
0001	Enter Data	See Table
0002	Get Buffer	1. Issue command and ID
0003	Write Buffer	2. Wait for done
0004	Write Command	3. Check buffer for done
0101	Test Device	Feed Definition from Data
1000	Not used	1. Issue buffer and ID
1001	Read Buffer	2. Wait for done
1100	Write Buffer with related data	3. Check buffer for related data
1101	Read Error Code	

CONTROL STATUS REGISTER



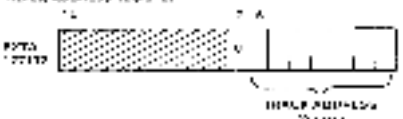
ADDRESS REGISTER



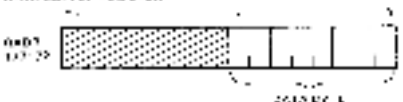
ADDRESS REGISTER (AR)



ADDRESS REGISTER (AR)



ADDRESS REGISTER (AR)



FREE COPIES AND MEANINGS

[illegible]

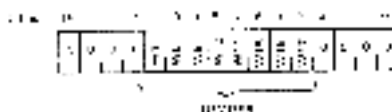
182110312 4521912-7 62

*18 00 09 15:07:30.000000000

[illegible]

1. ADDRESS (REV 0)

2. 10 11 12 13 14 15 16 17 18 19 20 21

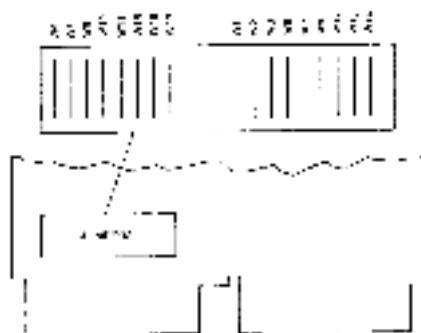


3. 10 11 12 13 14 15 16 17 18 19 20 21



4. 10 11 12 13 14 15 16 17 18 19 20 21

5. 10 11 12 13 14 15 16 17 18 19 20 21



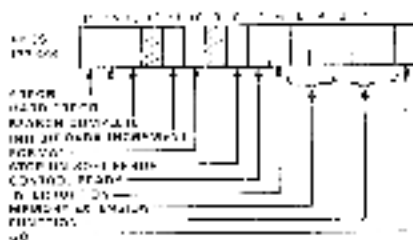
REV 1

HSM-1 (Cont.)

ADDRESS REGISTER



CONTROL REGISTER



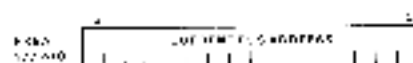
DATA REGISTER



COUNTER REGISTER



CURRENT ADDRESS REGISTER



7-028 BTJ-114 PB-J18 48



U.S. EPA, 2002; WHO, 2002; WHO, 2003).

1. *in situ* polymerization

[illegible]

DEPARTMENT OF THE ARMY

0000-0001-9300-6417

06170530050 123676113

ACLS SYNDROME: 2, 4, 4, 2, 2

△ 5: 51000000 4.15%

Abstract. $\mathcal{H}^1$ and $\mathcal{H}^2$ are the Hardy spaces of holomorphic functions on the unit disk $\mathbb{D}$. We study the following problem: given a function $f \in \mathcal{H}^1$, does there exist a function $g \in \mathcal{H}^2$ such that fg is a constant function? We show that the answer is affirmative if and only if f is a constant function.

● 2013 年 12 月 1 日起, 凡在《中国药典》2010 年版二部收载的药品, 其说明书中凡有“不良反应”、“禁忌”、“注意事项”、“慎用”、“慎用”、“慎用”等字样, 均须按照《中国药典》2010 年版二部附录 X 的有关规定, 进行修订。

1110 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105 106 107 108 109 110 111 112 113 114 115 116 117 118 119 120 121 122 123 124 125 126 127 128 129 130 131 132 133 134 135 136 137 138 139 140 141 142 143 144 145 146 147 148 149 150 151 152 153 154 155 156 157 158 159 160 161 162 163 164 165 166 167 168 169 170 171 172 173 174 175 176 177 178 179 180 181 182 183 184 185 186 187 188 189 190 191 192 193 194 195 196 197 198 199 200 201 202 203 204 205 206 207 208 209 210 211 212 213 214 215 216 217 218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 233 234 235 236 237 238 239 240 241 242 243 244 245 246 247 248 249 250 251 252 253 254 255 256 257 258 259 260 261 262 263 264 265 266 267 268 269 270 271 272 273 274 275 276 277 278 279 280 281 282 283 284 285 286 287 288 289 290 291 292 293 294 295 296 297 298 299 300 301 302 303 304 305 306 307 308 309 310 311 312 313 314 315 316 317 318 319 320 321 322 323 324 325 326 327 328 329 330 331 332 333 334 335 336 337 338 339 340 341 342 343 344 345 346 347 348 349 350 351 352 353 354 355 356 357 358 359 360 361 362 363 364 365 366 367 368 369 370 371 372 373 374 375 376 377 378 379 380 381 382 383 384 385 386 387 388 389 390 391 392 393 394 395 396 397 398 399 400 401 402 403 404 405 406 407 408 409 410 411 412 413 414 415 416 417 418 419 420 421 422 423 424 425 426 427 428 429 430 431 432 433 434 435 436 437 438 439 440 441 442 443 444 445 446 447 448 449 450 451 452 453 454 455 456 457 458 459 460 461 462 463 464 465 466 467 468 469 470 471 472 473 474 475 476 477 478 479 480 481 482 483 484 485 486 487 488 489 490 491 492 493 494 495 496 497 498 499 500 501 502 503 504 505 506 507 508 509 510 511 512 513 514 515 516 517 518 519 520 521 522 523 524 525 526 527 528 529 530 531 532 533 534 535 536 537 538 539 540 541 542 543 544 545 546 547 548 549 550 551 552 553 554 555 556 557 558 559 560 561 562 563 564 565 566 567 568 569 570 571 572 573 574 575 576 577 578 579 580 581 582 583 584 585 586 587 588 589 590 591 592 593 594 595 596 597 598 599 600 601 602 603 604 605 606 607 608 609 610 611 612 613 614 615 616 617 618 619 620 621 622 623 624 625 626 627 628 629 630 631 632 633 634 635 636 637 638 639 640 641 642 643 644 645 646 647 648 649 650 651 652 653 654 655 656 657 658 659 660 661 662 663 664 665 666 667 668 669 670 671 672 673 674 675 676 677 678 679 680 681 682 683 684 685 686 687 688 689 690 691 692 693 694 695 696 697 698 699 700 701 702 703 704 705 706 707 708 709 710 711 712 713 714 715 716 717 718 719 720 721 722 723 724 725 726 727 728 729 730 731 732 733 734 735 736 737 738 739 740 741 742 743 744 745 746 747 748 749 750 751 752 753 754 755 756 757 758 759 760 761 762 763 764 765 766 767 768 769 770 771 772 773 774 775 776 777 778 779 780 781 782 783 784 785 786 787 788 789 790 791 792 793 794 795 796 797 798 799 800 801 802 803 804 805 806 807 808 809 810 811 812 813 814 815 816 817 818 819 820 821 822 823 824 825 826 827 828 829 830 831 832 833 834 835 836 837 838 839 840 841 842 843 844 845 846 847 848 849 850 851 852 853 854 855 856 857 858 859 860 861 862 863 864 865 866 867 868 869 870 871 872 873 874 875 876 877 878 879 880 881 882 883 884 885 886 887 888 889 890 891 892 893 894 895 896 897 898 899 900 901 902 903 904 905 906 907 908 909 910 911 912 913 914 915 916 917 918 919 920 921 922 923 924 925 926 927 928 929 930 931 932 933 934 935 936 937 938 939 940 941 942 943 944 945 946 947 948 949 950 951 952 953 954 955 956 957 958 959 960 961 962 963 964 965 966 967 968 969 970 971 972 973 974 975 976 977 978 979 980 981 982 983 984 985 986 987 988 989 990 991 992 993 994 995 996 997 998 999 1000 1001 1002 1003 1004 1005 1006 1007 1008 1009 1010 1011 1012 1013 1014 1015 1016 1017 1018 1019 1020 1021 1022 1023 1024 1025 1026 1027 1028 1029 1030 1031 1032 1033 1034 1035 1036 1037 1038 1039 1

www.elsevier.com/locate/jmb

117-107, 44.4%, 1120-107.2

11.9 2000, 2001, 2002, 2003, 2004

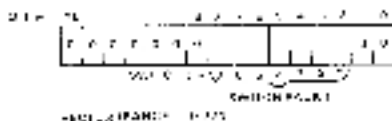
U. Jankowsky, F. R.

Figure 1. The effect of the concentration of the initiator on the polymerization of α -methylstyrene in the presence of $\text{Cu}(\text{NO}_3)_2$ and $\text{Cu}(\text{OAc})_2$ at 60°C.

01.08.2022 12:41:52

STANDARD DRAWING

ALL DIMENSIONS ARE IN INCHES



ALL DIMENSIONS ARE IN INCHES

ALL DIMENSIONS ARE IN INCHES



ADDRESS FORMAT



Mode	Op	Opends	Destination
0	LD	0	R ← PC
1	LD	0	R ← PC
2	LD	0	R ← PC
3	LD	0	R ← PC
4	LD	0	R ← PC
5	LD	0	R ← PC
6	LD	0	R ← PC
7	LD	0	R ← PC

PROGRAM COUNTER ADDRESSING

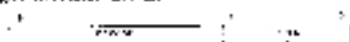
Op	Opends	Destination
0	LD	R ← PC
1	LD	R ← PC
2	LD	R ← PC
3	LD	R ← PC
4	LD	R ← PC
5	LD	R ← PC
6	LD	R ← PC
7	LD	R ← PC

REGISTER

Op	Opends	Destination
0	LD	R ← PC
1	LD	R ← PC
2	LD	R ← PC
3	LD	R ← PC
4	LD	R ← PC
5	LD	R ← PC
6	LD	R ← PC
7	LD	R ← PC

LEI-1 INSTRUCTION SET (Cont.)

COMPUT OPERAND, MPP-601

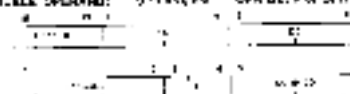


Min. words	Op Code	In. Address	Op. Result	H	Z	V	C
General							
ADD Rn	000000	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	000001	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	000010	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	000011	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	000100	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	000101	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	000110	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	000111	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	001000	COMPT	$Rn + Rn$	0	0	0	0
Rotate & Test							
ROT Rn	000000	COMPT	$Rn + Rn$	0	0	0	0
ROT Rn	000001	COMPT	$Rn + Rn$	0	0	0	0
ROT Rn	000010	COMPT	$Rn + Rn$	0	0	0	0
ROT Rn	000011	COMPT	$Rn + Rn$	0	0	0	0
ROT Rn	000100	COMPT	$Rn + Rn$	0	0	0	0
ROT Rn	000101	COMPT	$Rn + Rn$	0	0	0	0
ROT Rn	000110	COMPT	$Rn + Rn$	0	0	0	0
ROT Rn	000111	COMPT	$Rn + Rn$	0	0	0	0
Multiple MPP-601							
ADD Rn	000000	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	000001	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	000010	COMPT	$Rn + Rn$	0	0	0	0

Processor Status (PS) Operands

INTS	PS01	Processor Status	0	0	0	0
INTS	PS02	Processor Status	0	0	0	0

OP-CLASS OPERANDS: OP-CLASS and OP-CLASS or OP-CLASS



Min. words	Op Code	In. Address	Op. Result	H	Z	V	C
General							
ADD Rn	000000	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	000001	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	000010	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	000011	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	000100	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	000101	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	000110	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	000111	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	001000	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	001001	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	001010	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	001011	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	001100	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	001101	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	001110	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	001111	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	010000	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	010001	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	010010	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	010011	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	010100	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	010101	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	010110	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	010111	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	011000	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	011001	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	011010	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	011011	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	011100	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	011101	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	011110	COMPT	$Rn + Rn$	0	0	0	0
ADD Rn	011111	COMPT	$Rn + Rn$	0	0	0	0

LSI-1 INSTRUCTION SET (2011)

Optimal PIR

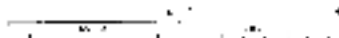
001	000000	empty	00000	00000
010	000000	empty	00000	00000
011	000000	empty	00000	00000
010	000000	empty	00000	00000
010	000000	empty	00000	00000
010	000000	empty	00000	00000

Optimal PIR

001	000000	empty	00000	00000
010	000000	empty	00000	00000
011	000000	empty	00000	00000
010	000000	empty	00000	00000

LSI-1: 000000

LSI-1: 000000
LSI-1: 000000
LSI-1: 000000
LSI-1: 000000
LSI-1: 000000



LSI-1: 000000

Max	Reg Code	Instruction	Max Code
-----	----------	-------------	----------

LSI-1

00	0000	empty	0000
00	0000	empty	0000
00	0000	empty	0000
00	0000	empty	0000
00	0000	empty	0000
00	0000	empty	0000
00	0000	empty	0000
00	0000	empty	0000

LSI-1: 000000

00	0000	empty	0000
00	0000	empty	0000
00	0000	empty	0000
00	0000	empty	0000

LSI-1: 000000

00	0000	empty	0000
00	0000	empty	0000
00	0000	empty	0000
00	0000	empty	0000

LSI-11 INSTRUCTION SET (Cont.)

GROUP A INSTRUCTIONS

Op Code	Op Code	Instruction	Notes
000	00000	ADD	R1 ← R1 + R2
001	00001	AND	R1 ← R1 AND R2
010	00010	OR	R1 ← R1 OR R2
011	00011	XOR	R1 ← R1 XOR R2
100	00100	SHL	R1 ← R1 << R2
101	00101	SHR	R1 ← R1 >> R2
110	00110	ASHL	R1 ← R1 << R2
111	00111	ASHR	R1 ← R1 >> R2

GROUP B INSTRUCTIONS

Op Code	Op Code	Instruction	Notes
000	01000	STOR	R1 ← R1
001	01001	STOR	R1 ← R1
010	01010	STOR	R1 ← R1
011	01011	STOR	R1 ← R1
100	01100	STOR	R1 ← R1
101	01101	STOR	R1 ← R1
110	01110	STOR	R1 ← R1
111	01111	STOR	R1 ← R1

INSTRUCTIONS

Op Code	Op Code	Instruction	Notes
000	10000	ADD	R1 ← R1 + R2
001	10001	AND	R1 ← R1 AND R2
010	10010	OR	R1 ← R1 OR R2
011	10011	XOR	R1 ← R1 XOR R2

CONDITION CODE CREATIONS



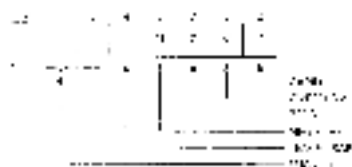
NOTE: The condition codes are created from the result of the instruction.

Op Code	Op Code	Instruction	Notes
000	10100	ADD	R1 ← R1 + R2
001	10101	AND	R1 ← R1 AND R2
010	10110	OR	R1 ← R1 OR R2
011	10111	XOR	R1 ← R1 XOR R2
100	11000	SHL	R1 ← R1 << R2
101	11001	SHR	R1 ← R1 >> R2
110	11010	ASHL	R1 ← R1 << R2
111	11011	ASHR	R1 ← R1 >> R2

7-BIT ASCII CODE

Hex	Hex	Hex	Hex	Hex	Hex
00H	10H	20H	30H	40H	50H
00	01	02	03	04	05
06	07	08	09	0A	0B
0C	0D	0E	0F	10	11
12	13	14	15	16	17
18	19	1A	1B	1C	1D
1E	1F	20	21	22	23
24	25	26	27	28	29
2A	2B	2C	2D	2E	2F
30	31	32	33	34	35
36	37	38	39	3A	3B
3C	3D	3E	3F	40	41
42	43	44	45	46	47
48	49	4A	4B	4C	4D
4E	4F	50	51	52	53
54	55	56	57	58	59
5A	5B	5C	5D	5E	5F

PROCESSOR STATUS WORD



Pin #	Signal Name	Pin #	Signal Name
1	NC	101	NC
2	NC	102	NC
3	NC	103	NC
4	NC	104	NC
5	NC	105	NC
6	NC	106	NC
7	NC	107	NC
8	NC	108	NC
9	NC	109	NC
10	NC	110	NC
11	NC	111	NC
12	NC	112	NC
13	NC	113	NC
14	NC	114	NC
15	NC	115	NC
16	NC	116	NC
17	NC	117	NC
18	NC	118	NC
19	NC	119	NC
20	NC	120	NC
21	NC	121	NC
22	NC	122	NC
23	NC	123	NC
24	NC	124	NC
25	NC	125	NC
26	NC	126	NC
27	NC	127	NC
28	NC	128	NC
29	NC	129	NC
30	NC	130	NC
31	NC	131	NC
32	NC	132	NC
33	NC	133	NC
34	NC	134	NC
35	NC	135	NC
36	NC	136	NC
37	NC	137	NC
38	NC	138	NC
39	NC	139	NC
40	NC	140	NC
41	NC	141	NC
42	NC	142	NC
43	NC	143	NC
44	NC	144	NC
45	NC	145	NC
46	NC	146	NC
47	NC	147	NC
48	NC	148	NC
49	NC	149	NC
50	NC	150	NC
51	NC	151	NC
52	NC	152	NC
53	NC	153	NC
54	NC	154	NC
55	NC	155	NC
56	NC	156	NC
57	NC	157	NC
58	NC	158	NC
59	NC	159	NC
60	NC	160	NC
61	NC	161	NC
62	NC	162	NC
63	NC	163	NC
64	NC	164	NC
65	NC	165	NC
66	NC	166	NC
67	NC	167	NC
68	NC	168	NC
69	NC	169	NC
70	NC	170	NC
71	NC	171	NC
72	NC	172	NC
73	NC	173	NC
74	NC	174	NC
75	NC	175	NC
76	NC	176	NC
77	NC	177	NC
78	NC	178	NC
79	NC	179	NC
80	NC	180	NC
81	NC	181	NC
82	NC	182	NC
83	NC	183	NC
84	NC	184	NC
85	NC	185	NC
86	NC	186	NC
87	NC	187	NC
88	NC	188	NC
89	NC	189	NC
90	NC	190	NC
91	NC	191	NC
92	NC	192	NC
93	NC	193	NC
94	NC	194	NC
95	NC	195	NC
96	NC	196	NC
97	NC	197	NC
98	NC	198	NC
99	NC	199	NC
100	NC	200	NC

NOTES:

Prepared by Professional Services



DIGITAL
EQUIPMENT
CORPORATION

MAYNARD, MASSACHUSETTS

NOVEMBER 1987

Copyright © 1987 Digital Equipment Corporation

PK-5011-K00001