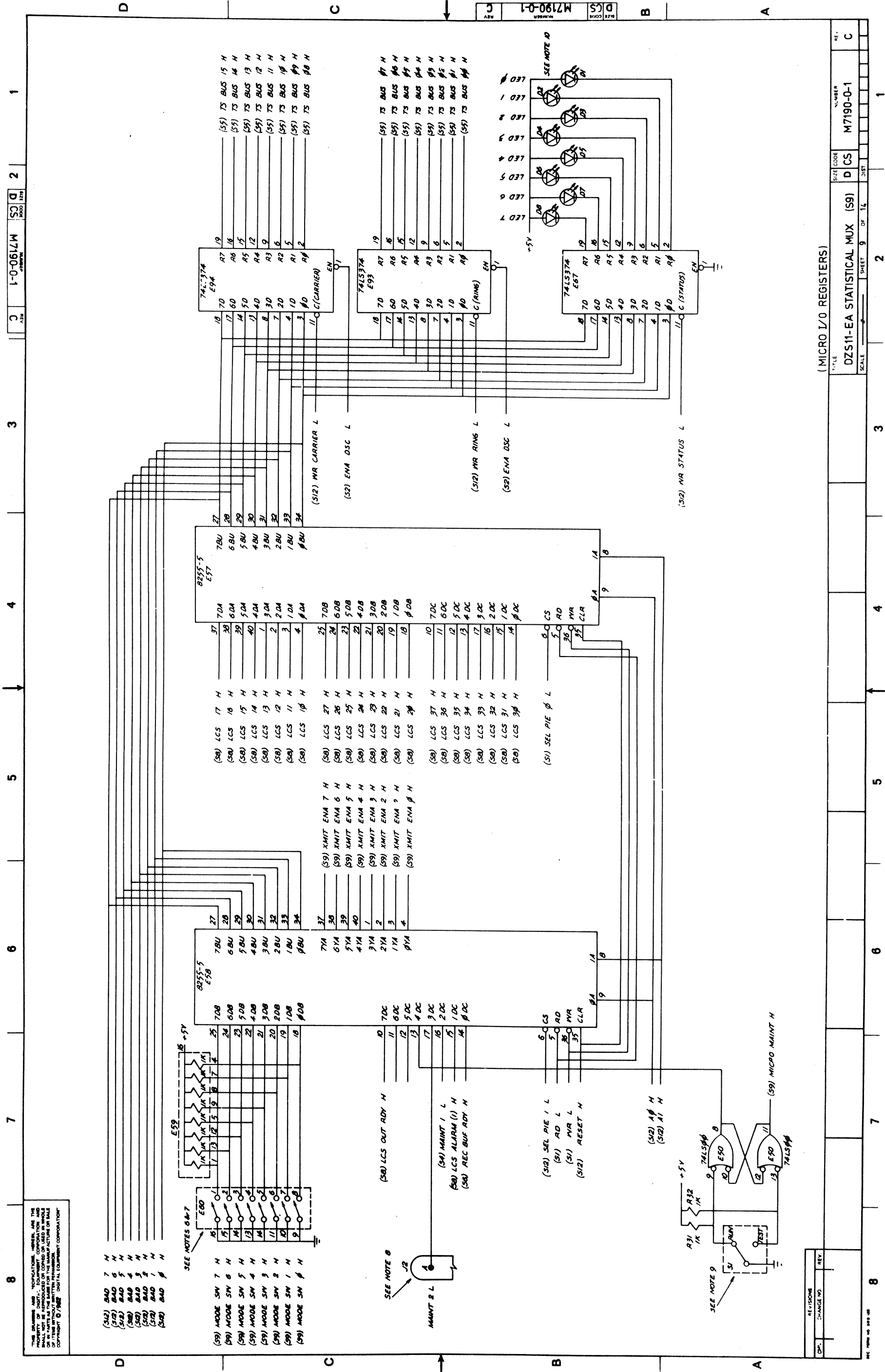
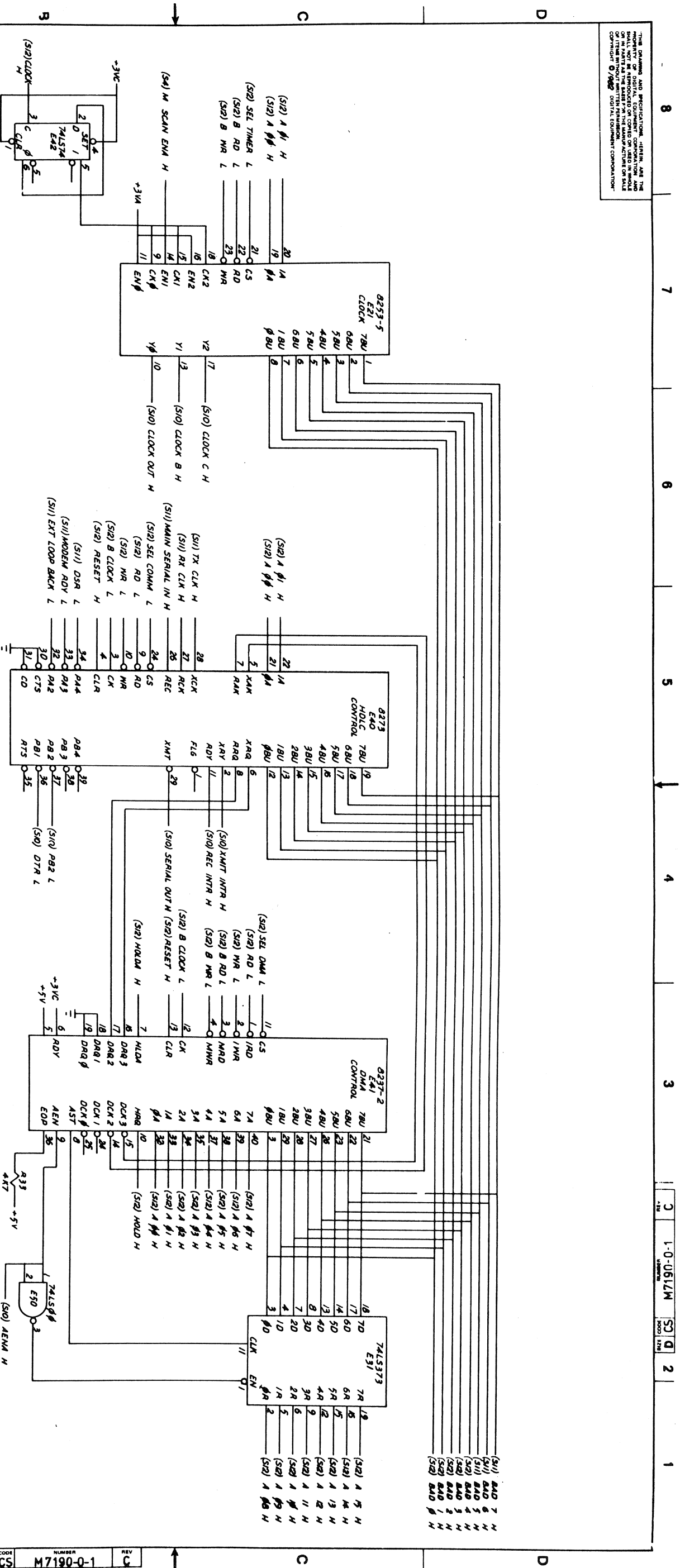




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(COMPOSITE PORT COMMUNICATION CONTROLLER)

FILE	SIZE CODE	NUMBER	REV
DZS11-EA STATISTICAL MUX (S10)	D CS	M7190-0-1	C
SCALE	SHEET 10 OF 14	DIST	

REV M7190-0-1 D CS

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8 7 6 5 4 3 2 1

C 1-0-061LW S2

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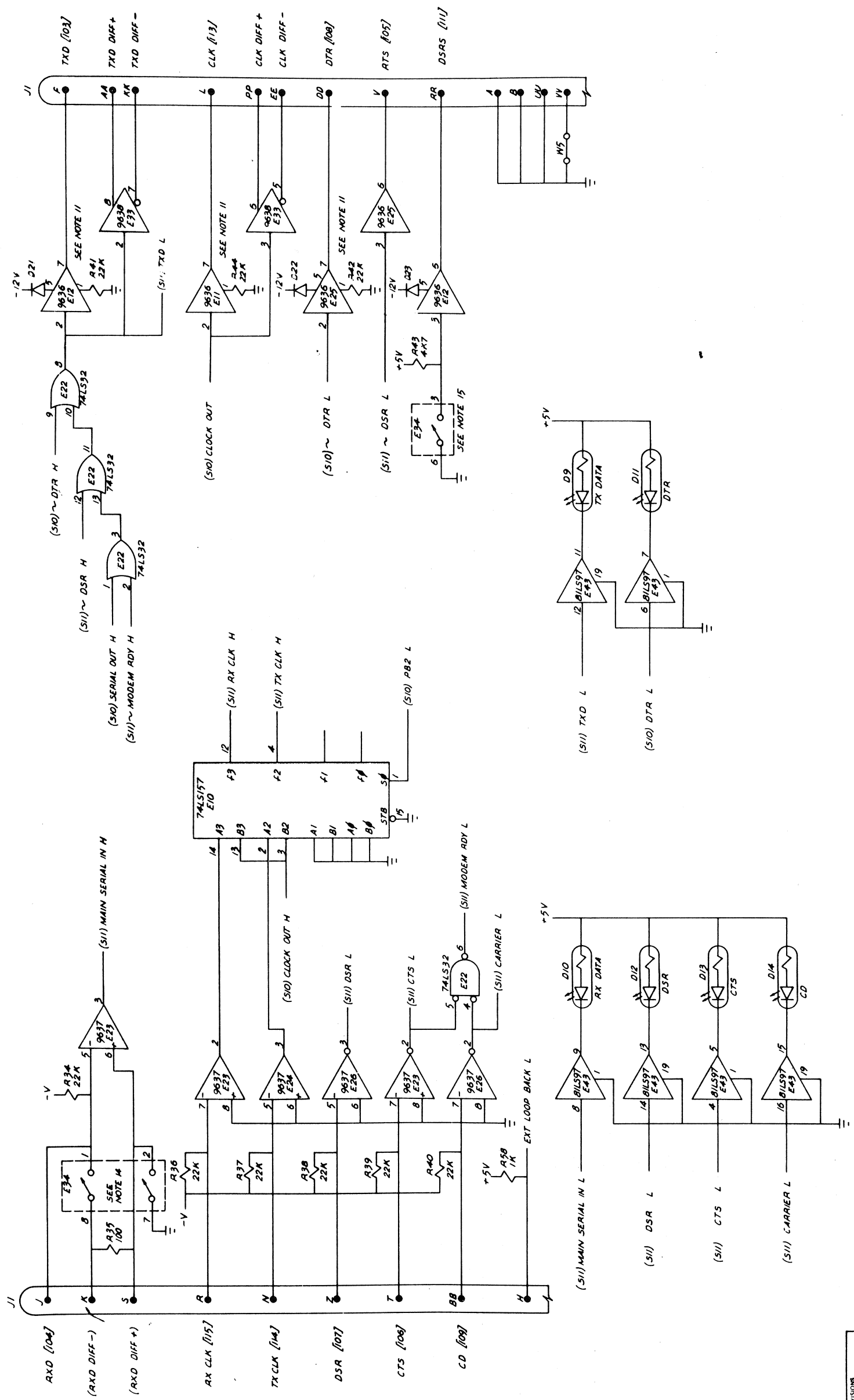
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REVISIONS
CHANGE NO. REV

(COMPOSITE PORT TRANSCEIVERS)

SIZE CODE

NUMBER

REV

D CS

M7190-0-1

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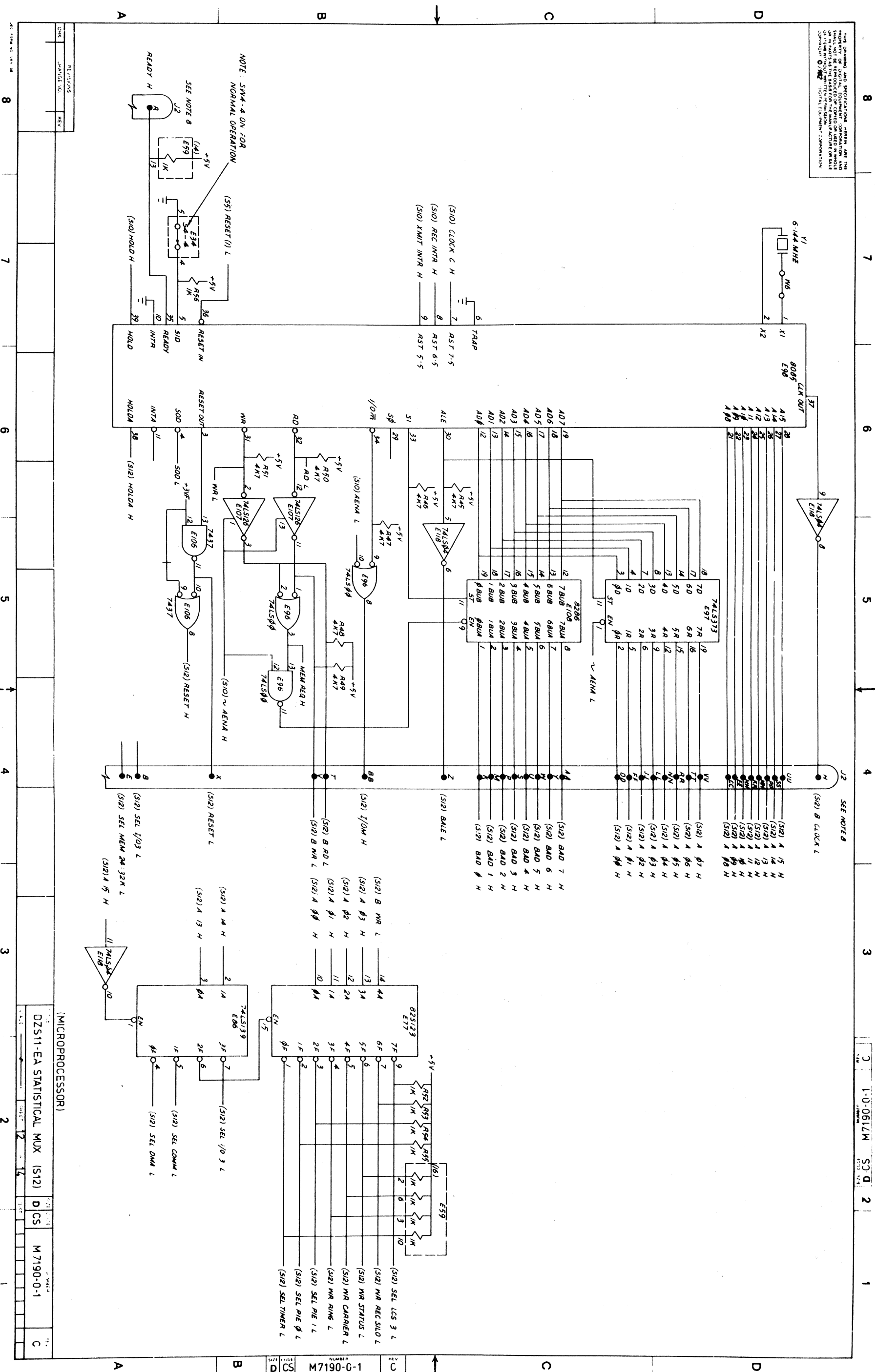
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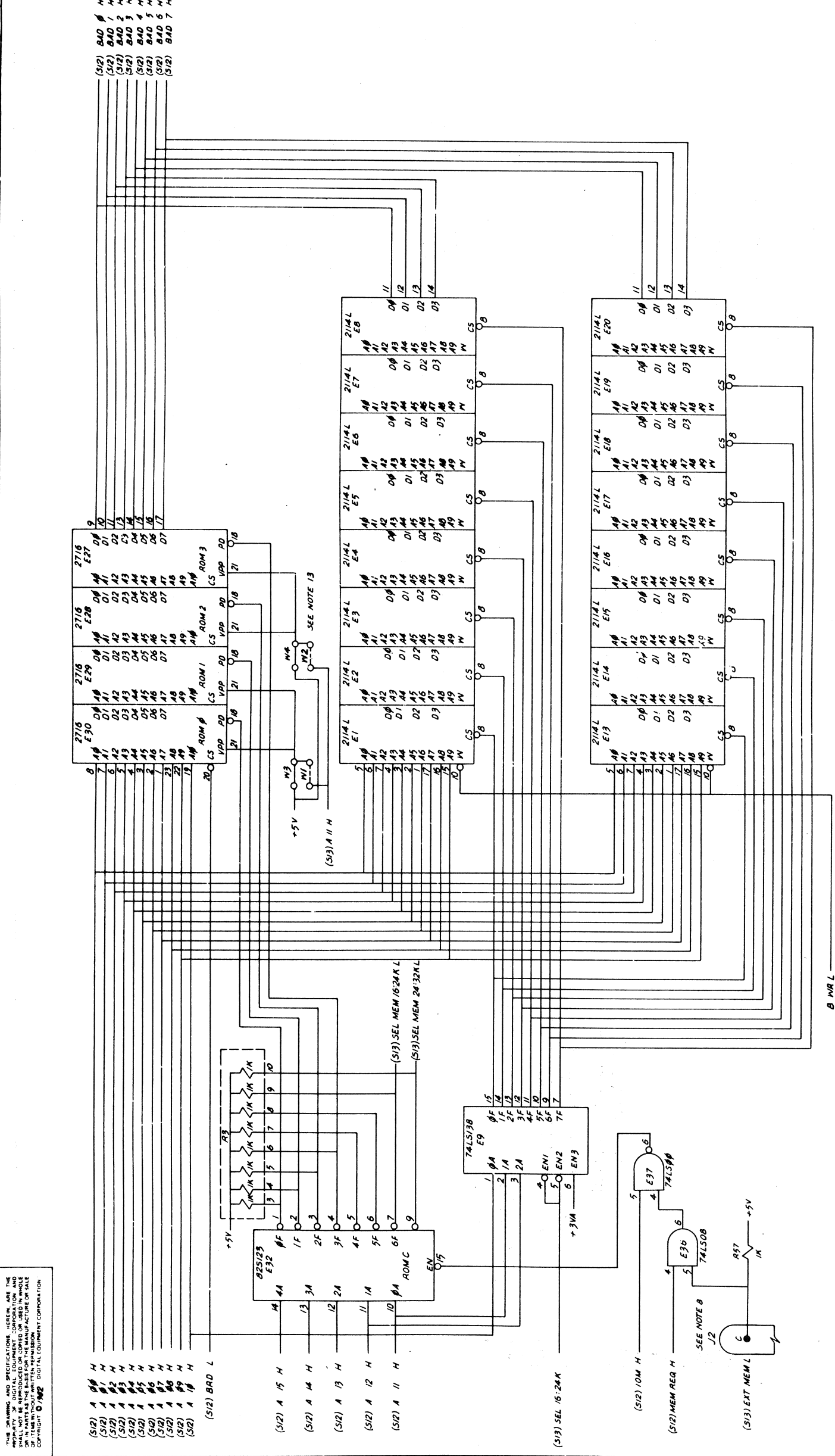
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CHK	REV

(MICROPROCESSOR ROM / RAM)

DZS11-EA STATISTICAL MUX (S13)

D CS

M7190-0-1

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1. WHEN A SWITCH IS CLOSED (ON), THE COMPARATOR RECOGNIZES THE RESPECTIVE UNIBUS ADDRESS BIT WHEN IT IS A LOGIC ONE (LOW). WHEN A SWITCH IS OPEN (OFF) THE COMPARATOR RECOGNIZES THE RESPECTIVE UNIBUS ADDRESS BIT WHEN IT IS A LOGIC ZERO (HIGH).
2. THE "B" AND "D" INPUTS ON THE 8136 LOGIC SYMBOL HAVE THE FOLLOWING MEANING:
B = UNIBUS RECEIVER INPUT
D = TTL LOGIC INPUT.
3. THE 8136 CONTAINS SIX VOLTAGE COMPARATORS WITH A COMMON OUTPUT. EACH COMPARATOR MUST HAVE ITS "B" AND "D" INPUTS THE SAME, AT THE SAME TIME, FOR A TRUE (HIGH) OUTPUT.
4. PRIORITY 5 IS THE STANDARD FOR THIS MODULE, THEREFORE E68 SHOWS THE REQUEST AND GIANT PATHS FOR PRIORITY 5.
5. WHEN THE SWITCH IS CLOSED (ON), THE RESPECTIVE VECTOR BIT IS SET TO A LOGICAL ONE. WHEN A SWITCH IS OPEN (OFF), THE RESPECTIVE VECTOR BIT IS SET TO A LOGICAL ZERO.
6. SWITCH PACK E68 ENABLES SELECTION OF COMPOSITE PORT BAUD RATES, COMPOSITE PORT TIMING SOURCE, AND ENABLES CONTINUOUS OPERATION OF SELECTED OFFLINE MICRODIAGNOSTICS.

COMPOSITE PORT BAUD RATE

BAUD RATE	SW6	SW7	SW8
1200	ON	ON	ON
1800	ON	ON	OFF
2400	ON	OFF	ON
3600	ON	OFF	OFF
4800	OFF	ON	ON
7200	OFF	ON	OFF
9600	OFF	OFF	ON
19200	OFF	OFF	OFF

NOTE:
SW5 = ON (CLOSED) SELECTS EXTERNAL CLOCK
SW5 = OFF (OPEN) SELECTS INTERNAL CLOCK

MICRODIAGNOSTIC CONTROL

TEST	SW2	SW3	SW4
ROM	ON	ON	OFF
RAM	ON	OFF	ON
CLOCK	ON	OFF	OFF
X.25 LOGIC	OFF	ON	ON
X.25 INT DATA	OFF	ON	OFF
DMA LOGIC	OFF	OFF	ON
X.25 EXT DATA	OFF	OFF	OFF

NOTE:
SW1 = OFF (OPEN) FOR LOOP ON SELECT TEST
SW1 = ON (CLOSED) FOR RUN ALL TESTS

7. BAUD RATE SELECT SWITCHES E68-6, E68-7, AND E68-8, MUST BE SET TO OPERATIONAL BAUD RATE FOR BOTH INTERNAL AND EXTERNAL TIMING CLOCK.
8. J2 IS AN OPTION PORT, AND IS RESERVED FOR FUTURE EXPANSION.

9. S1 IS THE RUN/MAINTENANCE SWITCH AND IS USED TO ENABLE OFF LINE MICRODIAGNOSTIC. WHEN IN THE RUN POSITION (RUN LOW) THE MICROCONTROLLER WILL BE IN STATISTICAL RUN MODE. WHEN IN THE TEST POSITION (TEST LOW) THE MICROCONTROLLER WILL BE IN THE DIAGNOSTIC MODE.

10. LEDS 8 THRU 7 DISPLAY BOTH OPERATIONAL AND MAINTENANCE STATUS. LEDS 7 THRU 6 DISPLAY THE MODE, I.E. WHEN LEDS 7 THRU 6 ARE FLASHING, LEDS 8 THRU 5 DISPLAY STATISTICAL MUX OPERATIONAL STATUS. WHEN LEDS 7 THRU 6 ARE IN ANY STEADY STATE, LEDS 8 THRU 5 DISPLAY MAINTENANCE STATUS.

11. R35, 41, 42, AND 44 SELECT RS-423, RS-232C SLEW RATE. OPTION IS SHIPPED WITH 22K RESISTOR INSTALLED ENABLING ALL BAUD RATES TO OPERATE OVER 50 FEET.

EIA RS-423/RS-232C SLEW RATE RESISTOR VALUES

BAUD RATE	RESISTOR VALUE
38.4K BAUD	22K OHMS
19.2K BAUD	57K OHMS
9.6K BAUD	120K OHMS
4.8K BAUD	200K OHMS
2.4K BAUD	430K OHMS
1.2K BAUD	820K OHMS

12. W8 SHOULD BE INSTALLED FOR NORMAL OPERATION.
13. W3 AND W4 SHOULD BE INSTALLED. (2716 OPERATION), W1 AND W2 SHOULD NOT BE INSTALLED (2732 OPERATION).
14. TO ENABLE MAIN COMPOSITE PORT FOR EIA RS-423/232C OPERATION, SET E34-1 OFF AND E34-2 ON. TO ENABLE MAIN COMPOSITE PORT FOR EIA LONG LINE DRIVE OPERATION SET E34-1 ON AND E34-2 OFF.
15. E34-3 IS THE MODERN BAUD RATE SELECTOR. SET E34-3 ON TO ENABLE HIGH BAUD RATE. SET E34-3 OFF TO ENABLE LOW BAUD RATE. NORMAL OPERATION IS WITH E34-3 ON.

(NOTES)

REVISIONS	
CHK	CHANGE NO
REV	

TITLE		SIZE	CODE	NUMBER	REV
DZS11-EA STATISTICAL MUX(S14)		C	CS	M7190-0-1	C
SCALE		SHEET	14	OF	14
		DIST			

SIZE CODE C CS NUMBER M7190-0-1

REV C

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0-0-35081 (A) (D)

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WIRE TABLE

ITEM NO.	DESCRIPTION	FROM		TO		REMARKS
		AWG	COLOR	CONN	WITH	
26	BLU	PI-1*	12	JI-VV	7	
	WHT	PI-2	8	JI-F		
	ORN	PI-3		JI-J		
	WHT	PI-4		JI-V		
	GRN	PI-5		JI-T		
	WHT	PI-6	8	JI-Z		
	GRN	PI-7**	12	JI-UU		
	BRN	PI-8	8	JI-BE		
	WHT	PI-9		JI-Y		
	GRY	PI-10		JI-W		
	WHT	PI-11		JI-FF		
	BLU	PI-12		JI-UJ		
	RED	PI-13		JI-D		
	GRN	PI-14		JI-LL		
	RED	PI-15	8	JI-N	7	
26	GRY	PI-15				

WIRE TABLE CONT.

ITEM NO.	DESCRIPTION	FROM		TO		REMARKS
		AWG	COLOR	CONN	WITH	
26	RED	PI-16	8	JI-NN	7	
	GRY	PI-17		JI-R		
	RED	PI-18		JI-U		
	GRY	PI-19		JI-P		
	BLU	PI-20		JI-DD		
	BLK	PI-21		JI-MM		
	ORN	PI-22		JI-X		
	BLK	PI-23		JI-RR		
	GRN	PI-24		JI-L		
	RED	PI-25	8	JI-C		
	ORN	PI-1*	12	JI-A		
	SHIELD	PI-7**	12	JI-B	7	
3	BLK	PI-1*	12	PI-7**	12	
2	RED	JI-E	7	JI-M	7	

* DENOTES THREE WIRES ARE SOLDERED INTO THE PI-1 SOLDER CUP.
** DENOTES THREE WIRES ARE SOLDERED INTO THE PI-7 SOLDER CUP.

REVISION HISTORY	REV
DATE	ECO NUMBER

TITLE	TABLE WIRE
SCALE	NONE
DATE	10/25/60
BY	WIK
CHECKED	
APPROVED	

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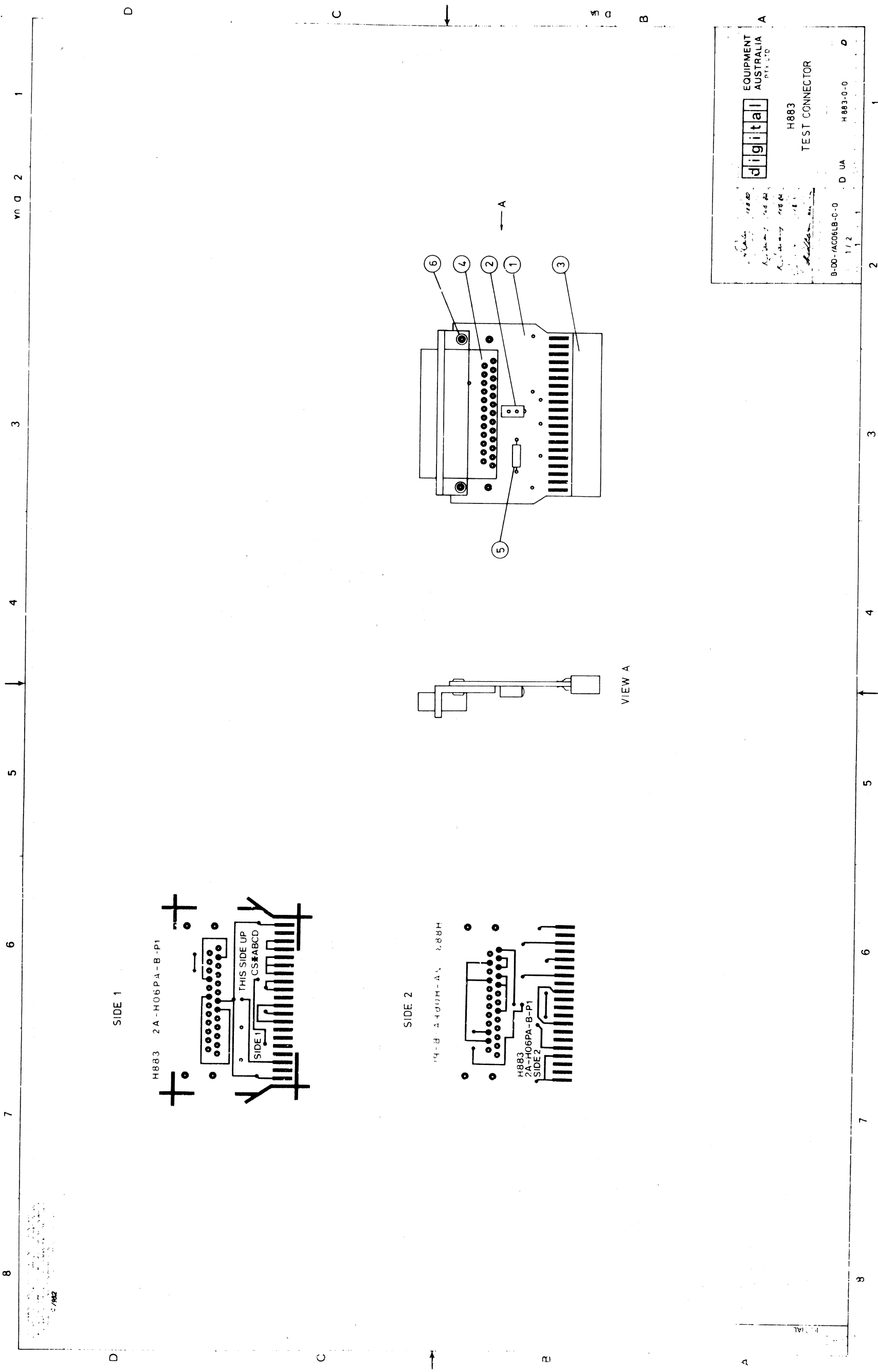
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		H883 TEST CONNECTOR	
B-DD-1AC06LB-C-0 1/2	D UA	H883-0-0	0

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1REV

