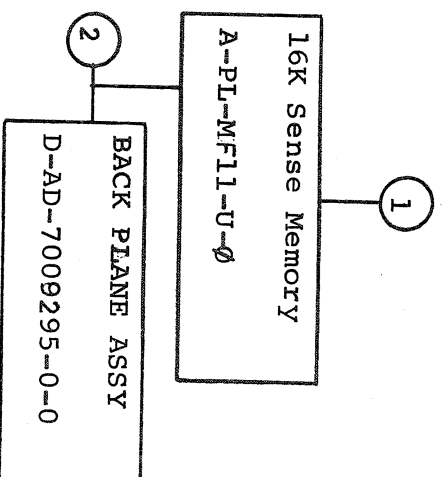




TITLE	SIZE	CODE	NUMBER	REV
16K SENSE MEMORY	SHEET 2 OF 3	B DD	MF11-U	A

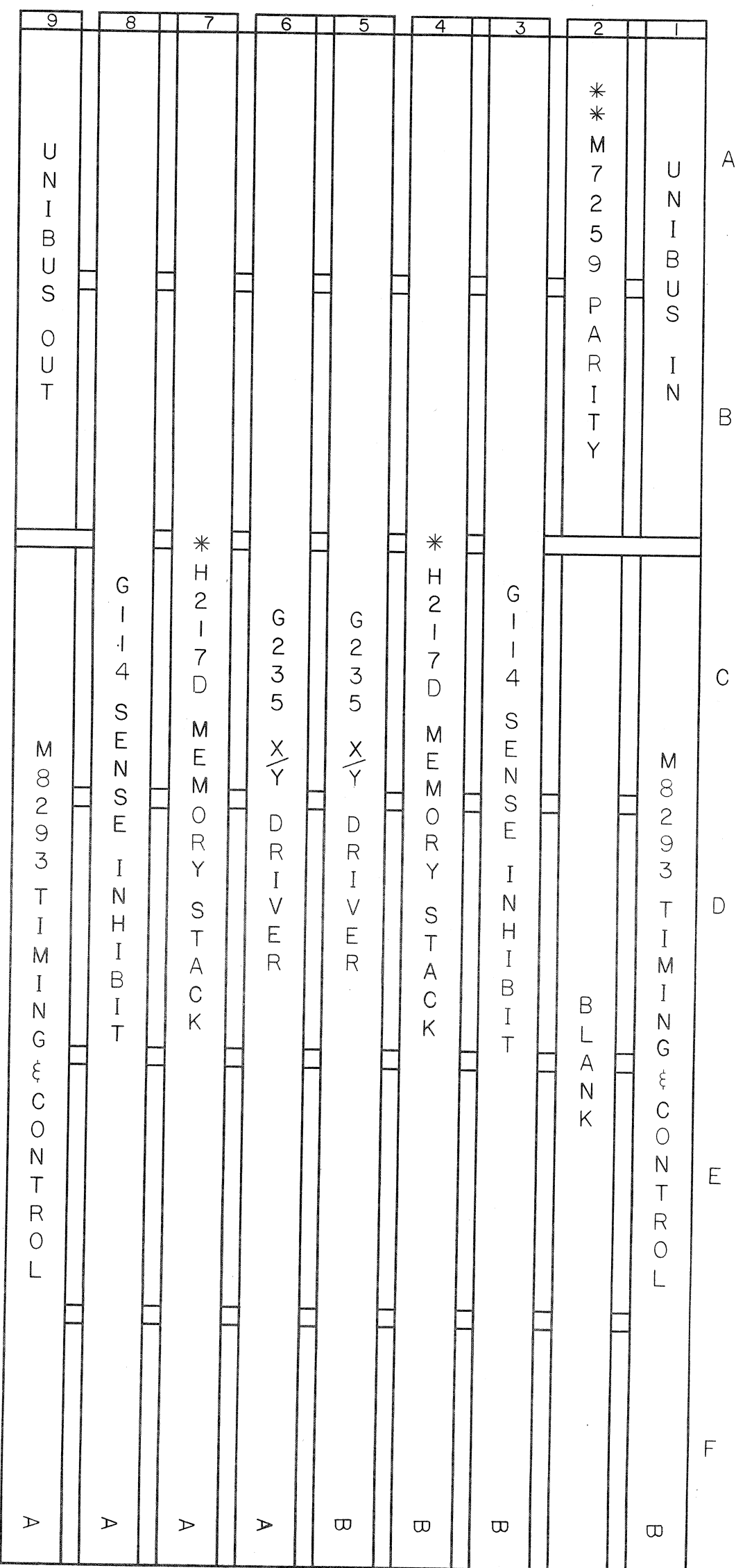
[illegible]

DRB 108

- * 1. FOR MFLUP(MEMORY WITH EASY-), USE
H2IC MEMORY STACK.
- * 2. M7259 IS USED WITH MFLUP ONLY
- * 3. INSTALL "A" MODULES FOR FIRST 16K, B MODULES

NOTES:

- * 1. FOR MFLUP(MEMORY WITH EASY-), USE
H2IC MEMORY STACK.
- * 2. M7259 IS USED WITH MFLUP ONLY
- * 3. INSTALL "A" MODULES FOR FIRST 16K, B MODULES



SLOT SIDE

SIZE	CODE	NUMBER	REV.
D	MU	MFII-U-MU	A

[illegible]

P DURANT		1-3-74	
82	MF11U-00001	A	
CHK	CHANGE NO.	REV	
REVISIONS			
DEC NO.	EIA NO.	DEC NO.	EIA NO.
SEMICONDUCTOR CONVERSION CHART			
ETCH BOARD REV		PARTS LIST	
DRN.	DATE	TITLE	ITEM NO.
CHK'D.	DATE	 digital EQUIPMENT CORPORATION MILWAUKEE, WISCONSIN 53219	QTY
DRN.	DATE		REF DESIGNATION
CHK'D.	DATE		
DRN.	DATE		
CHK'D.	DATE		
PROJ. ENG.	DATE		
PROD.	DATE		
NEXT HIGHER ASSY			
B-DD-MF11U			
SCALE	1 OF 1	SIZE CODE	REV.
SHEET		D MU	A
MODULE UTILIZATION		PART NO.	

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MODE

SIGNAL

DATI

BUS MSYN L

BUS CØ = H
BUS CI = H

MATB READ LOCKOUT H
MATD STACK CHG H
MATB LOCK MAR 2:1 L
MATC READ EARLY L
MATC READ LATE L

MATC END STROBE L
MATC OUTPUT ENABLE H
BUS SSYN L
BUS DATA L

MATD INH TIME H
MATD WRITE LATE L
MATB CLEAR MDR 1:Ø L

DATIP

BUS MSYN L

BUS CØ = L
BUS CI = H
FOLLOWED BY

MATB READ LOCKOUT H
MATD STACK CHG H
MATB LOCK MAR L
MATC READ EARLY L
MATC READ LATE L

BUS CØ = H
BUS CI = L

MATC END STROBE L
MATC OUTPUT ENABLE H
BUS SSYN L
BUS DATA L

OR

DATOB

BUS CØ = L
BUS CI = L

MATD INH TIME H
MATD WRITE LATE L
MATB CLEAR MDR 1:Ø L
MATB CLOCK MDR 1:Ø L

MATB PAUSE H

DATO

BUS MSYN L

BUS CØ = H
BUS CI = L

OR
DATOB

MATB READ LOCKOUT H
MATD STACK CHG H
MATB LOCK MAR L
MATC READ EARLY L
MATC READ LATE L

BUS CØ = L
BUS CI = L

MATD INH TIME H
MATD WRITE LATE L
MATB CLEAR MDR 1:Ø L
MATB CLOCK MDR 1:Ø L
MATC END STROBE L

NOTES:

1. * IN THE DATOB MODE CLEAR MDR ONLY OCCURS IN THE BYTE NOT BEING ADDRESSED.
 2. ** IN THE DATOB MODE CLOCK MDR ONLY OCCURS IN THE BYTE BEING ADDRESSED.
 3. *** ACTUAL TIME DEPENDS ON BUS AND PROCESSOR DELAYS.
 4. **** IN PARTIAL SYSTEMS BUS SSYN WILL BE 20 NS LATER THAN SHOWN FOR DATO-DATOB
- BUS MODES AND 150 NS LATER FOR DATI-DATIP MODES.

A 1-U-11 FW DTD 2

1

REVISIONS		
CHK	CHANGE NO	REV
27	MF11U-00001	A
P. DURANT		
1-7-74		

REV. 105C

8

7

6

5

4

3

2

1

FIRST USED ON OPTION MODEL		QTY.	DESCRIPTION		PART NO.	ITEM NO.		
MF11-U								
UNLESS OTHERWISE SPECIFIED								
DIMENSION IN INCHES								
TOLERANCES								
DECIMALS	ANGLES	+0.30	DATE	4-19-73	digital EQUIPMENT CORPORATION			
xxx = .005			DATE	4/30/73	MAYNARD MASSACHUSETTS			
xx = .02			DATE	5/24/73				
x = .1			DATE	6/11/73				
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY Y								
MATERIAL								
NEXT HIGHER ASSY.								
FINISH								
B-DD-MF11-U								
SCALE 1 OF 1								
SHEET 1 OF 1								
DIST.								

SIZE CODE NUMBER REV
DTD MF11-U-1 A