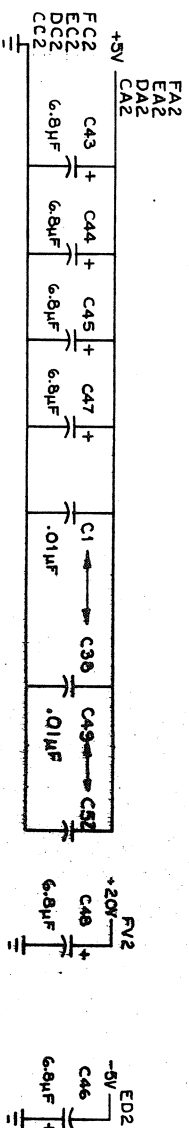
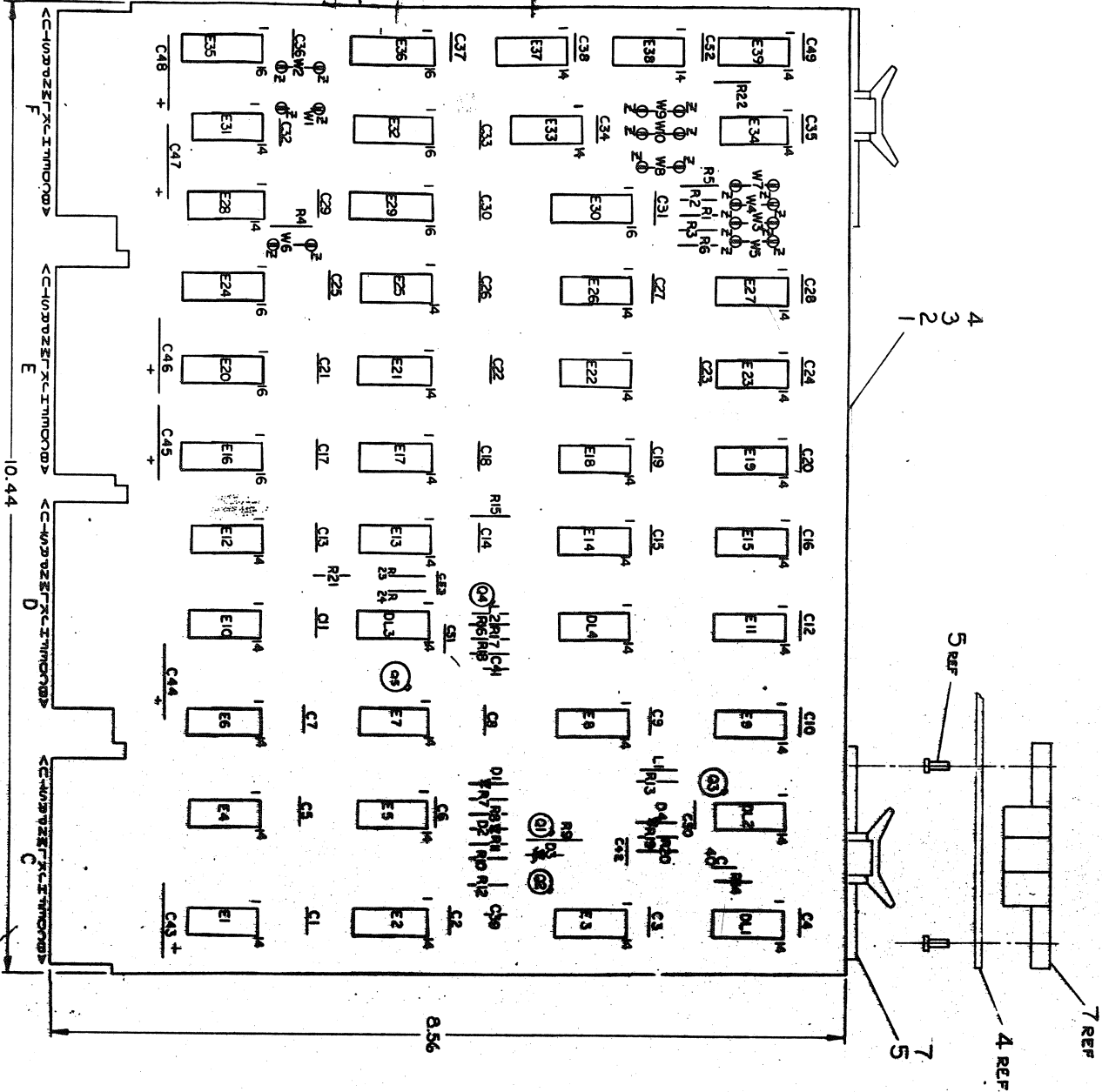


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NOTES: 1. UNLESS OTHERWISE SPECIFIED: RESISTANCE IS IN OHMS. CAPACITANCE IS IN MICROFARADS.



IC TYPE	IC PIN LOCATIONS
7483	12 5
7485	8 16
380	1 8
7475	12 5
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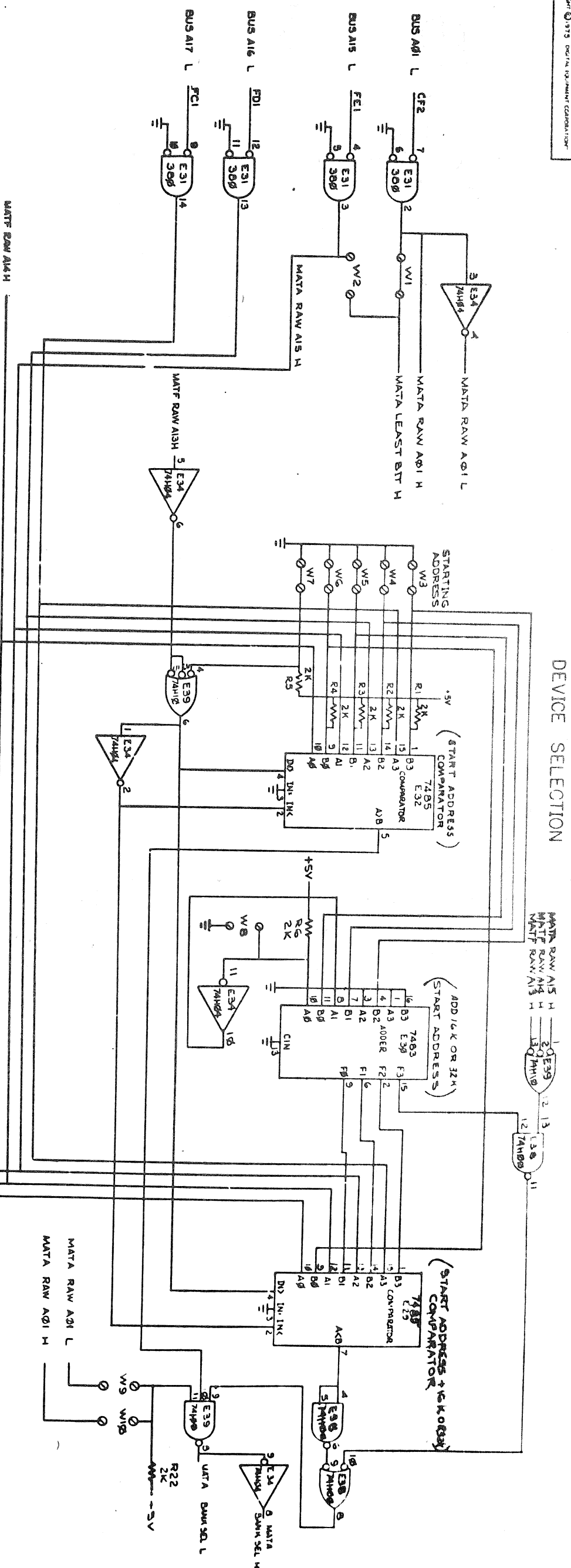
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NOTE: INTERLEAVE CONTROL IS ACHIEVED
AS FOLLOWS:

2) NON-INTERLEAVED

W1 IN
W2, W8, W9 IN
W10 OUT

b) INTERLEAVED

W1 OUT
W2 IN

WE IN 3 ONE MEMORY
WITHOUT 3

W9 OUT 3 THE OTHER MEMORY
WID IN 3

STARTING ADDRESS FOR THE COMBINED INTERLEAVED MEMORY IS THE SAME AS FOR THE NON-INTERLEAVED CASE (MATHRAW17 MUST BE CUT THE SAME FOR BOTH INTERLEAVED MEMORIES) BUT THE INTERLEAVED MAX ADDRESS IS INCREASED BY 16 K (1000000000)

NON-INTERLEAVED		0 = IN / = OUT							NON-INTERLEAVED	
START ADDRESS		W3	W4	W5	W6	W7		MAX ADDRESS		
000000 (0)	0 IN	0 IN	0 IN	0 IN	0 IN	0 IN		077776		
020000 (4)	0 IN	0 IN	0 IN	0 IN	0 IN	1 OUT	117776			
040000 (8)	0	0	0	0	1	1	157776			
060000 (12)	0	0	0	0	1	1	157776			
080000 (16)	0	0	0	0	0	0	177776			
100000 (20)	0	0	0	1	0	0	217776			
120000 (24)	0	0	0	1	0	0	237776			
140000 (28)	0	0	1	1	1	0	257776			
160000 (32)	0	1	0	0	0	0	277776			
180000 (36)	0	1	0	0	0	1	317776			
200000 (40)	0	1	0	1	0	1	337776			
220000 (44)	0	1	0	0	1	1	357776			
240000 (48)	0	1	0	1	0	0	377776			
260000 (52)	0	1	1	1	1	0	417776			
280000 (56)	0	1	1	1	1	0	437776			
300000 (60)	0	1	1	1	1	1	457776			
320000 (64)	0	1	0	0	0	0	477776			

* THE MEMORY WILL NOT RESPOND TO BUS ADDRESSES BETWEEN 124-1204K									
	W3	W4	W5	W6	W7				
44 0 0 0 0 (604)	0	0	0	0	1	317776			
42 0 0 0 0 (724)	0	0	0	0	1	537776			
40 0 0 0 0 (724)	0	0	0	1	1	537776			
00 0 0 0 0 (004)	1	0	1	0	0	577776			
52 0 0 0 0 (044)	1	0	1	0	1	617776			
54 0 0 0 0 (084)	1	0	1	1	0	657776			
56 0 0 0 0 (324)	1	0	1	1	0	657776			
60 0 0 0 0 (964)	1	0	0	0	0	677776			
62 0 0 0 0 (004)	1	1	0	1	0	717776			
64 0 0 0 0 (044)	1	1	0	1	0	717776			
66 0 0 0 0 (084)	1	1	0	1	1	737776			
70 0 0 0 0 (1124)	1	1	1	1	0	751776			
72 0 0 0 0 (1144)	1	1	1	1	1	751776			
74 0 0 0 0 (1204)	1	1	1	1	0	757776			

* THE MEMORY WILL NOT RESPOND TO BUS ADDRESSES BETWEEN 124-128K

DEVICE SELECTION LOGIC

Size	Color	Material
XS	Black	100% Cotton
S	Black	100% Cotton
M	Black	100% Cotton
L	Black	100% Cotton
XL	Black	100% Cotton

DCS M8293-0-

SCALE	SHEET	OF	DIST
	2	10	

[illegible]

Table 1. *Salmonella* serotypes and their associated diseases

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1-0-6628-1 5310 2

D

MATD 450NS DLY L
MATB START READ L
(LOCK MAR FLIP-FLOP)
MATB LOCK MAR 1 H
MATB LOCK MAR 2 H

MATH WRITE BYTE 0 L
DNI MATB CLEAR MDR 0 L
DDI MATB CLEAR MDR 1 L

MATA BANK SEL H
MATD END OF WRITE L
MATI IE L

MATH WRITE BYTE 1 L
(PAUSE FLIP-FLOP)
MATD 125NS DLY L
MATC DEL DATA L
MATB PAUSE H
MATB WRITE BYTE 0 H
MATB WRITE BYTE 1 H

BUS MSYN L CAL 10
MATD MSYN STILL LO L
MATB MSYN H
MATB PAUSE H
MATB WRITE BYTE 1 H
MATB CLK MDR 1 L

C

MATB READ LOCK OUT H
(READ LOCKOUT FLIP-FLOP)

MATB 75NS DLY L
MATB 75NS DLY H
MATB 50NS DLY H
MATB 150NS DLY H
MATB 250NS DLY H

MATB 75NS DLY L
MATB 75NS DLY H
MATB 50NS DLY H
MATB 150NS DLY H
MATB 250NS DLY H

B

(DEL DRIVER GATE)

MATB MSYN H
MATB MSYN L
MATB BANK SEL L

A

REVISIONS	
CHK	REV

READ START TIMING

READ START TIMING

DATE: M8293

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1-0-3628W SCD 2

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D

MATC 300 NS DLY H

MATC 325 NS DLY H

MATB START READ H

(READ EARLY FLIP-FLOP)

CDI MATC END STROBE L

MATB 150 NS DLY H

(END STROBE FLIP-FLOP)

MATH C0 H
MATH C1 L

MATC DATIP CYCLE L

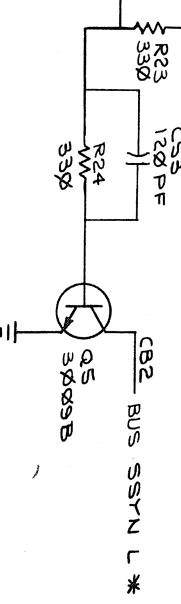
MATC DEL. DATIP L

MATH C1 L

MATB MSYN H

MATB CLK MDR 0 L
MATB CLK MDR 1 L
(SSYN FLIP-FLOP)

MATC SSYN L



B

MATC 12 H

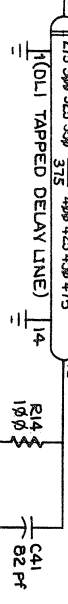
MATC 350 NS DLY H

MATB 75 NS DLY H

(READ LATE FLIP-FLOP)

CMI MATC READ LATE L

MATB 250 NS DLY H



* THIS SIGNAL IS CALLED
'BUS SSYN L' FOR MFI-U
'SSYN INT BUS L' FOR MFI-U-P

A

REVISIONS	
CHK	CHANGE NO
REV	

READ TIMING

TITLE		SIZE CODE	NUMBER	REV.
I/O UNIBUS TIMING		DCS	M8293-0-1	C
SCALE		DATE	OF	

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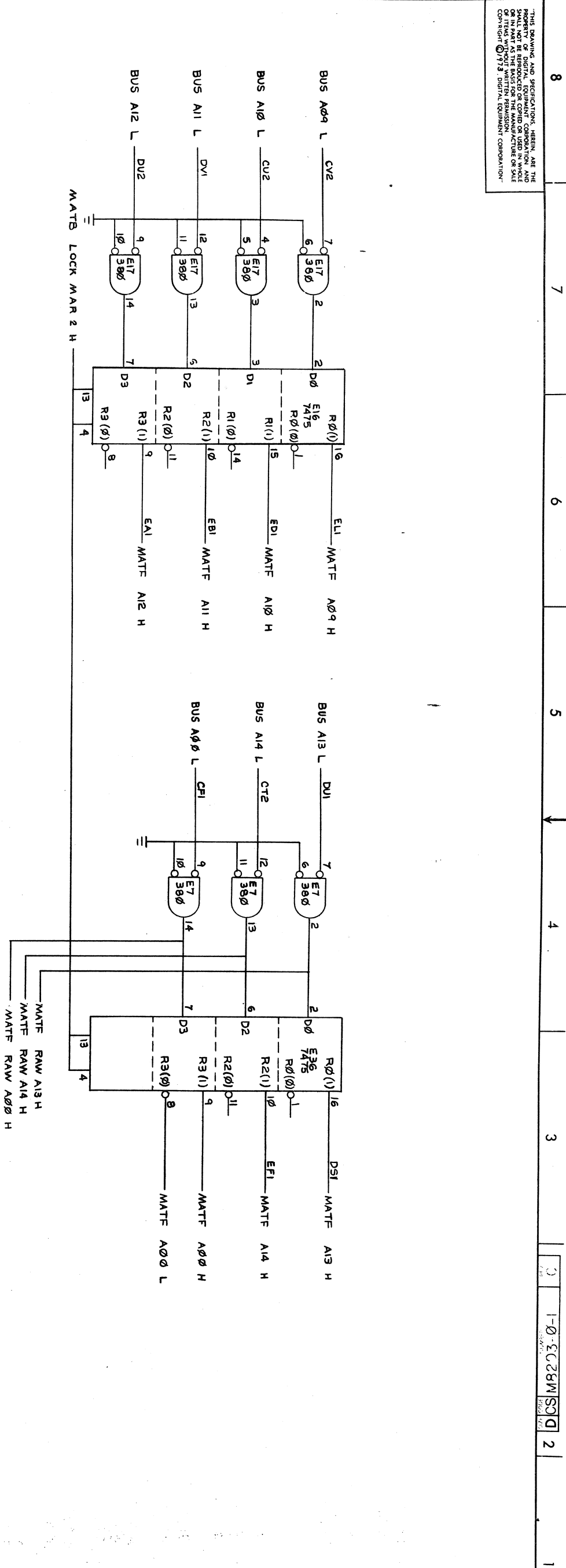
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Diagram of a 1D lattice with 8 sites. Sites 1 and 2 are grouped in a box labeled "1-0-CC28W SCD". Site 2 is also labeled "2". Site 3 is labeled "3". Site 4 has an arrow pointing to it from the left. Site 5 is labeled "5". Site 6 is labeled "6". Site 7 is labeled "7". Site 8 is labeled "8".



TITLE	SIZE CODE	NUMBER	REV.
16K UNIBUS TIMING (MATH)	D 05	M0293-0-1	C

TITLE	SIZE CODE	NUMBER	REV.
16K UNIBUS TIMING (MATH)	D 05	M0293-0-1	C

