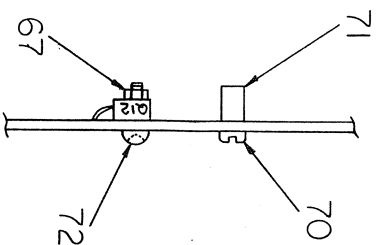
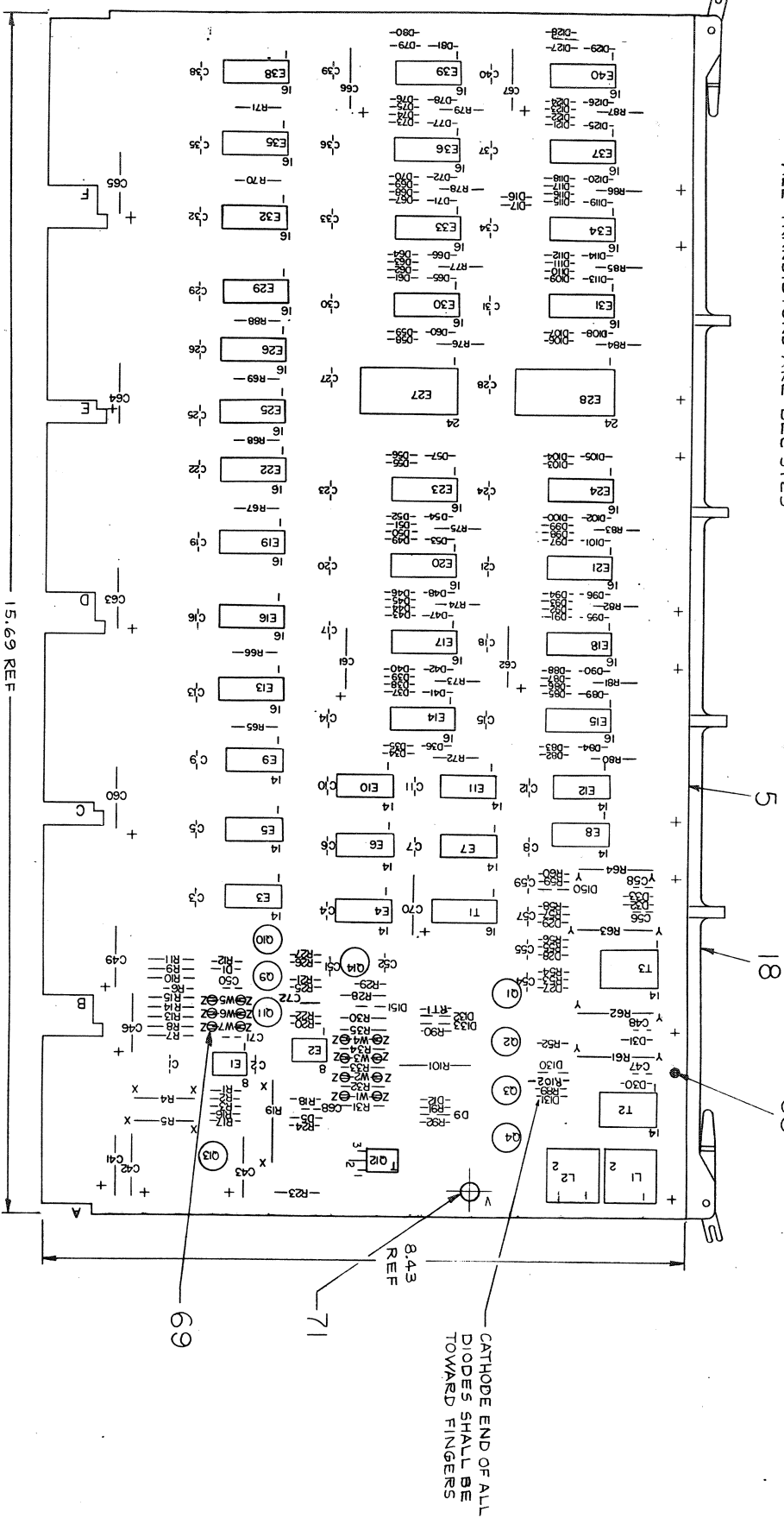


1. * INDICATES NOT USED ON MFII - U & MFII-UP (2 PLACES).
2. ** INDICATES NOT USED ON MFII-U & MFII-UP, BUT ARE TIED TO UNUSED TERMINATORS ON THE G114 MODULE, WHICH FORCES THEM TO +3V (5 PLACES).
3. 1 THERMISTOR LOCATED ON H217 STACK MODULE, 1 ON G235
4. UNLESS OTHERWISE INDICATED;
 - ALL DIODES ARE D672
 - ALL CAPACITORS ARE .01 U^F
 - ALL RESISTORS ARE 1/4 W
 - ALL TRANSISTORS ARE DEC 3725



22 16699	A								
	74121	7	—	—					
	741	—	—	—					
	75325	—	9	16					
	7442	8	16	—					
	74154	12	24	—					
	IC TYPE	GND	+ 5V	+20V					

**GND AND 5V ARE USUALLY PIN 7 AND 14
RESPECTIVELY. EXCEPTIONS ARE STATED ABOVE**

IC PIN LOCATIONS

DEC FORM
DRD-135A

7

9

5

4

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2

SIZE CODE	NUMBER	RFV
D CS	G235-0-1	M

SIZE	CODE	NUMBER	REV.
D	CS	G235-Ø-1	M

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REVISIONS	
CHK	CHANGE NO.
	REV.

DEC FORM NO
DSD 137

8

7

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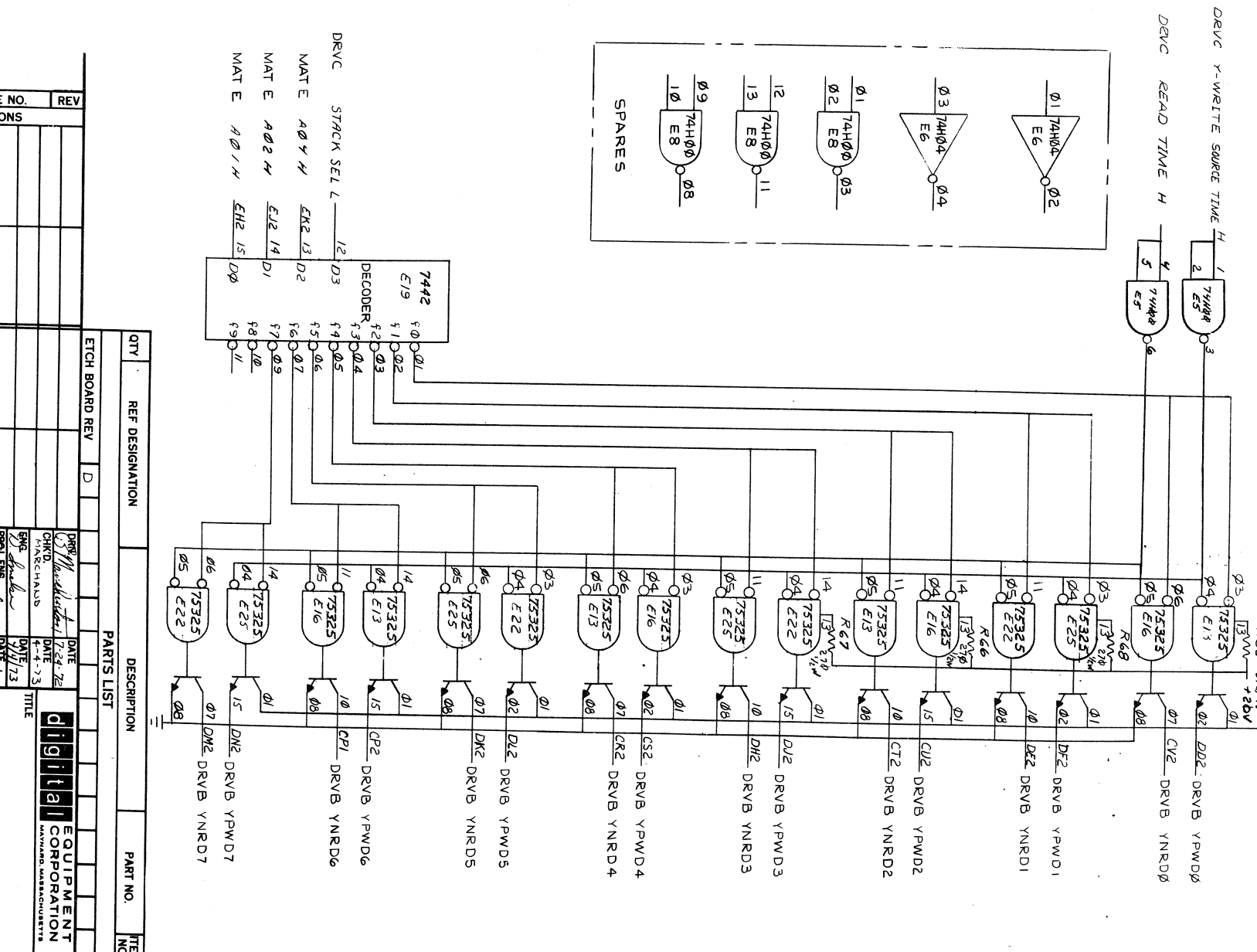
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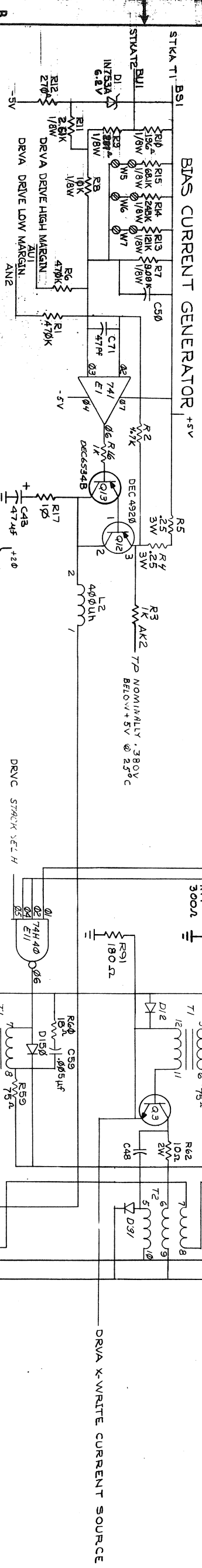
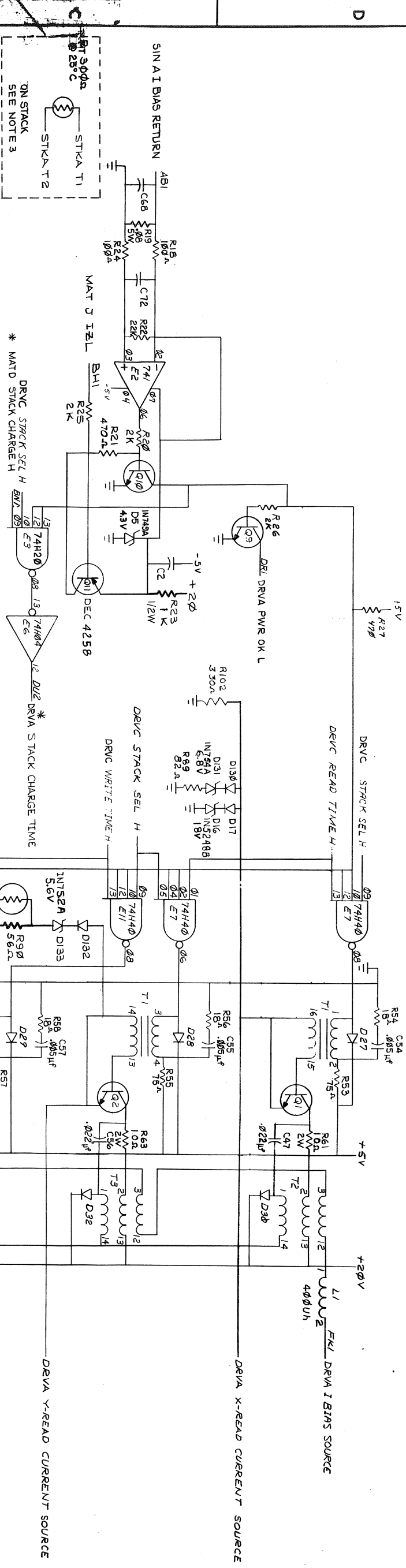
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CHK					
SEMICONDUCTOR CONVERSION CHART					
SHEET	OF	DIST	SCALE		
4	6		1"	IN	

[illegible]

[illegible]