

DH11 SPEED SELECTION LOGIC TEST

ABSTRACT :

The DH11 speed selection logic test verifies that the speed selection functions of the line parameter register operate properly for each transmitter and receiver line. Transmitter timing is checked first, and then receiver timing is tested. The program uses a relative timing comparison to determine if line speed selection is correct. No TURN-ABOUND connector is needed.

OPERATING PROCEDURES :

.R ZDHDD0

*This starts the test at address 200 (normal start).
The program will type:*

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*VECTOR ADDRESS- enter vector
CONTROL REGISTER ADDRESS- enter CSR addree
R indicates run state:
006222 NO CLOCK
LINE SPEED
00 01
00 02
... ...

01 01
01 02
01 03*

SWITCH SETTINGS :

*SW15= 1 halt on error
SW14= 1 scope loop
SW13= 1 inhibit error print out
SW12= 1
SW11= 1 inhibit iteration
SW10= 1 escape to next test on error
SW09= 1 freeze variable parameter in current test
SW08= 1
SW01= 1 start program at selected test
SW00= 1 change parameters at program restart*