

NEW NUMBER = DZRPF

PRODUCT NAME = RP11 Disk Data and Address Test

ABSTRACT

This program tests both the addressing capability and the data reliability of the RP11 and the RP02. The program consists of seven tests any one of which is selectable by the operator. A conversion mode exists which allows the operator to define test parameters.

REQUIREMENTS

HARDWARE = PDP-11 standard family processor
 RP11 Disk pack controller with up to eight RP02 drives
 ASR33 or equivalent

SOFTWARE

STORAGE = 8K of storage

LOADING = Absolute Loader

EXECUTION TIME = 5 hours

STARTING PROCEDURE = The program must always be started at 200.

PRINTOUTS = Yes

SWITCH REGISTER OPTIONS

SW15 = 1 ... HALT ON ERROR
 SW14 = 1 ... LOOP ON ERROR
 SW13 = 1 ... INHIBIT PRINTOUT
 SW11 = 1 ... RING BELL ON ERROR
 SW10 = 1 ... LOOP ON TEST
 SW09 = 1 ... INHIBIT DATA COMPARISON
 SW08 = 1 ... ENTER CONVERSATION MODE
 SW05 = 1 ... USED TO CONTROL HOW MANY COMPARE ERRORS WILL BE
 TYPED OUT AS A RESULT OF A READ OPERATION IN
 THE DATA TEST;
 SW05 = 1 ... CHECK FOR UP TO THREE COMPARE ERRORS WITHIN THE
 READ BUFFER AND TYPE ALL APPROPRIATE ERROR
 INFORMATION FOR EACH ERROR;
 SW05 = 0 ... CHECK FOR ONLY ONE COMPARE ERROR WITHIN THE
 READ BUFFER;
 SW04 ... USED TO CONTROL THE AMOUNT OF INFORMATION TYPED
 ON REREAD ATTEMPTS
 SW04 = 1 ... TYPE ALL ERROR INFORMATION ON EACH REREAD
 ATTEMPT;
 SW04 = 0 ... TYPE THE ERROR INFORMATION ON THE FIRST READ
 ERROR ONLY, AFTER THE ERROR GOES AWAY OR IS

UNRECOVERABLE, THE NUMBER OF REREADS IS THEN TYPED.

SW03 = 1 ... RUN TEST SELECTED BY SWITCH POSITIONS SW0 THRU SW2;

SW00 THRU SW02	TEST SELECTED
0	ADDRESS TEST 0
1	ADDRESS TEST 1
2	ADDRESS TEST 2
3	TEST 3 = WRITE CHECK TEST
4	TEST 4 = MEMORY ADDRESS TEST
5	TEST 5 = DATA RELIABILITY
6	TEST 6 = RANDOM TEST
7	TEST 7 = POWER FAIL TEST

If it is desired to select an individual test, also set SW10 loop on test.