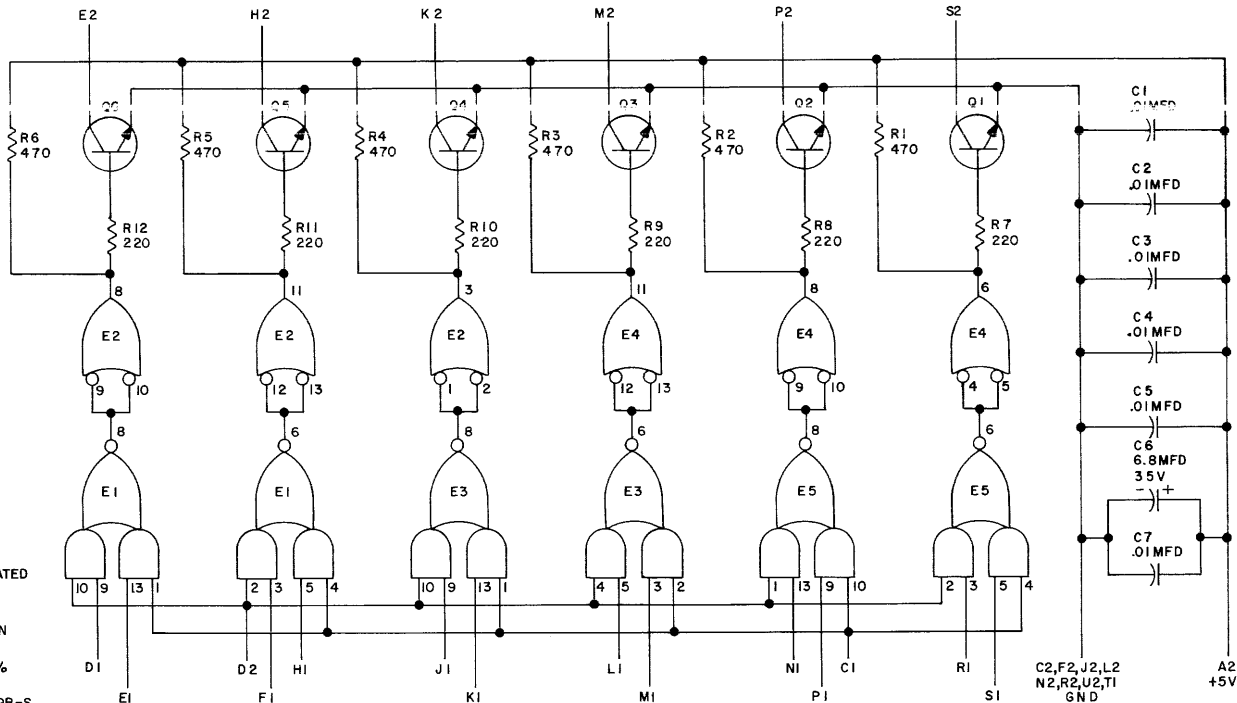


THIS SCHEMATIC IS FURNISHED ONLY FOR TEST AND MAINTENANCE PURPOSES. THE
CIRCUITS ARE PROPRIETARY IN NATURE AND SHOULD BE TREATED ACCORDINGLY.
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UNLESS OTHERWISE INDICATED
PIN 7 ON EACH IC = GND
PIN 14 ON EACH IC = +5V
E1,E3,E5 ARE DEC 74H50N
E2,E4 ARE DEC 74HOON
CAPACITORS ARE 100V, 20%
RESISTORS ARE 1/4W 10%
TRANSISTORS ARE DEC3009B-S

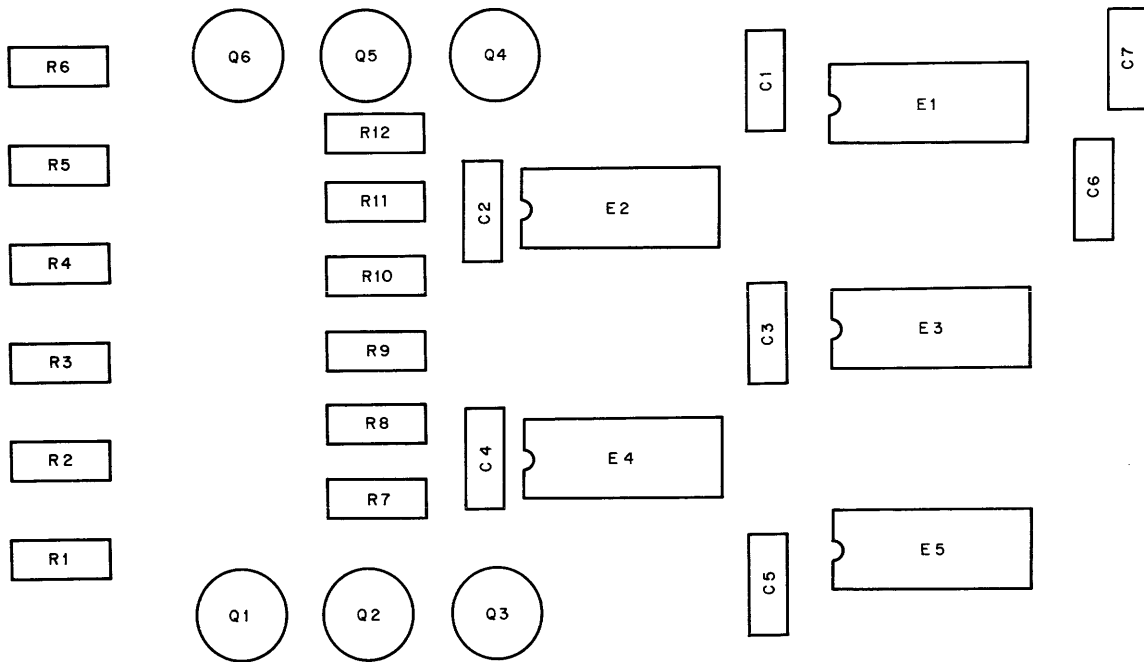
REVISIONS		
CHK	CHG NO.	REV.
MR	00001	B

DRN. BUTLER	DATE 5/17/69
CHK'D. T. A. Hallette	DATE 5/17/69
ENG. A. E. Shaul	DATE 6-16-69
PROD.	DATE

[illegible]

digital
EQUIPMENT
CORPORATION
MAYNARD, MASSACHUSETTS

TITLE										DATA BUS DRIVER									
										M62I									
SIZE		CODE		NUMBER						REV.									
B		CS		M62I-0-I						B									
PRINTED CIRCUIT REV.										A									



M621