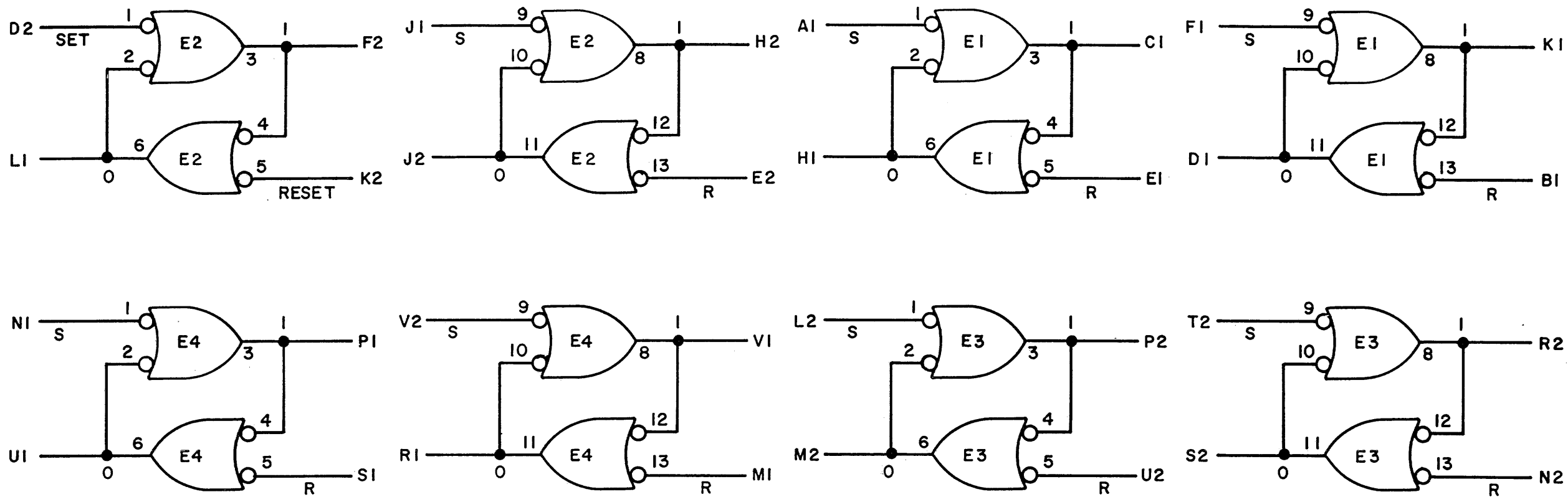
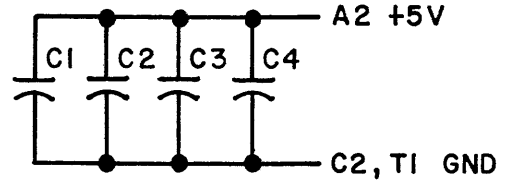


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NOTES
PIN 7 ON EACH IC = GND
PIN 14 ON EACH IC = +5V



CI THRU C4	CAP. .01MFD 50V-0+20% DISC	1000001
E1 THRU E4	INTEGRATED CKT. DEC7400N	1905575
	PARTS LIST	A-PL-M203-0-0
REFERENCE DESIGNATION	DESCRIPTION	PART NO.
PARTS LIST		

REVISIONS	CHK CHG NO. REV.	DRN. <i>M. Haller</i>	DATE <i>7-11-67</i>	TRANSISTOR & DIODE CONVERSION CHART		digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	TITLE SET-RESET FLIP FLOP MODULE M203	
		CHK'D	DATE	DEC	EIA		SIZE B	CODE CS
		ENG. <i>M. S. G. G. G.</i>	DATE				NUMBER M203-0-1	REV. B
		PROD.	DATE				PRINTED CIRCUIT REV. C	