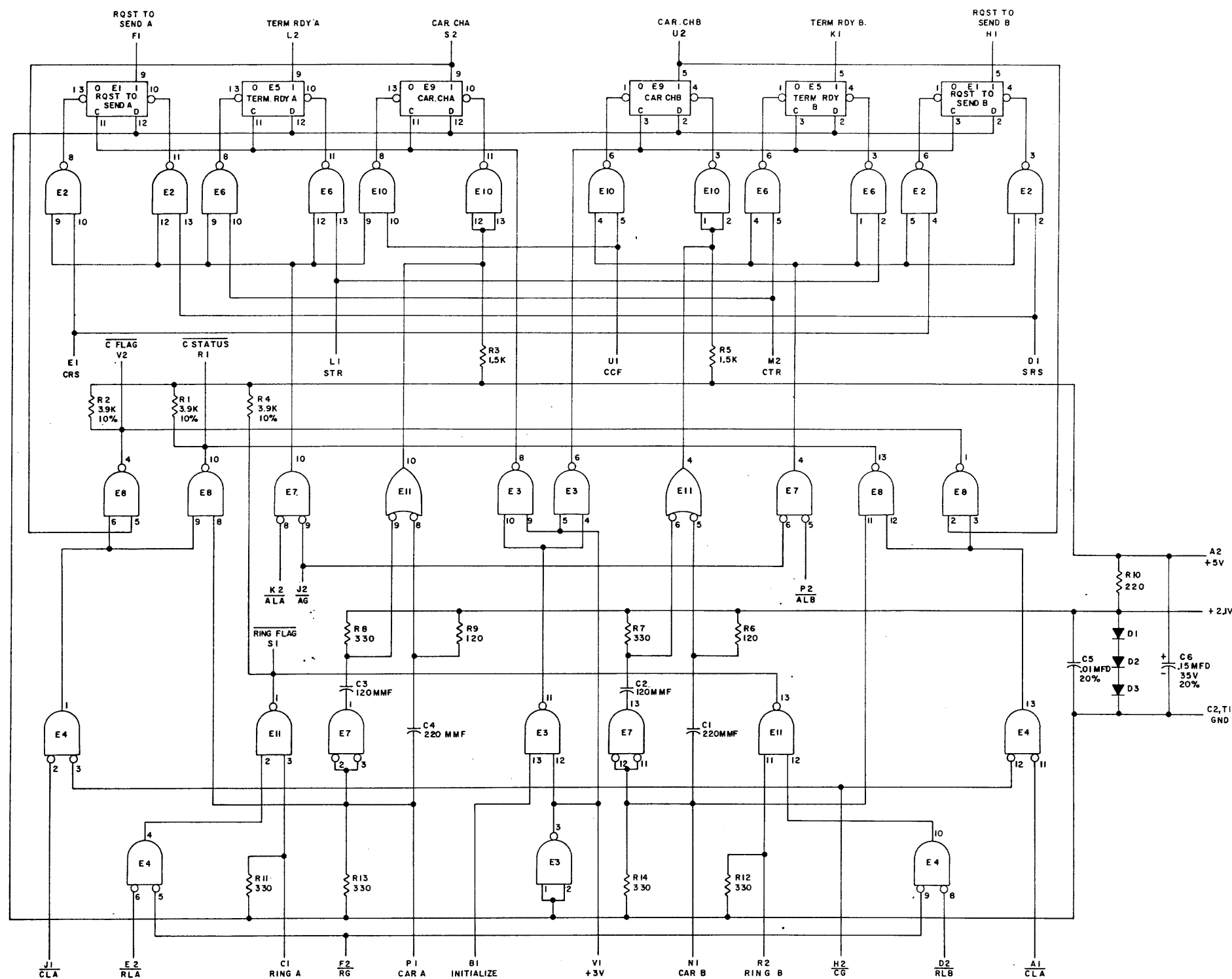


THIS SCHEMATIC IS FURNISHED ONLY FOR TEST AND MAINTENANCE PURPOSES. THE
CIRCUITS ARE PROPRIETARY IN NATURE AND SHOULD BE TREATED ACCORDINGLY
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UNLES OTHERWISE INDICATED
PIN 7 ON EACH IC = GND
PIN 14 ON EACH IC = +5V
E1, E5, E9 ARE DEC 7474N

E4, E7, ARE DEC 7402N
E8, E11 ARE DEC 7401N
E2, E3, E6, E10 ARE DEC 7400N
DIODES ARE D662
RESISTORS ARE 1/4W, 5%
CAPACITORS ARE 100V 5%

REVISIONS		
CHK	CHG NO.	REV.
24	00001	C

DRN.	DATE
GP Chroma	8-19
CHMD	DATE
A. J. Hedlett	5/19/
ENG.	DATE
A. L. Hedlett	7-1
PROD.	DATE

TRANSISTOR & DIODE CONVERSION CHART			
DEC		EIA	
D662		IN645	

digital	TITLE ASYNC MODEM LINE CONTROL		
	M 753		
EQUIPMENT CORPORATION	SIZE D	CODE CS	NUMBER M 753-0-1
MATTHEW NAKAGUCHI/ETS			REV. C