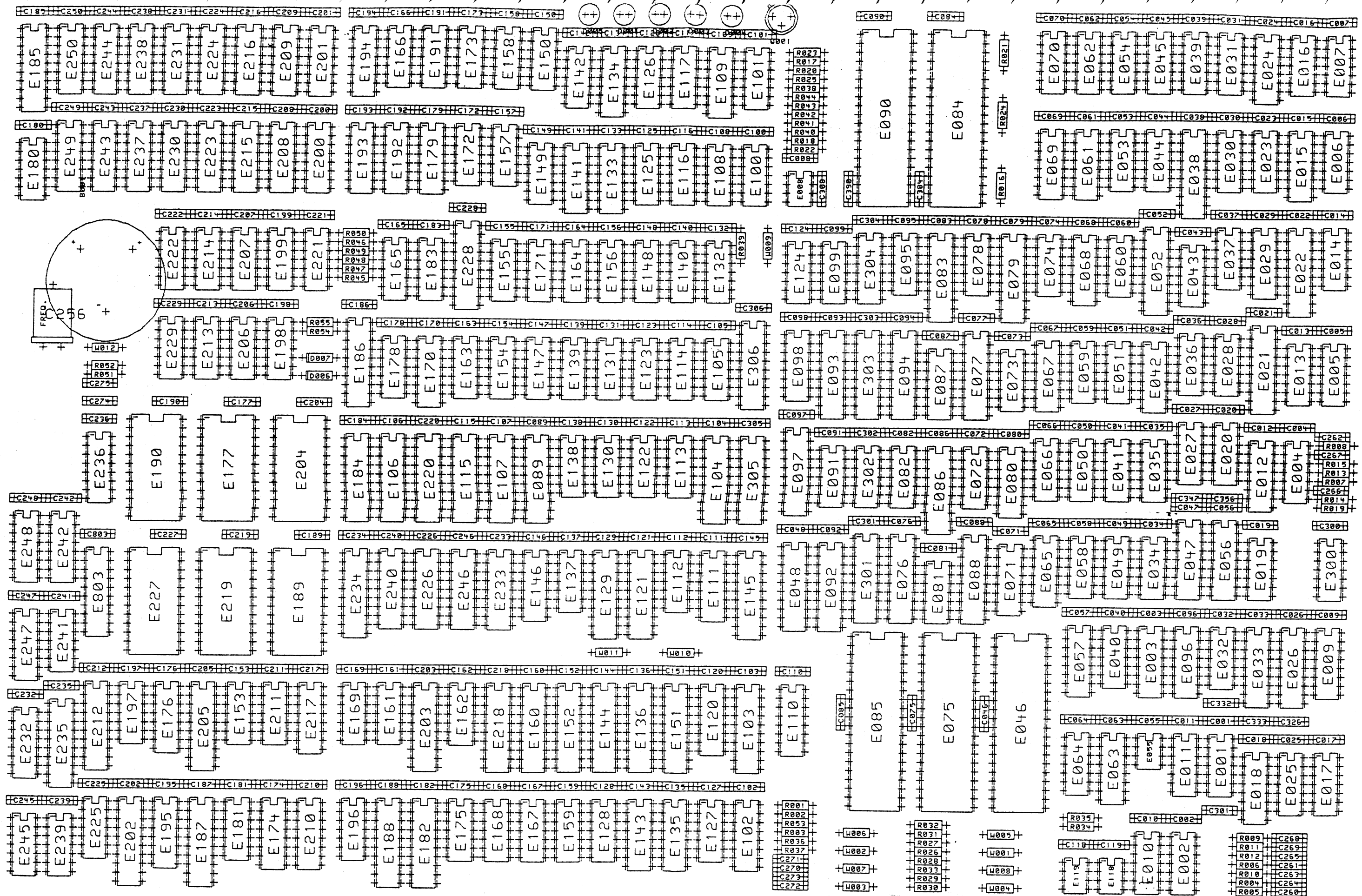
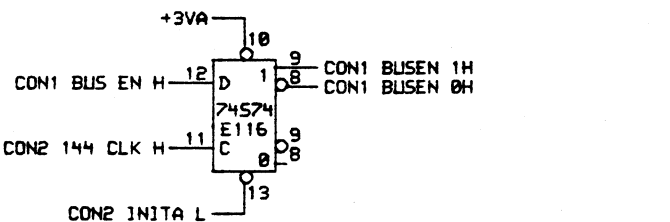
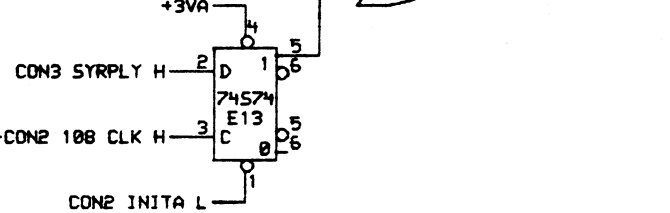
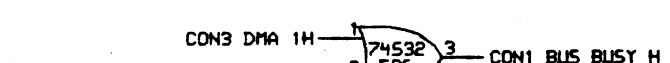
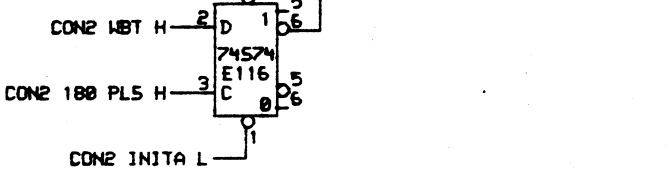
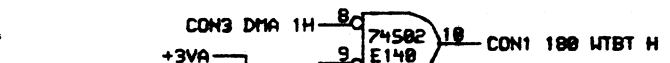
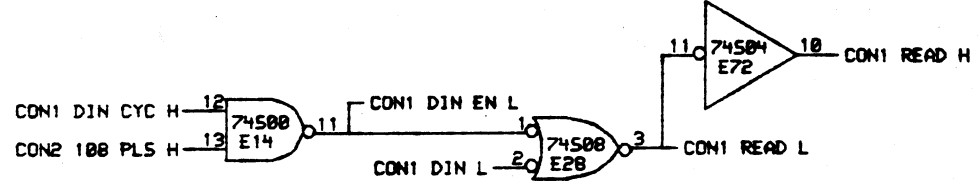
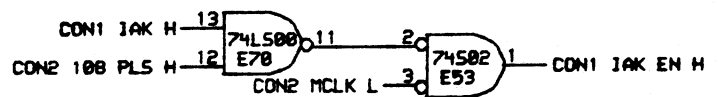
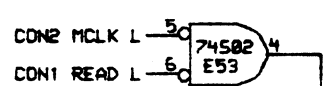
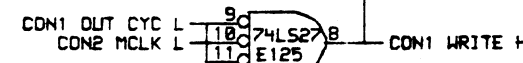
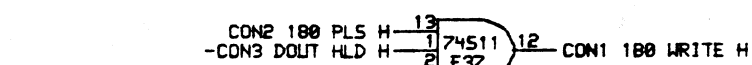
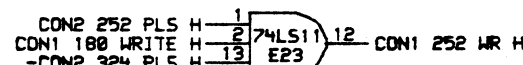
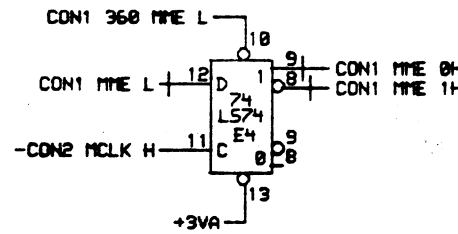
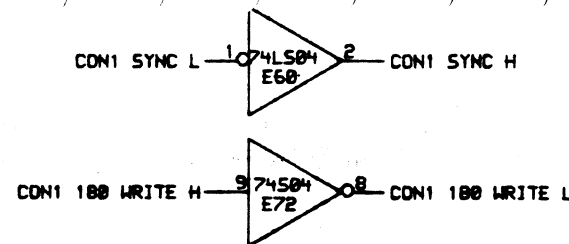
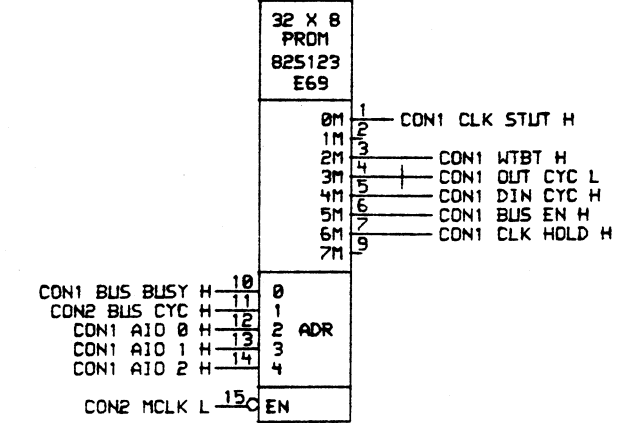
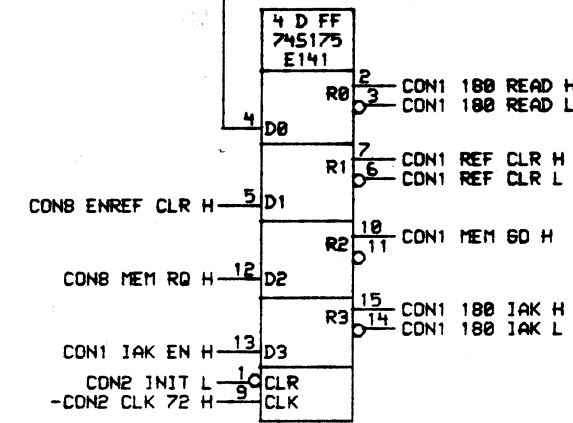
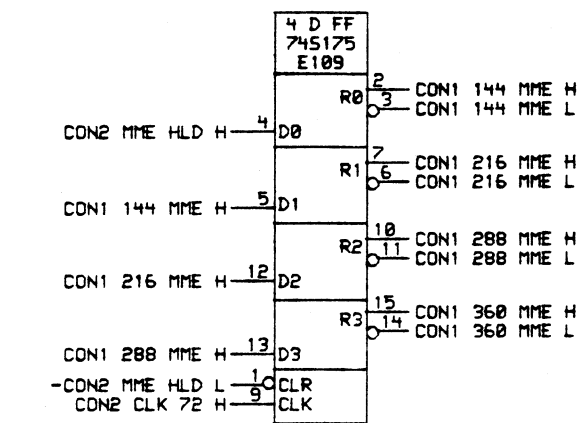
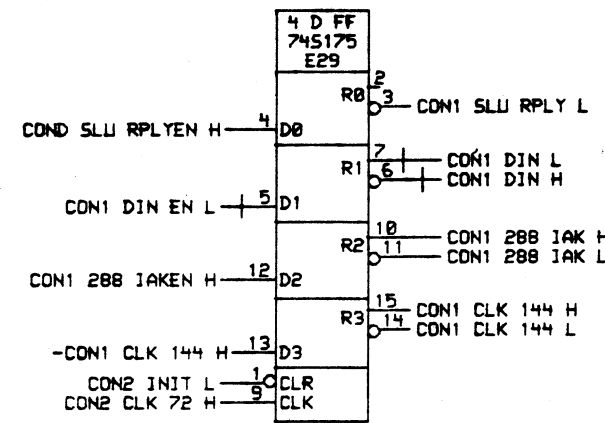
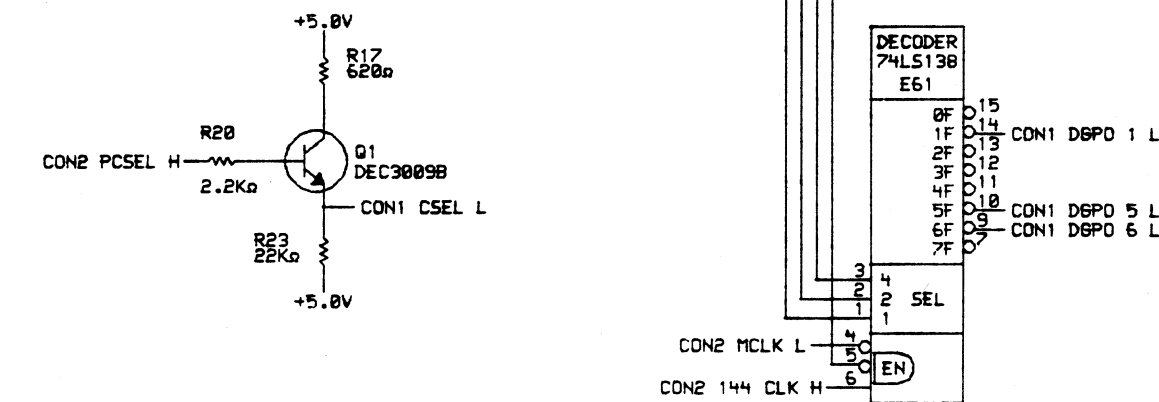
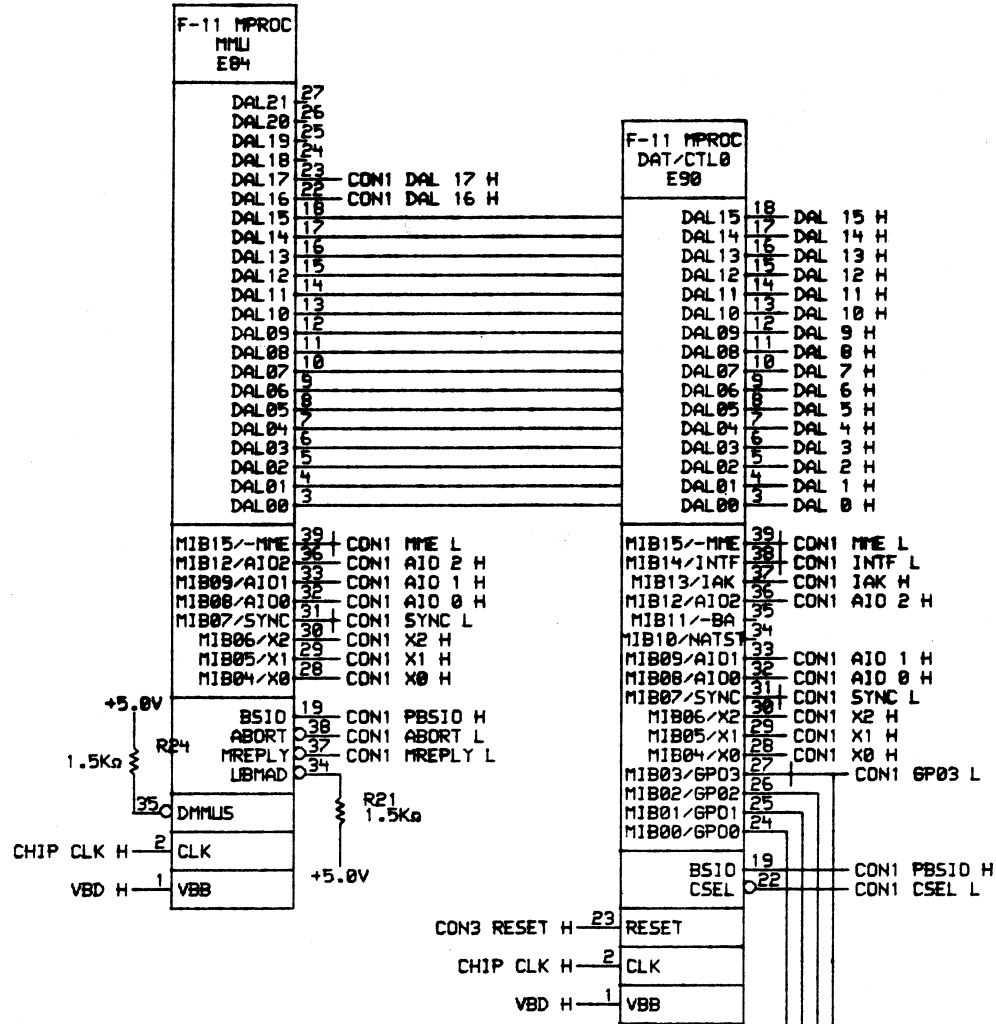


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3CSL3
5CSL4
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3CSL9
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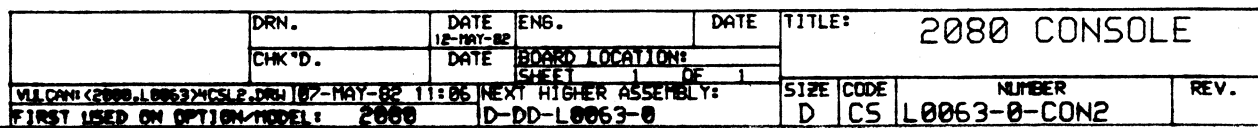


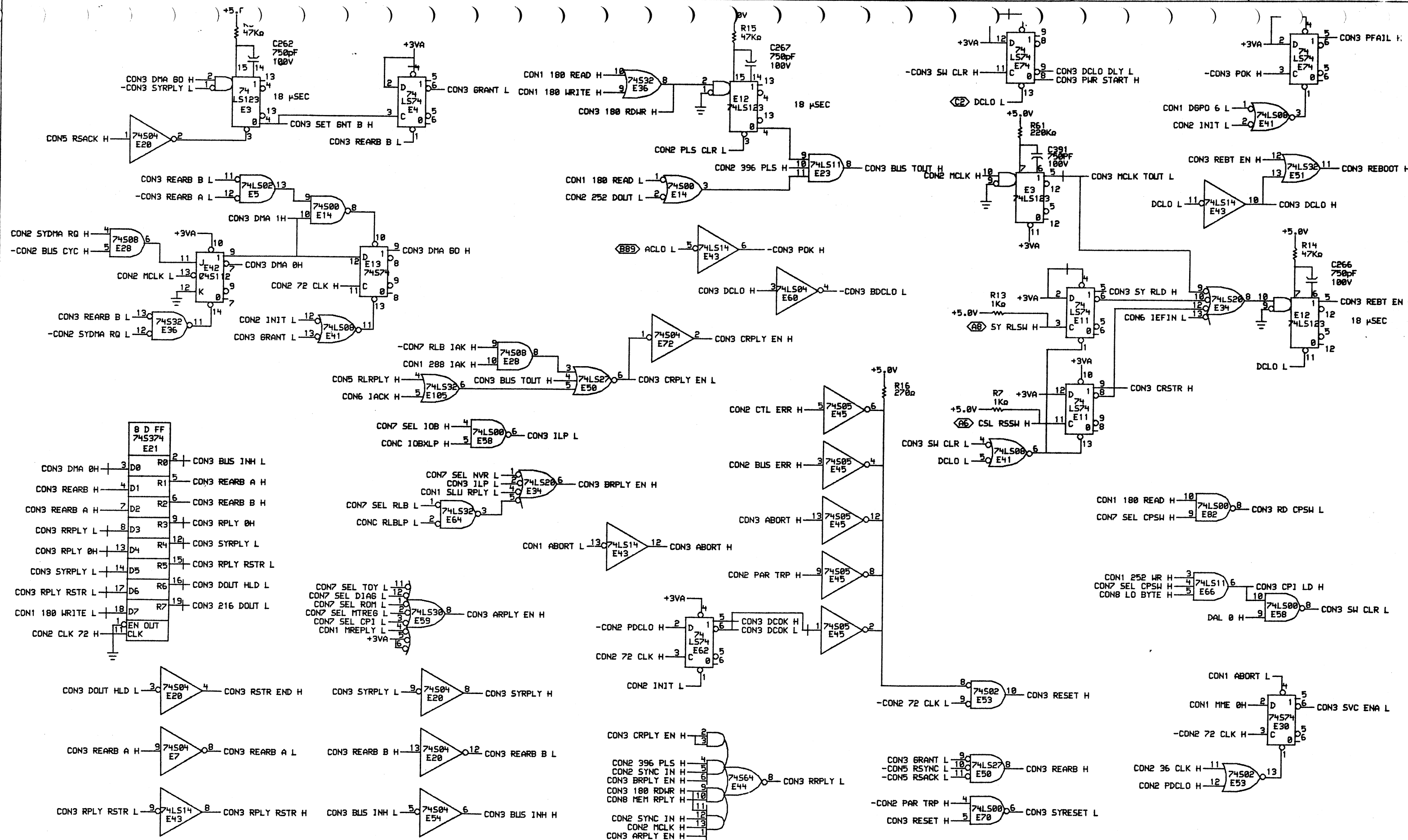


2080 CONSOLE CPU



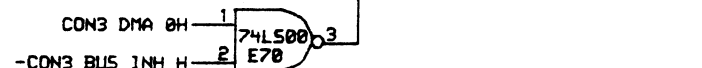
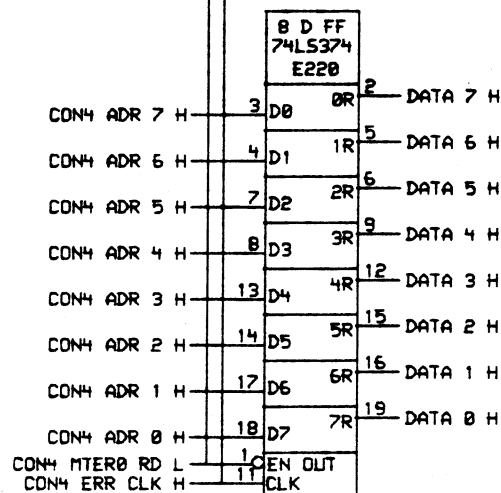
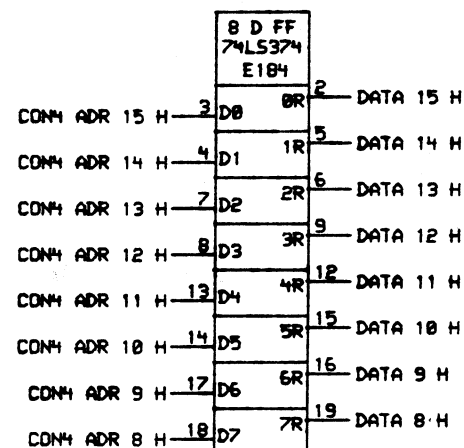
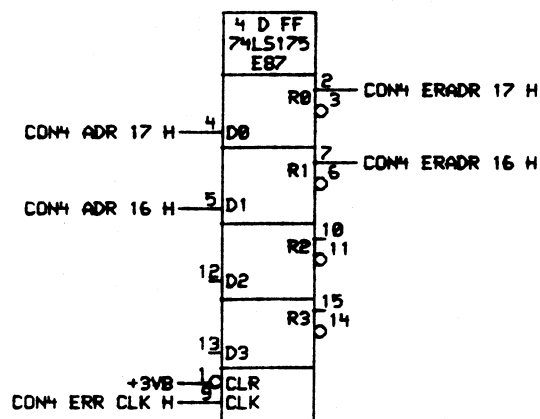
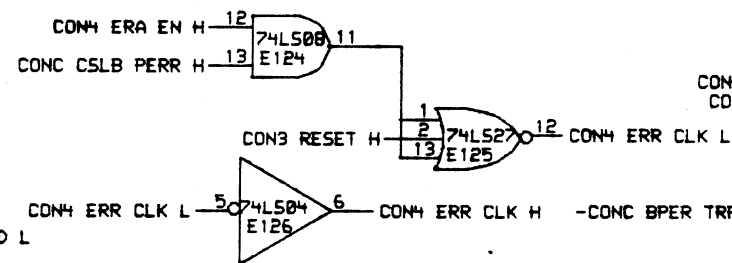
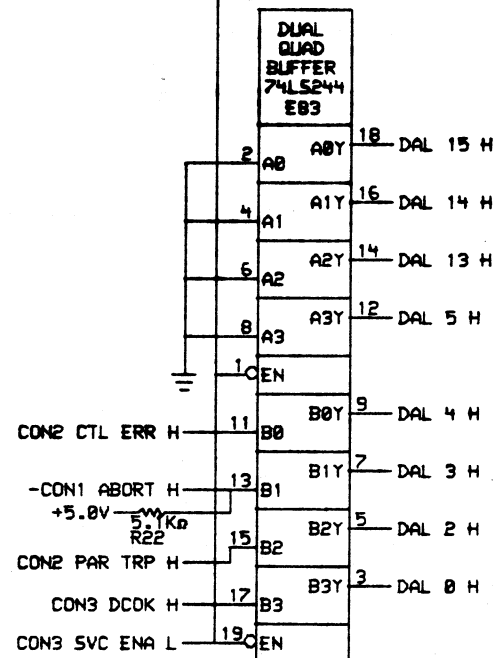
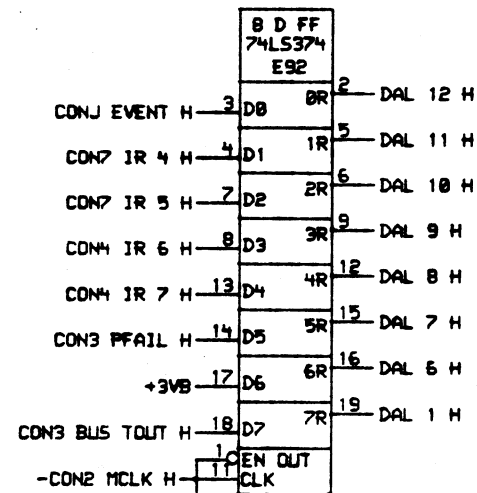
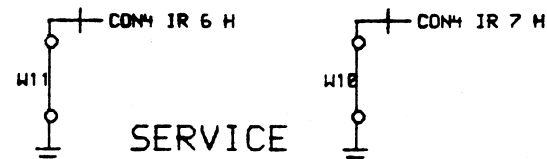
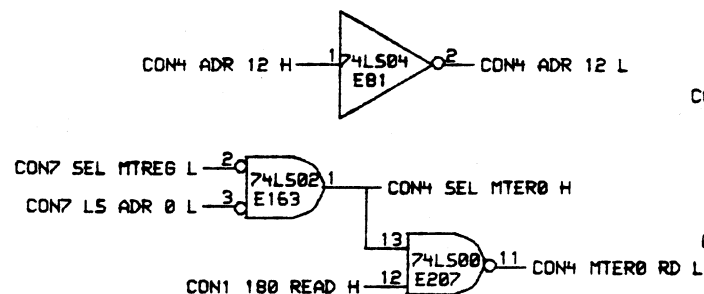
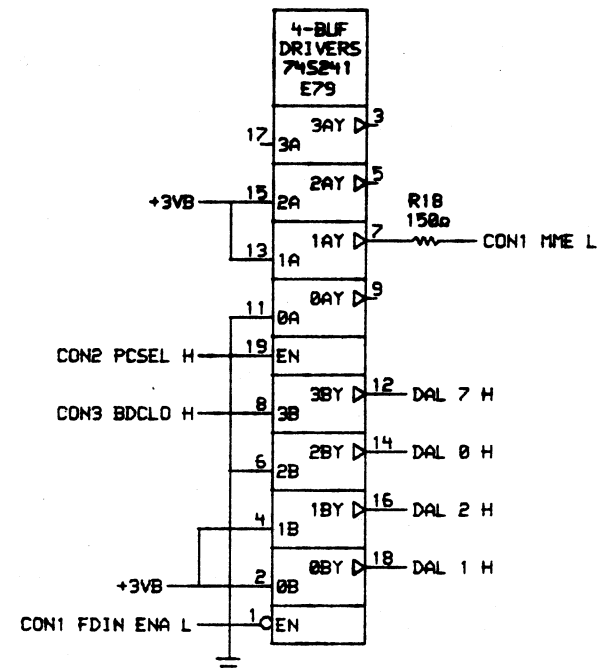
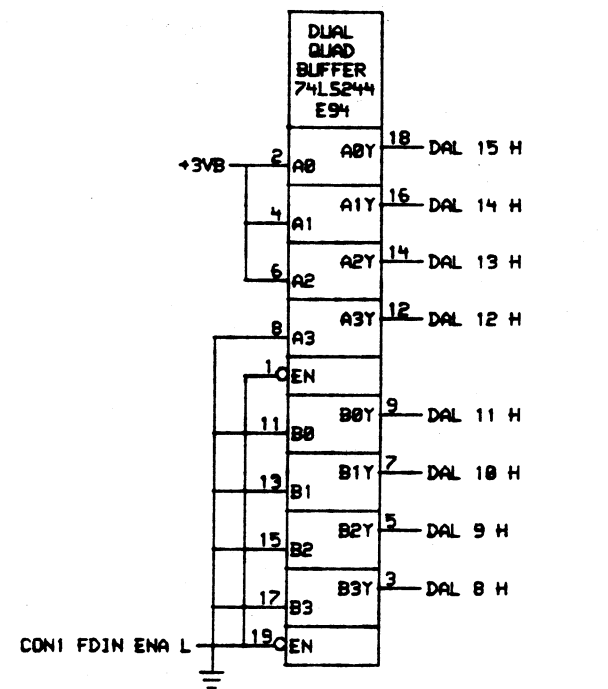
DRN.	DATE	ENG.	DATE	TITLE:	2080 CONSOLE CPU
CHK'D.	DATE	BOARD LOCATION:	SHEET	1	OF 1
VLCAN (2080-10063) HCL 1	DATE	NEXT HIGHER ASSEMBLY:	SIZE	CODE	NUMBER
FIRST USED ON OPTION/MODEL:	2080	D-DD-L0063-0	D	CS	L0063-0-CON1
					REV.



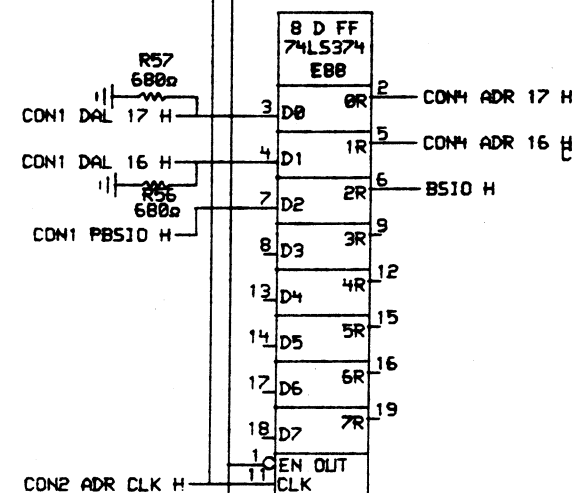
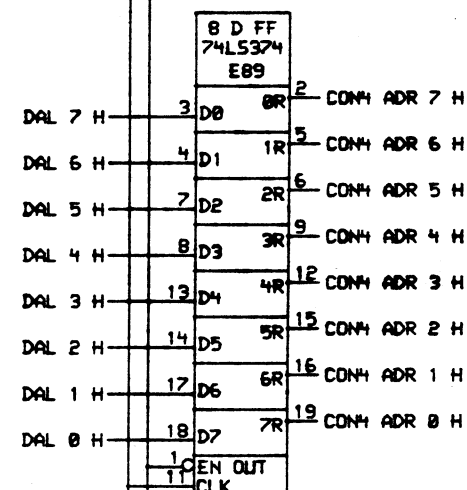
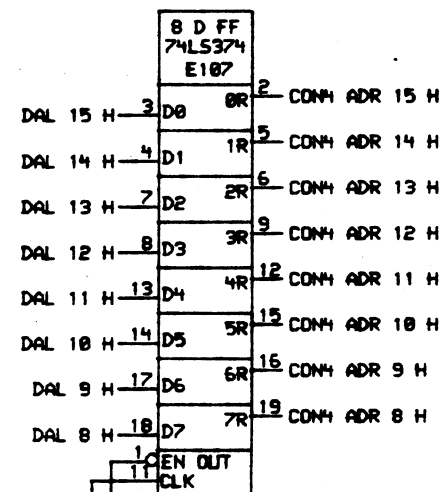


DRN.	DATE	ENG.	DATE	TITLE:	2080 CONSOLE
CHK'D.	DATE	BOARD LOCATION:			
VIL CANI CP080.L0063.DRAW 131-MAR-82 10:09		SHEET 1 OF 1			
FIRST USED ON OPTION MODEL: 2080		NEXT HIGHER ASSEMBLY:			
		SIZE	CODE	NUMBER	REV.
		D	C5	L0063-0-CON3	

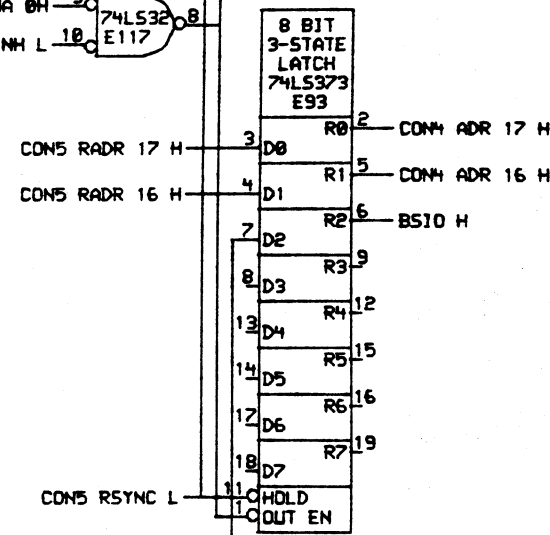
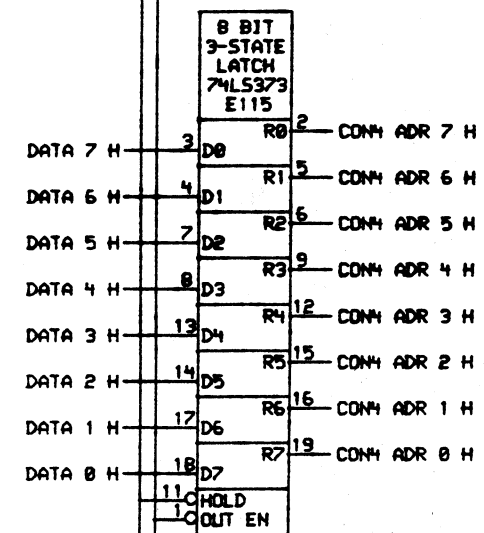
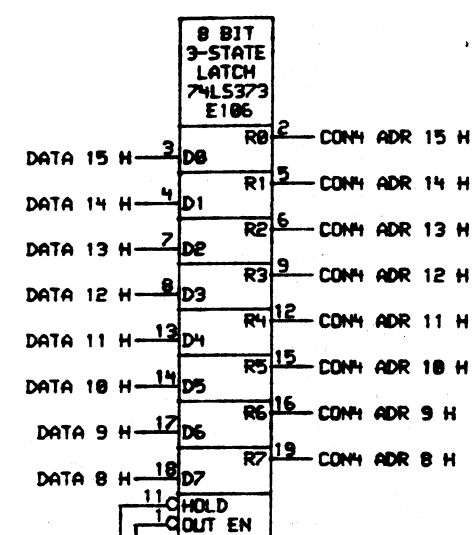
POWER UP



AD. RES. LATCH

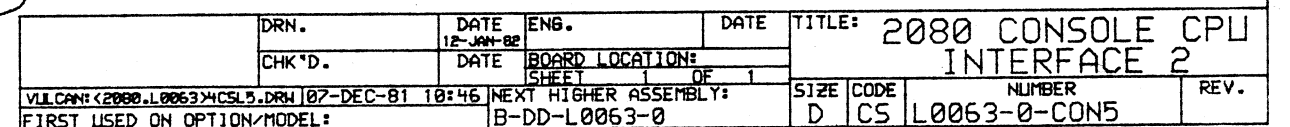


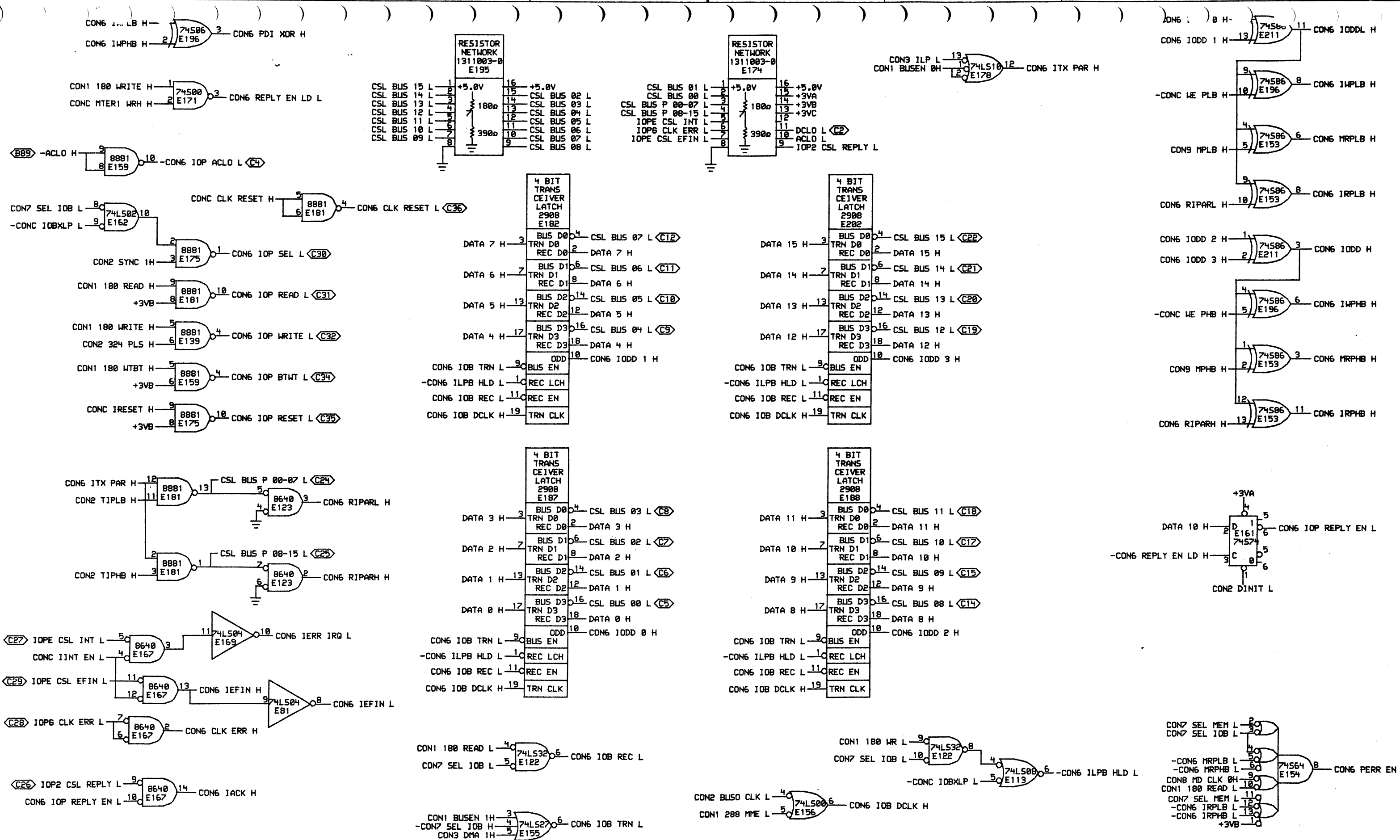
DM1 ADDRESS LATCH



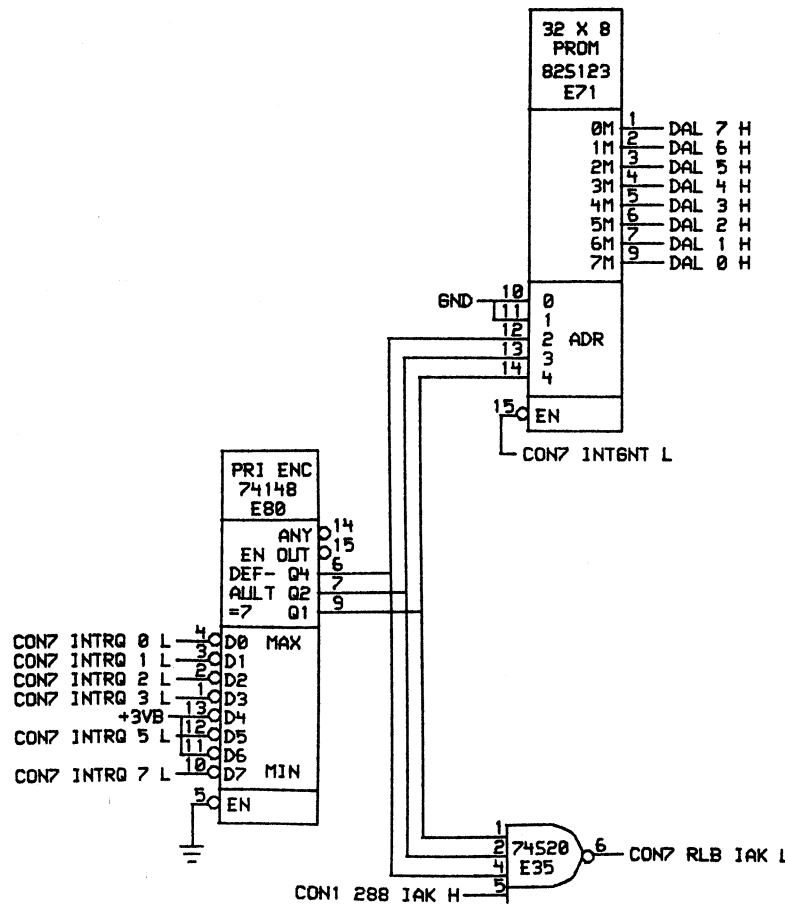
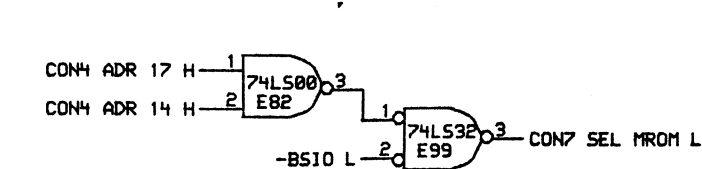
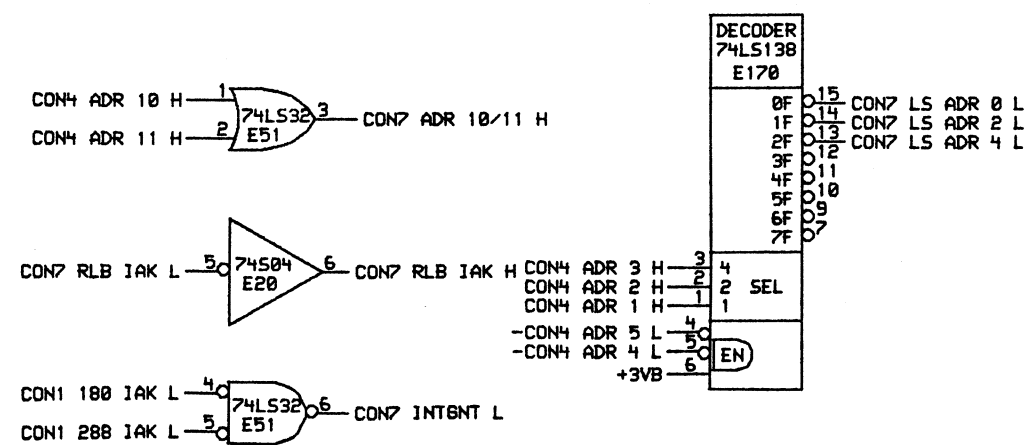
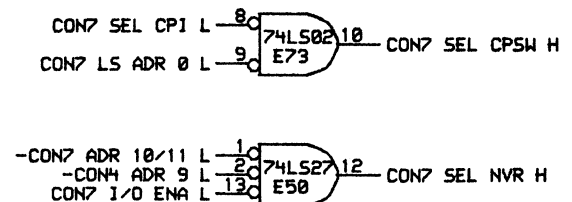
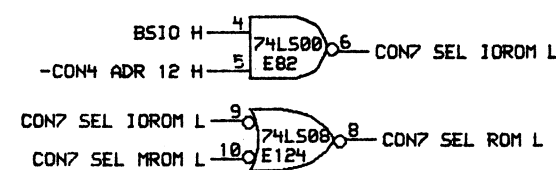
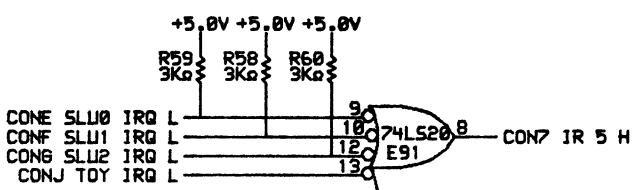
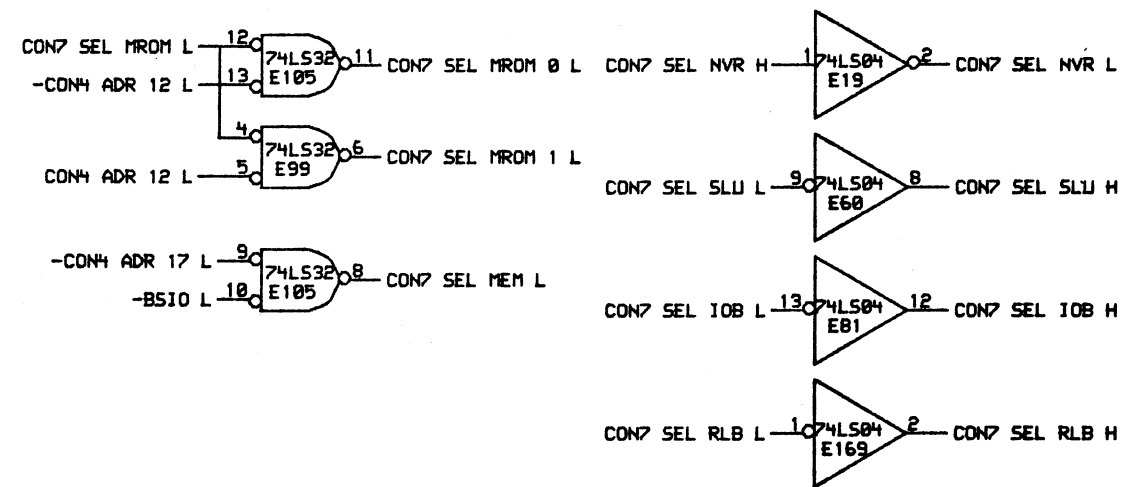
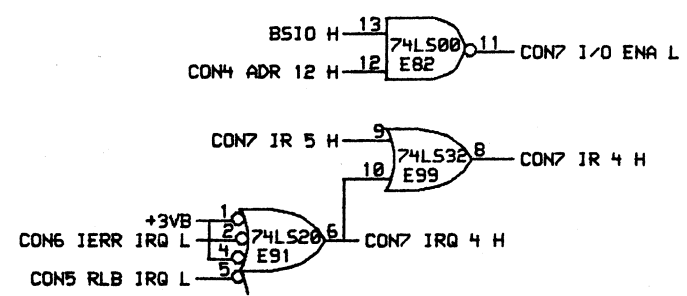
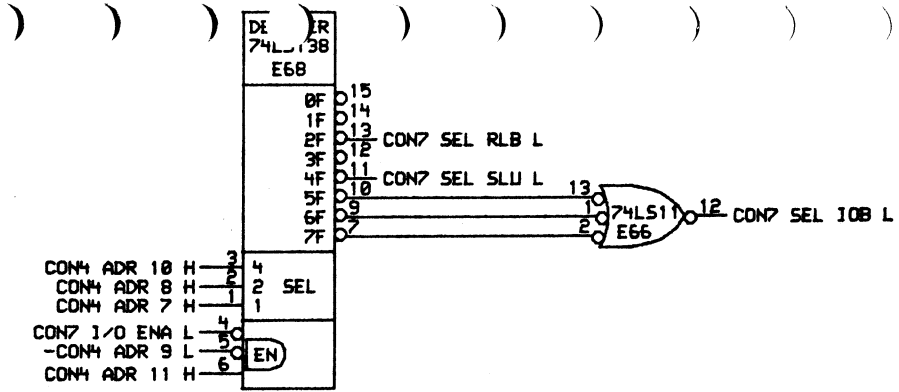
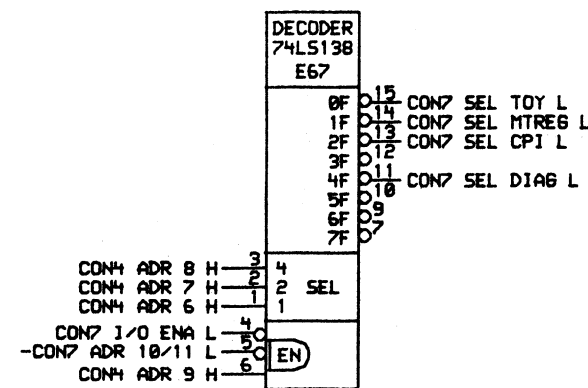
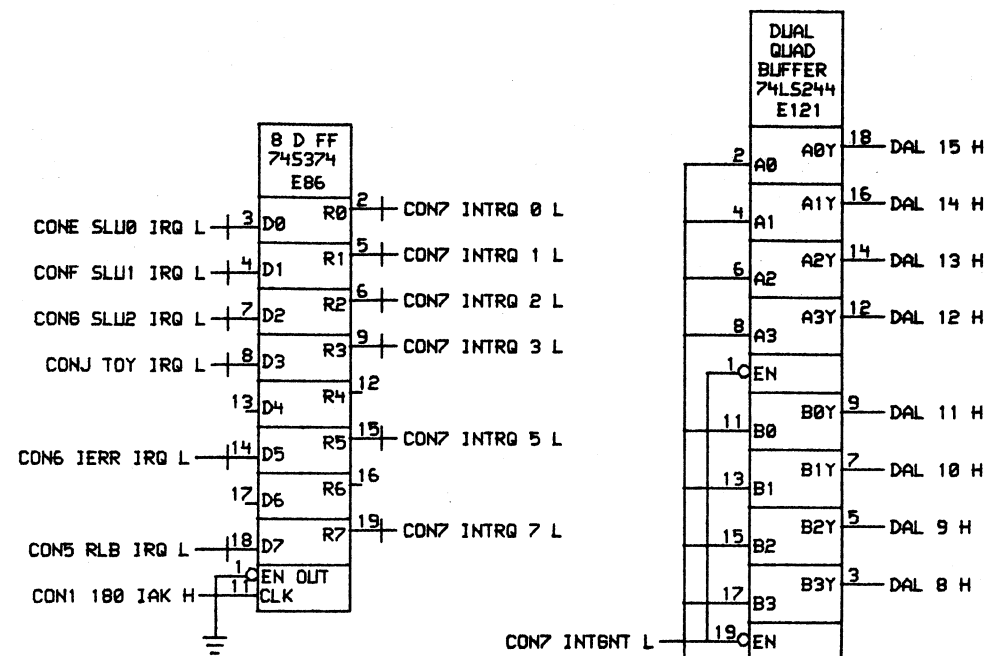
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		CONSOLE
VILCANI (2000.L0063)SCSL4.DRW	07-MAY-82	11:25	NEXT HIGHER ASSEMBLY:	SIZE CODE
FIRST USED ON OPTION/MODEL:	2080	D-DD-L0063-0		NUMBER
				REV.

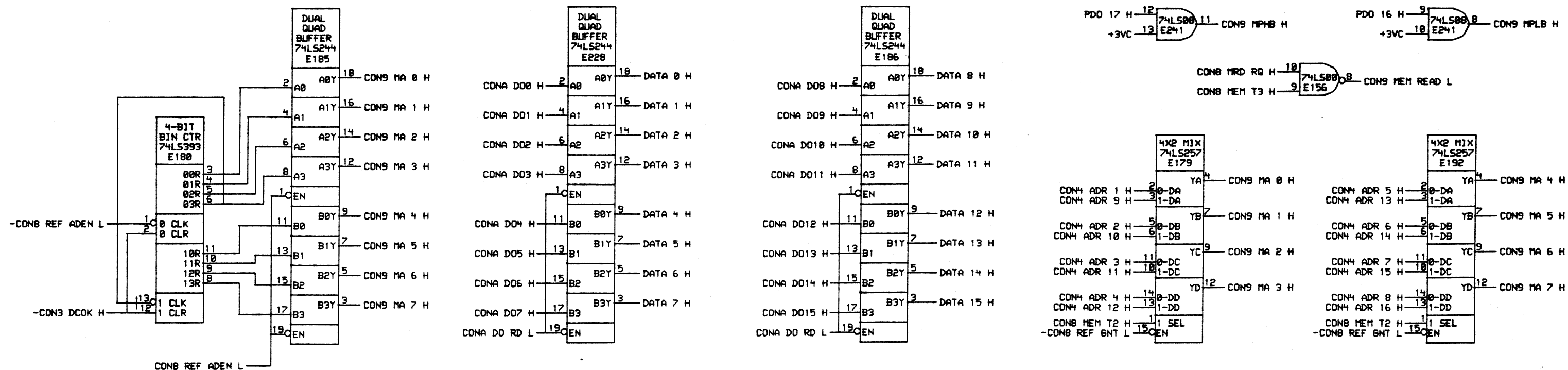
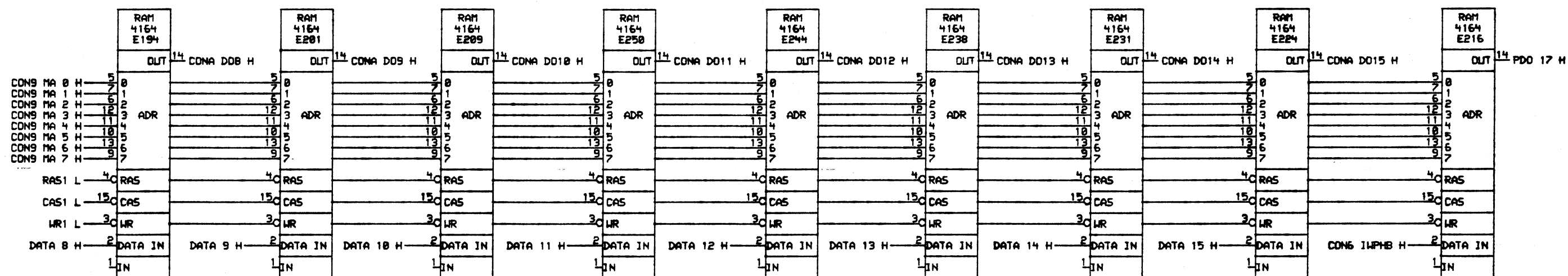
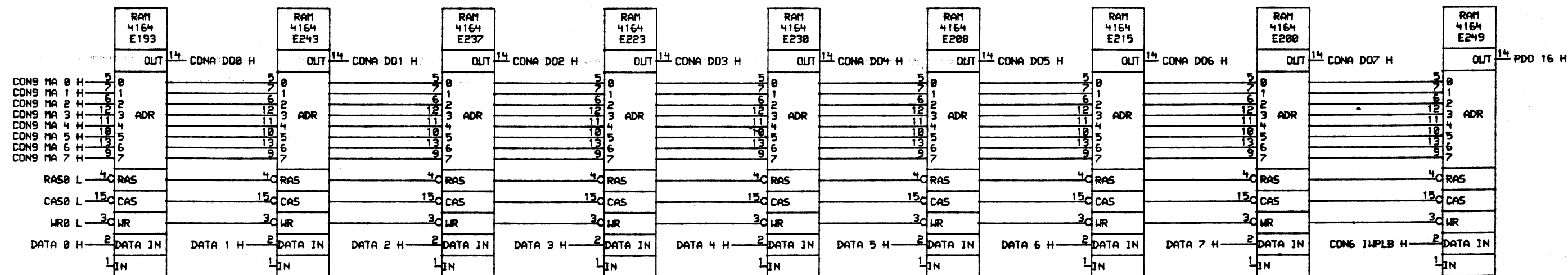
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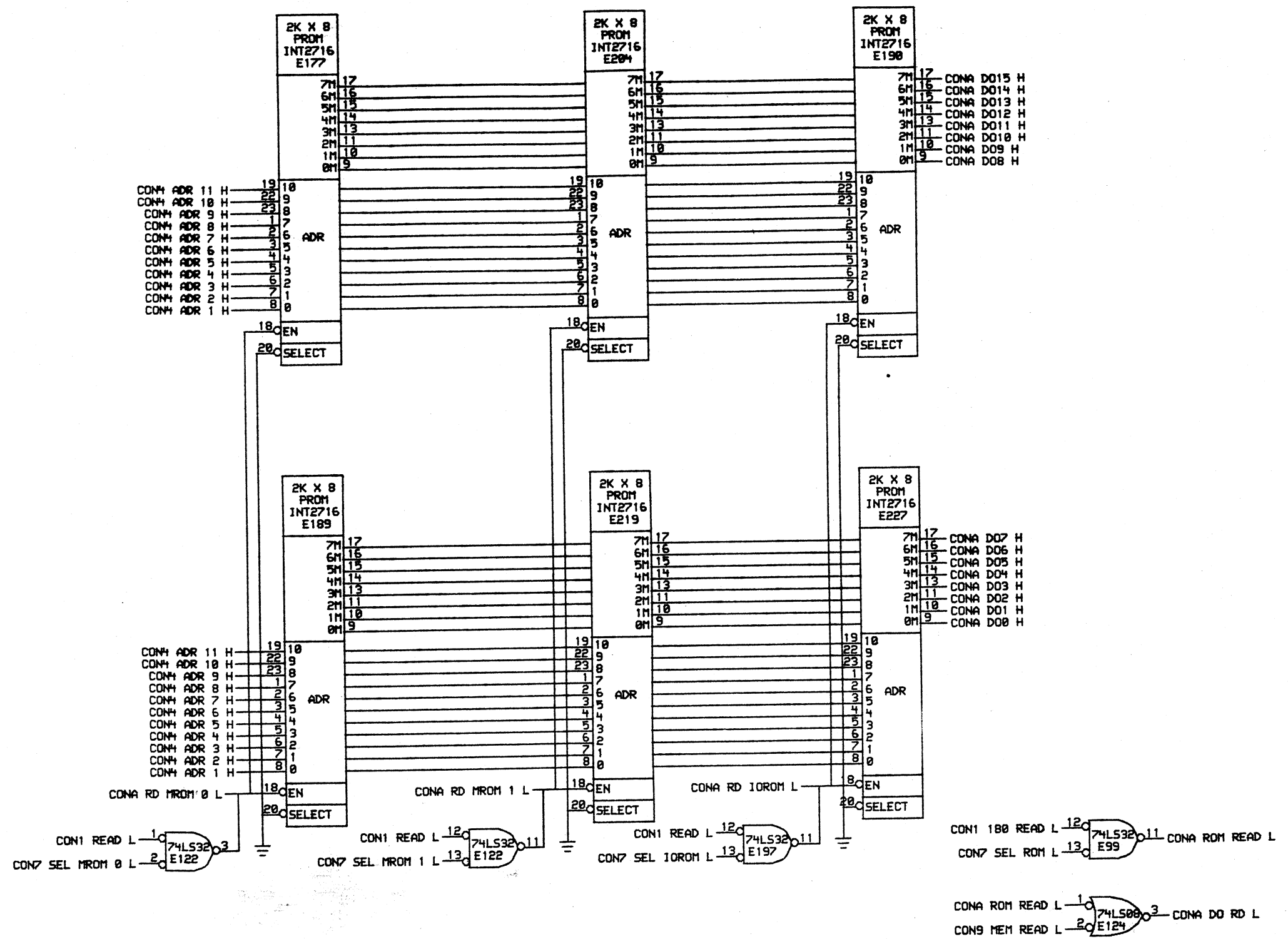


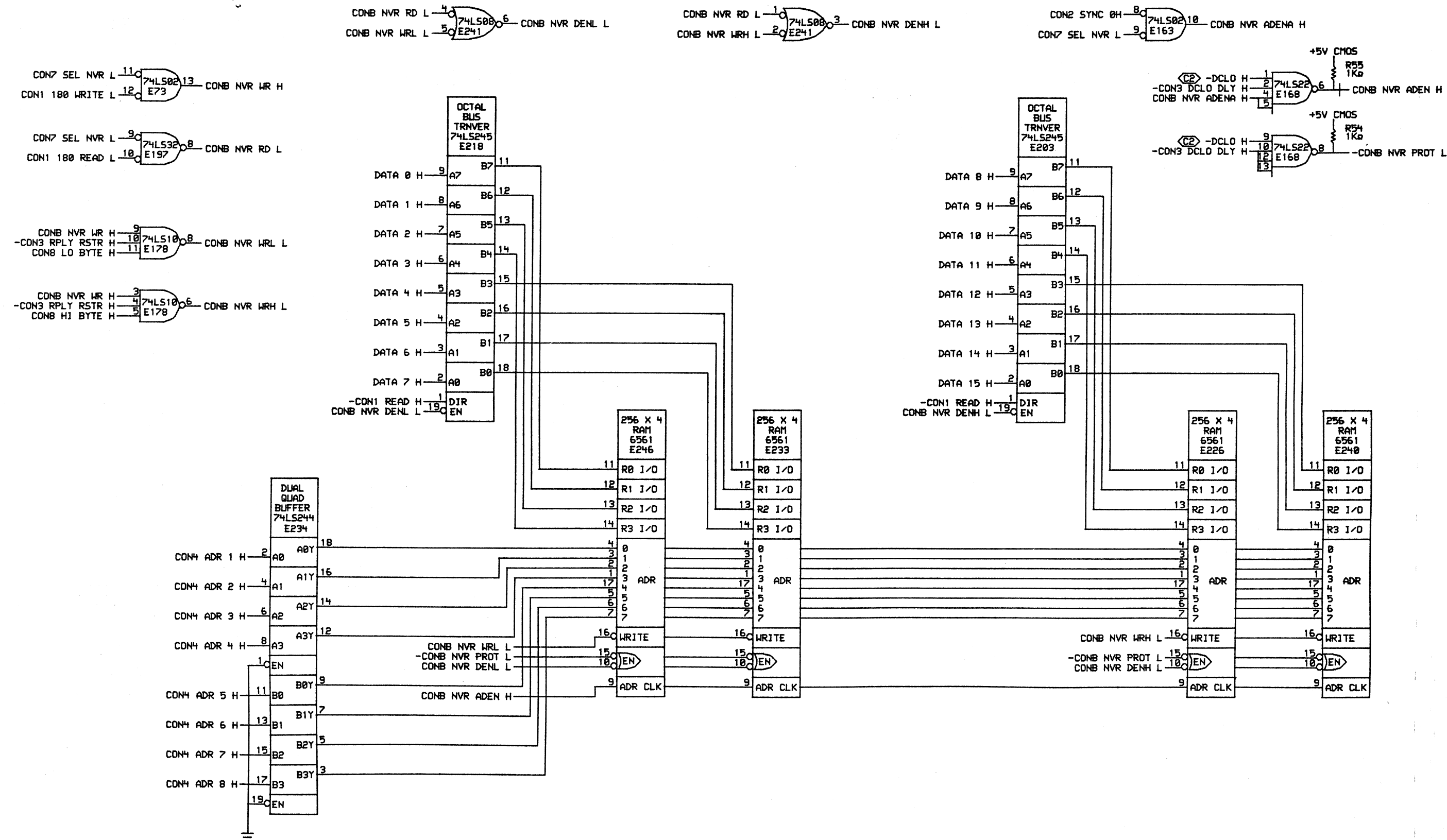


INT VECTOR REG









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D

C

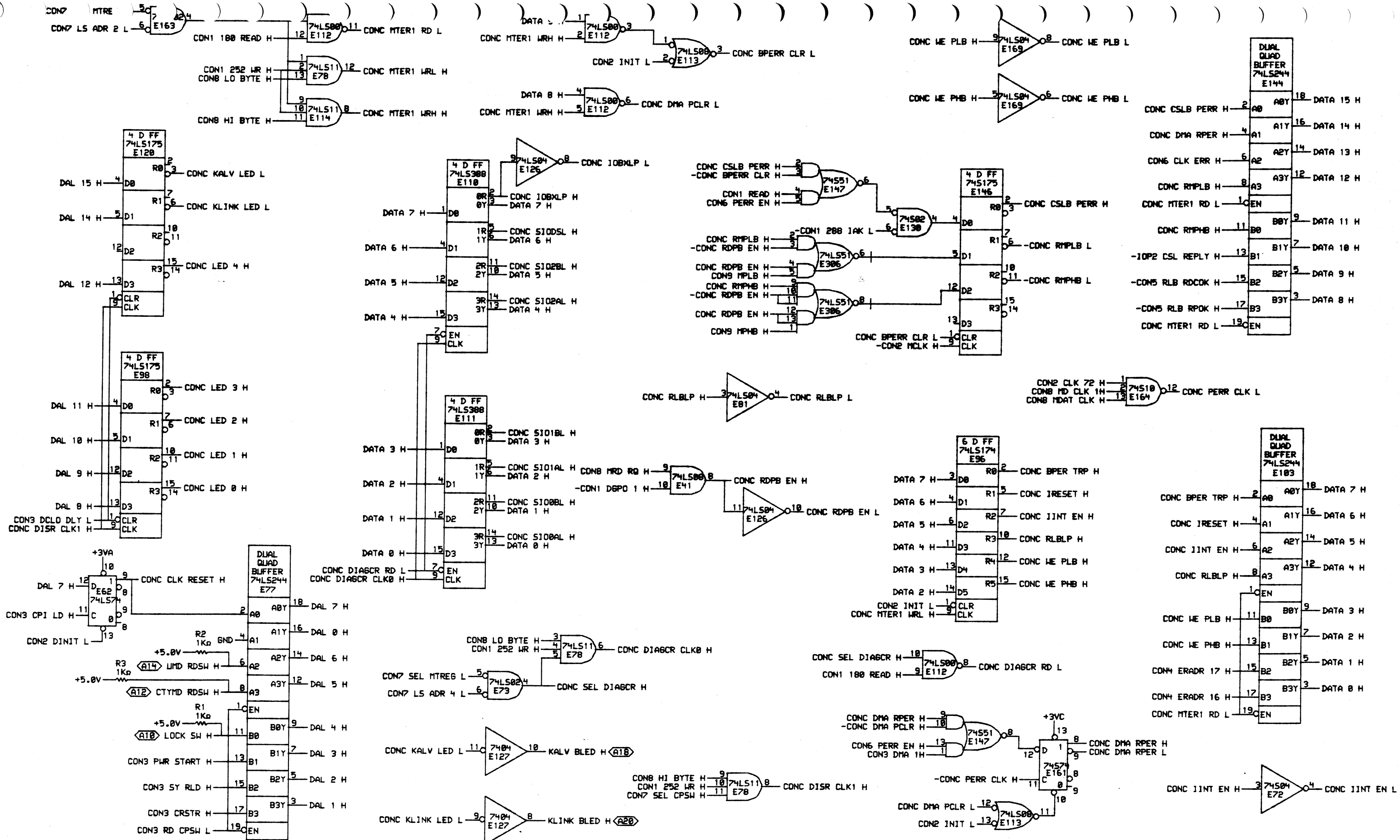
C

B

B

A

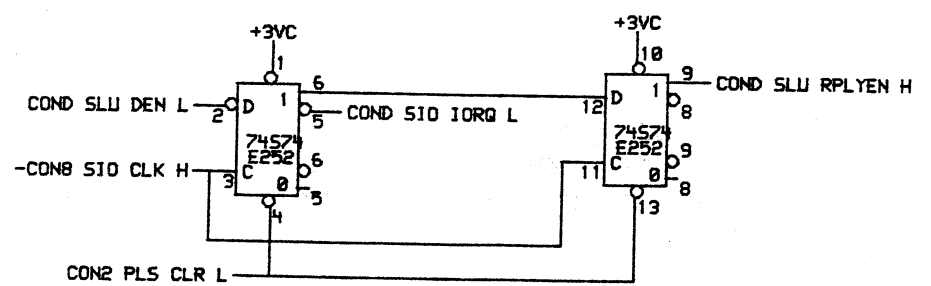
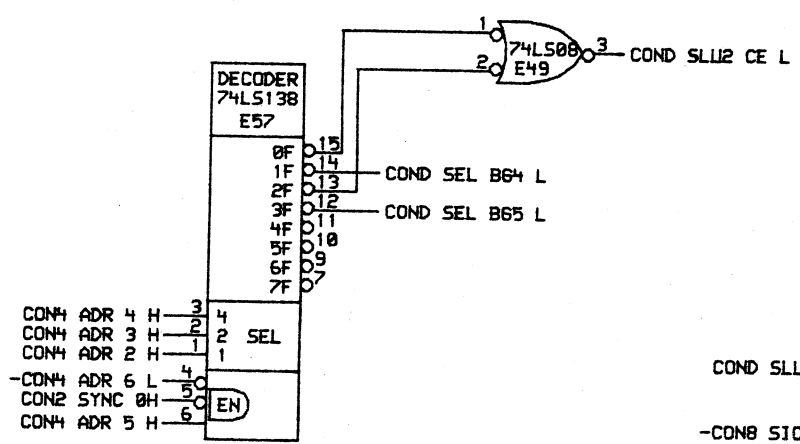
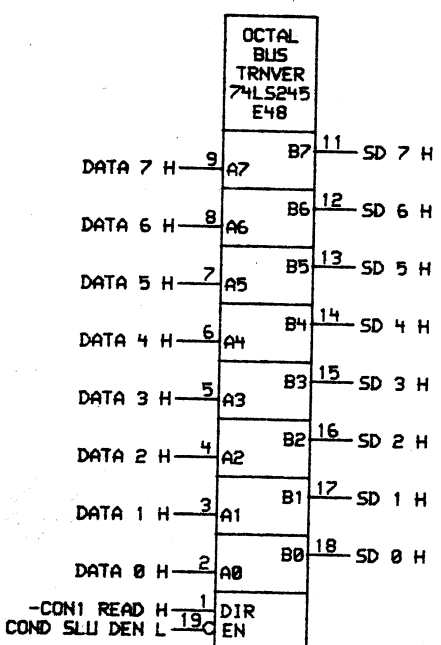
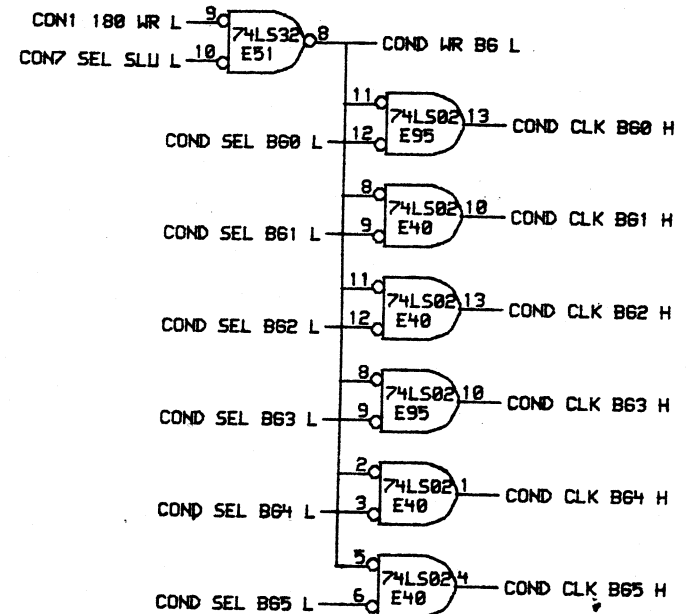
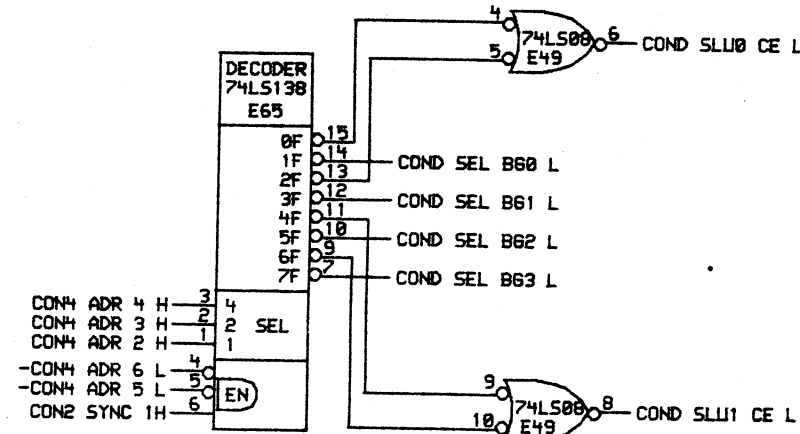
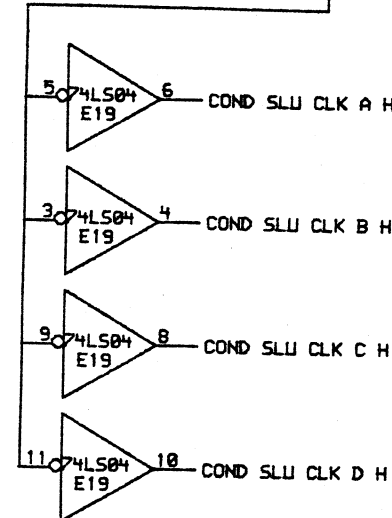
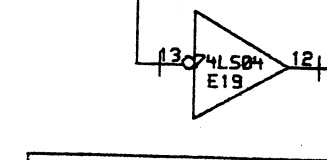
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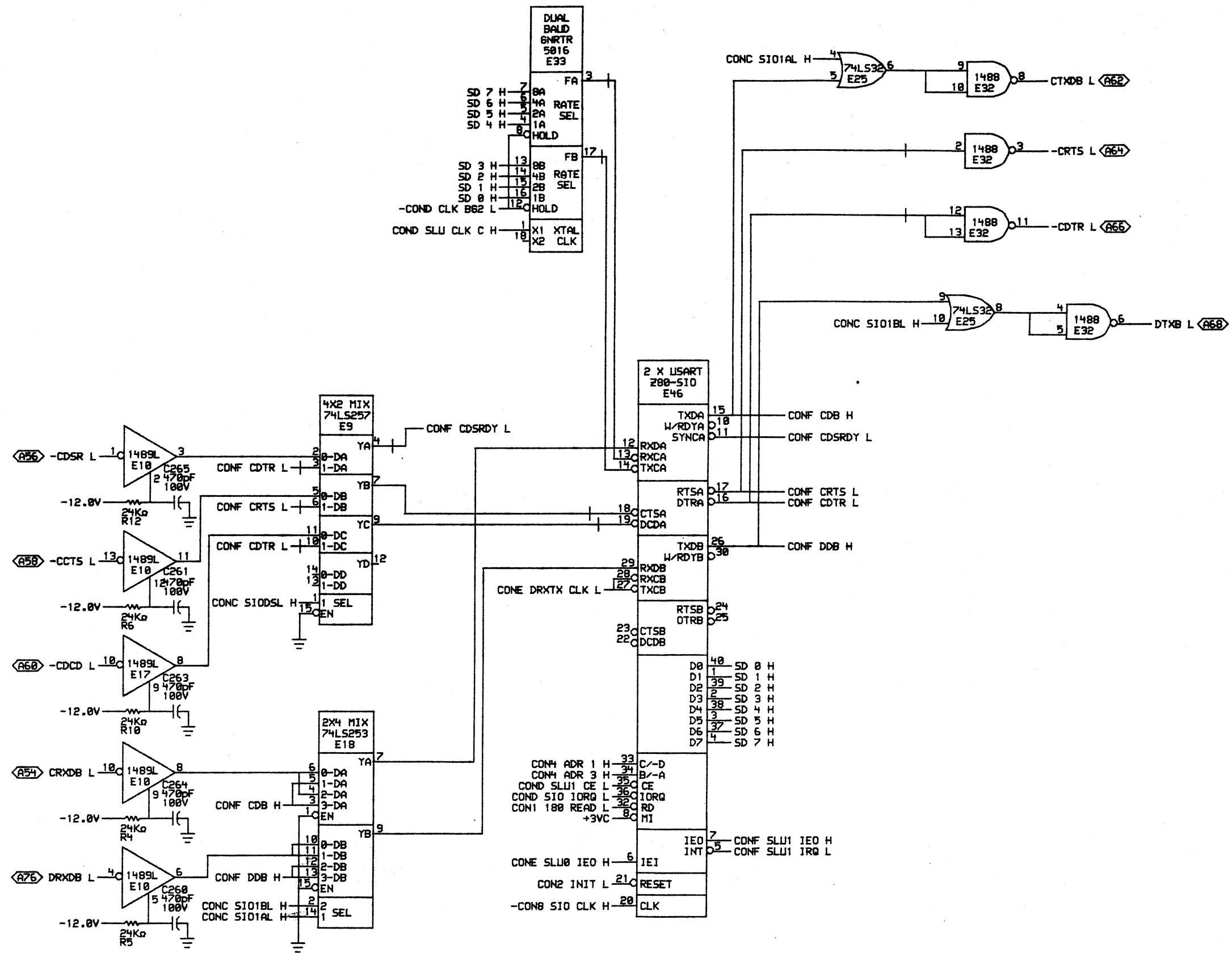


DRN.	DATE	ENG.	DATE	TITLE:	2080 CONSOLE
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	1
VLCAN (2080) L0063-0-CONC	105-MAY-82 10:42	NEXT HIGHER ASSEMBLY:	SIZE	CODE	NUMBER
FIRST USED ON OPTION/MODEL: 2080	D-DD-L0063-0		D	CS	L0063-0-CONC
					REV.

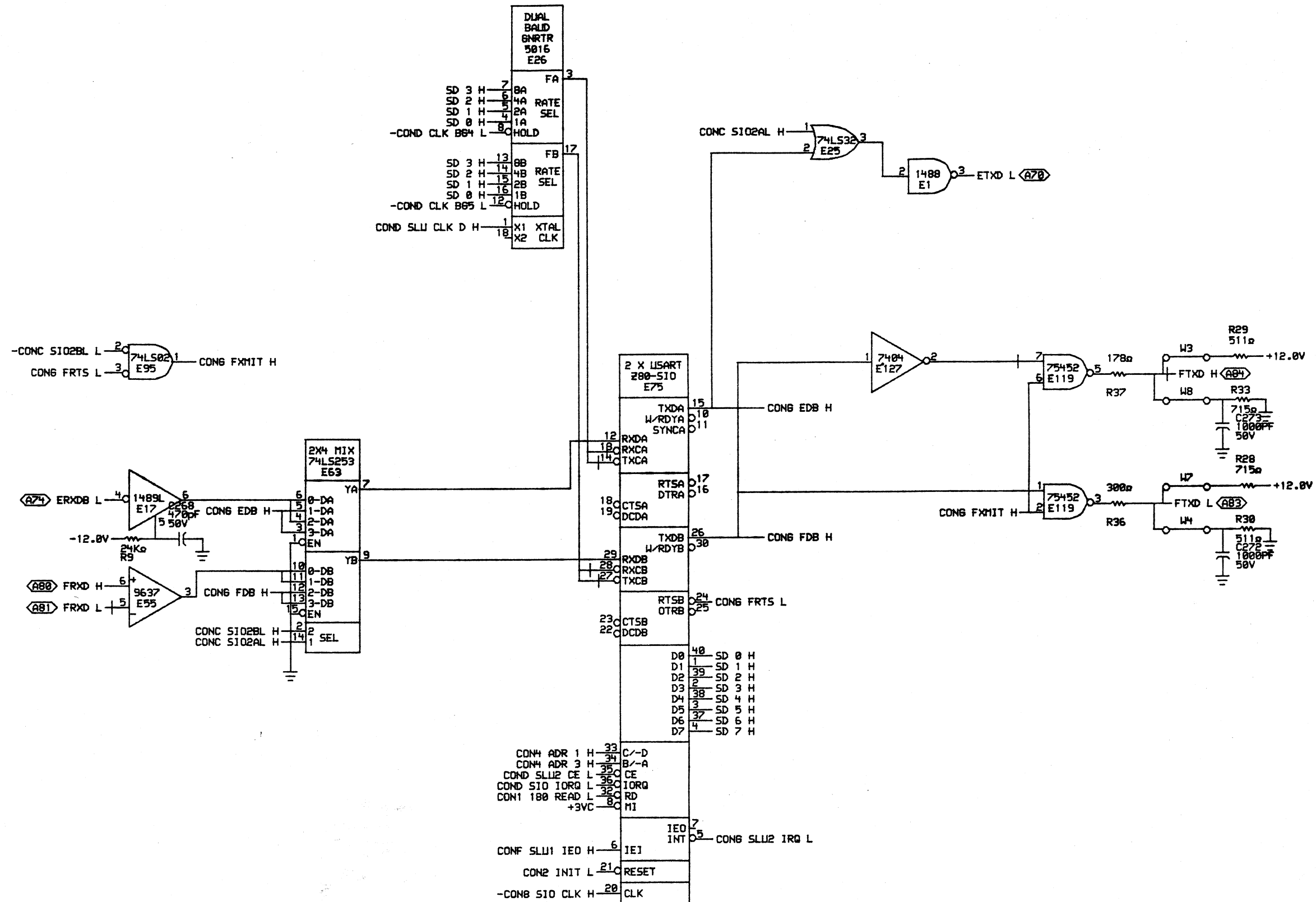
5.0688 MHZ
CRYSTAL
OSC
E300

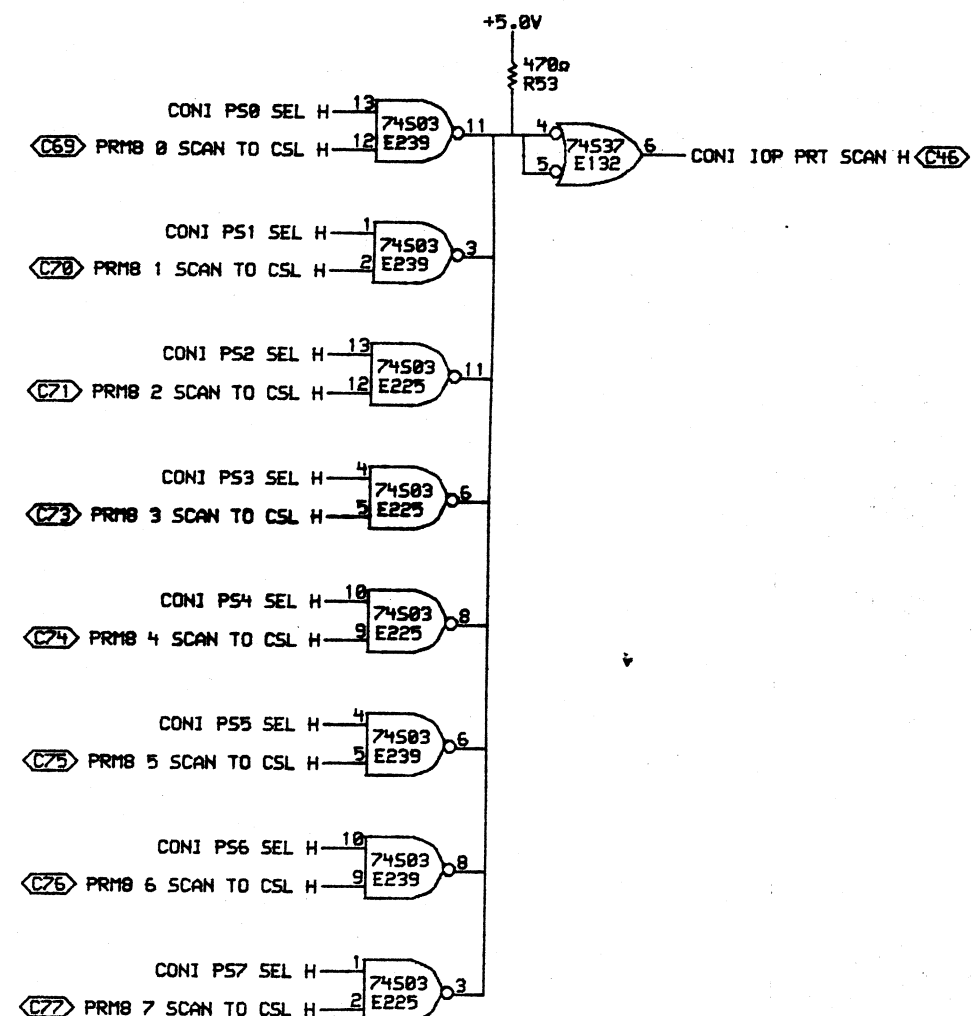
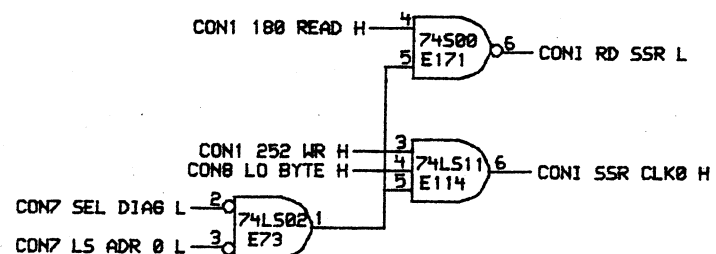
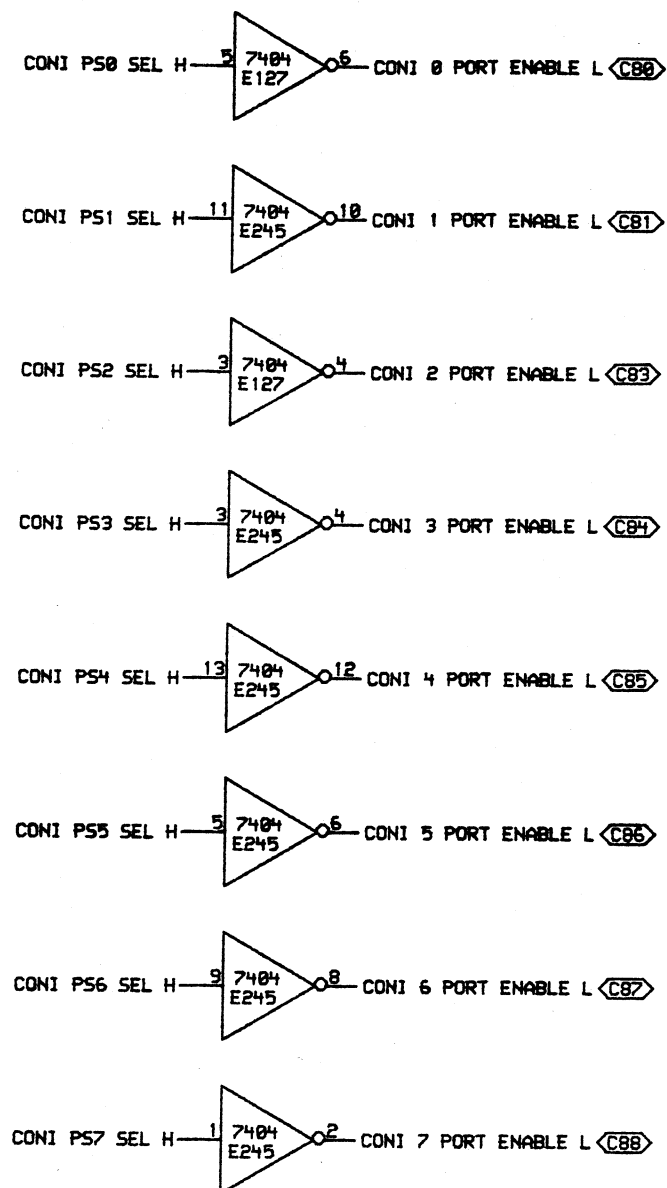
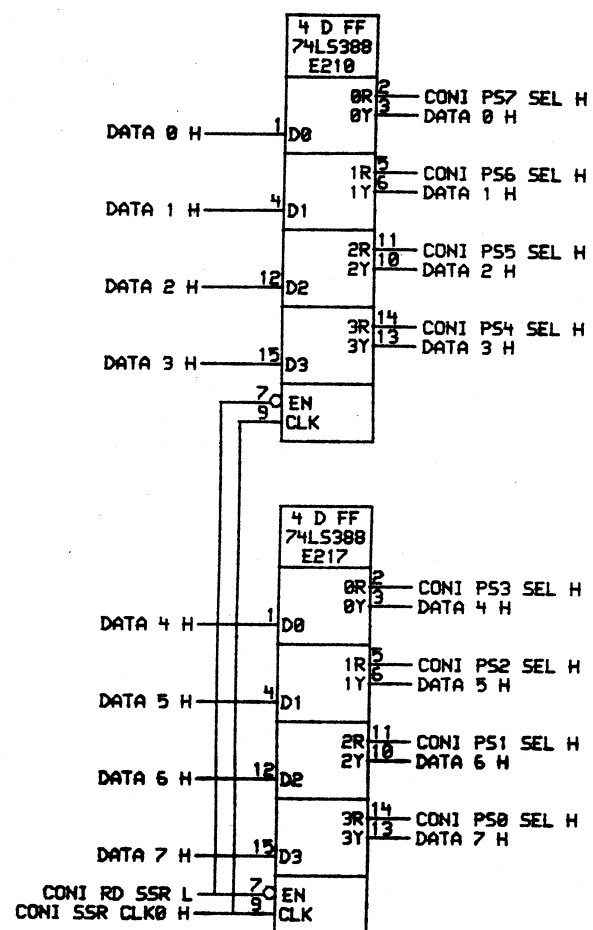
CON7 SEL SLU H 4
CON3 180 RDWR H 5
74500 E14
COND SLU DEN L 6

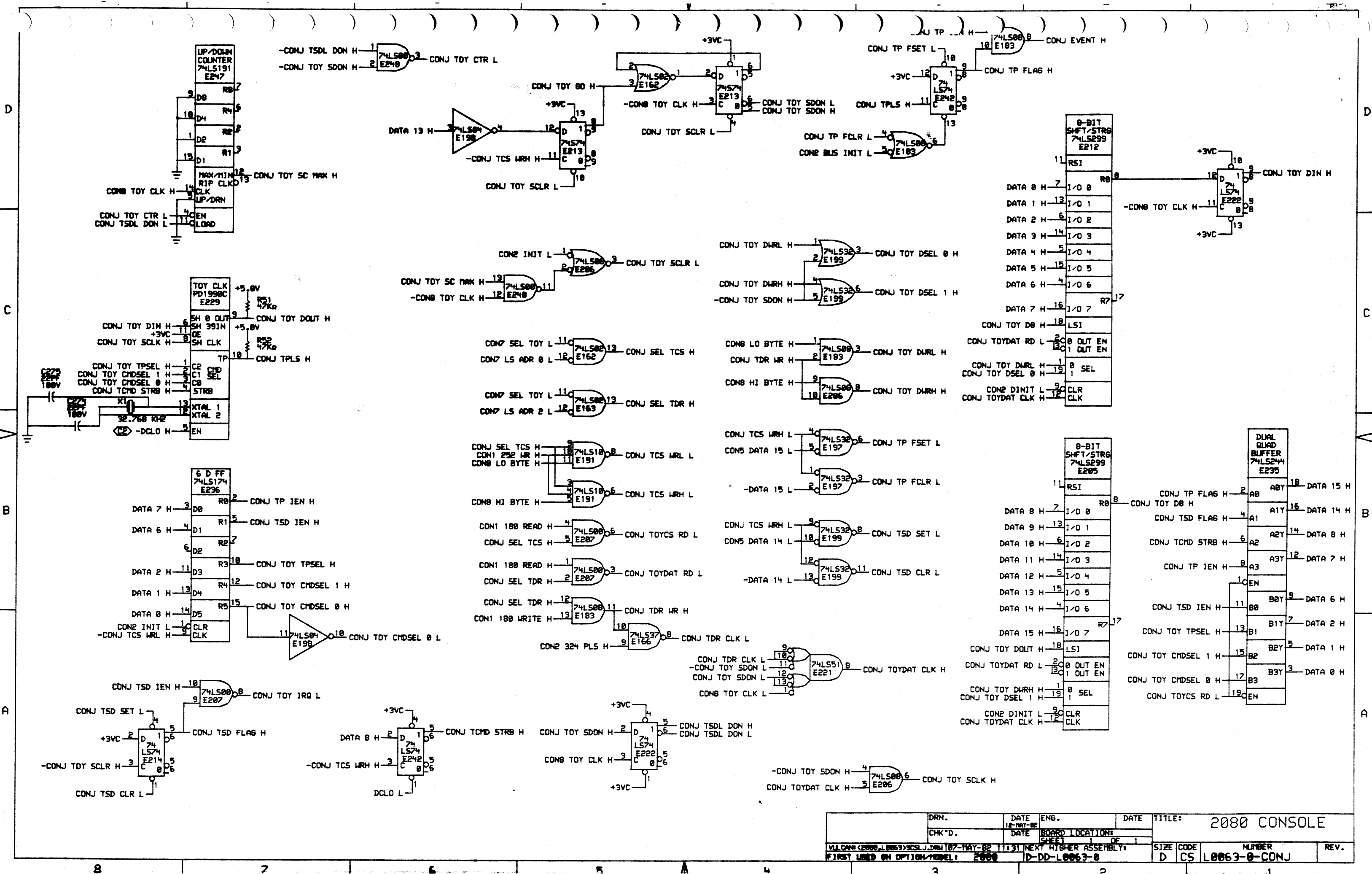


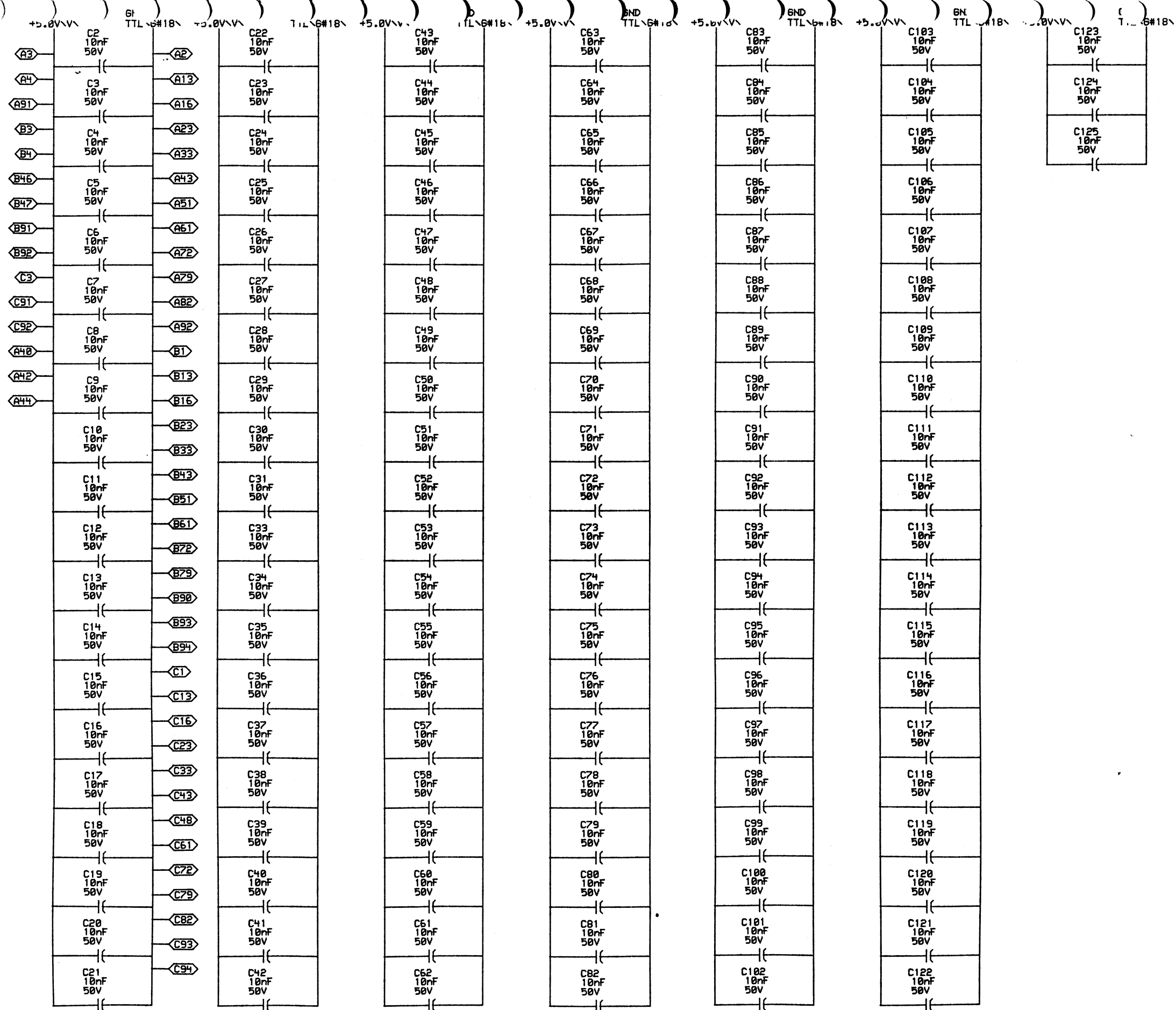


DRN.	DATE	ENG.	DATE	TITLE:	2080 CONSOLE
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	1
VULCAN (2080-L0063)2CSLF.DRN	20-OCT-81 13:00	NEXT HIGHER ASSEMBLY:	SIZE	CODE	NUMBER
FIRST USED ON OPTION/MODEL:	2080	D-DD-L0063-0	D	CS	L0063-0-CONF
					REV.









DRN.	DATE	ENG.	DATE	TITLE
CHK'D.	DATE	BOARD LOCATION	SHEET	OF
VULCANI (2000.L0063)CSLM.DRW 31-MAR-82 14:05				NEXT HIGHER ASSEMBLY:
FIRST USED ON OPTION MODEL:				B-DD-L0063-0
SIZE	CODE	NUMBER	REV.	
D	CS	L0063-0-CSLM	*	

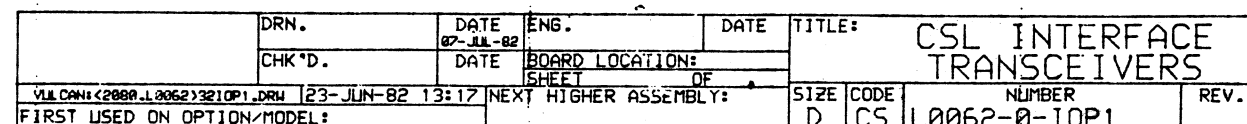
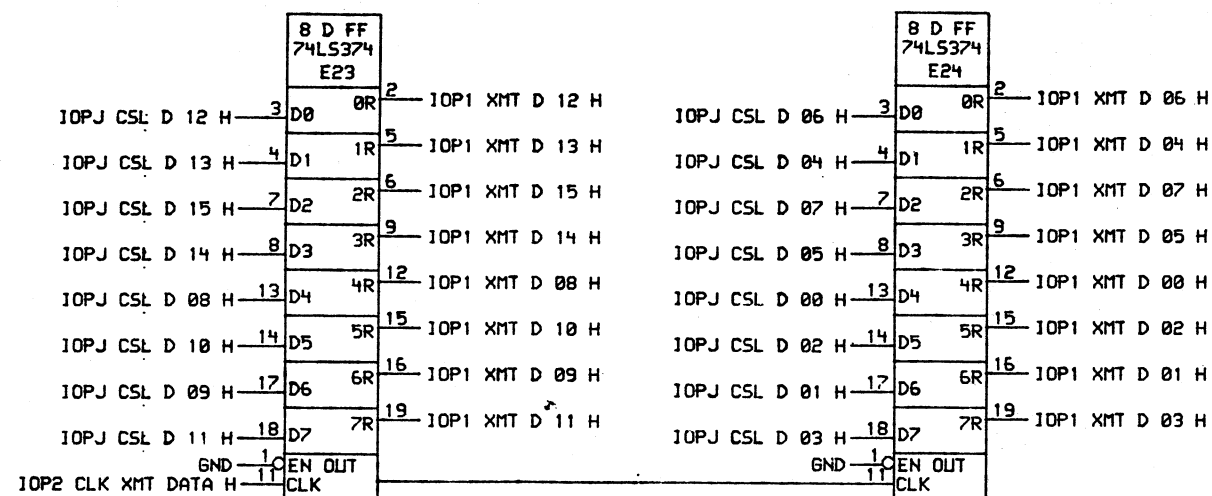
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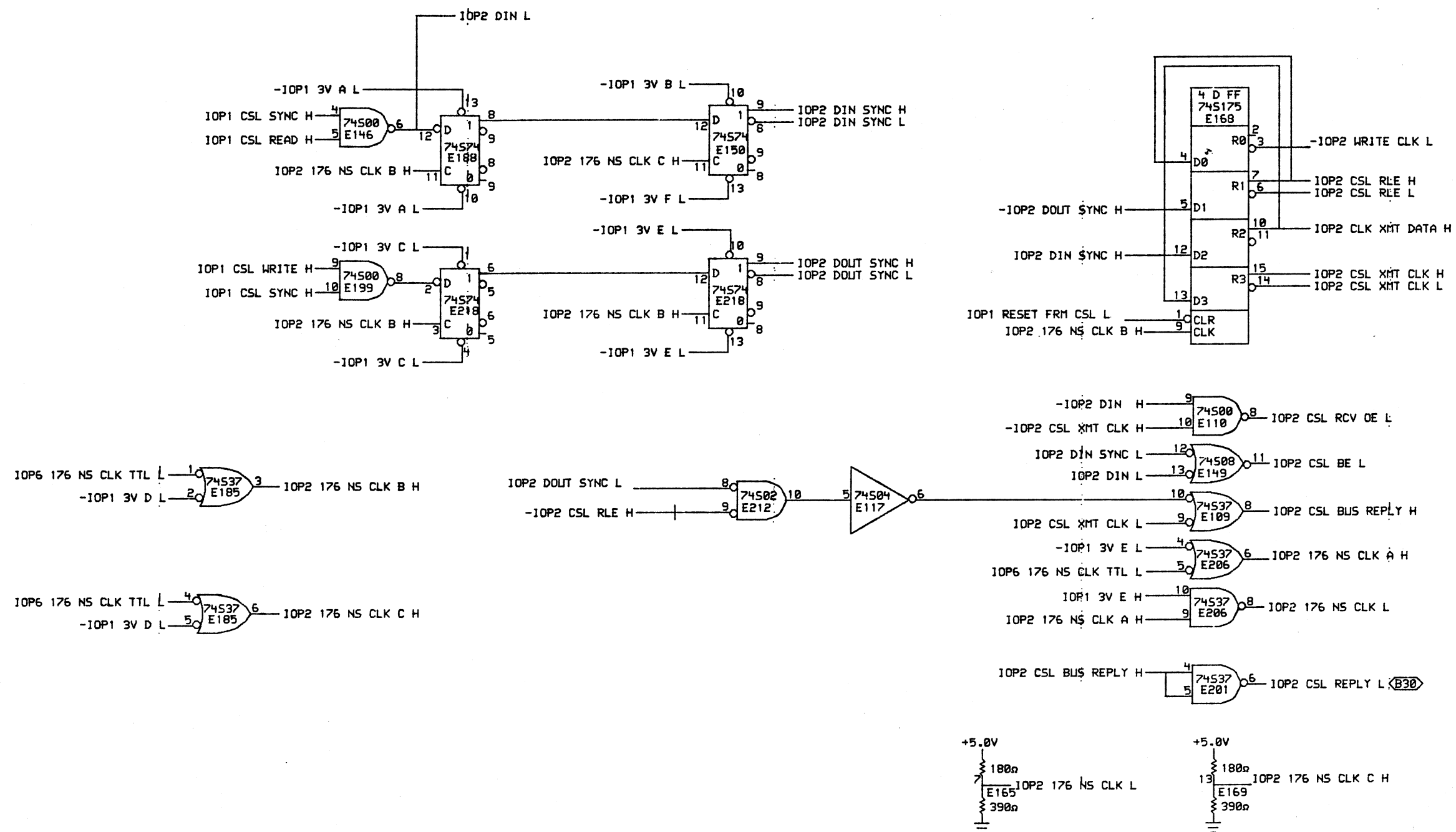
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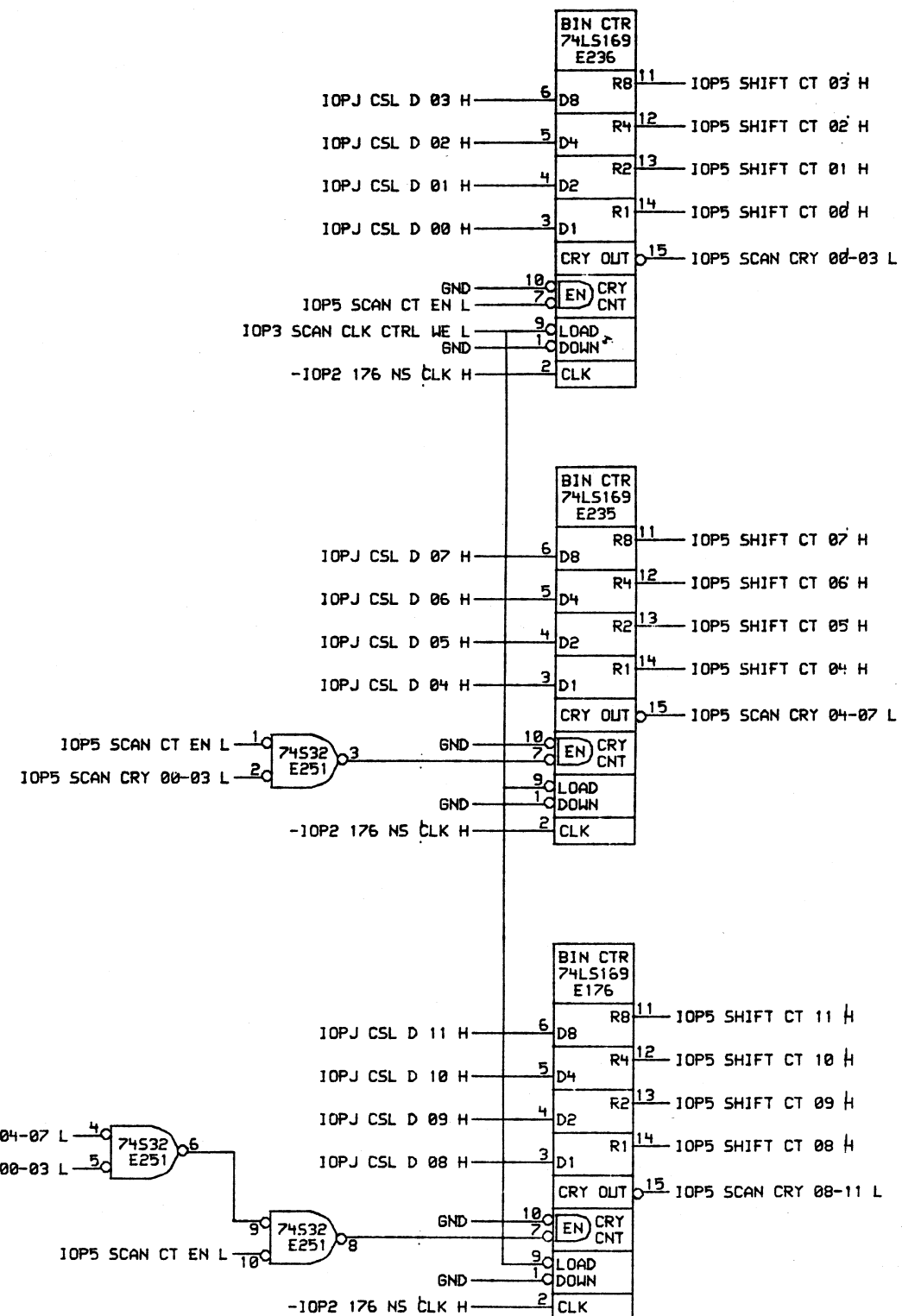
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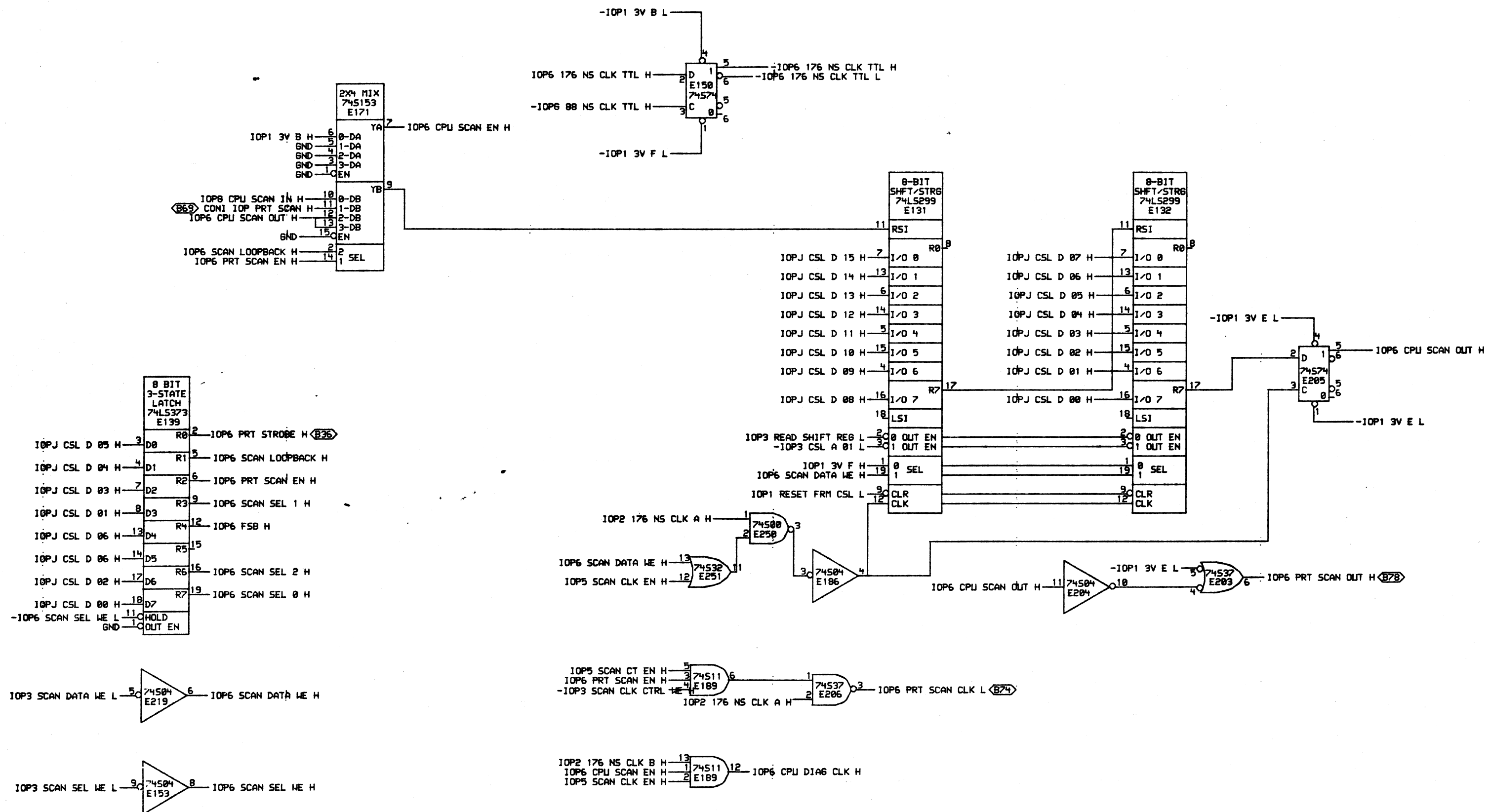
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VULCAN:<2080.L0062>54IOPH
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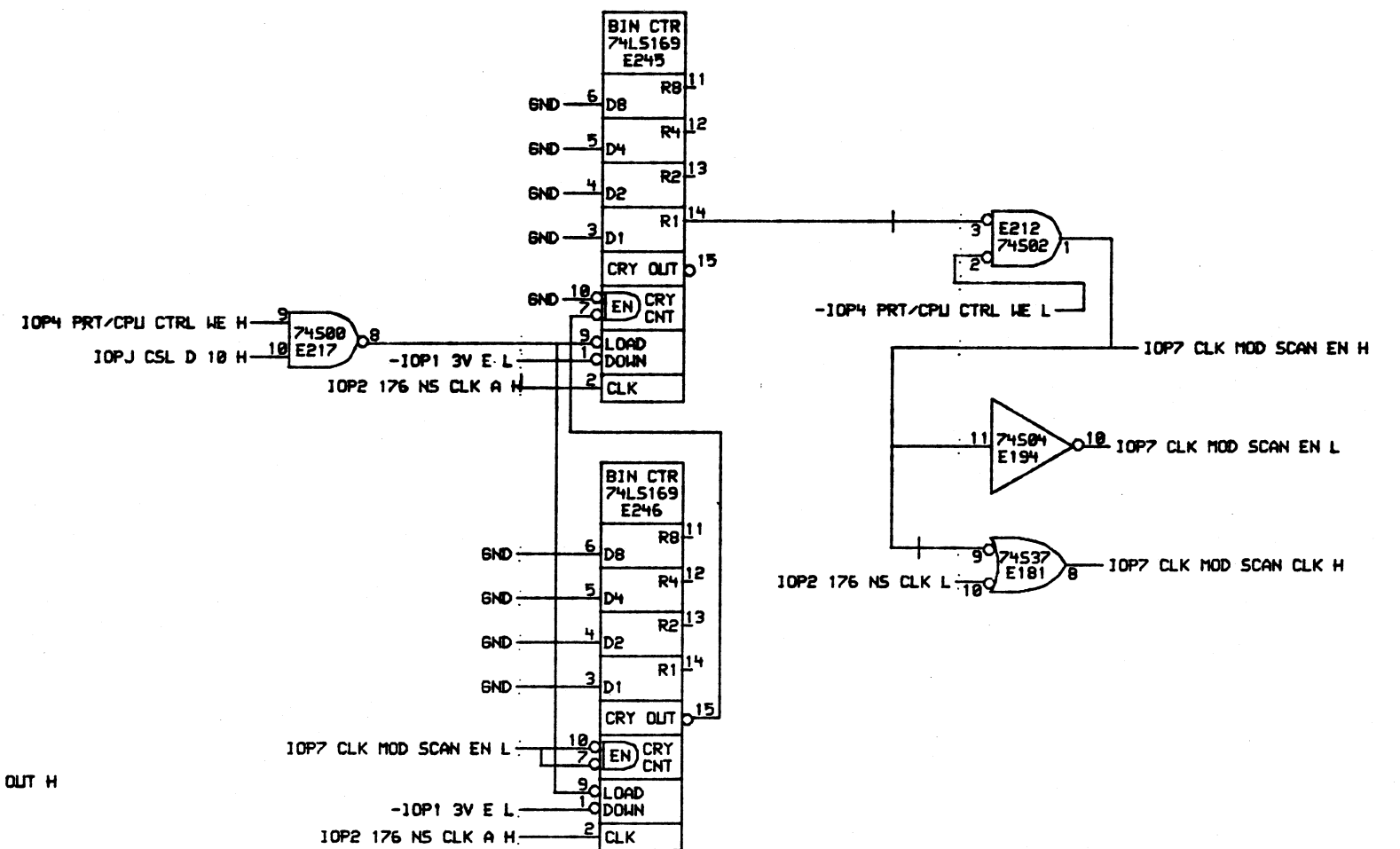
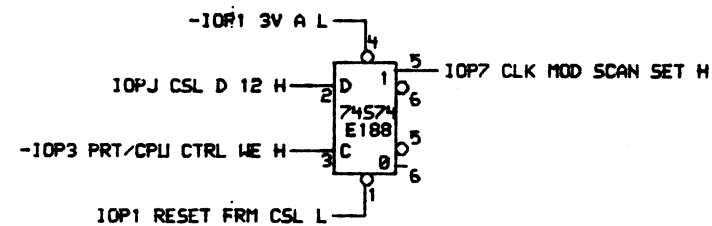


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CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	SIZE	CODE	NUMBER	REV.
VULCAN (2000-10062) 2010P2.DRW	04-JUN-82	13:45	NEXT HIGHER ASSEMBLY: *		D	CS	L0062-0-IOP2	
FIRST USED ON OPTION/MODEL:								

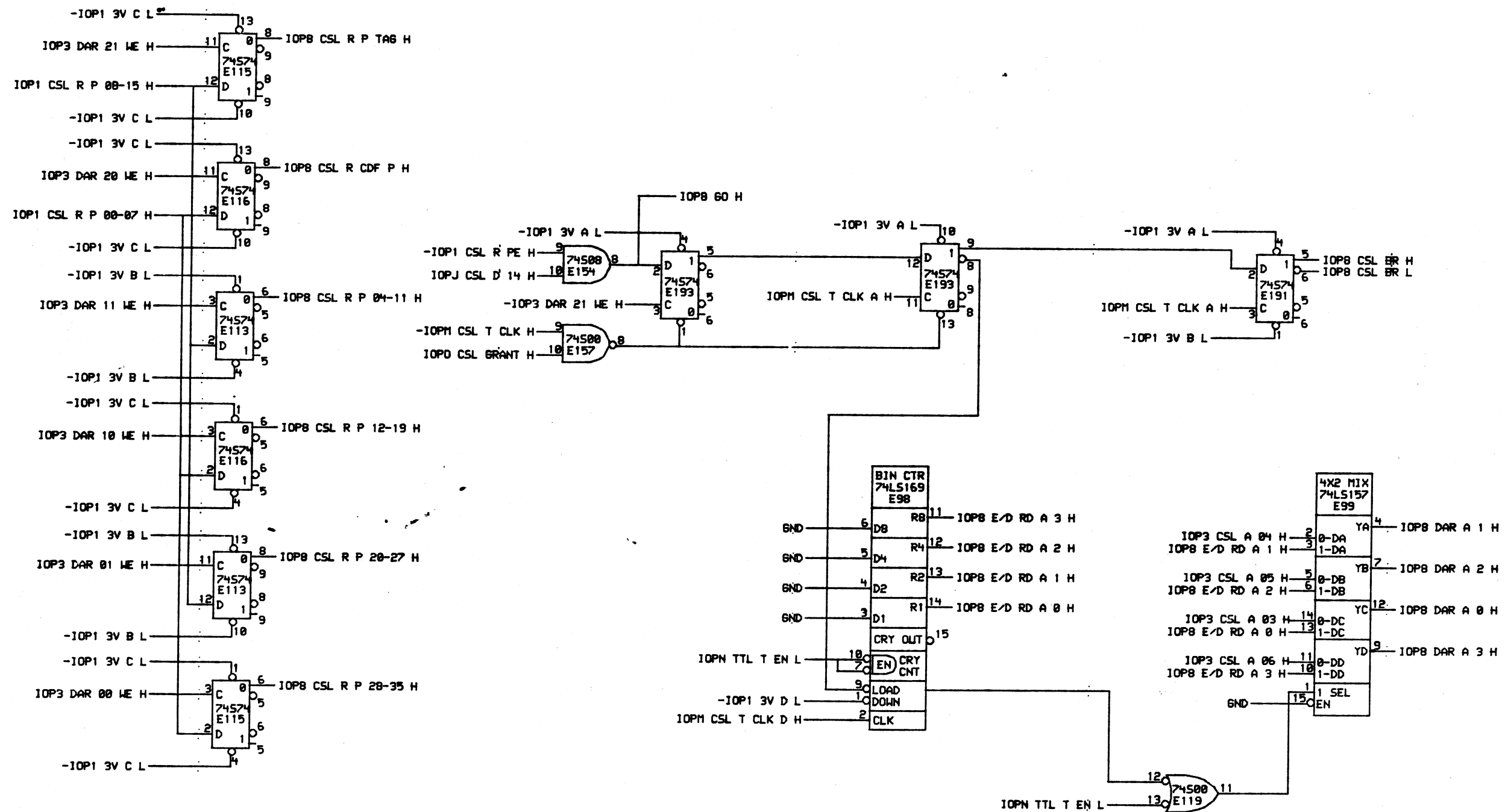




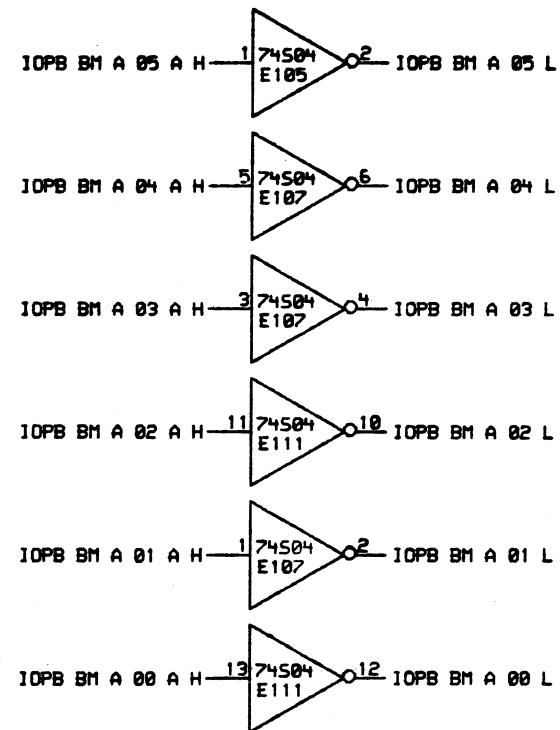
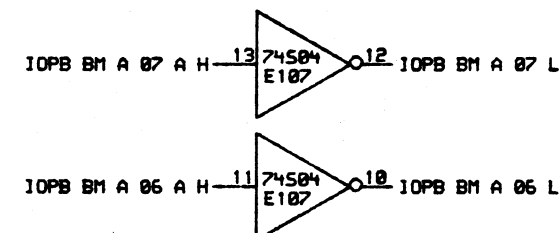
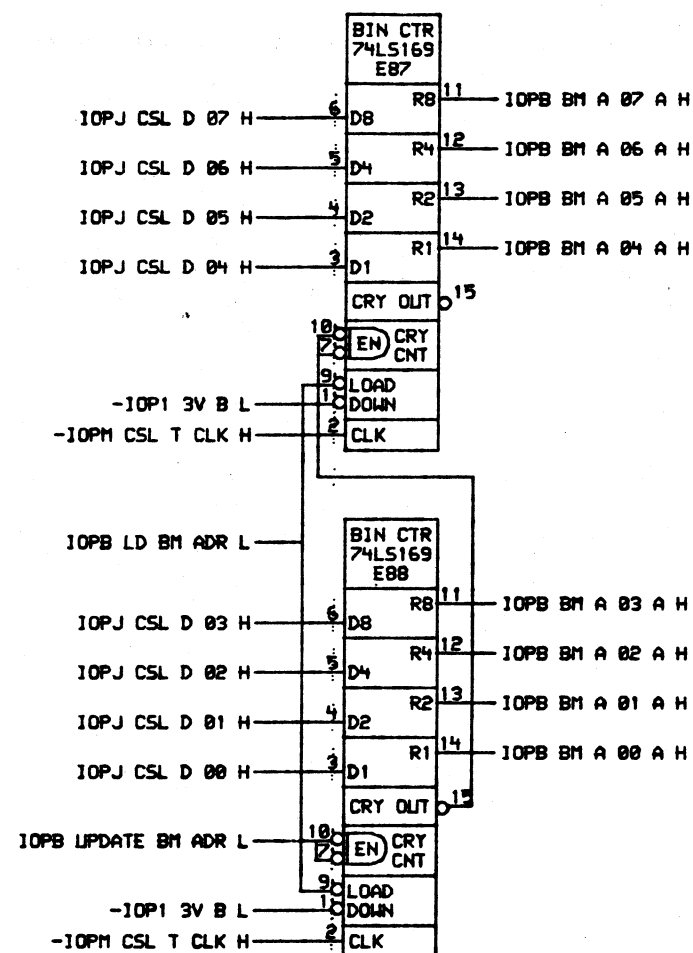
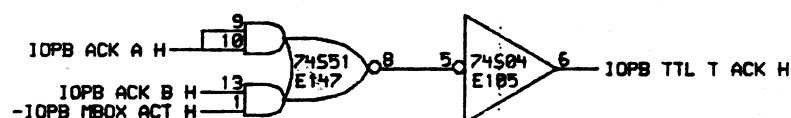
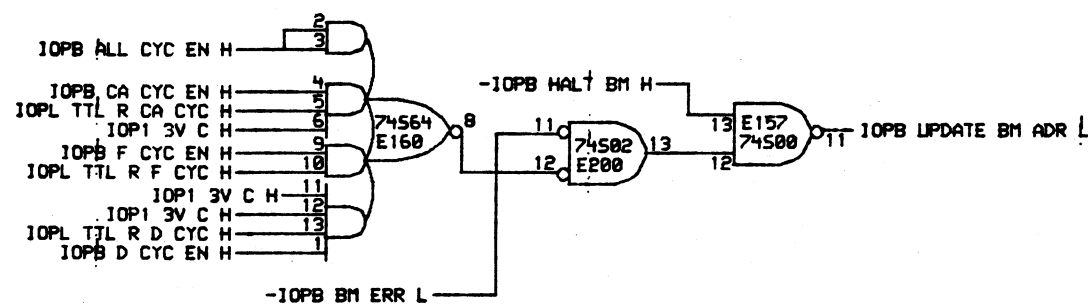
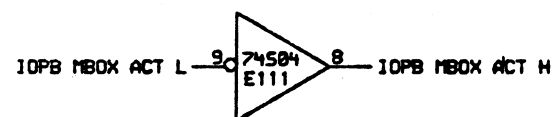
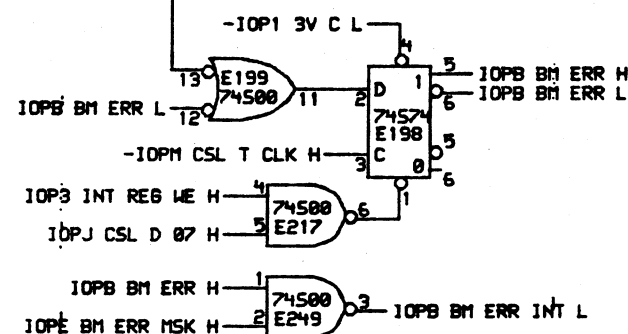
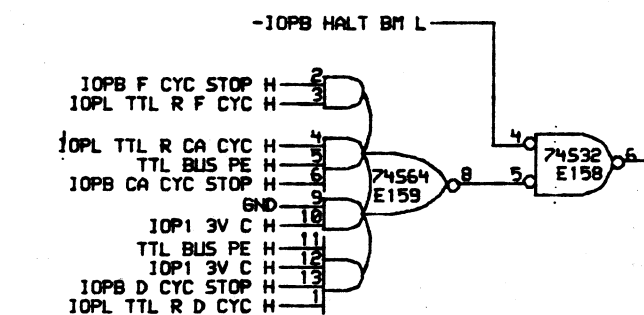
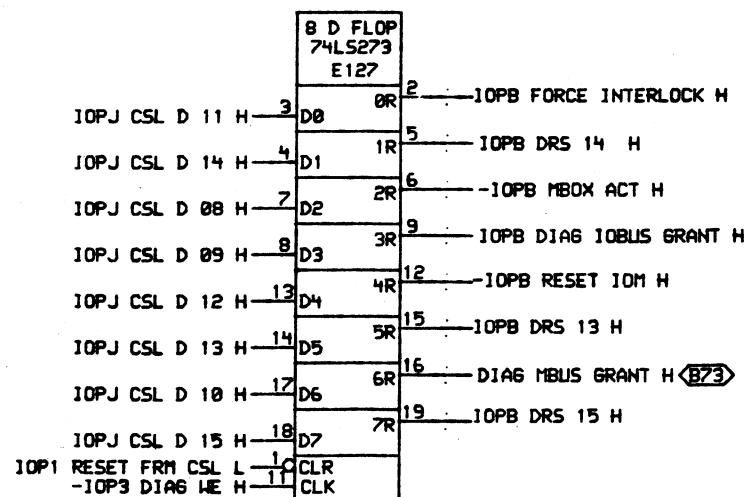
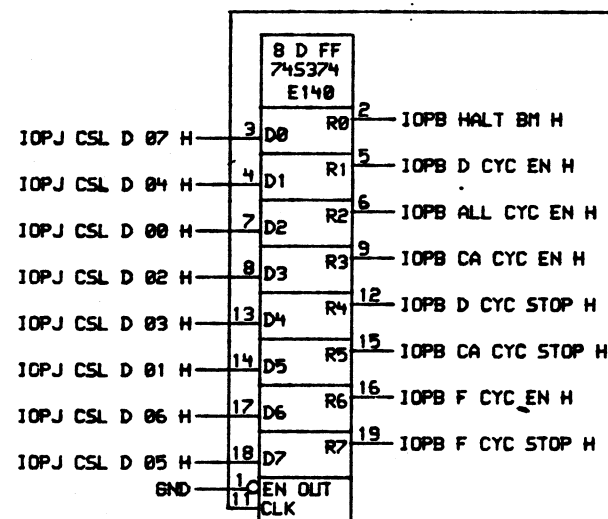
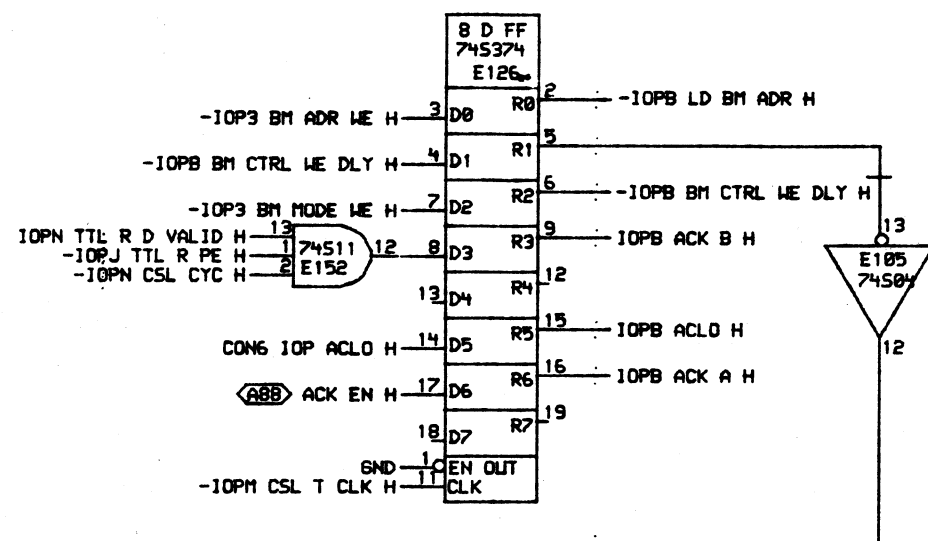
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CHK'D.	DATE	BOARD LOCATION:			
VULCAN: <2000.L0062>S10P6.DRW	23-JUN-82 13:23	NEXT HIGHER ASSEMBLY:			
FIRST USED ON OPTION/MODEL:					
SIZE	CODE	NUMBER	REV.		
D	CS	L0062-0-IOP6			



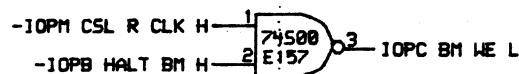
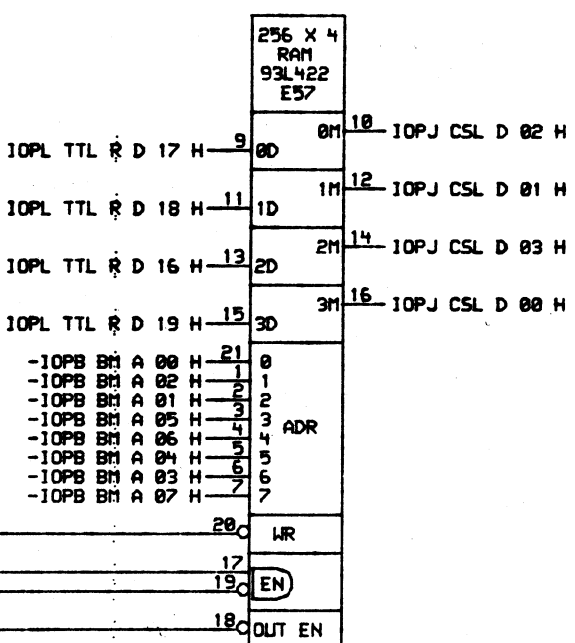
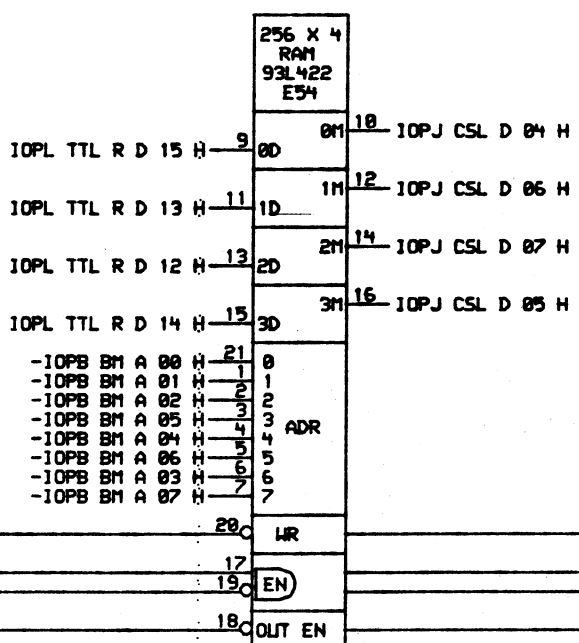
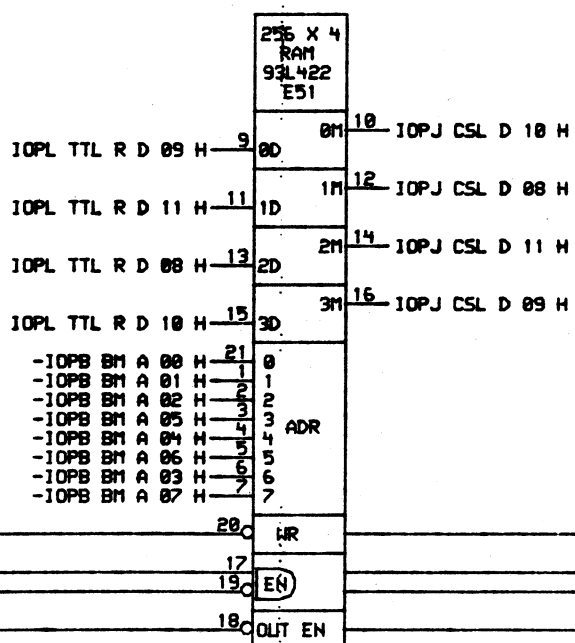
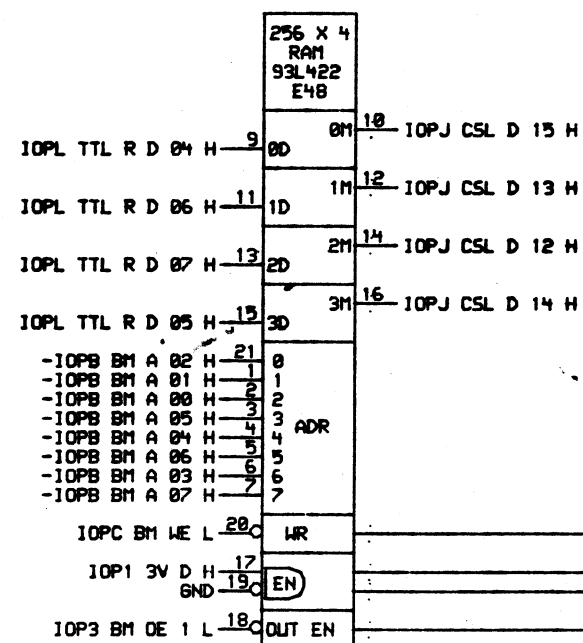
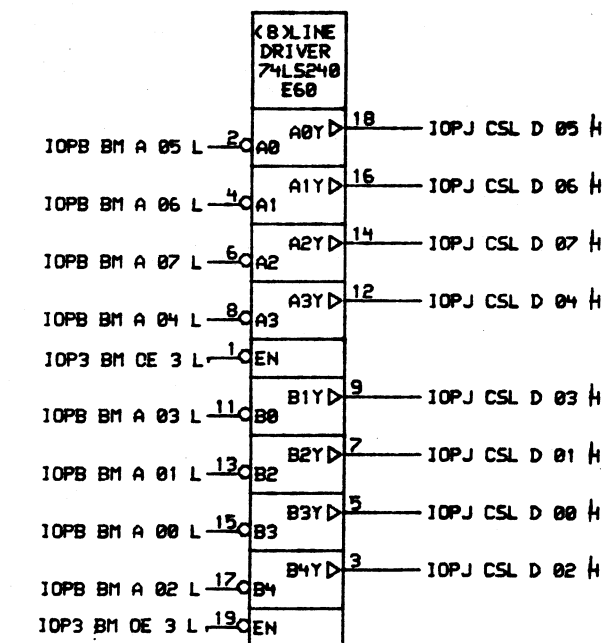
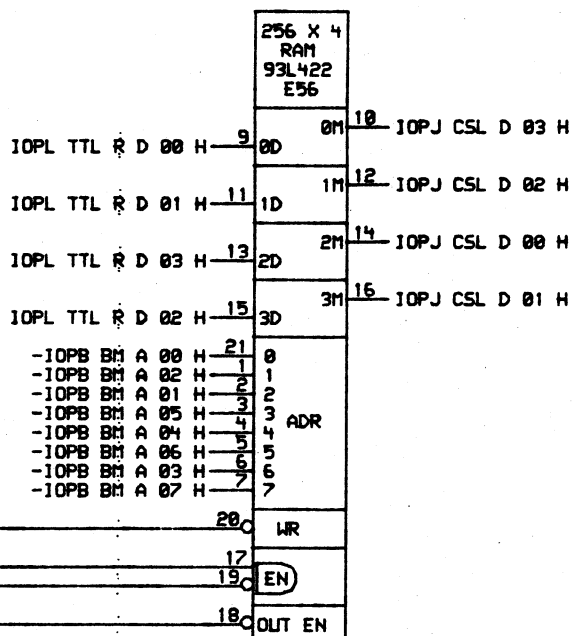
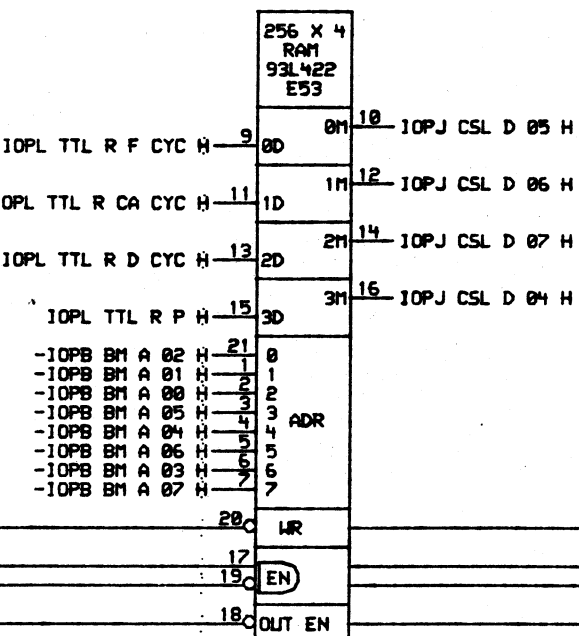
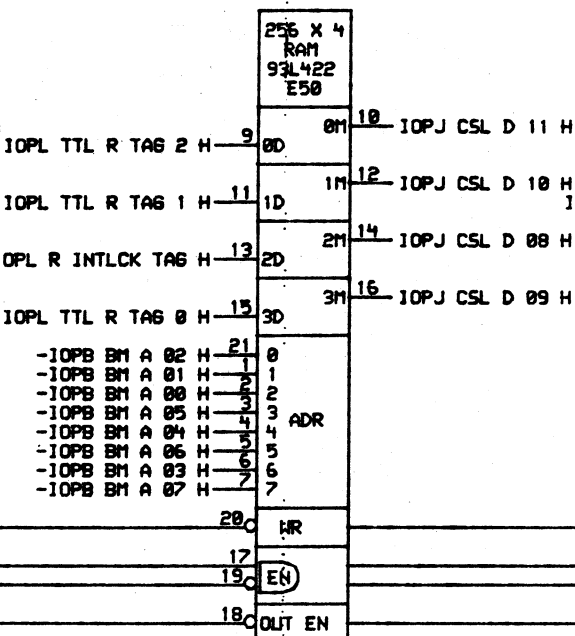
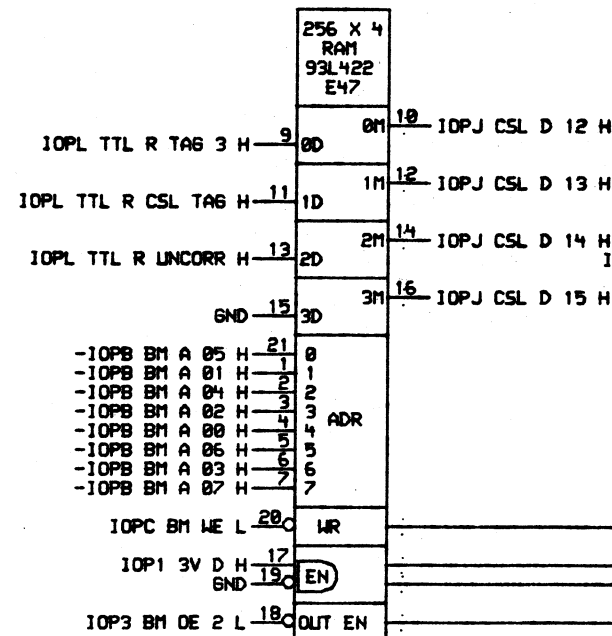
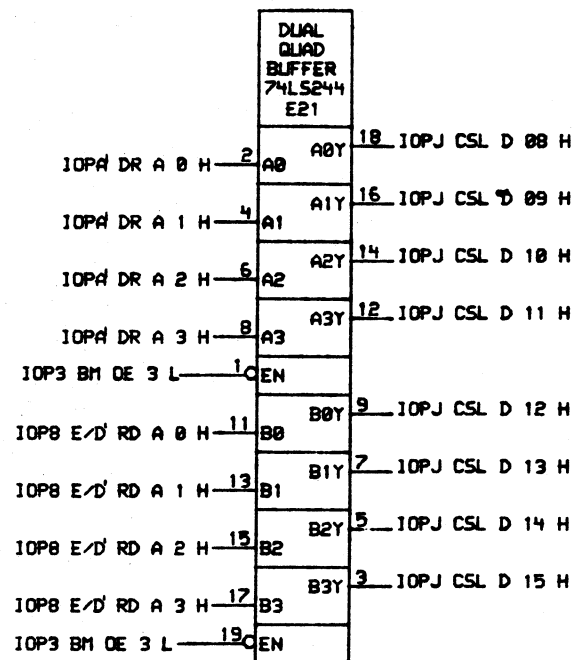
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CHK'D.		DATE	BOARD LOCATION:							
			SHEET OF							
VIL CAN: C2000.L0062*010P7.DRW			13-MAY-82 14:52		NEXT HIGHER ASSEMBLY:		SIZE	CODE	NUMBER	REV.
FIRST USED ON OPTION/MODEL:							D	CS	L0062-0-10P7	

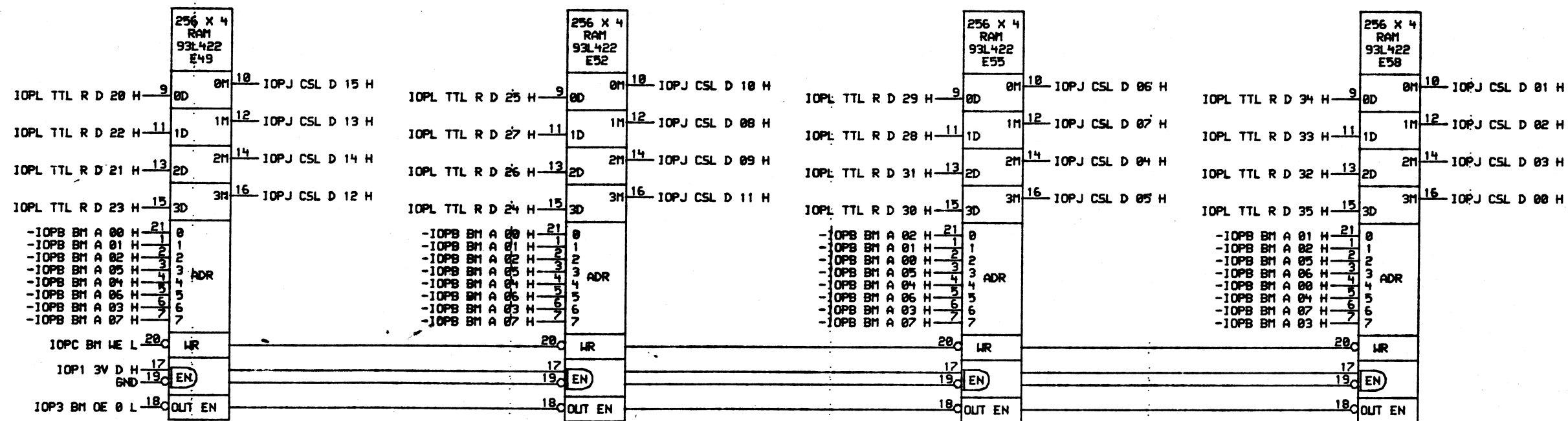


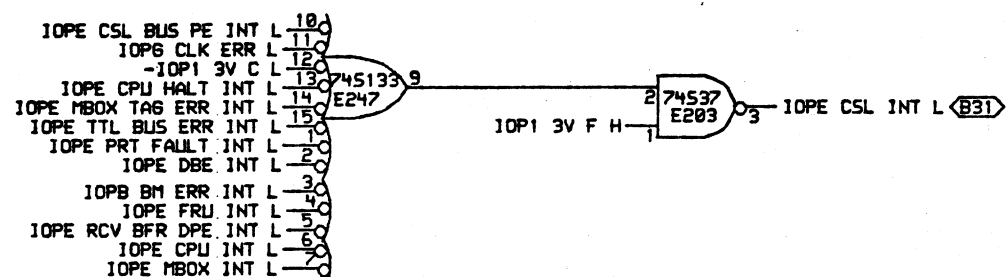
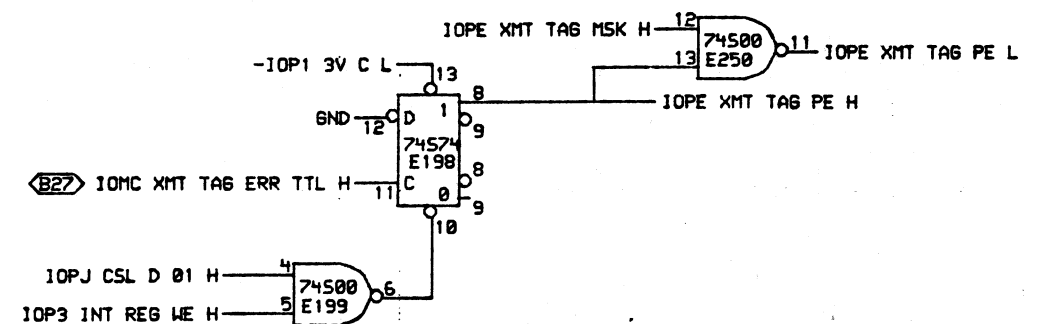
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CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	SIZE	CODE	NUMBER
VULCAN (P000-10062) 410P8.DRW 113-MAY-82 14:52				NEXT HIGHER ASSEMBLY:		D	CS
FIRST USED ON OPTION/MODEL:				L0062-0-IOP8		REV.	

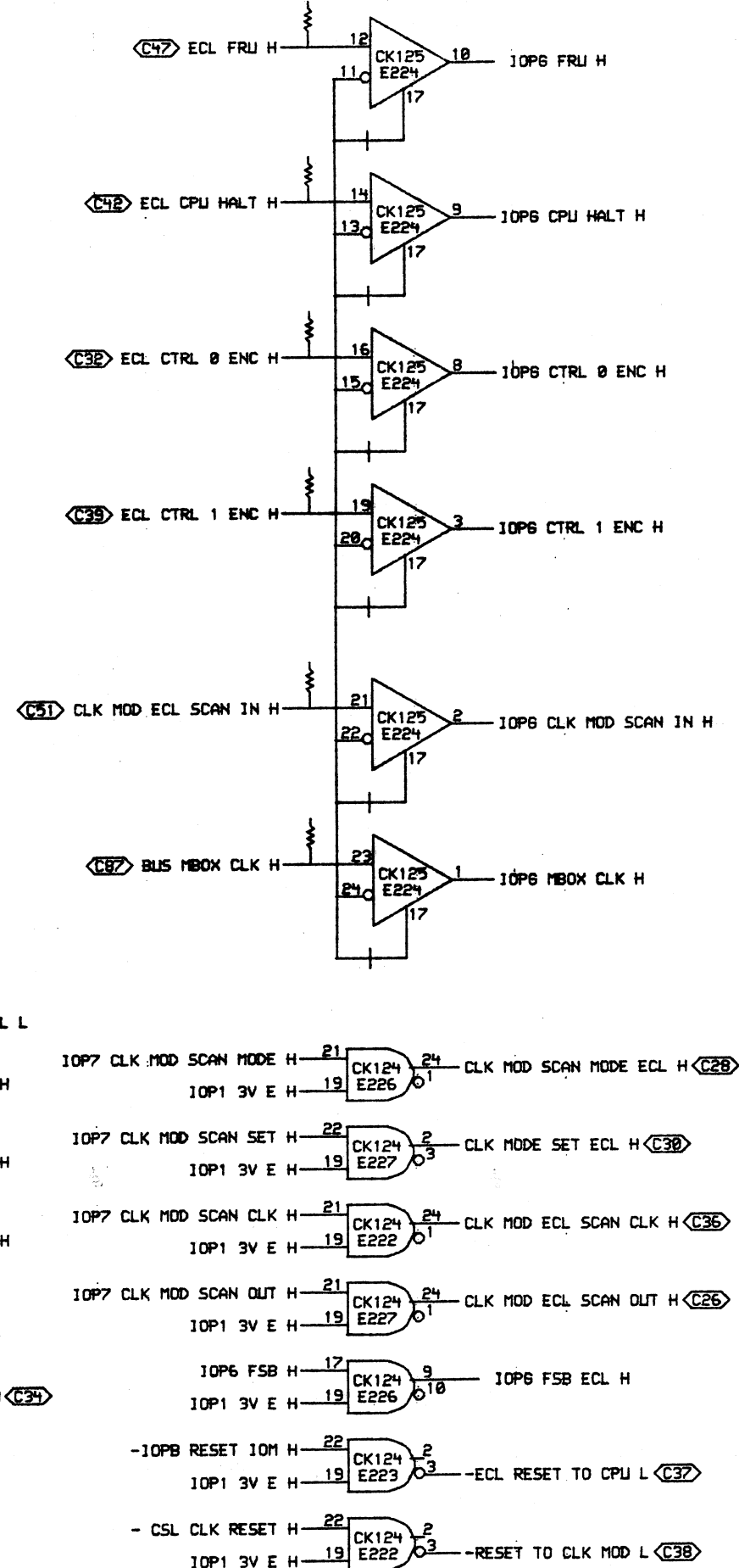
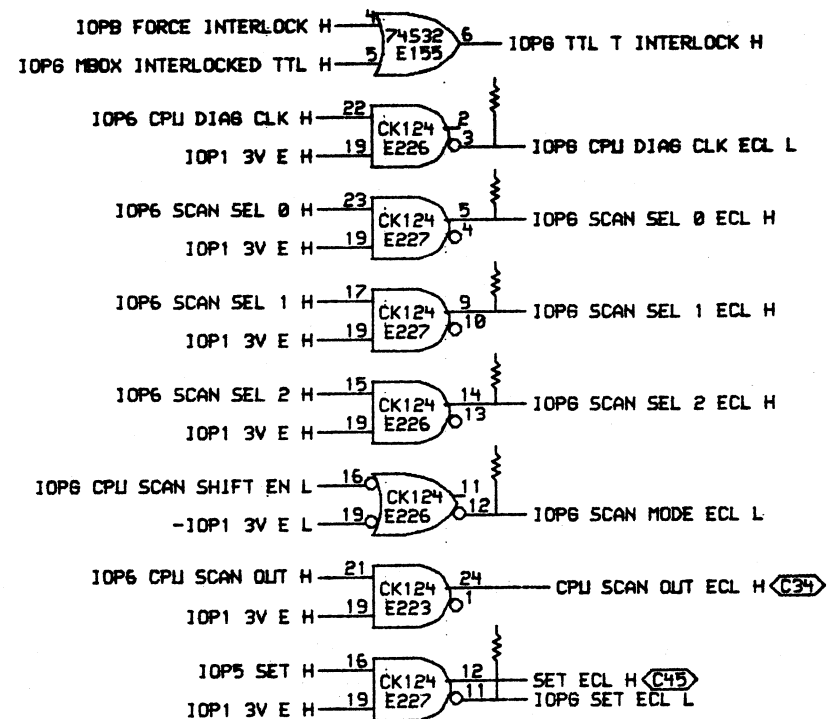
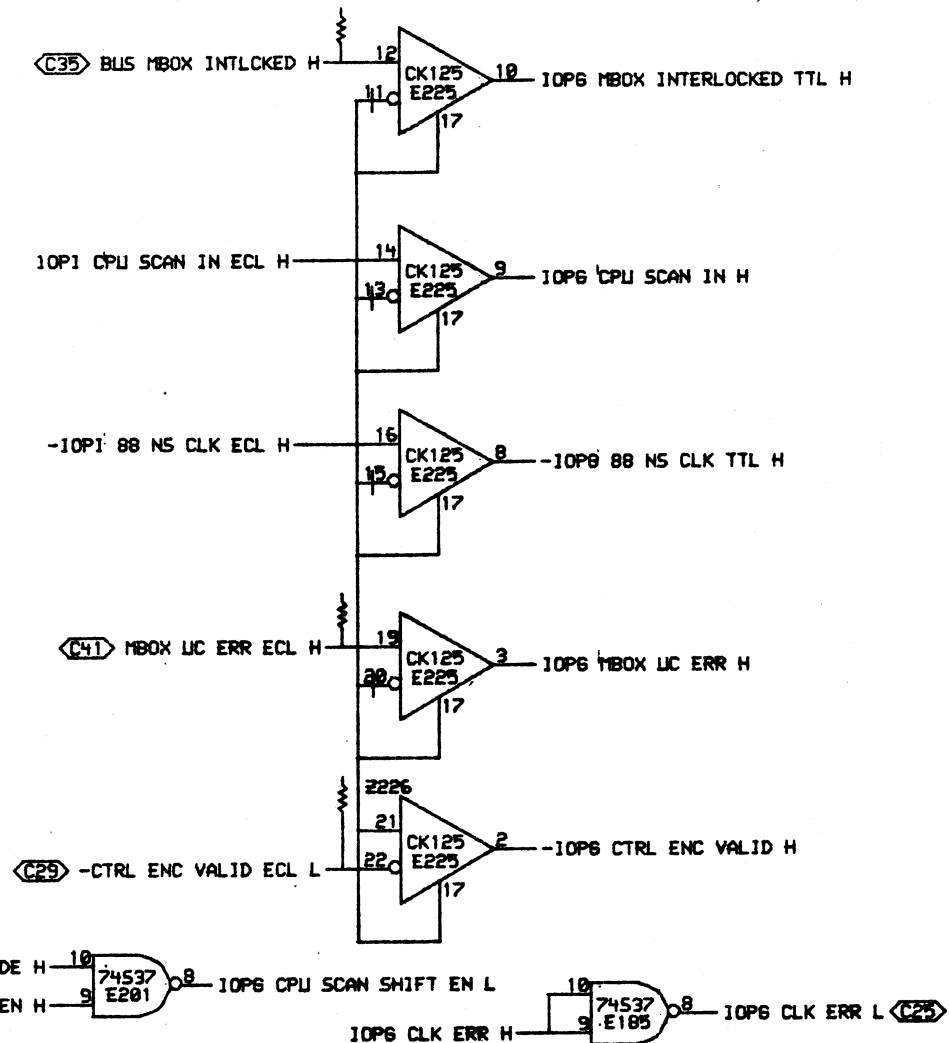
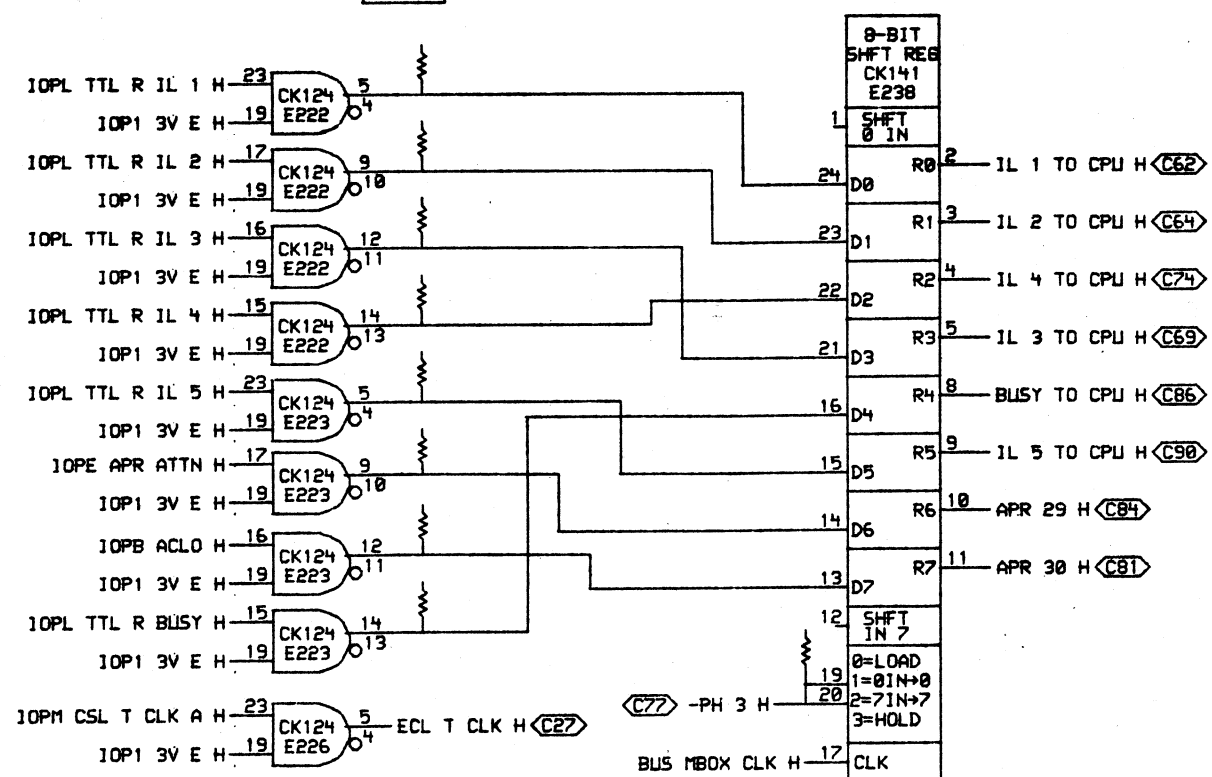
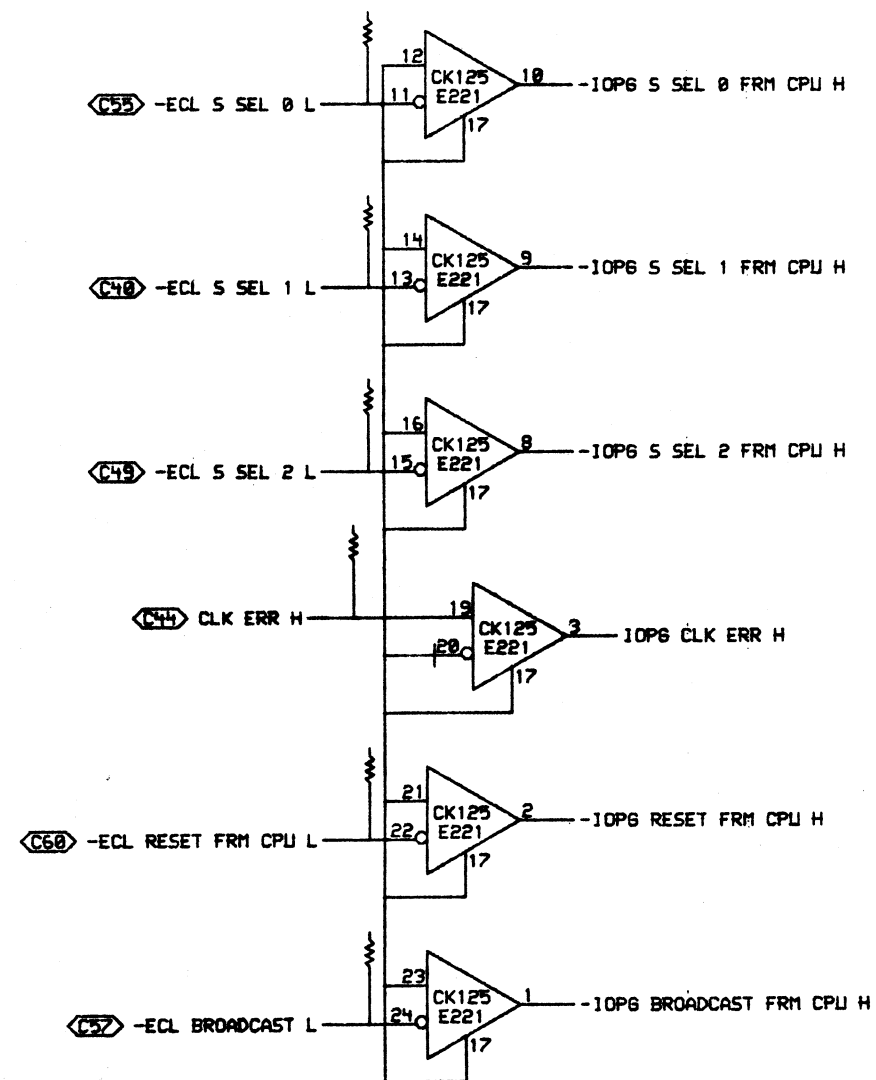


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CHK'D.	DATE	BOARD LOCATION:	DATE	BM ADR	
VIL CMM (2000) L0062-0-10PB.DRW	23-JUN-82 13:33	NEXT HIGHER ASSEMBLY:	SIZE	CODE	NUMBER
FIRST USED ON OPTION/MODEL:			D	CS	L0062-0-10PB
					REV.









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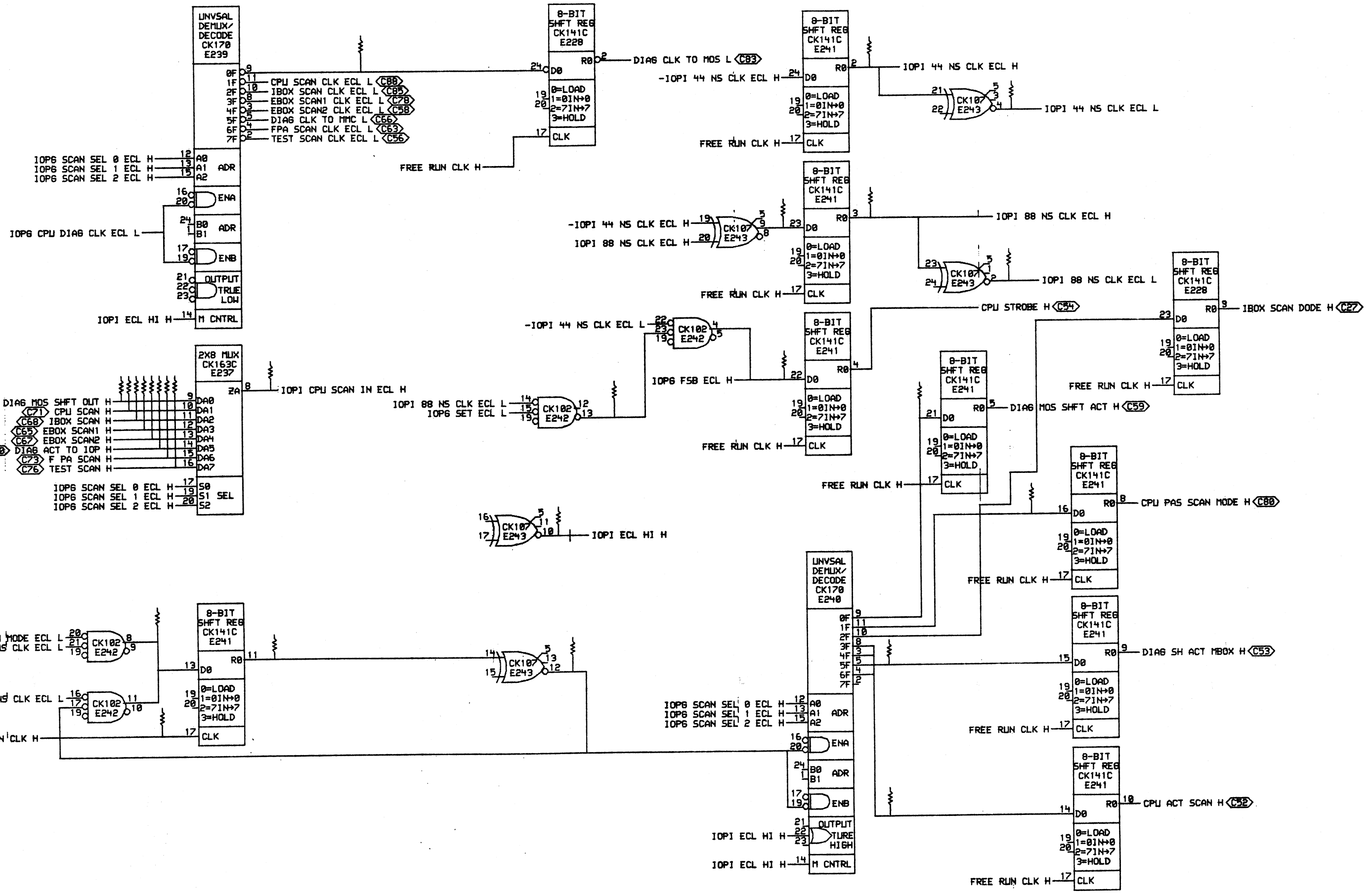
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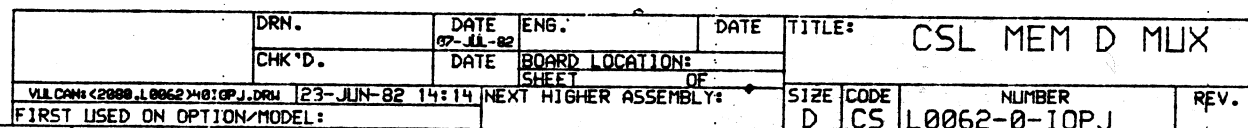
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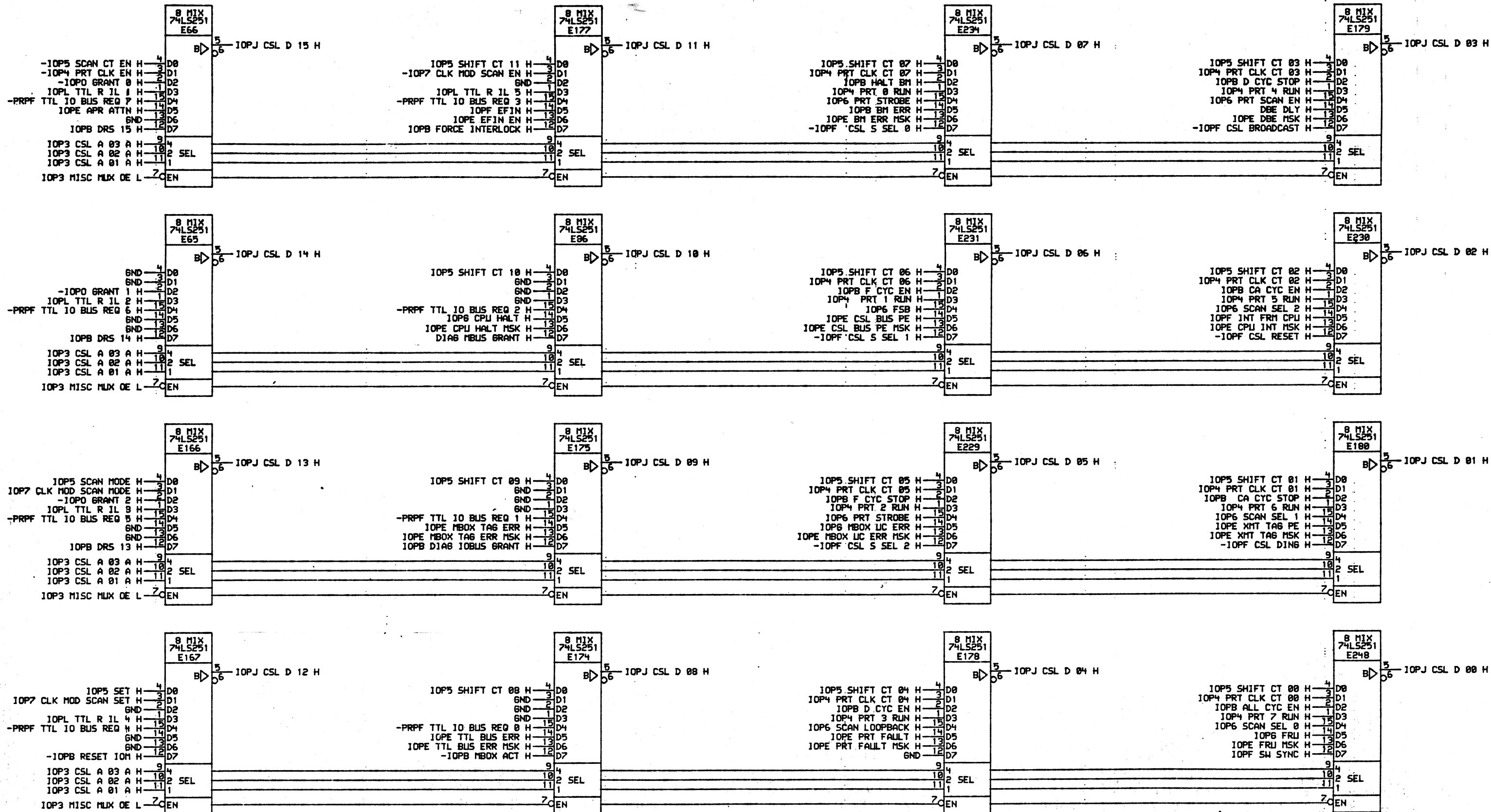
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REV. NUMBER L0062-0-10PK

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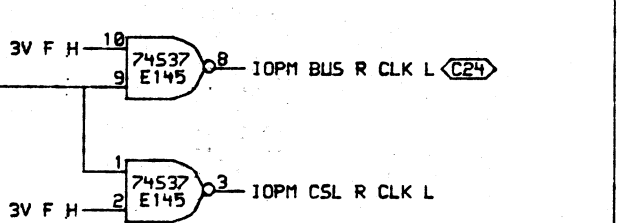
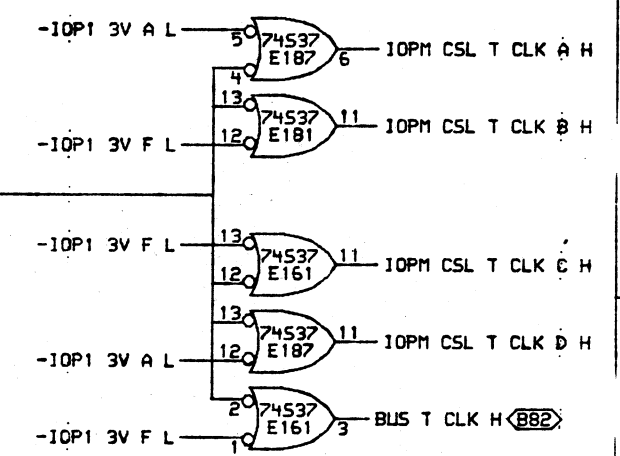
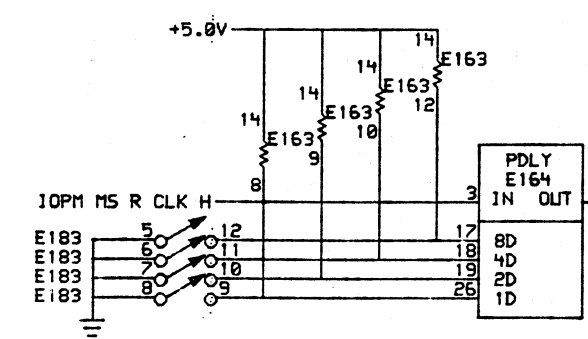
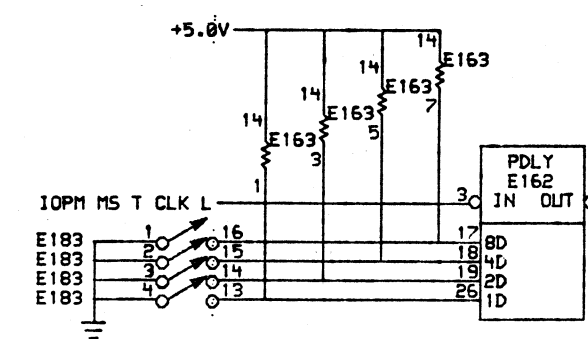
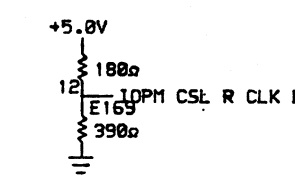
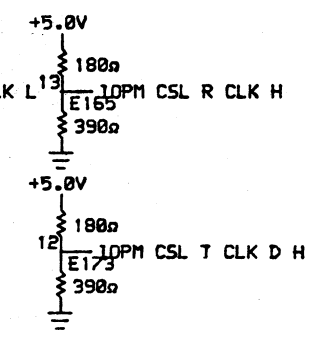
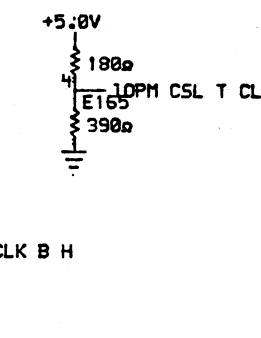
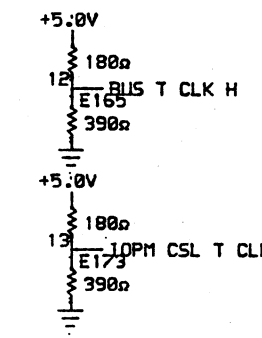
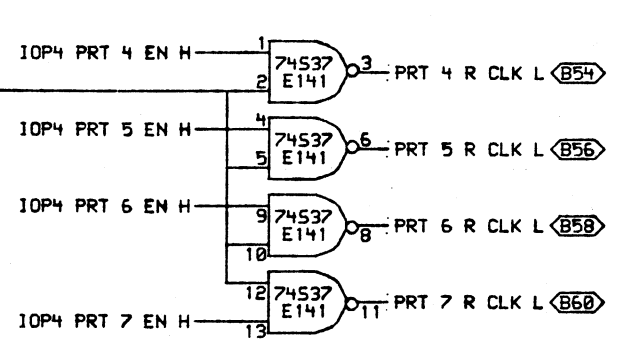
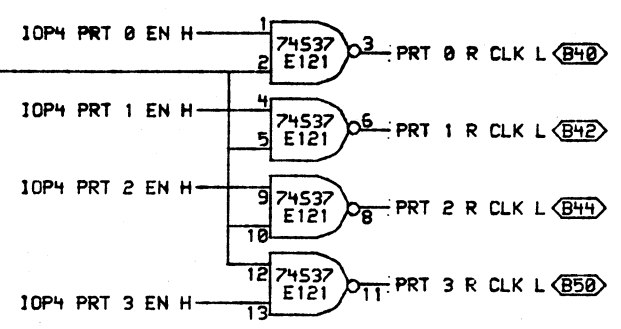
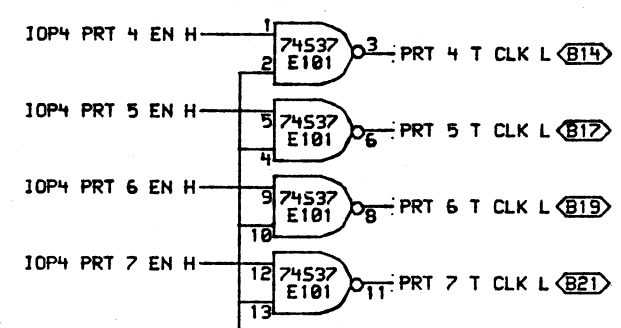
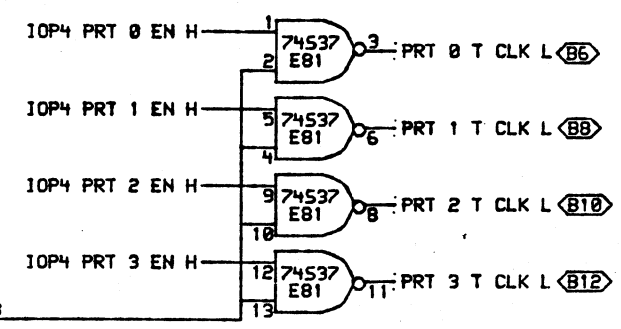
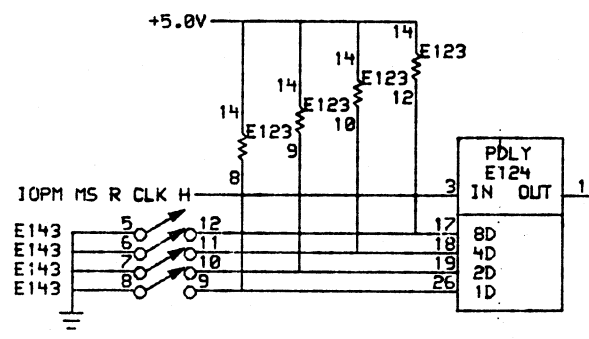
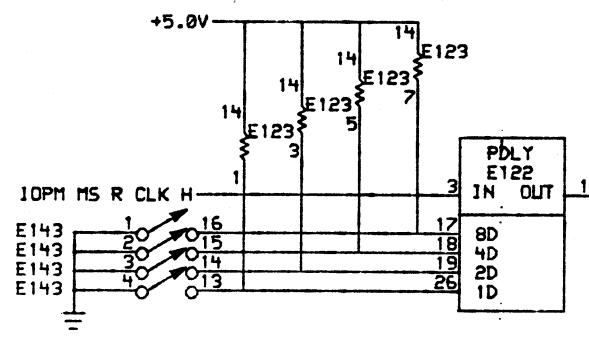
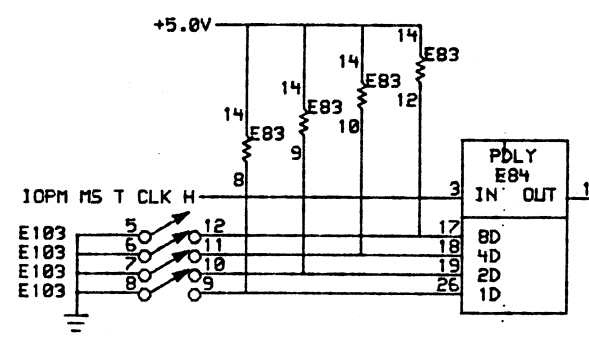
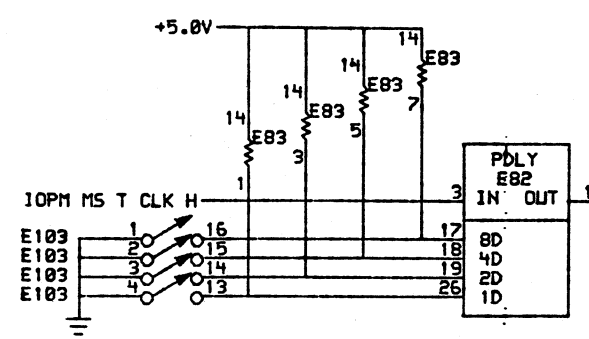
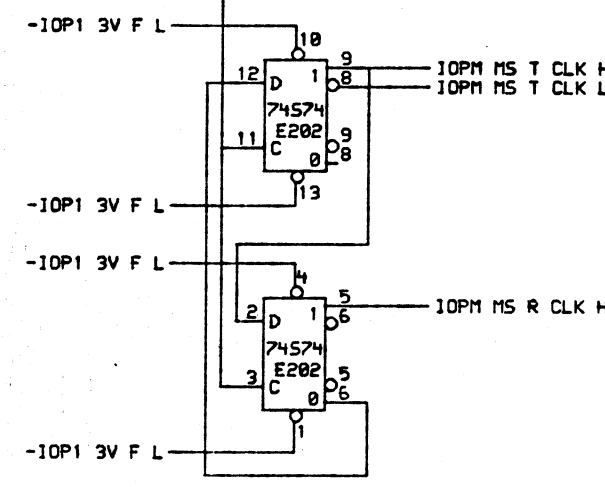
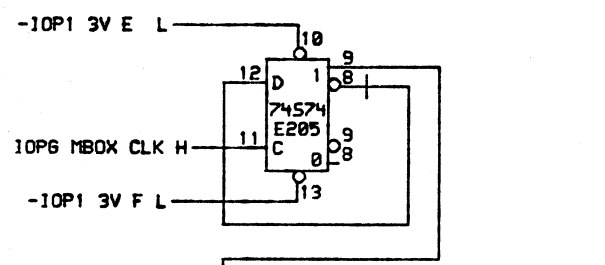
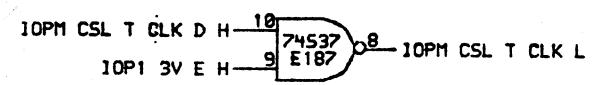
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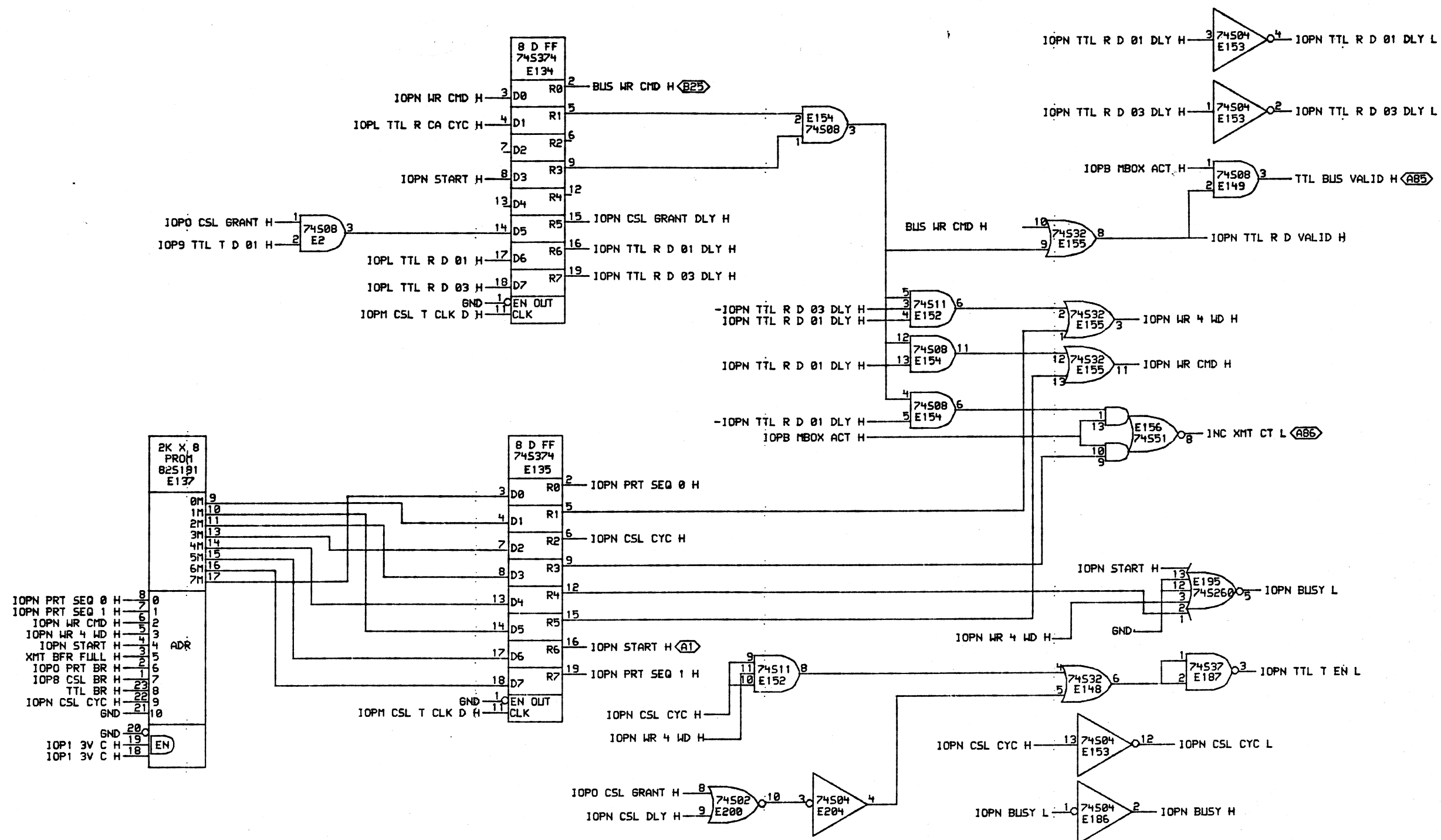
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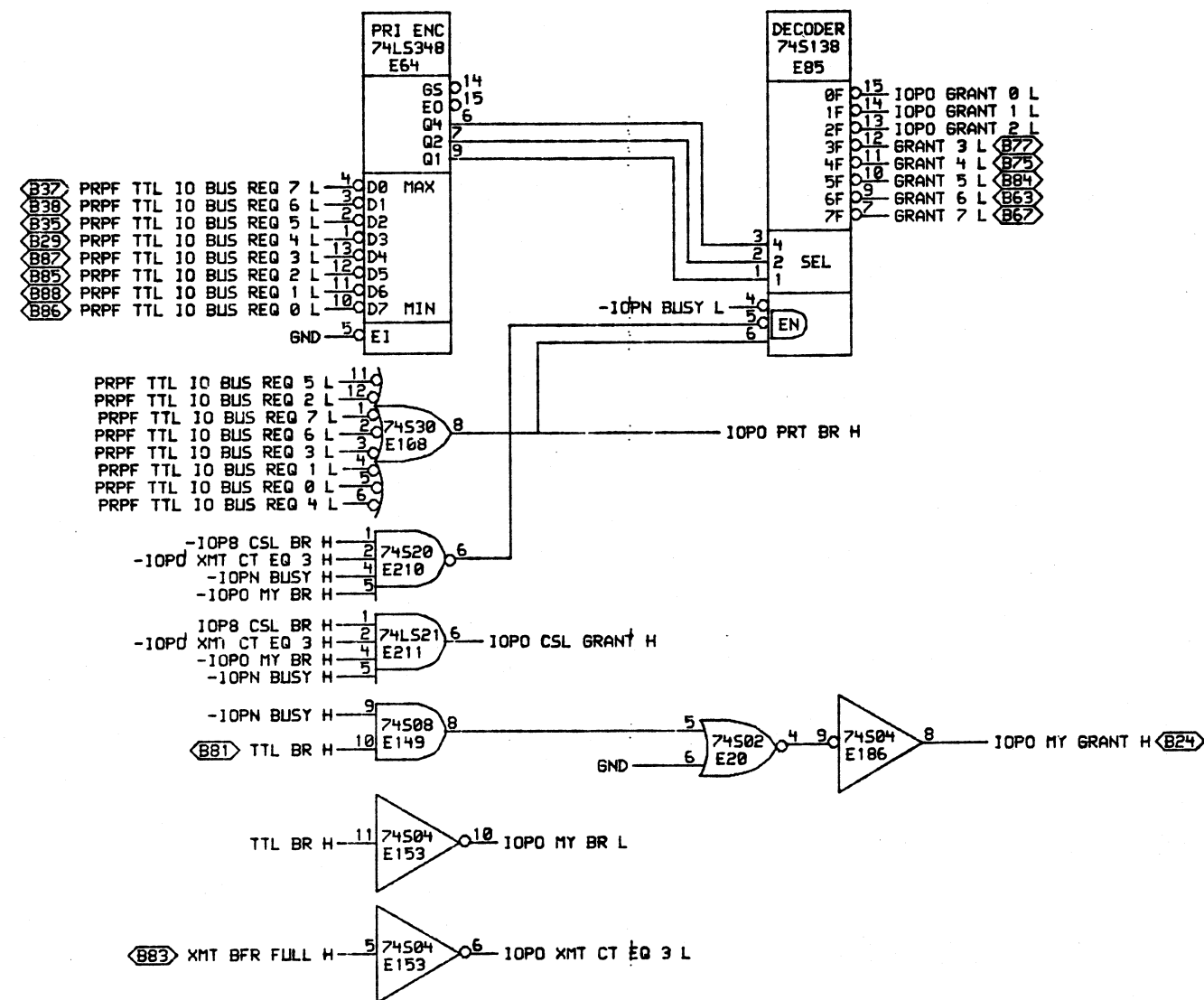
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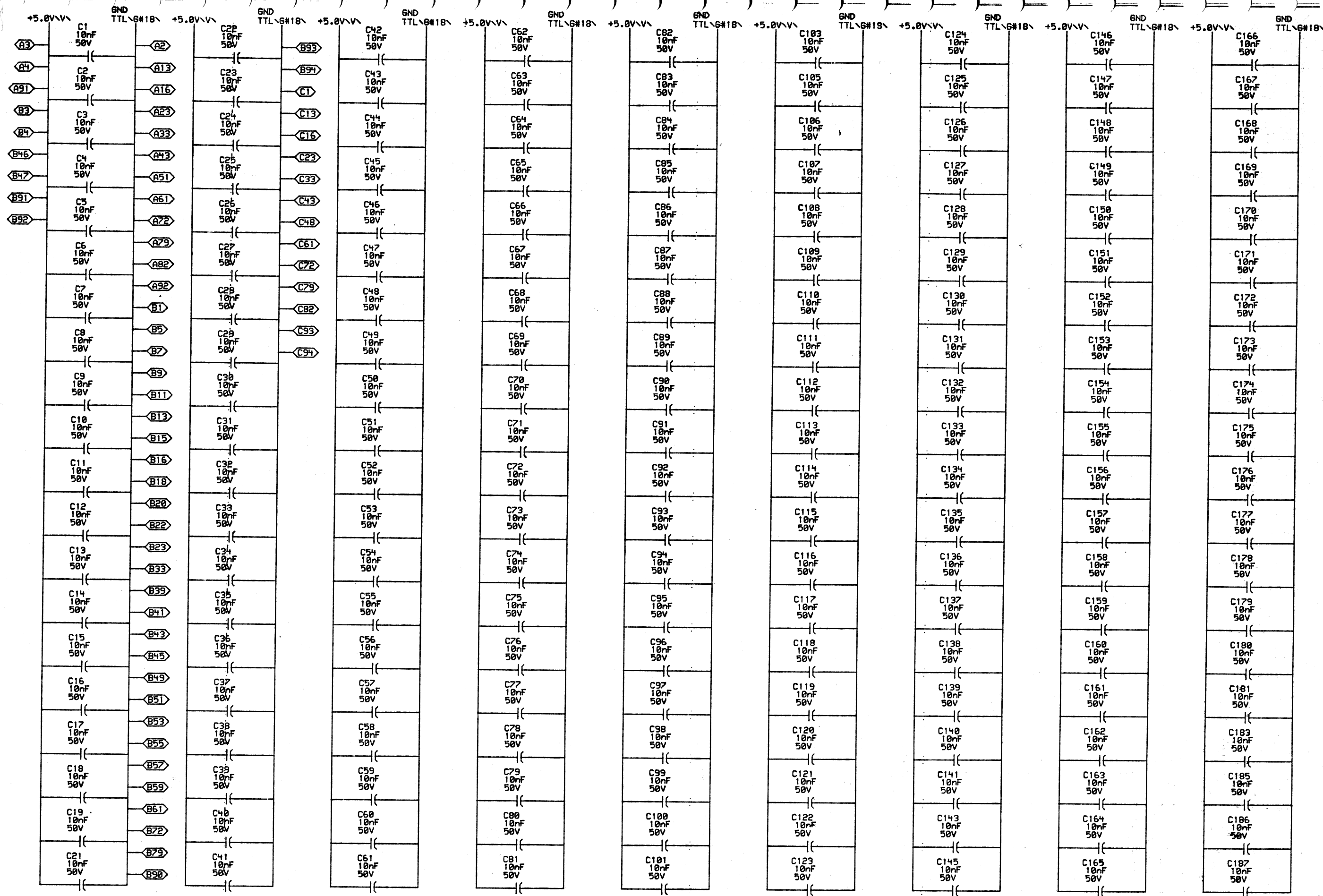
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DRN.	DATE	ENG.	DATE	TITLE:	T CLK
CHK'D.	DATE	BOARD LOCATION:		R CLK	
VLL CAN1 (2000, L0062) IOPM.DRW	127-JAN-82 14:25	NEXT HIGHER ASSEMBLY:		SIZE	CODE
FIRST USED ON OPTION/MODEL:				D	CS L0062-0-IOPM







DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		IOP MODULE
VULCAN: (2000.L0062) IOP.DRW	19-FEB-01	14:06	NEXT HIGHER ASSEMBLY:	PWR & GND 1
FIRST USED ON OPTION/MODEL:				
SIZE	CODE	NUMBER	REV.	
D	CS	L0062-0-IOPR		

D

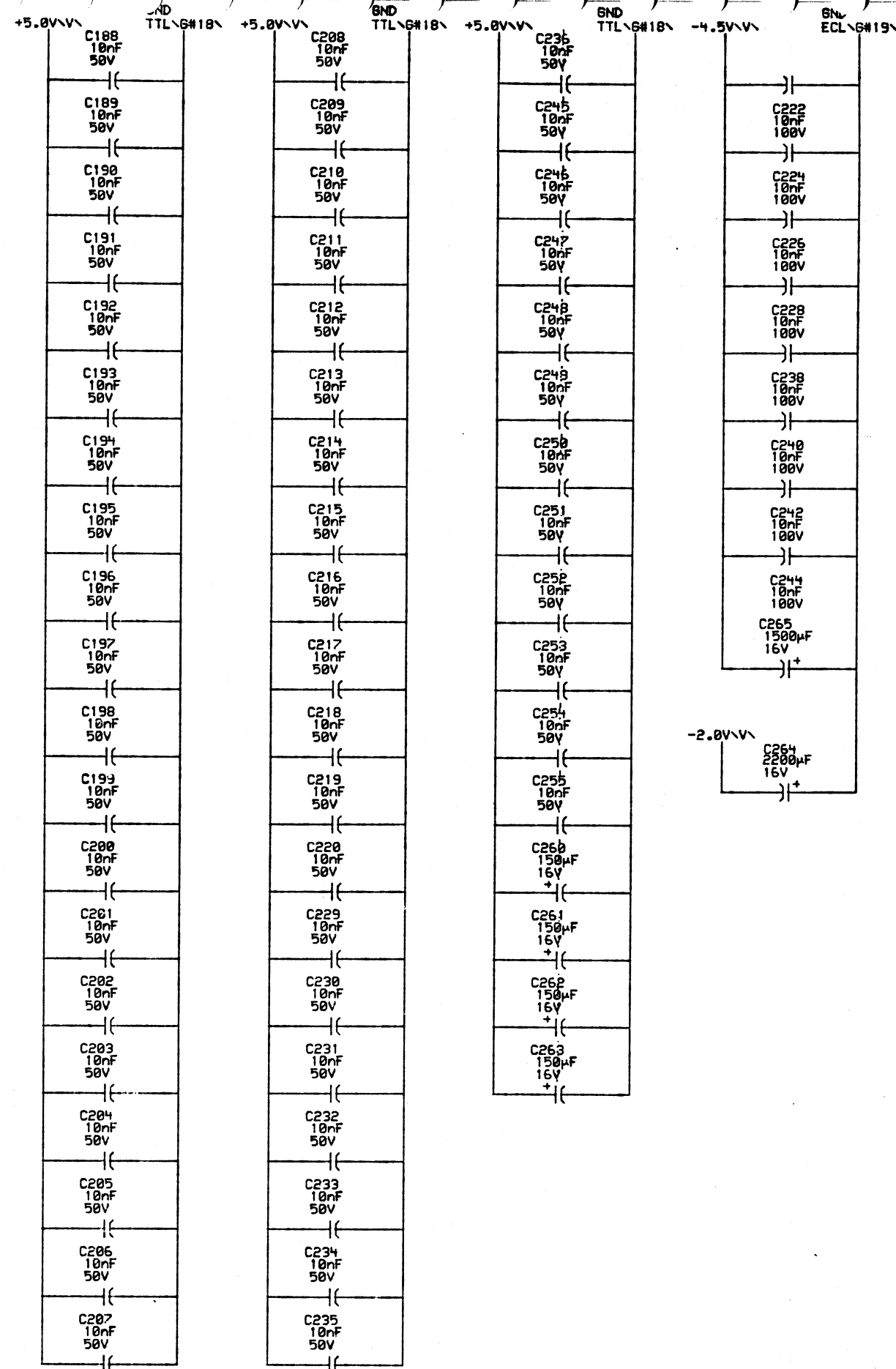
C

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GND ECL
GND TTL

- AB NC
- A45 NC
- A46 NC
- A47 NC
- A48 NC
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- A60 NC
- A62 NC
- A69 NC
- A87 NC
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- C31 NC
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- C50 NC
- A90 NC
- B2 NC
- C3 NC
- C91 NC
- C92 NC

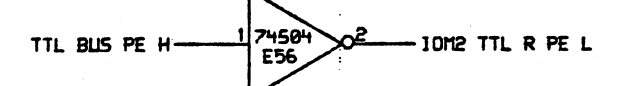
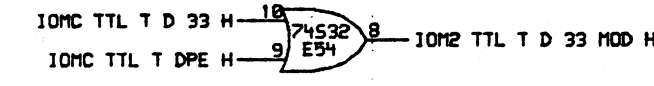
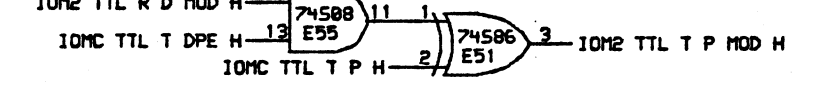
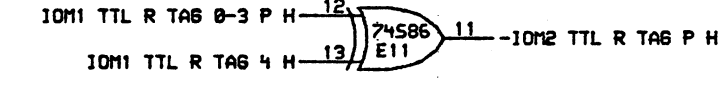
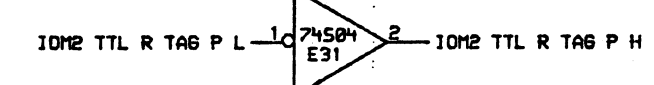
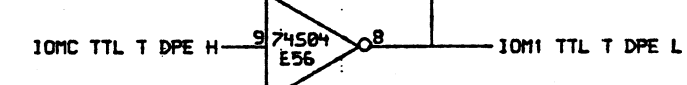
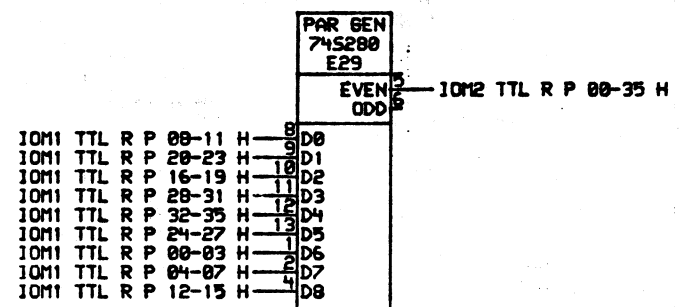
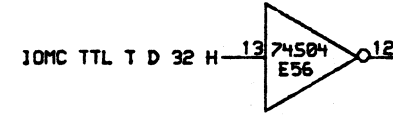
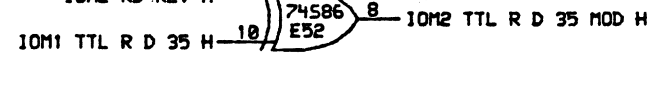
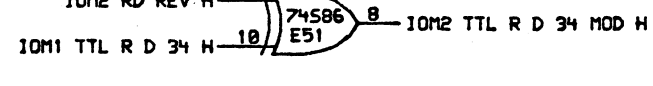
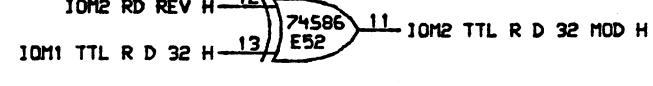
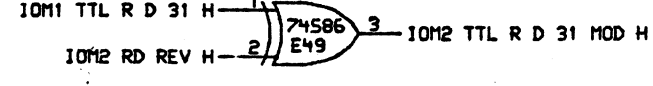
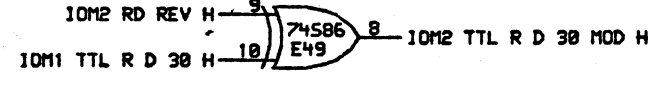
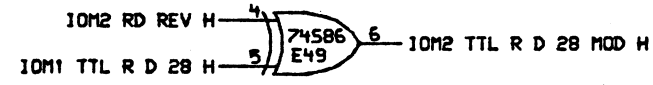
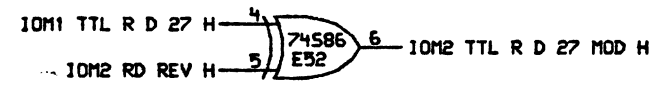
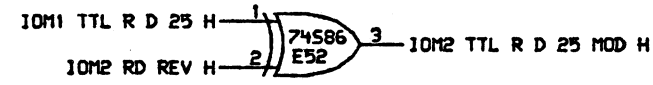
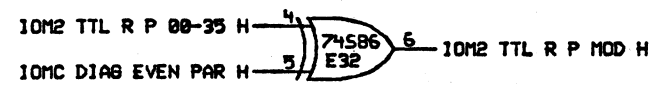
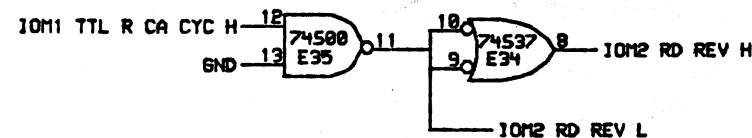
SPARE
E228
SPARE
E244

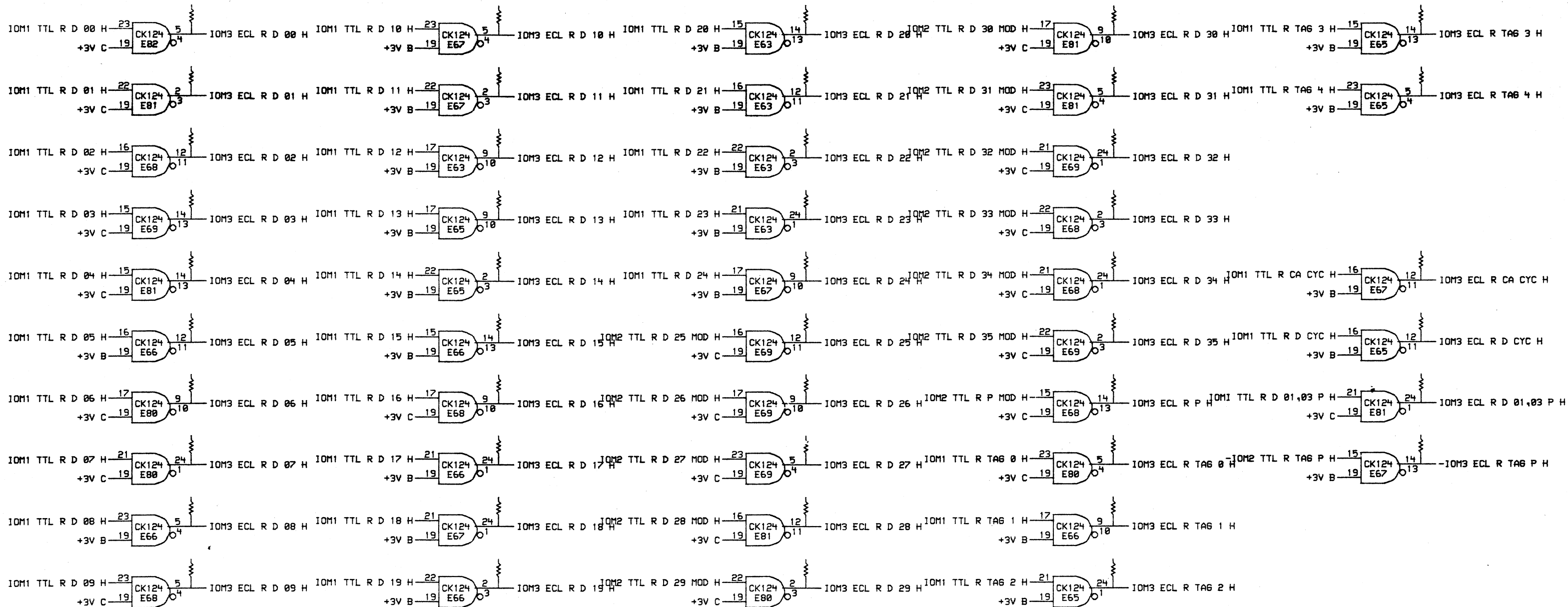
DRN.	DATE	ENG.	DATE	TITLE:	IOP MODULE PWR & GND 2		
CHK'D.	DATE	BOARD LOCATION:	SHEET OF		SIZE	CODE	NUMBER
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FIRST USED ON OPTION/MODEL:					REV.		

VULCAN:<2080.L0068>16IOM1
VULCAN:<2080.L0068>20IOM2
VULCAN:<2080.L0068>9IOM3
VULCAN:<2080.L0068>9IOM4
VULCAN:<2080.L0068>20IOM5
VULCAN:<2080.L0068>21IOM6
VULCAN:<2080.L0068>21IOM7
VULCAN:<2080.L0068>IOM8
VULCAN:<2080.L0068>9IOM9
VULCAN:<2080.L0068>12IOMA
VULCAN:<2080.L0068>16IOMB
VULCAN:<2080.L0068>20IOMC
VULCAN:<2080.L0068>15IOMD
VULCAN:<2080.L0068>21IOME
VULCAN:<2080.L0068>22IOMF
VULCAN:<2080.L0068>22IOMG
VULCAN:<2080.L0068>14IOMH
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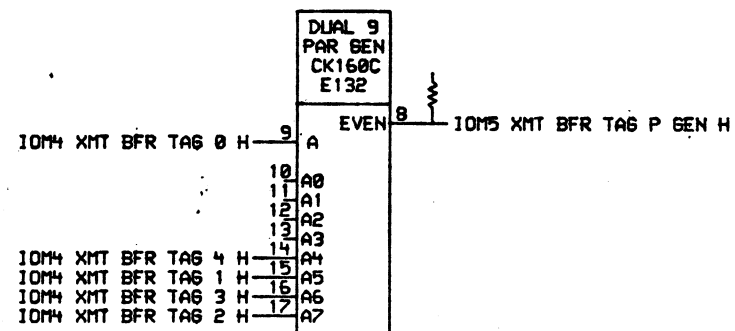
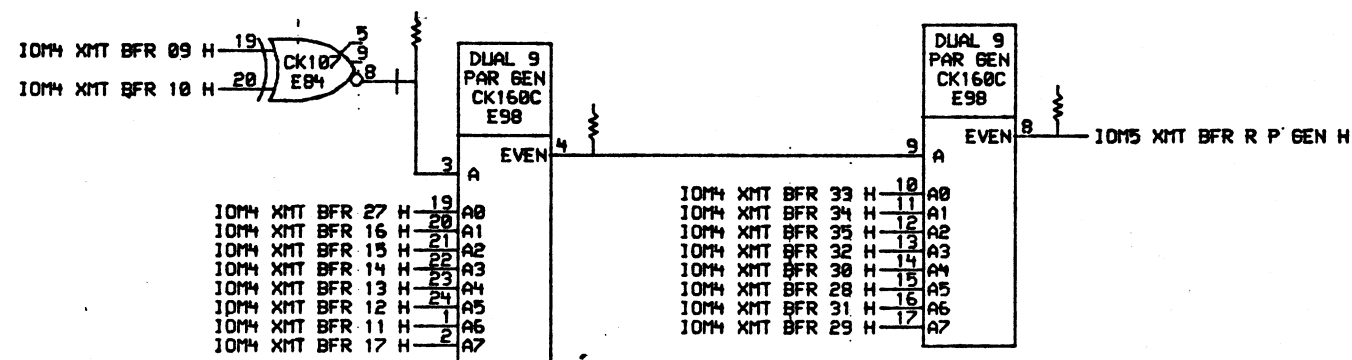
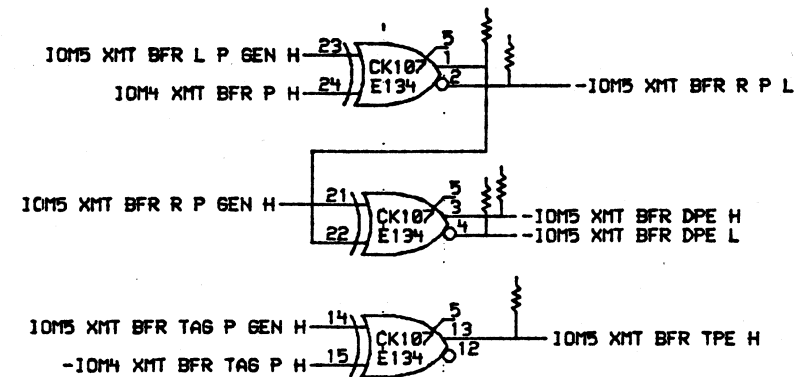
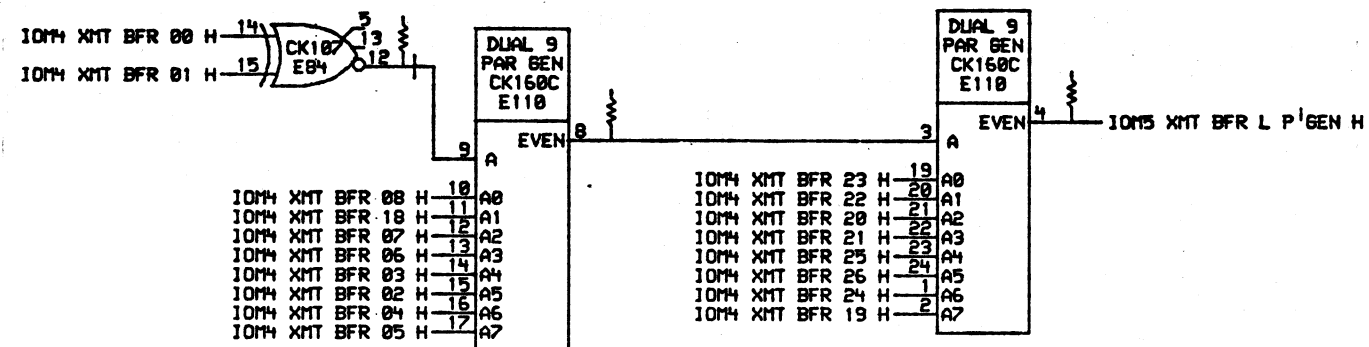
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E162	E149	E136	E123	E110	E097	E084	E071	E057	E037	E017
E161	E148	E135	E122	E109	E096	E083	E070	E056	E036	E016
E160	E147	E134	E121	E108	E095	E082	E069	E055	E035	E015
E159	E146	E133	E120	E107	E094	E081	E068	E054	E034	E014
E158	E145	E132	E119	E106	E093	E080	E067	E053	E033	E013
E157	E144	E131	E118	E105	E092	E079	E066	E052	E032	E012
E156	E143	E130	E117	E104	E091	E078	E065	E051	E031	E011
E155	E142	E129	E116	E103	E090	E077	E064	+R005+	+R003+	+R001+
E154	E141	E128	E115	E102	E089	E076	E063	+R006+	+R004+	+R002+
E153	E140	E127	E114	E101	E088	E075	E062	E049	E029	E009
E152	E139	E126	E113	E100	E087	E074	E061	E048	E028	E008
								E047	E027	E007
								E046	E026	E006
								E045	E025	E005
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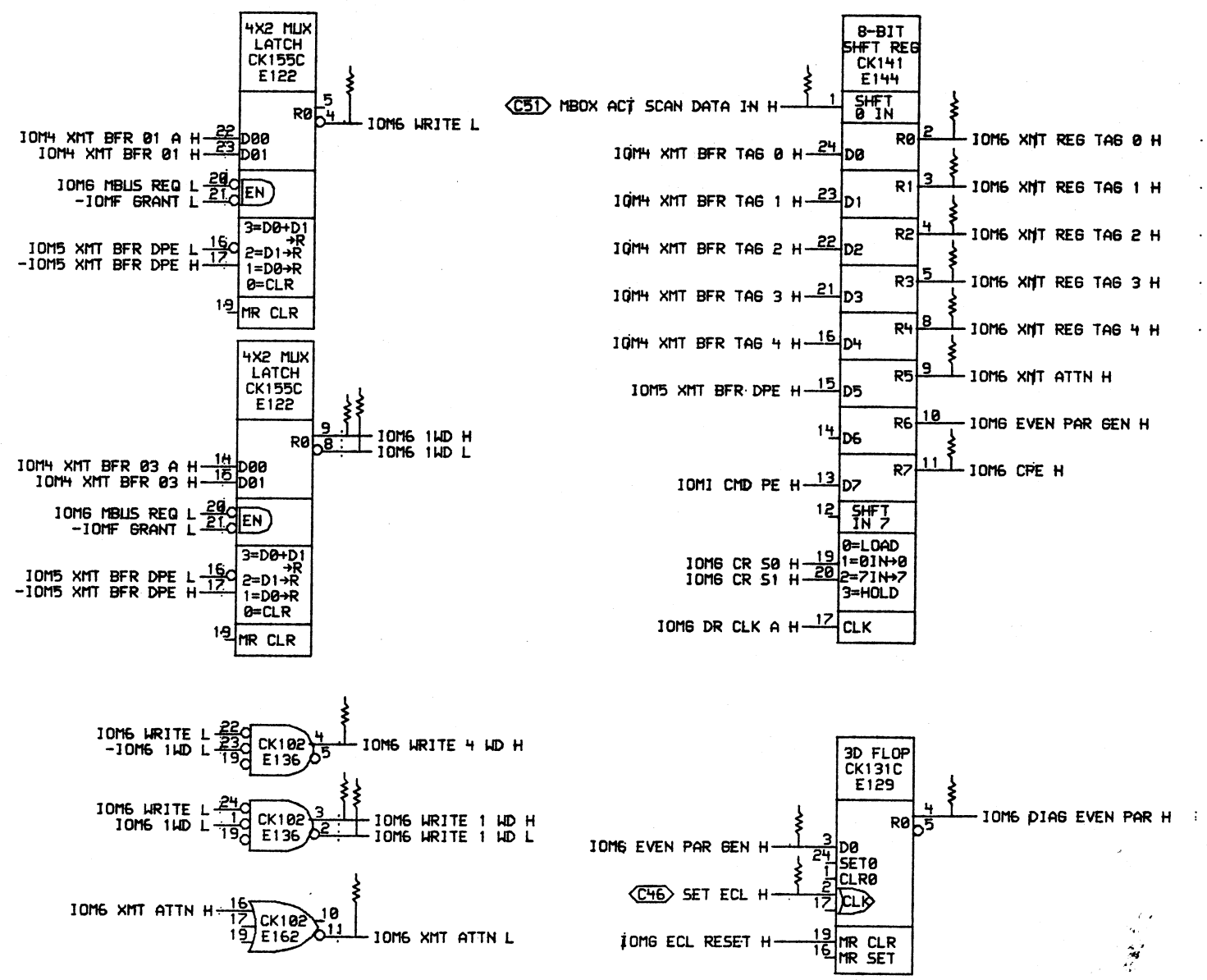
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DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		TTL-ECL
		SHEET	OF	TRANSLATORS
VULCAN: (2000.L0068) IOM3.DRW 15-DEC-81 09:42	NEXT HIGHER ASSEMBLY:			SIZE CODE NUMBER
FIRST USED ON OPTION/MODEL:				D CS L0068-0-IOM3
				REV.



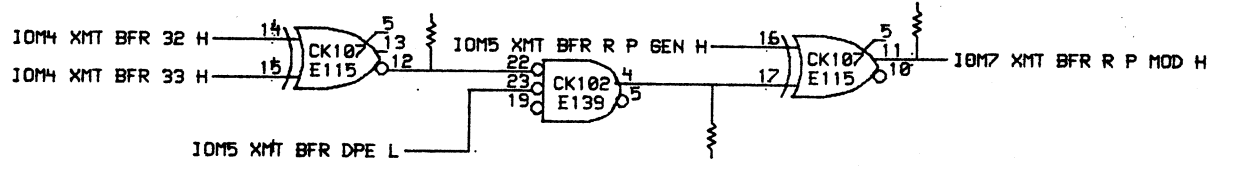
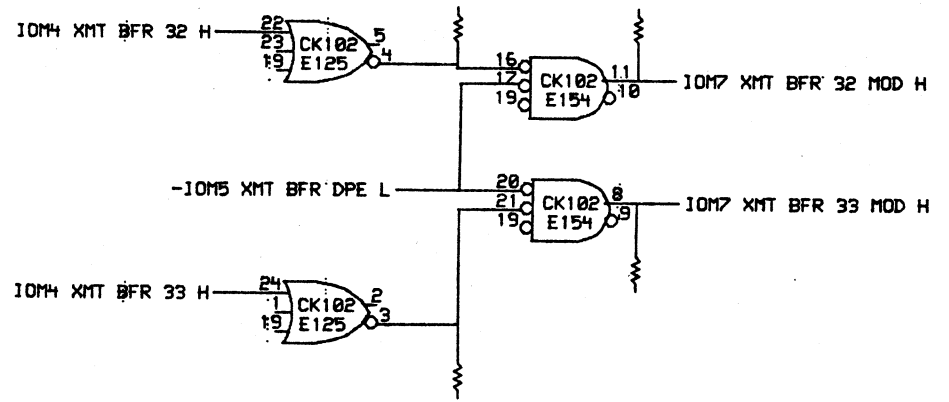
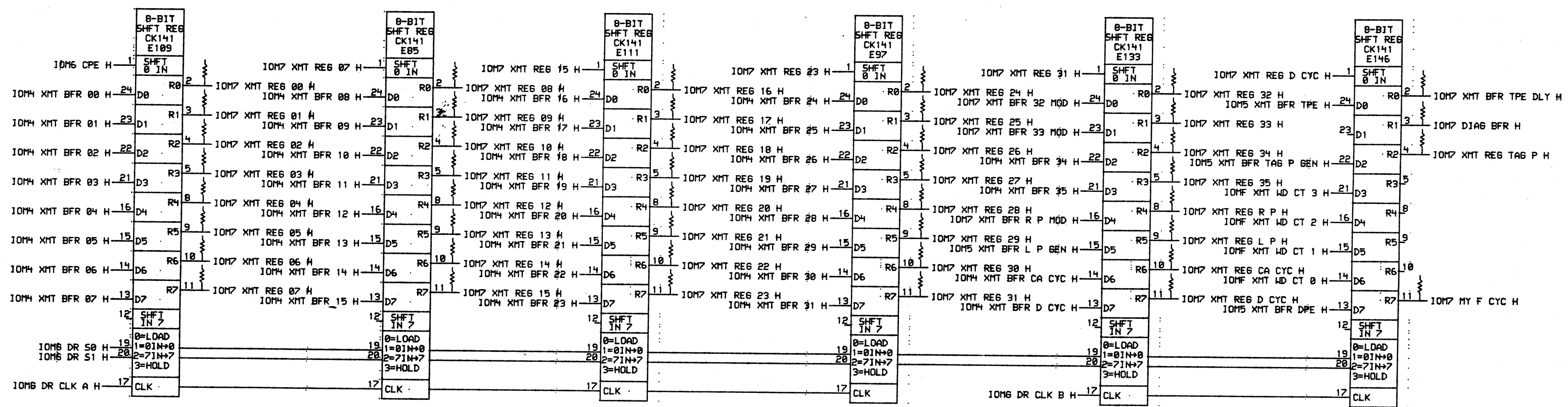


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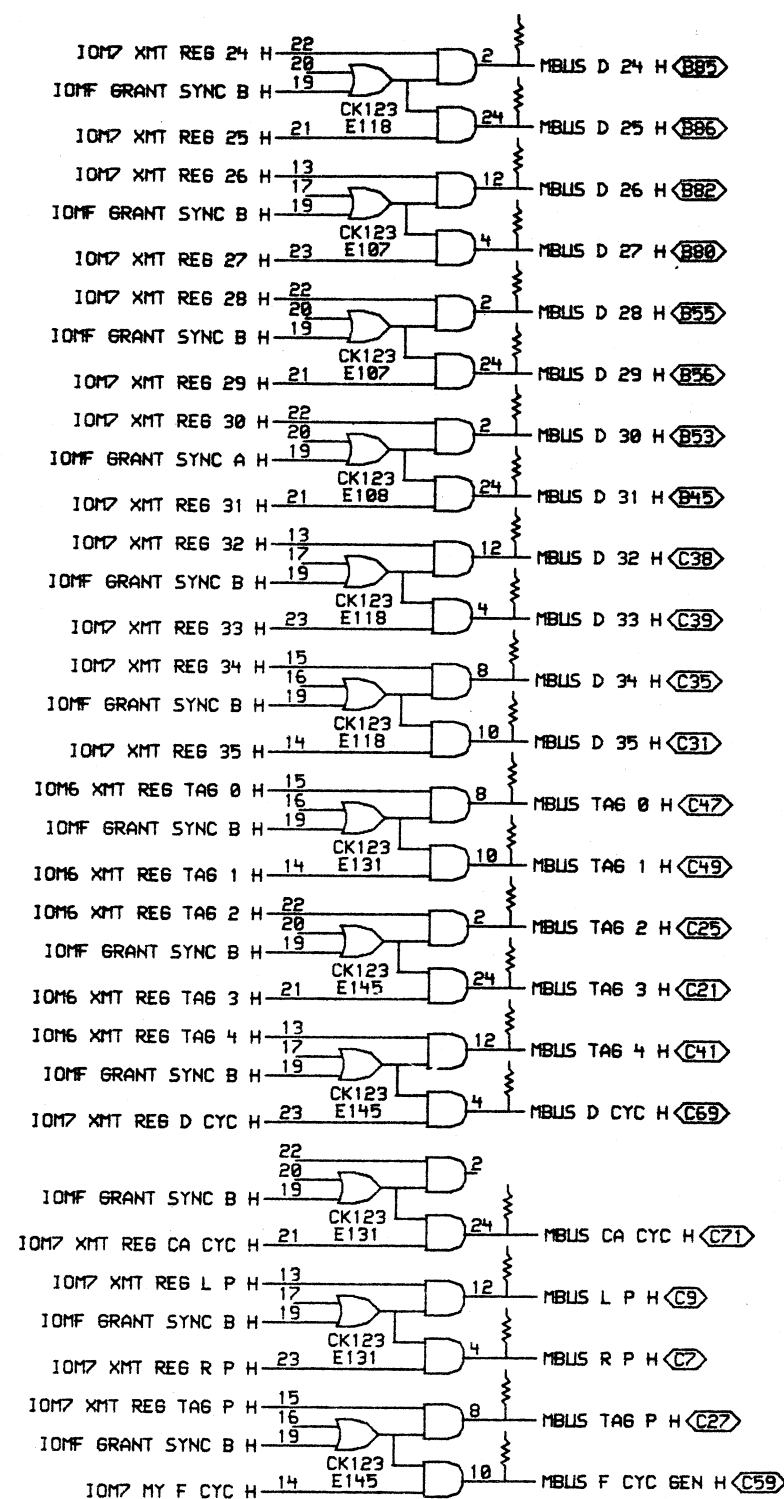
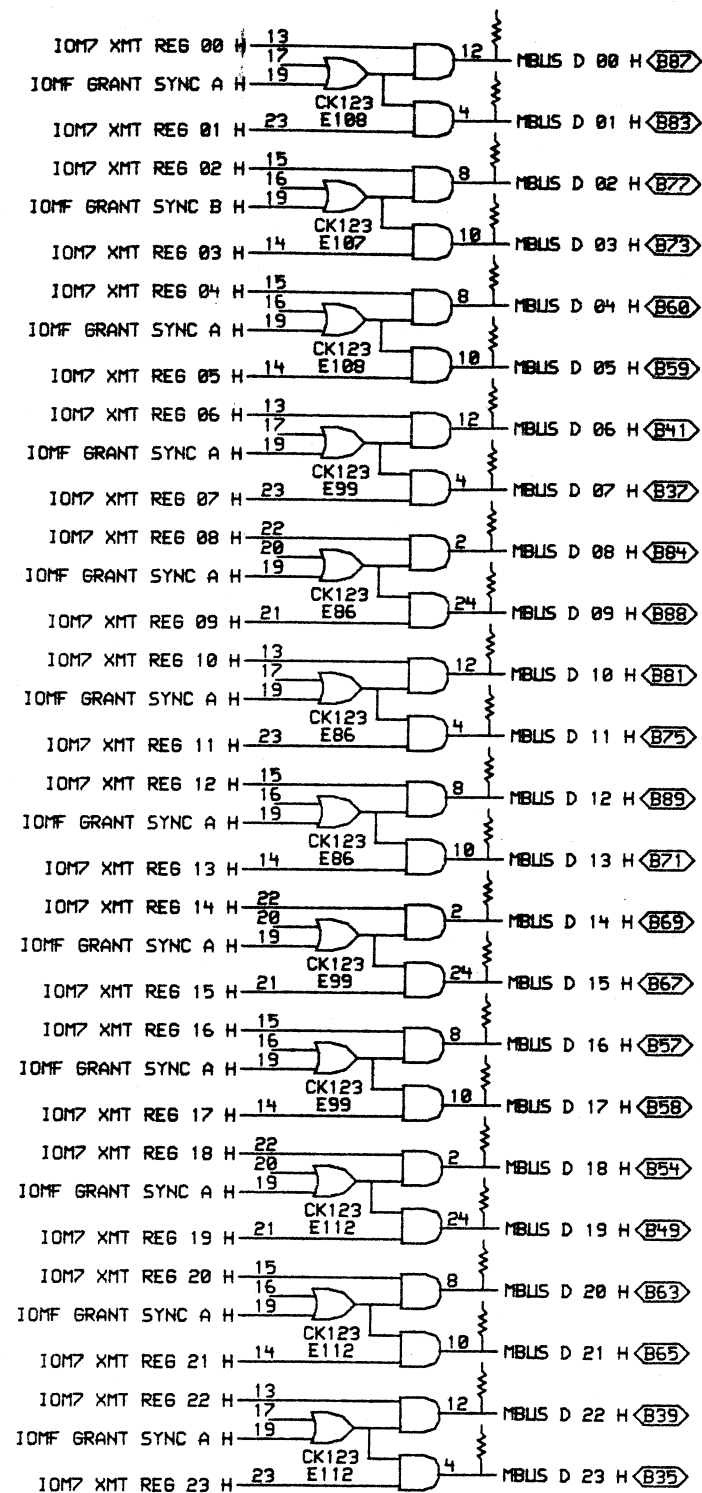
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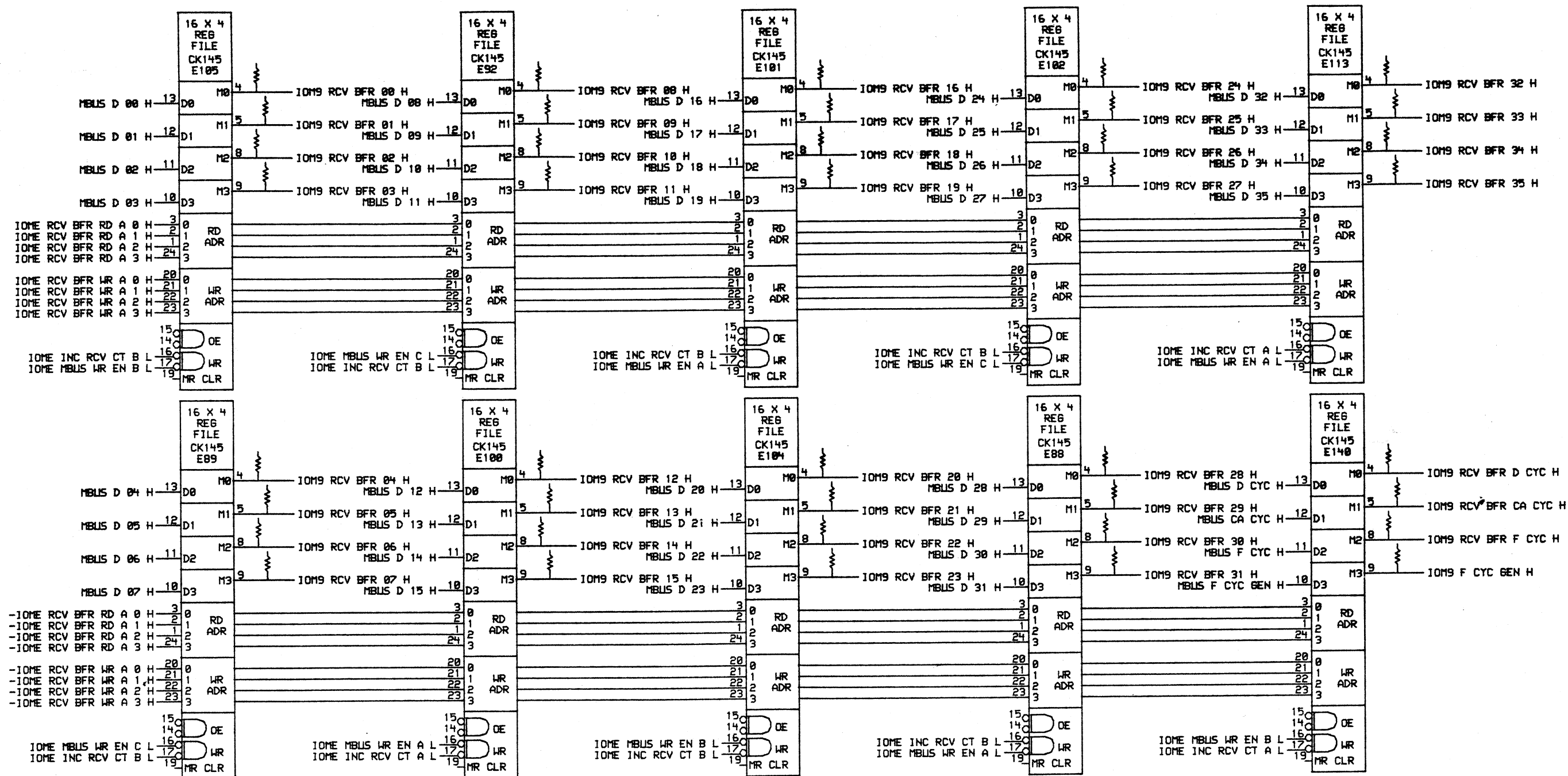
REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE:
	24-NOV-82			MBUS XMT REG
CHK'D.	DATE	BOARD LOCATION:	SIZE	CODE
VIL CAN 10000-10068-1007.DRW 110-NOV-82 15:31 INEXT HTHER ASSMRY:		NUMBER		
		REV.		

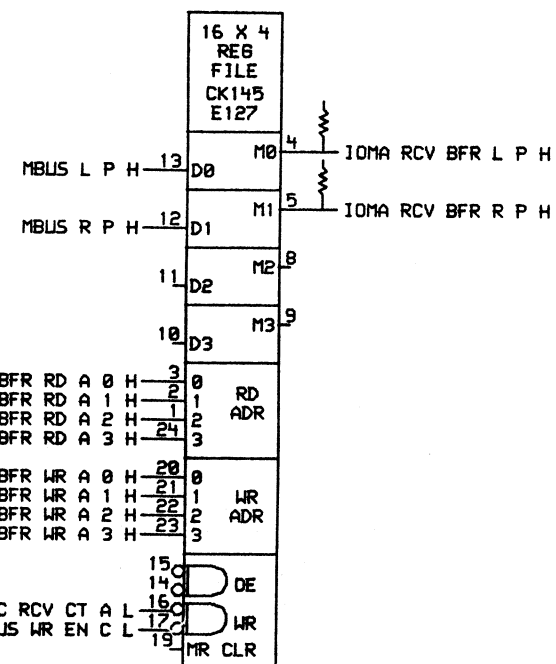
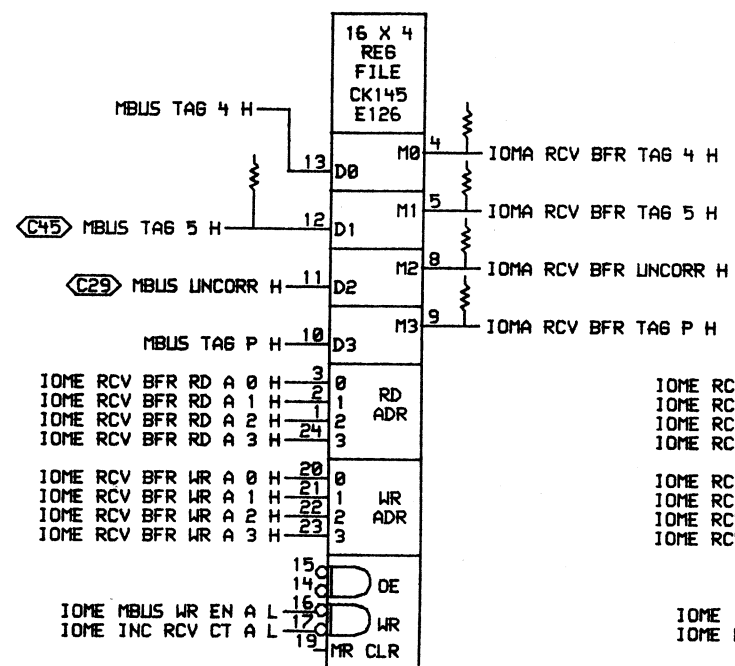
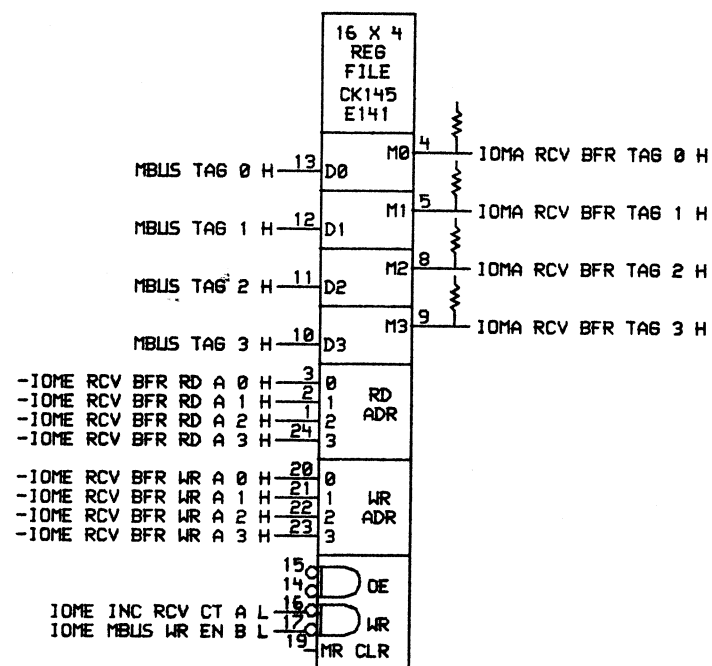


C63 MBLUS F CYC H

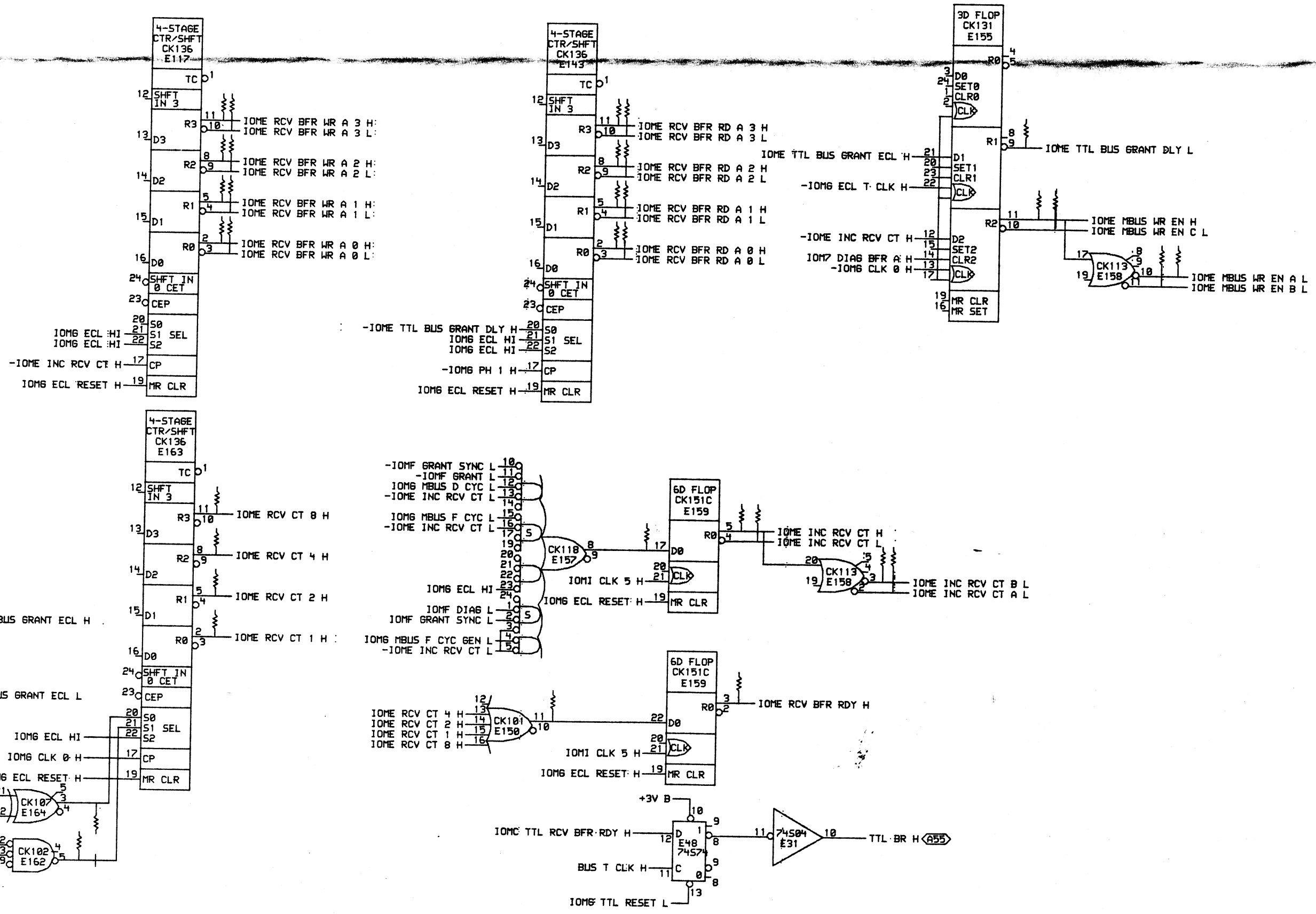
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	OF	MBLUS DRIVERS
VILCAN (2000.10068) IOM8.DRW 09-NOV-81 13:40 NEXT HIGHER ASSEMBLY:				SIZE CODE
FIRST USED ON OPTION/MODEL:				NUMBER
				REV.
				D C5 L0068-0-IOM8



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		RCV REG FILE
VULCAN (2000.L0068) 910M9.DRW 02-DEC-81 17:24 NEXT HIGHER ASSEMBLY:				D00-D35
FIRST USED ON OPTION/MODEL:	SIZE	CODE	NUMBER	REV.
	D	CS	L0068-0-10M9	





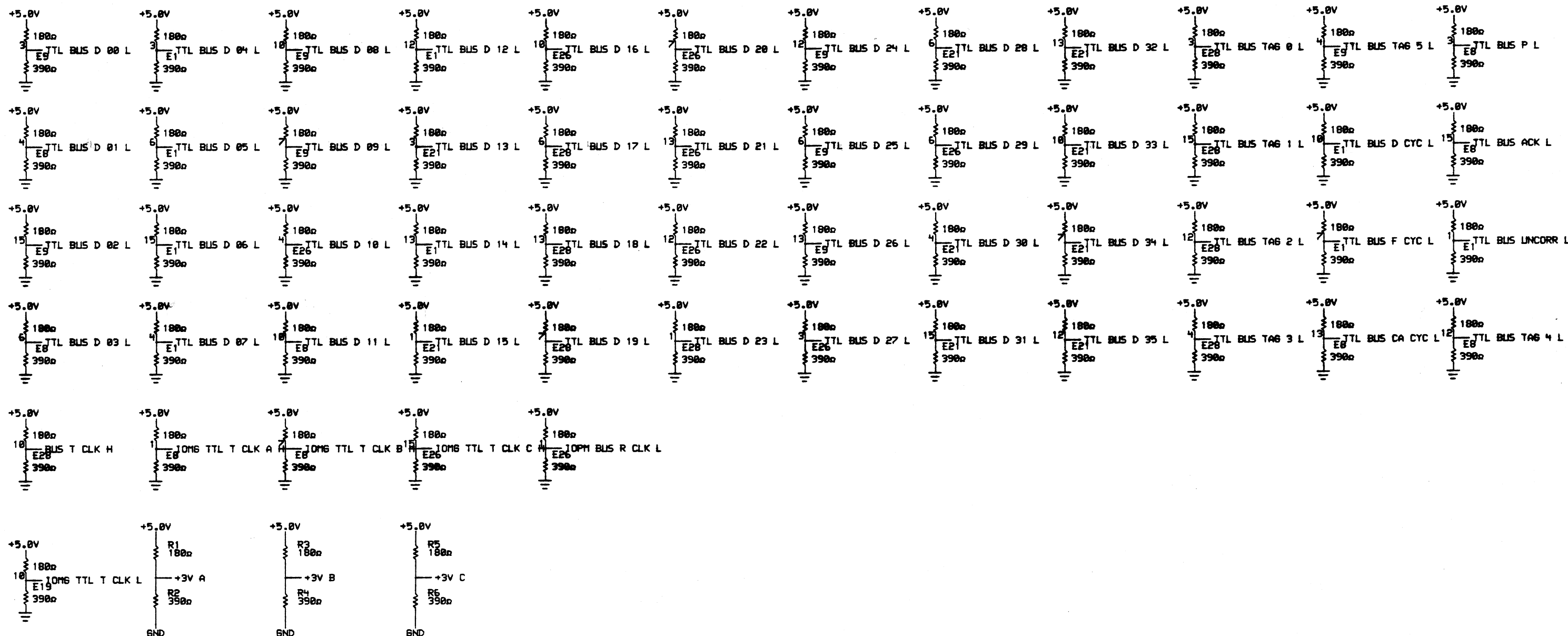


D

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D

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REV.

NUMBER

L0068-0-10MH

CODE

CS

SIZE

D

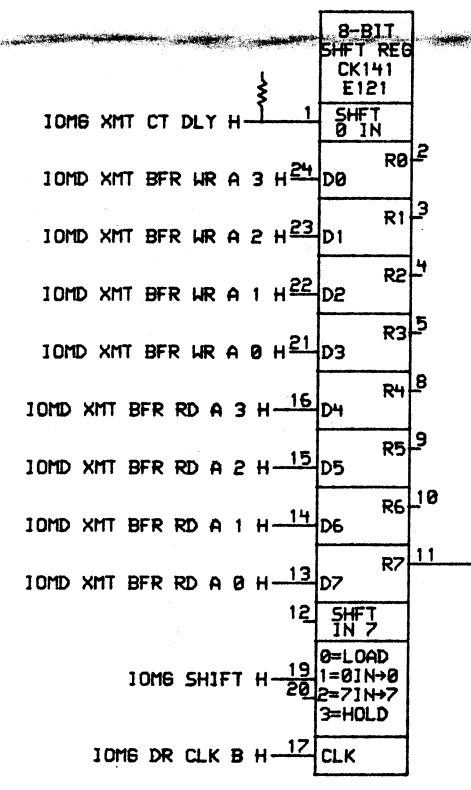
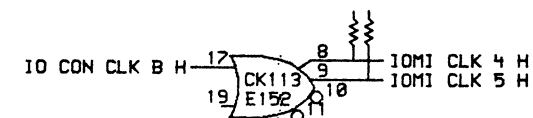
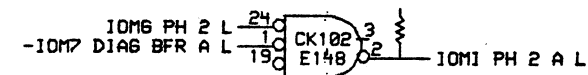
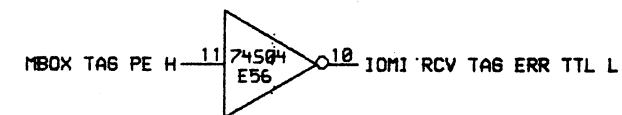
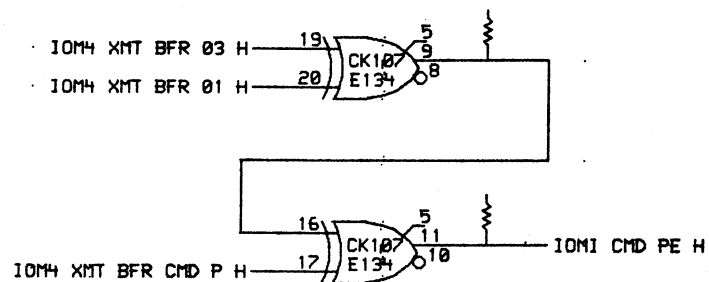
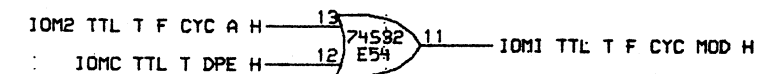
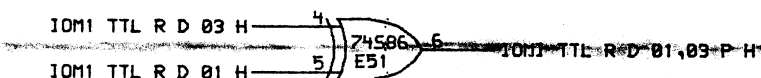
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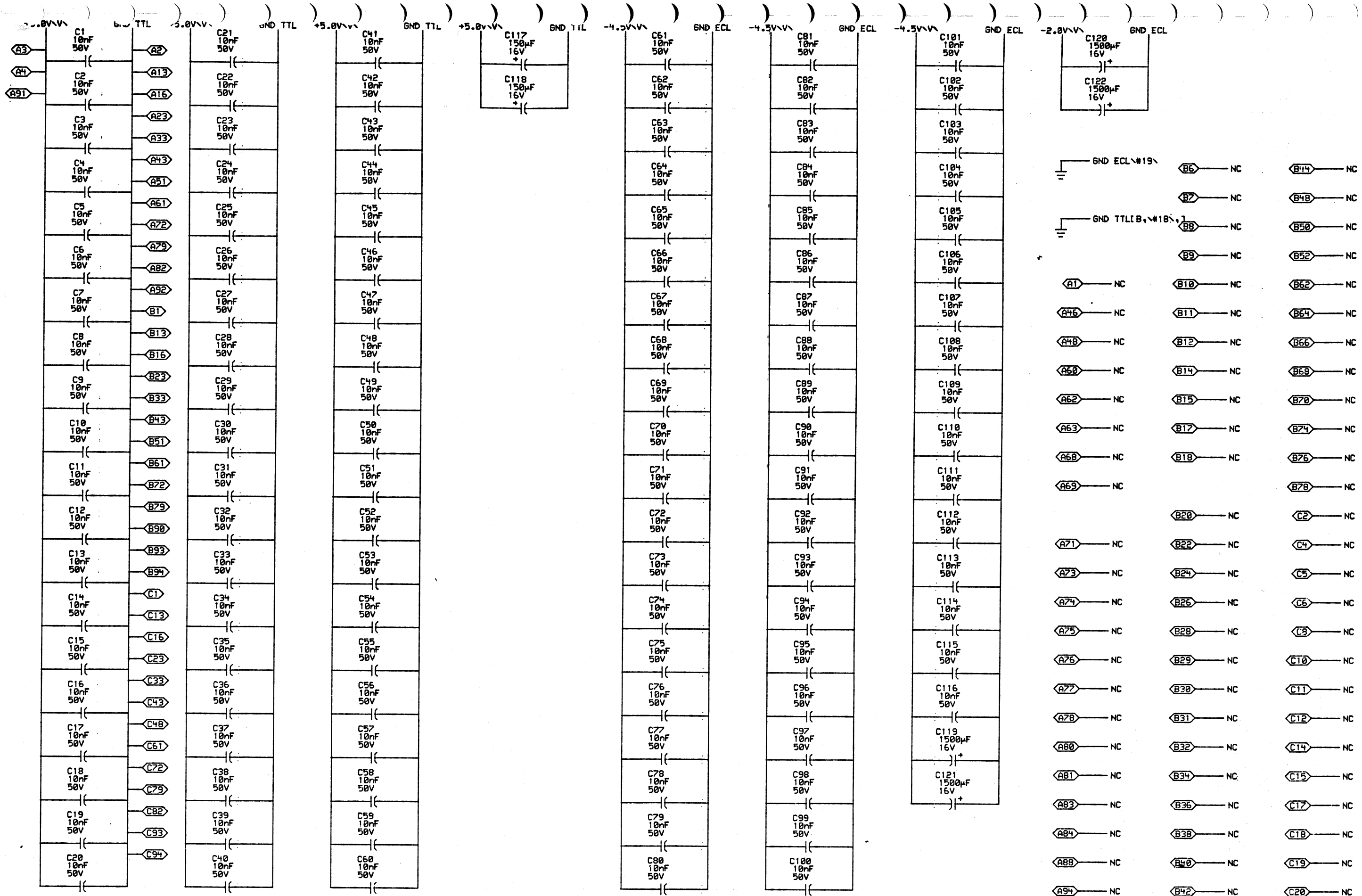
A

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	13-MAY-82	ENG.	DATE		TITLE:	TTL BUS TERMINATORS		
	CHK'D.								
VIA COM (2000-4-0000) 141000.DRM		12-MAY-82 16:39		NEXT HIGHER ASSEMBLY:		SIZE	CODE	NUMBER	REV.
FIRST USED ON OPTION/MODEL:						D	CS	L0068-0-10MH	





- | | | | |
|-----|----|-----|----|
| C22 | NC | C80 | NC |
| C24 | NC | C83 | NC |
| C26 | NC | C84 | NC |
| C28 | NC | C86 | NC |
| C30 | NC | C87 | NC |
| C32 | NC | C88 | NC |
| C34 | NC | C90 | NC |
| C36 | NC | A90 | NC |
| C40 | NC | B2 | NC |
| C42 | NC | B3 | NC |
| C44 | NC | B4 | NC |
| | | B46 | NC |
| C50 | NC | B47 | NC |
| C52 | NC | B91 | NC |
| C54 | NC | B92 | NC |
| C56 | NC | C3 | NC |
| C57 | NC | C91 | NC |
| C58 | NC | C92 | NC |
| C60 | NC | | |
| C62 | NC | | |
| C64 | NC | | |
| C68 | NC | | |
| C70 | NC | | |
| C73 | NC | | |
| C74 | NC | | |
| C75 | NC | | |
| C76 | NC | | |
| C77 | NC | | |
| C78 | NC | | |

NOTE: UNUSED +12.0V

NOTE: UNUSED -12.0V

NOTE: UNUSED +5.0V

NOTE: UNUSED +5.0V

NOTE: UNUSED +5.0V

NOTE: UNUSED +5.0V

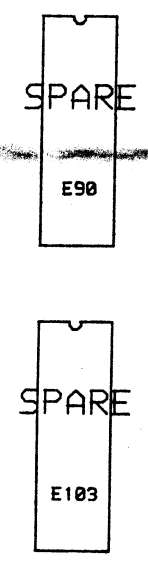
NOTE: UNUSED +5.0V

NOTE: UNUSED +5.0V

NOTE: UNUSED +5.0V

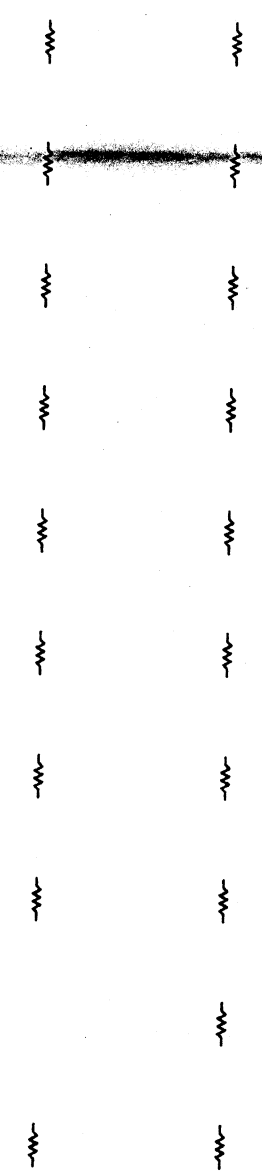
NOTE: UNUSED +5.0V

NOTE: UNUSED +5.0V



NOTE: THE FOLLOWING TTL DIP
LOCATIONS ARE ALSO SPARES

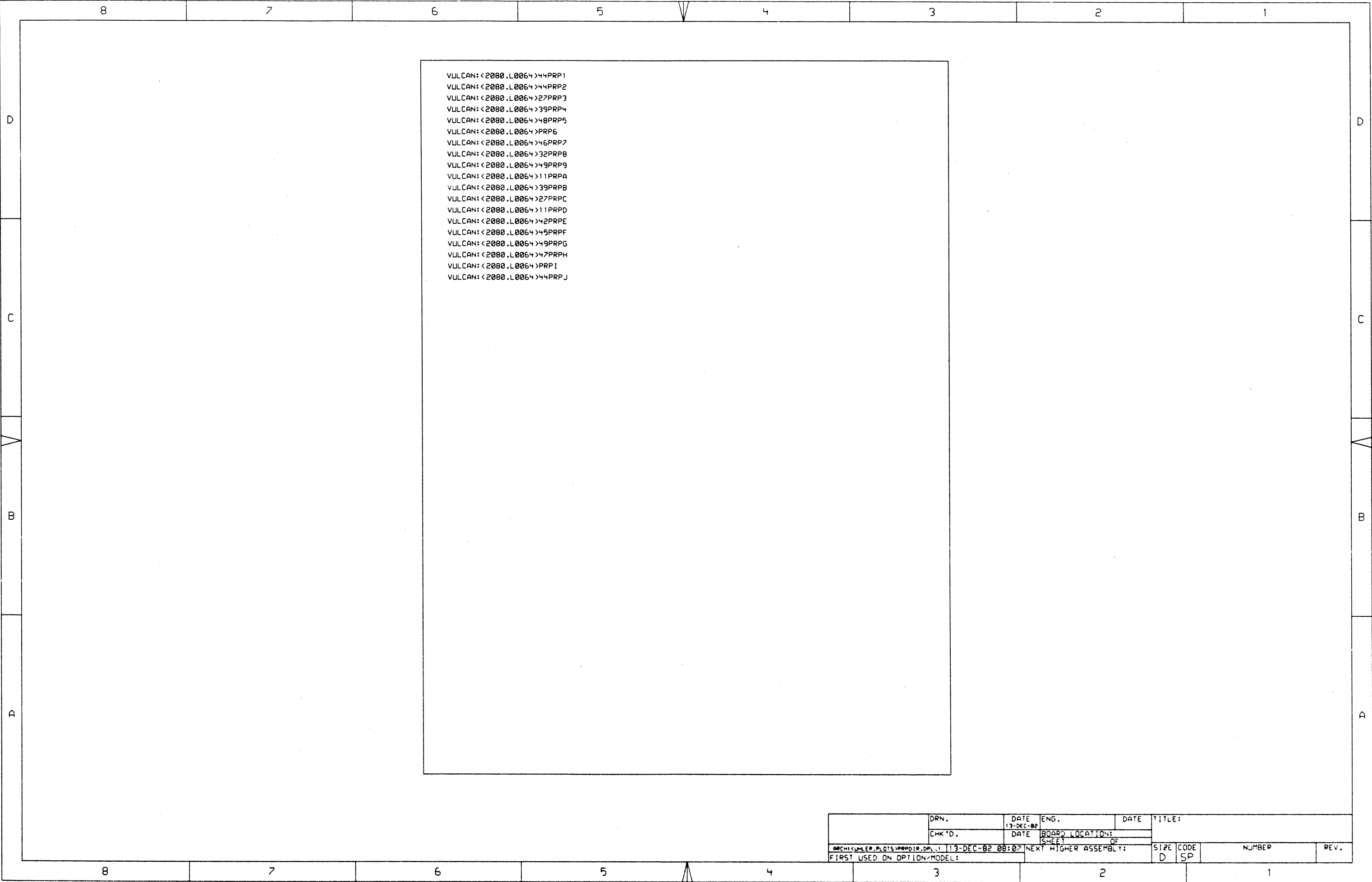
E7, E13, E14, E15, E16, E17, E18
E20, E27, E33, E47, E53



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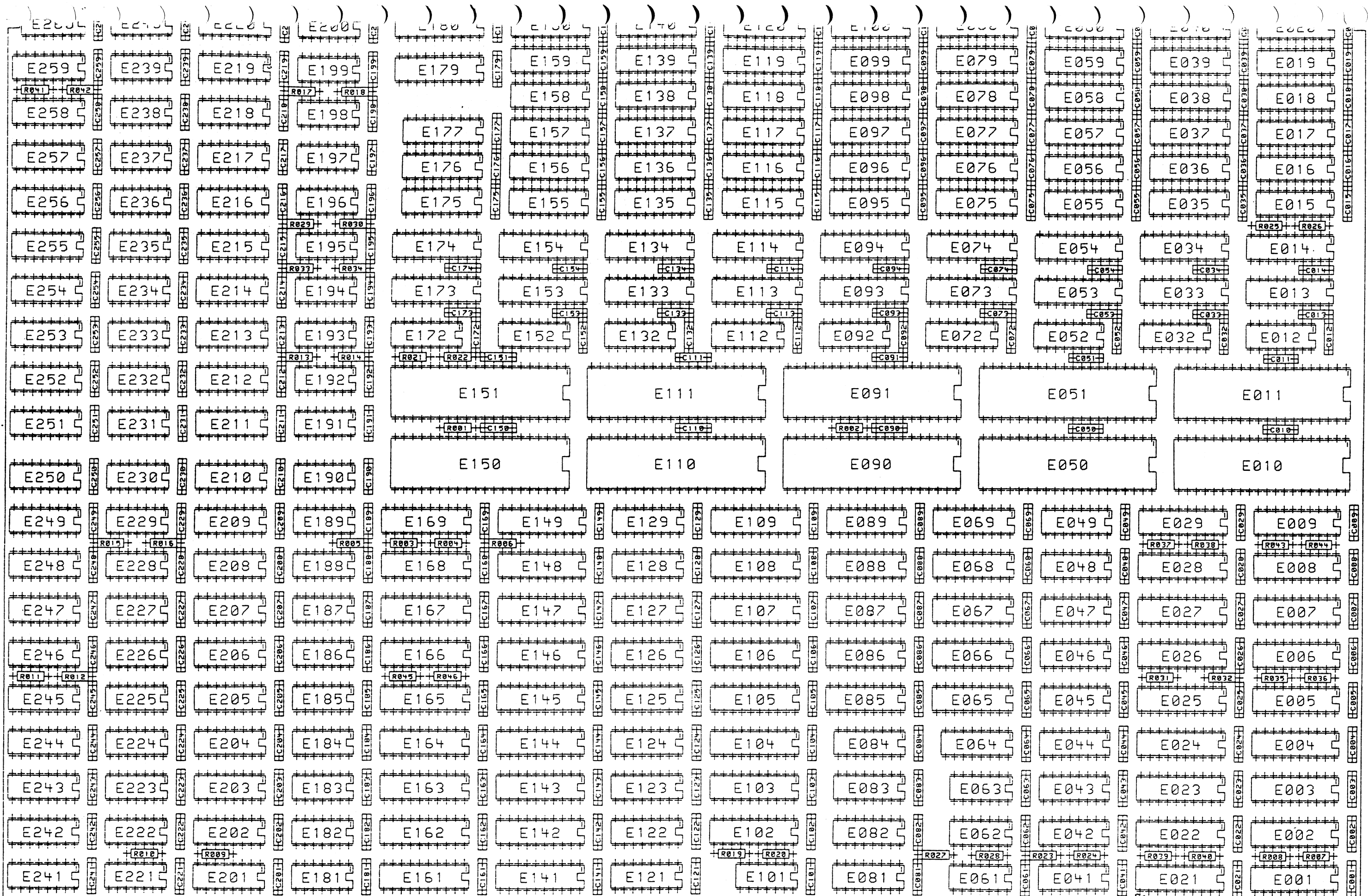
REVISIONS		
CHK	CHANGE NO.	REV

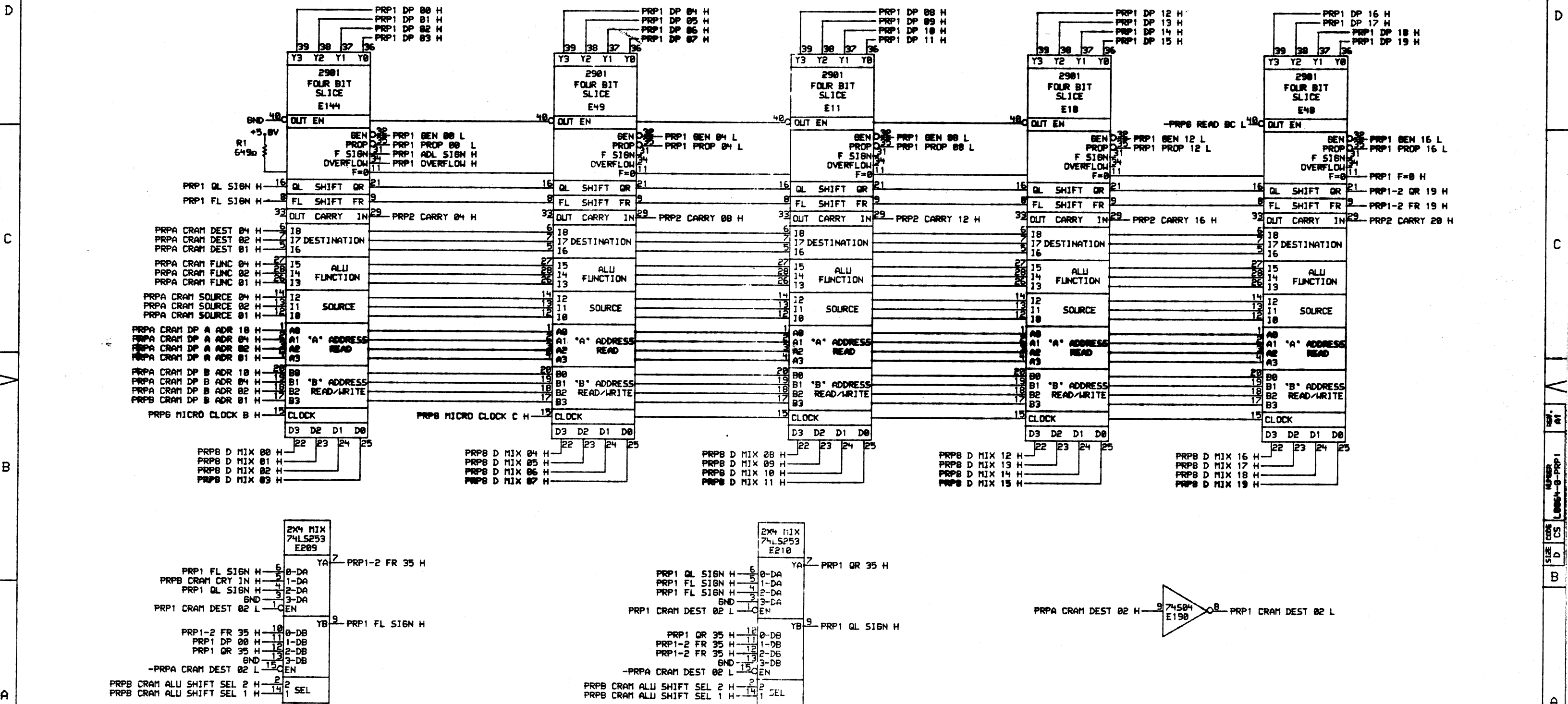
digital	DRN. <i>2.0</i>	DATE 24-NOV-82	ENG.	DATE	TITLE: IOM MODULE PWR & GND 2
	CHK'D.	DATE 17-NOV-82 12:08	BOARD LOCATION:	SHEET OF	
FIRST USED ON OPTION/MODEL:			NEXT HIGHER ASSEMBLY:		SIZE D CODE CS NUMBER L0068-0-10MK REV.

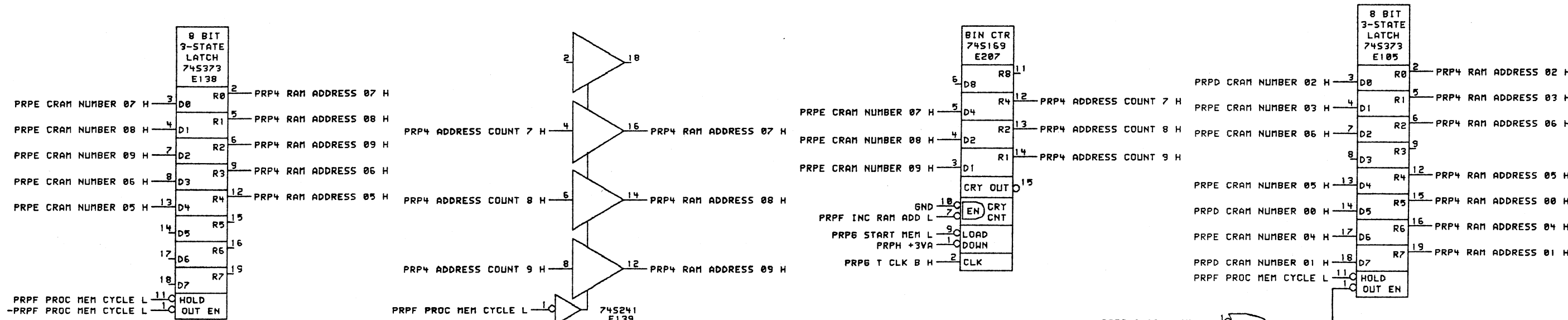
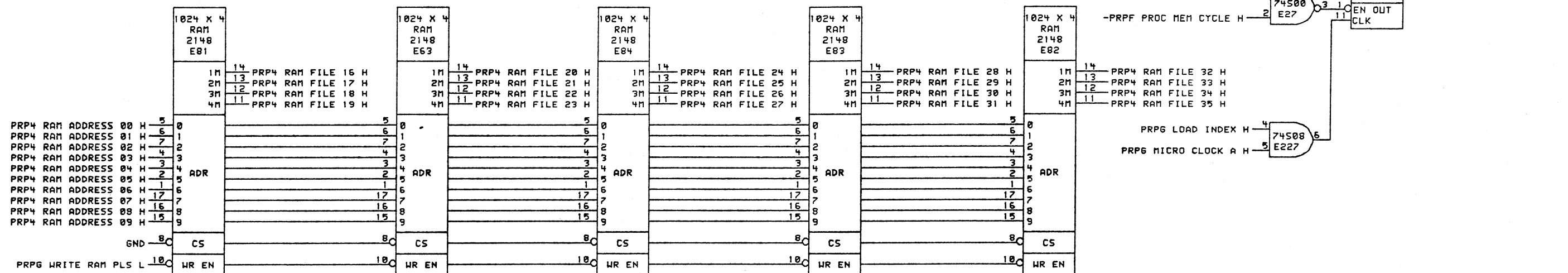
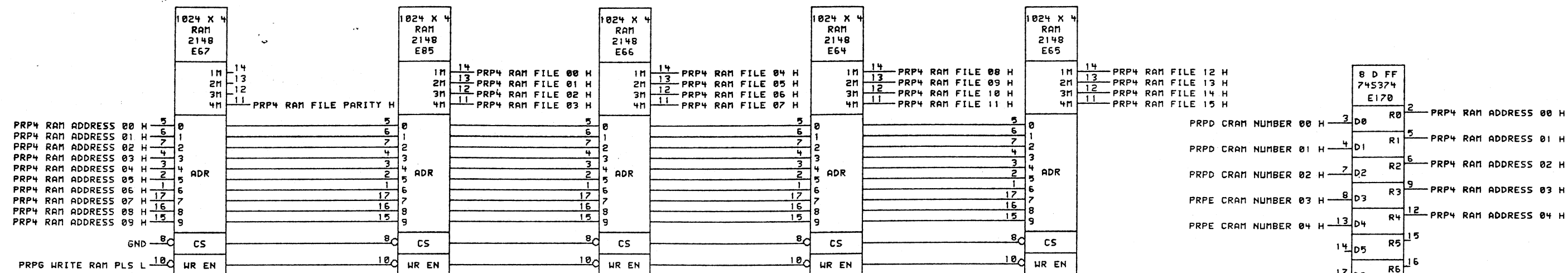


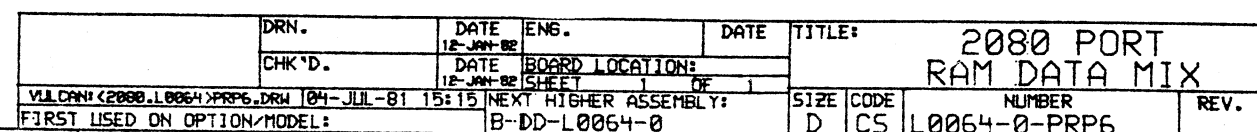
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VULCAN: <2000.L0064>44PRP2
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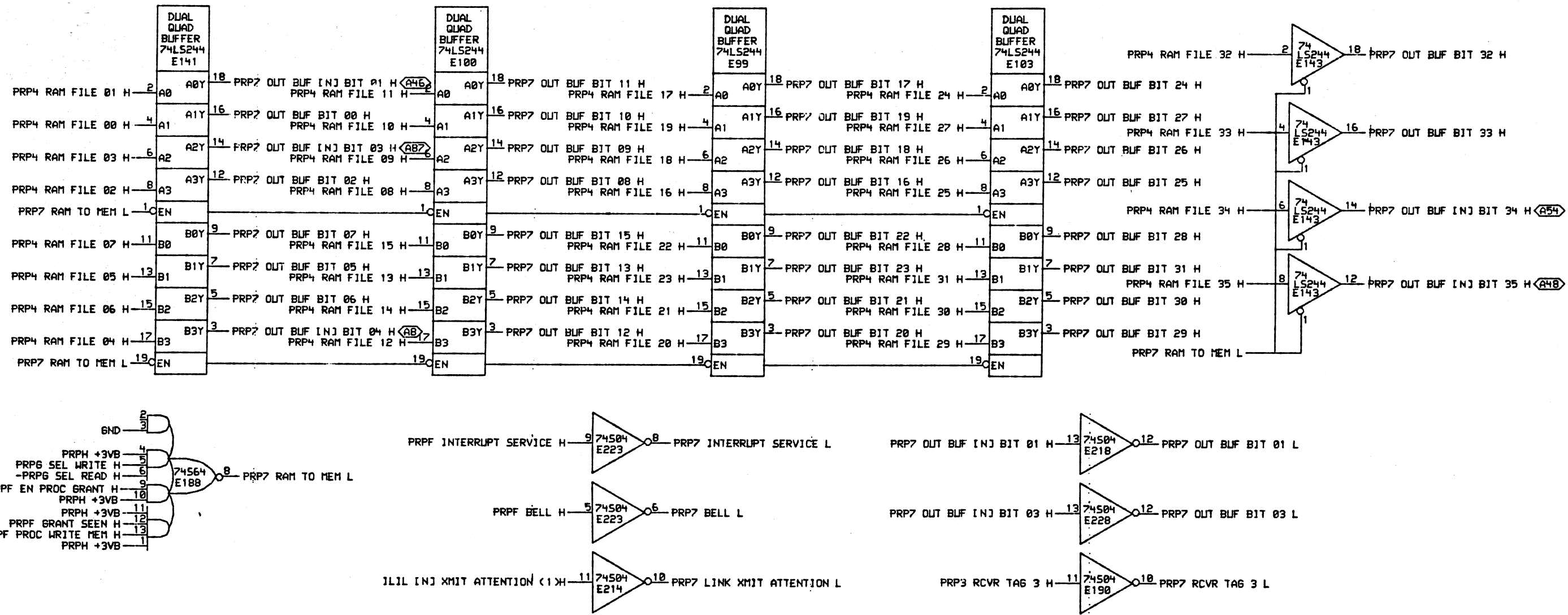
DRN.	DATE 13-DEC-02	ENG.	DATE	TITLE:	
CHK'D.	DATE	BOARD LOCATION:			
ARCHITECT/PLT/SPR/DIR/DPL/1		SHEET OF			
FIRST USED ON OPTION/MODEL:				SIZE D	CODE SP
NEXT HIGHER ASSEMBLY:				NUMBER	REV.

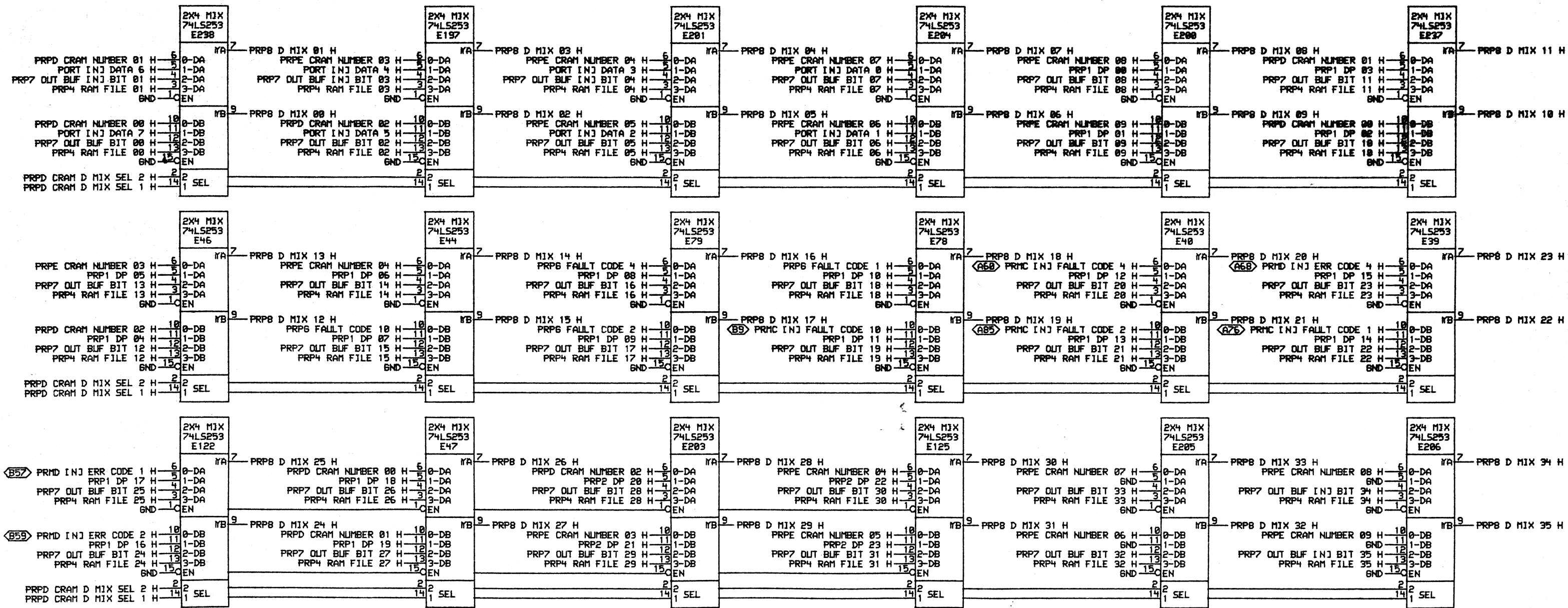


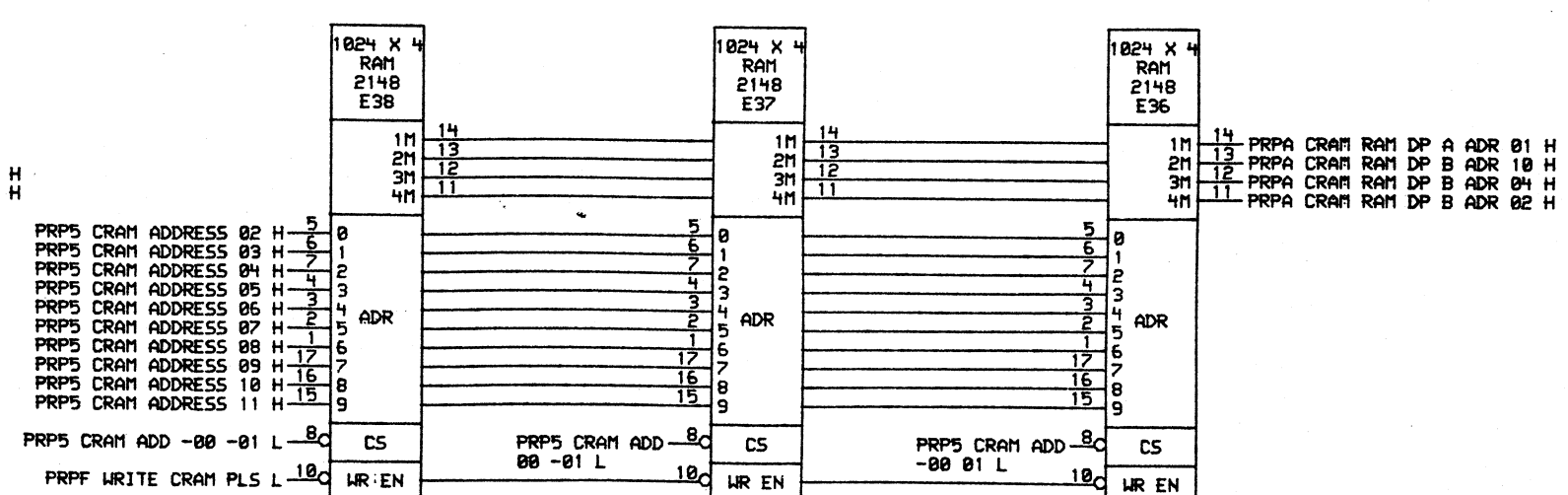
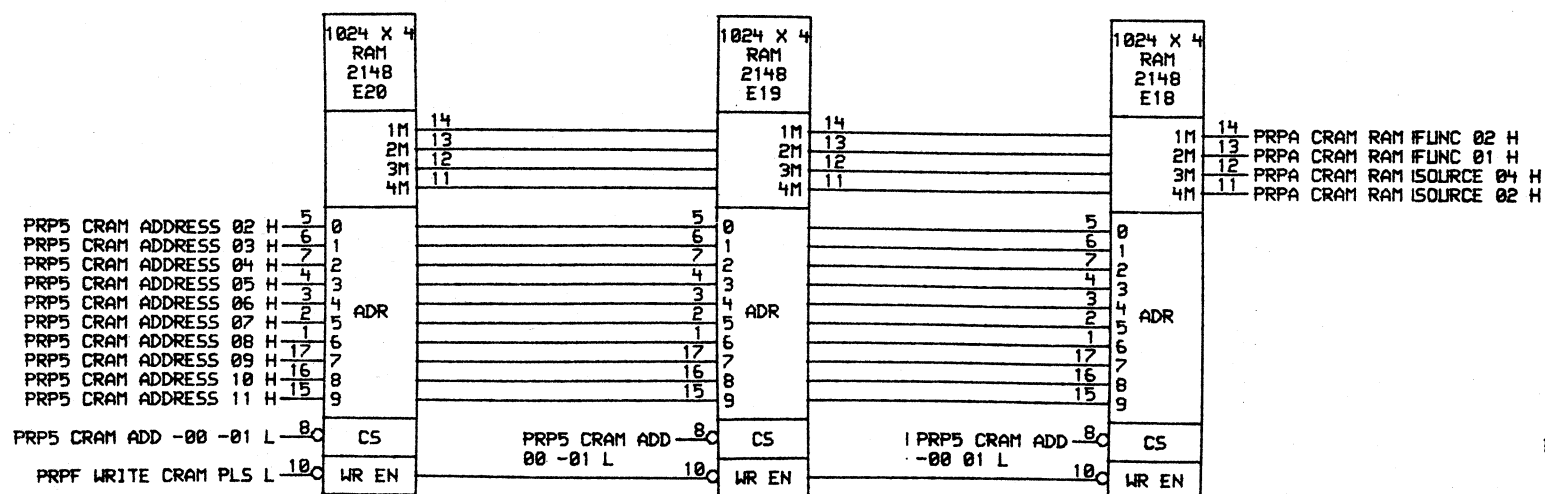
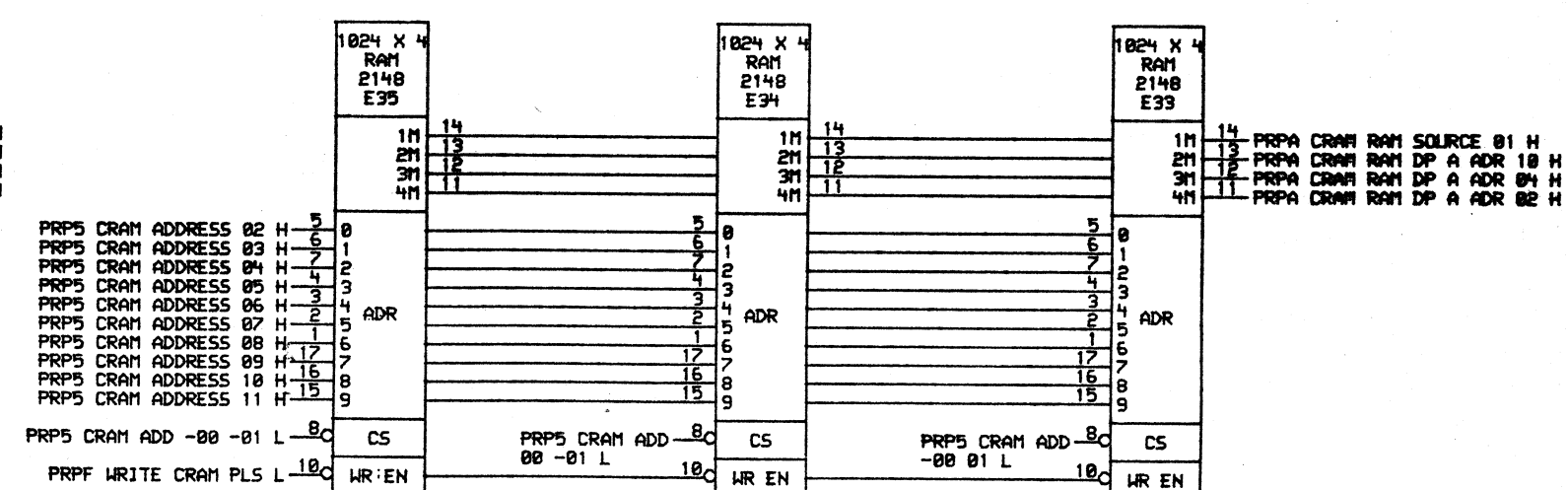
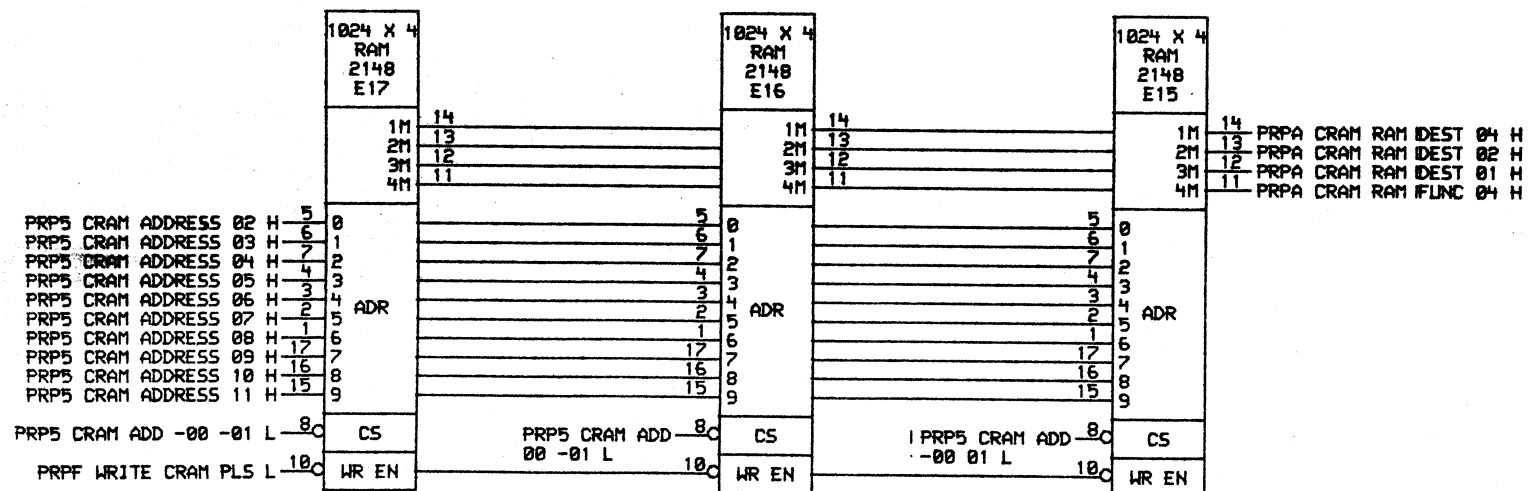
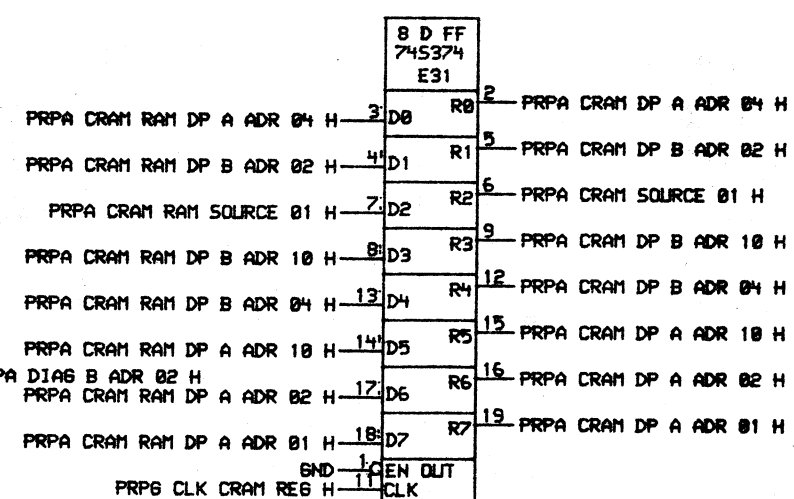
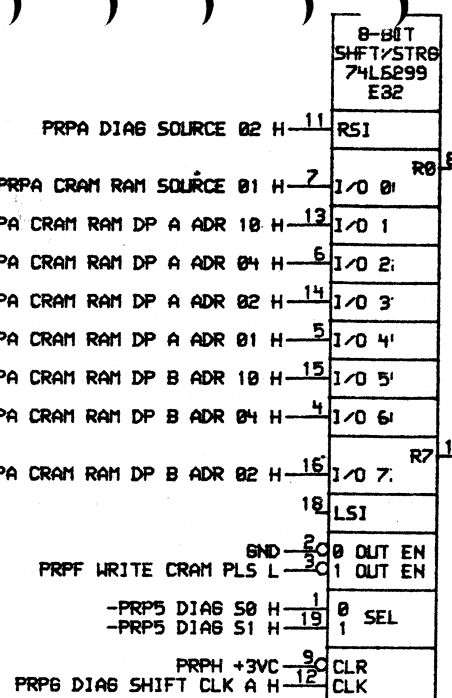
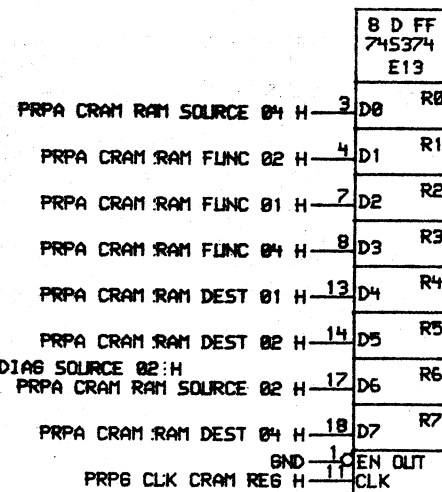
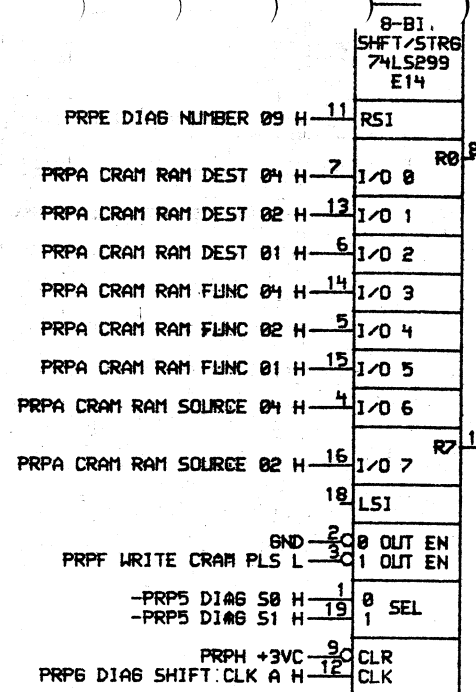




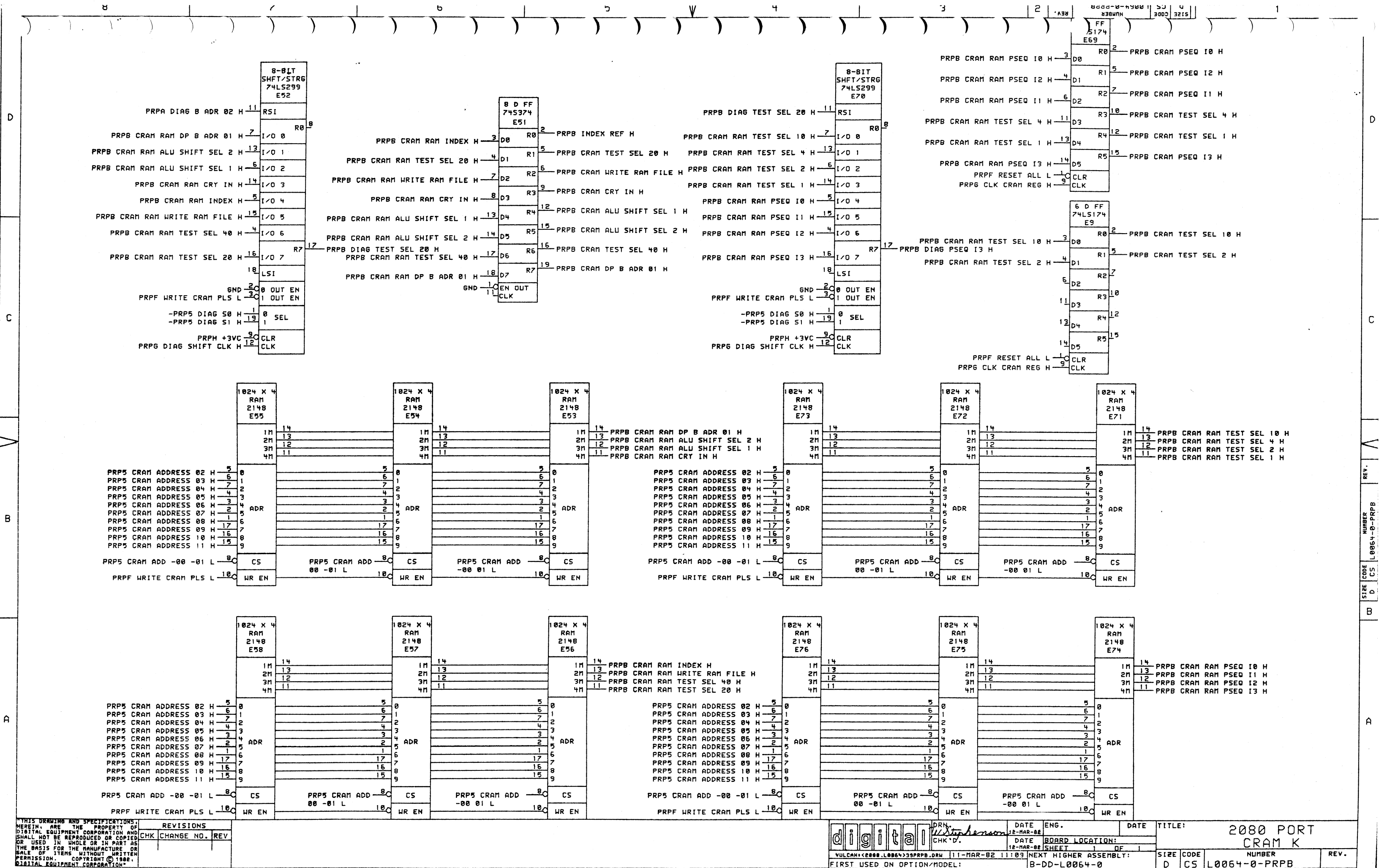


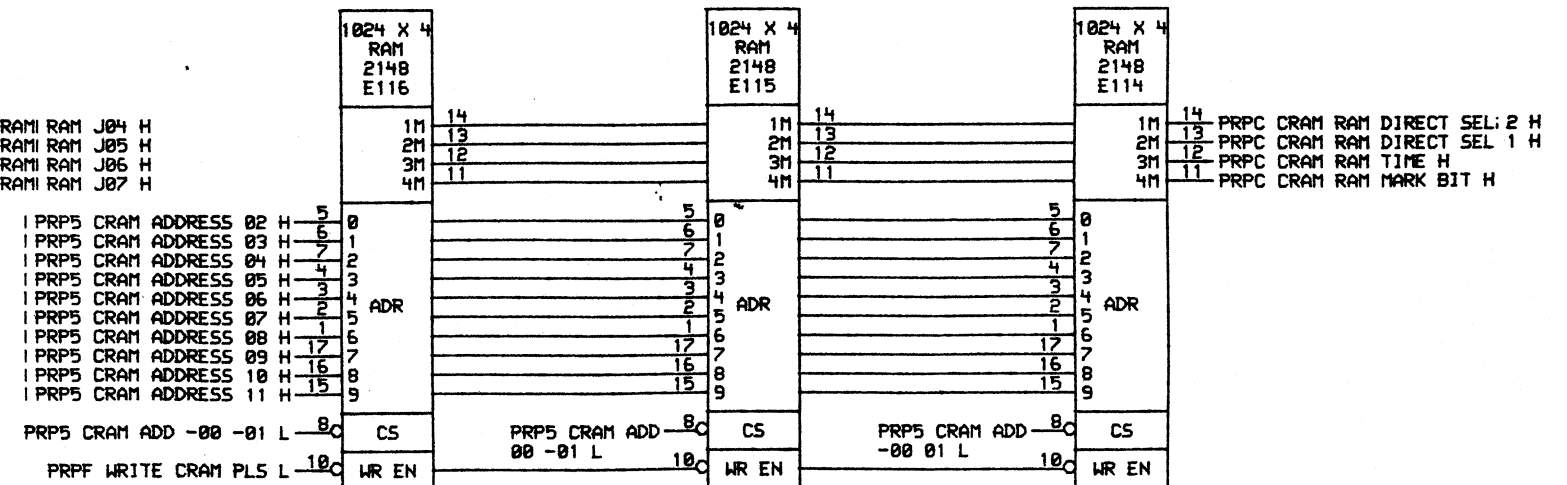
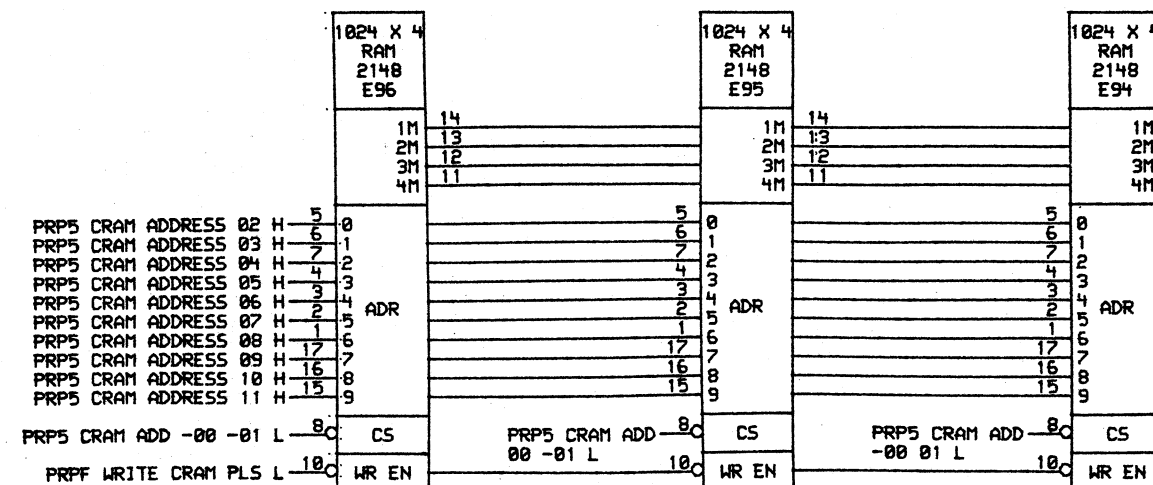
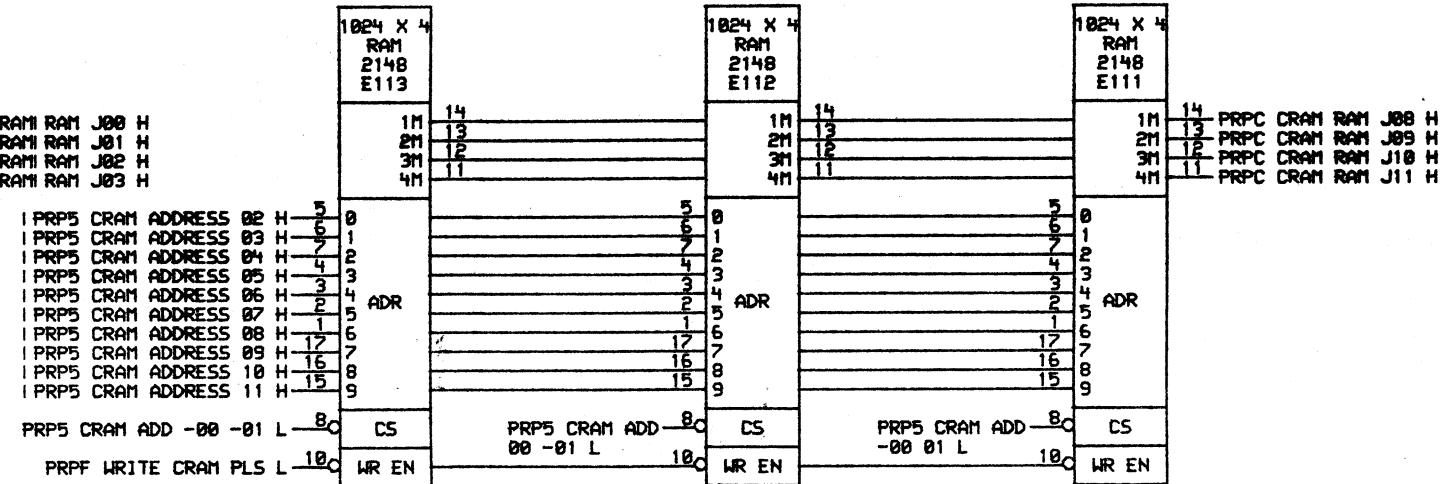
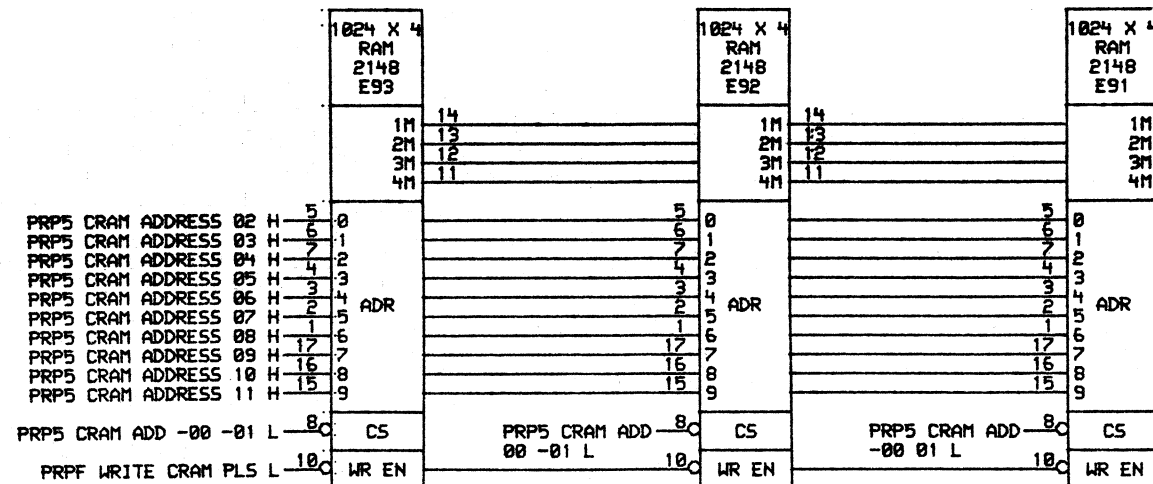
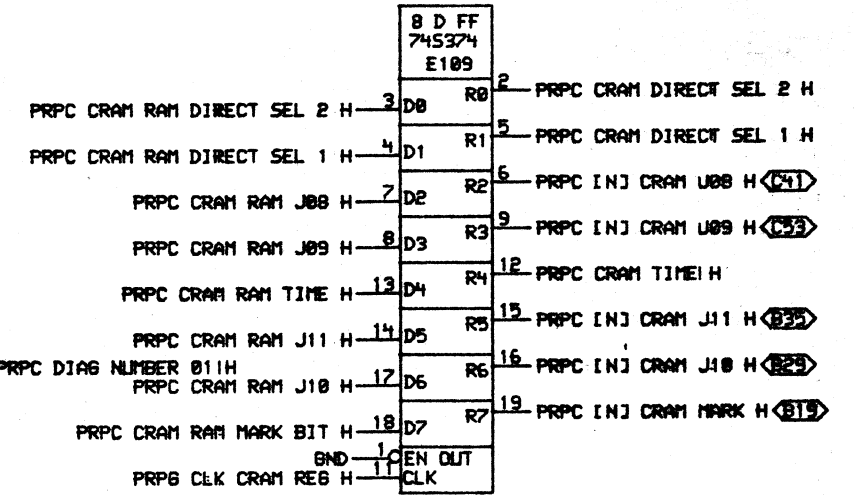
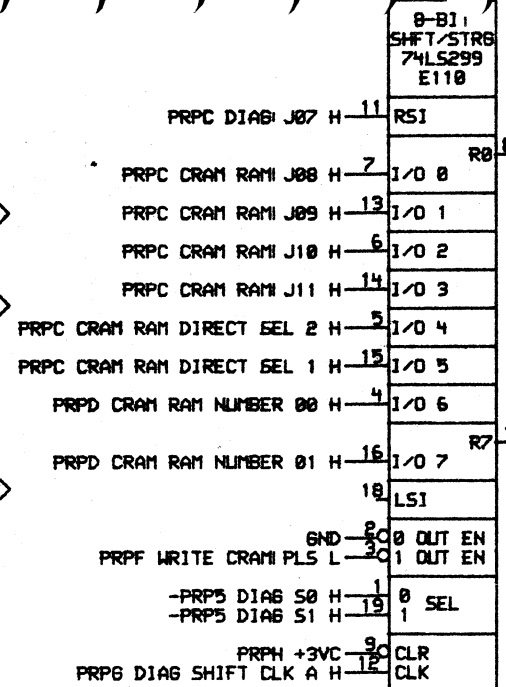
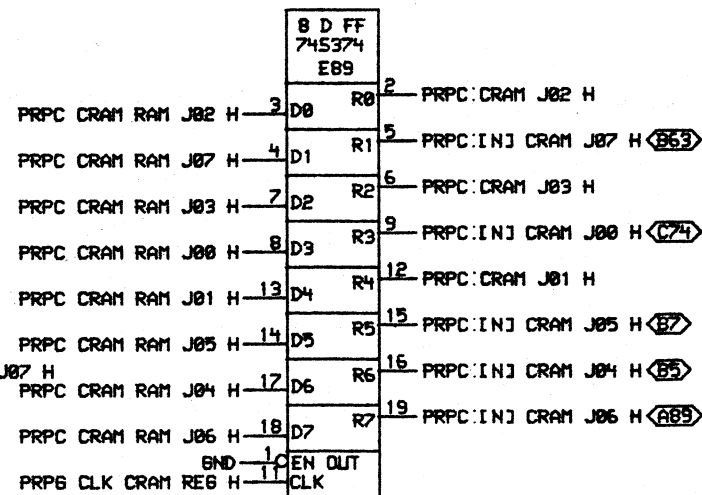
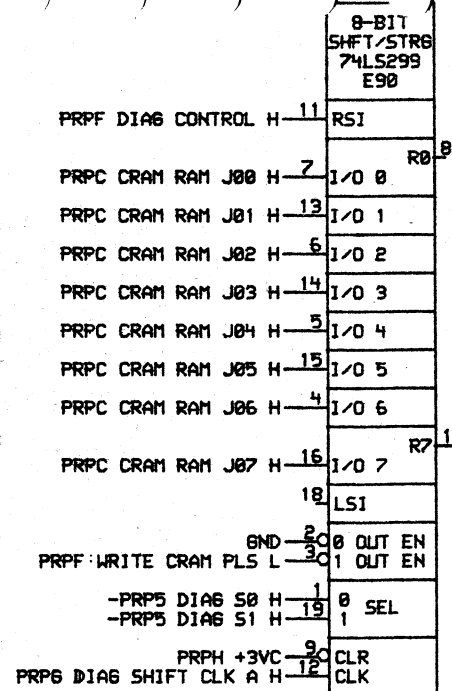




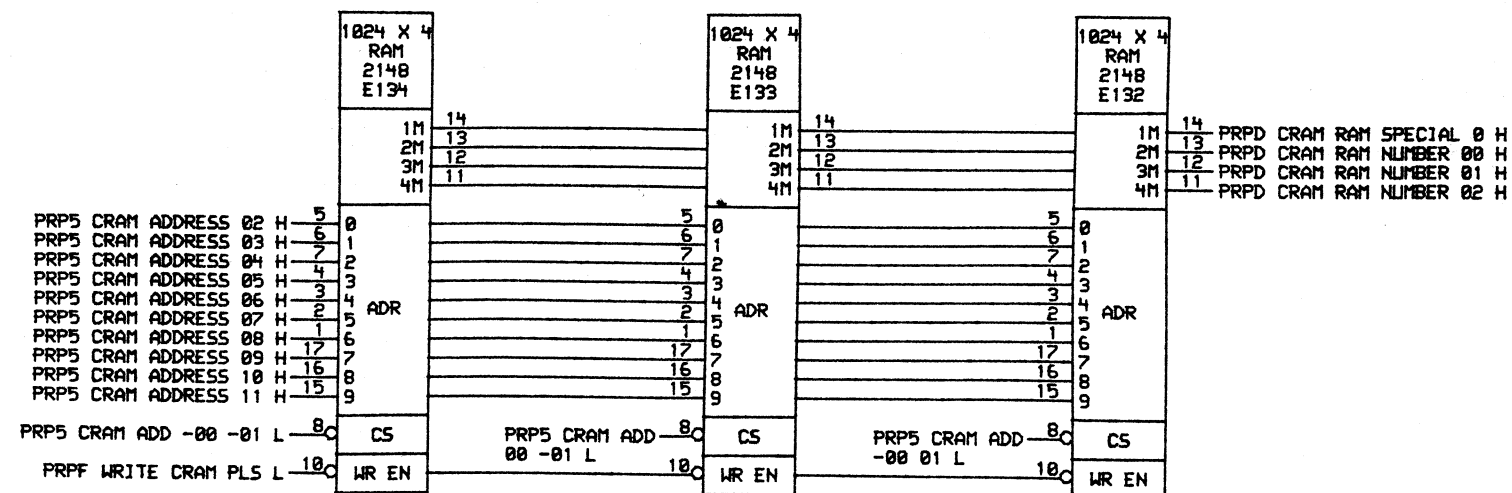
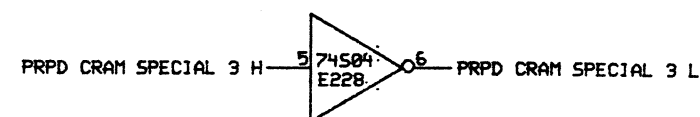
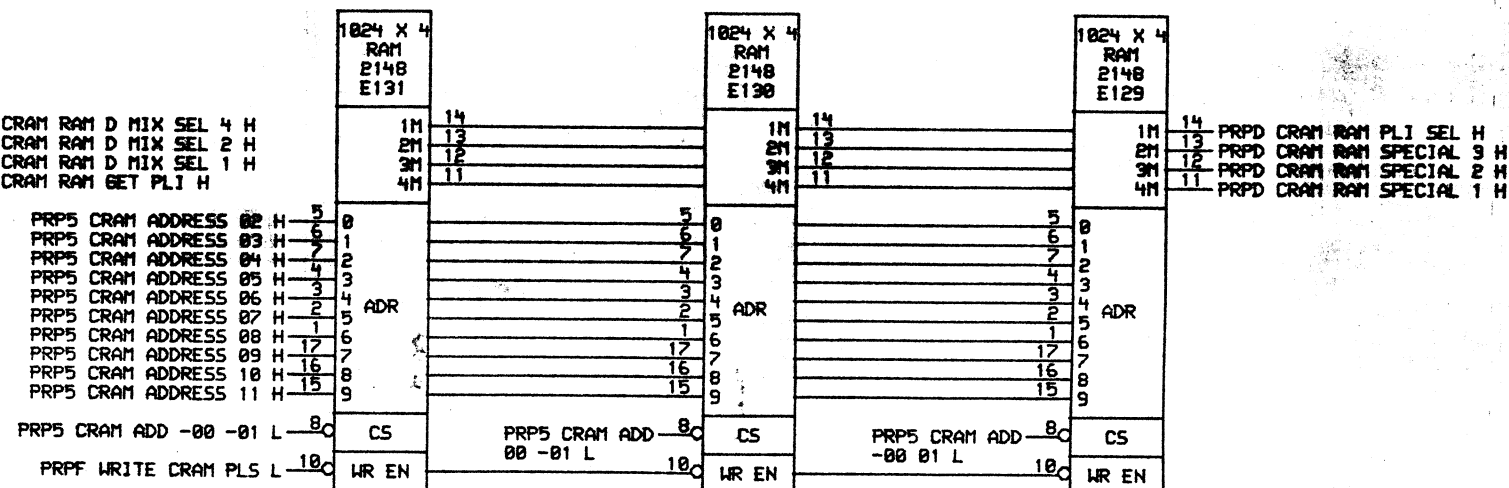
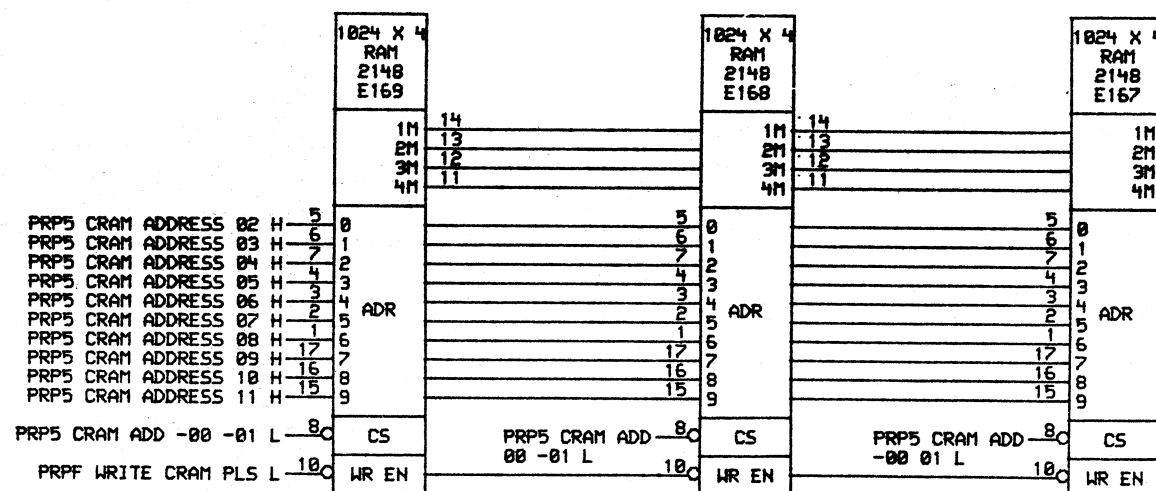
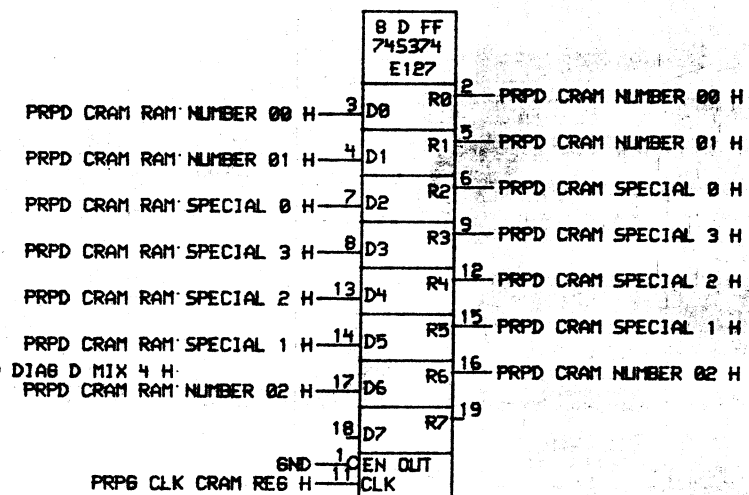
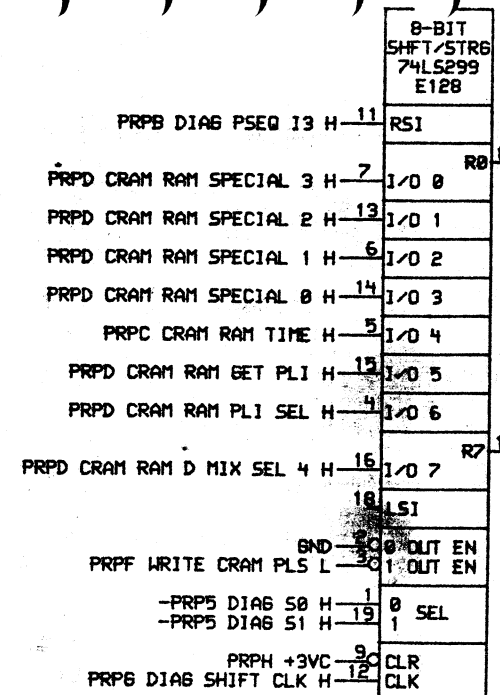
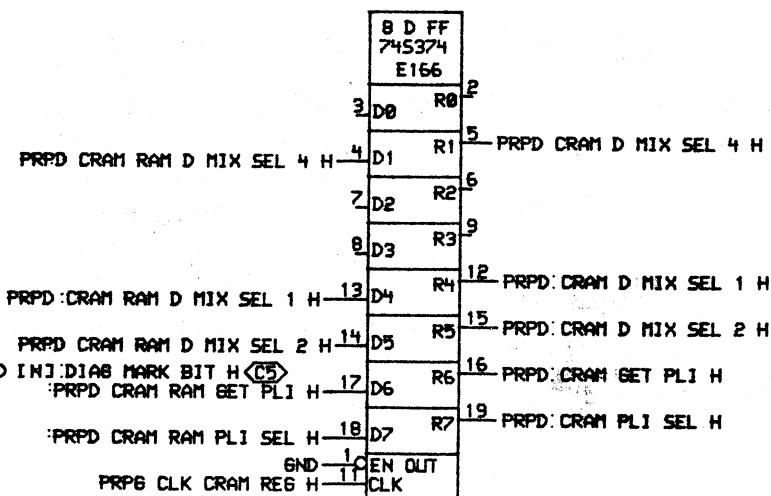
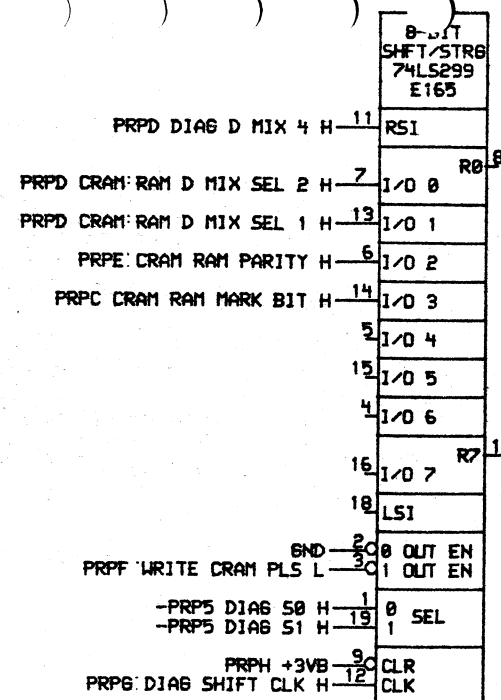


DRN.		DATE	ENG.	DATE	TITLE:			
CHK'D.		DATE	BOARD LOCATION:		2080 PORT CRAM J			
		DATE	SHEET OF 1					
YIL CAN: <2080 L0064>11PRPA.DRW		127-OCT-81 12:58	NEXT HIGHER ASSEMBLY:		SIZE	CODE	NUMBER	REV.
FIRST USED ON OPTION/MODEL:		B-DD-L0064-0			D	C5	L0064-0-PRPA	

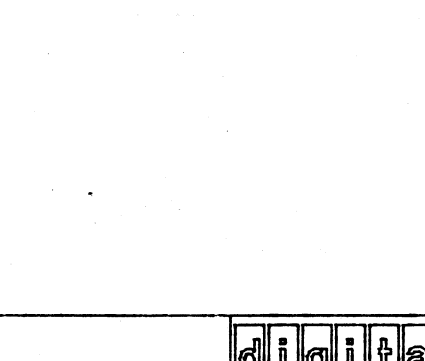
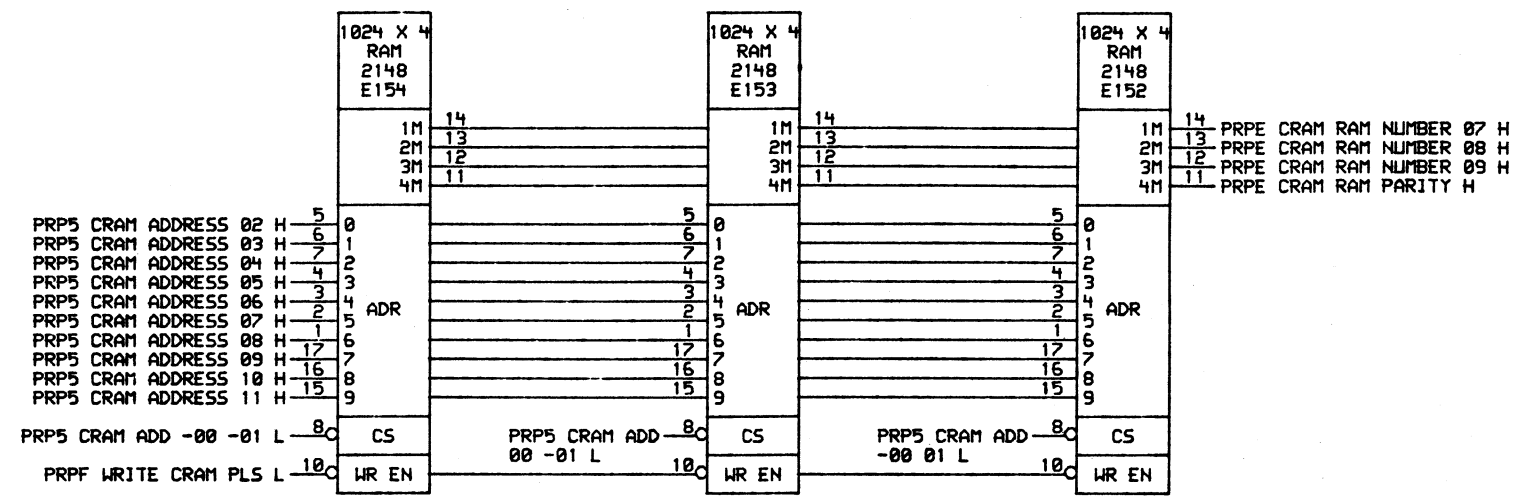
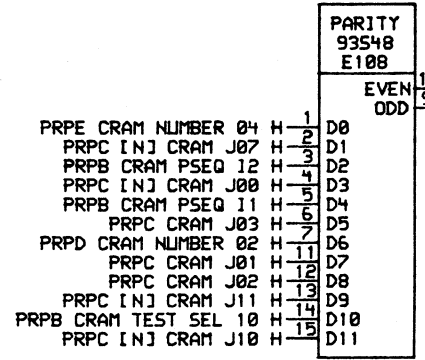
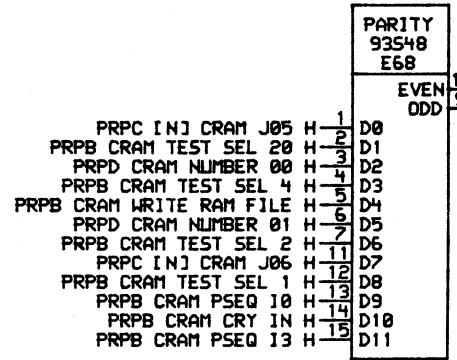
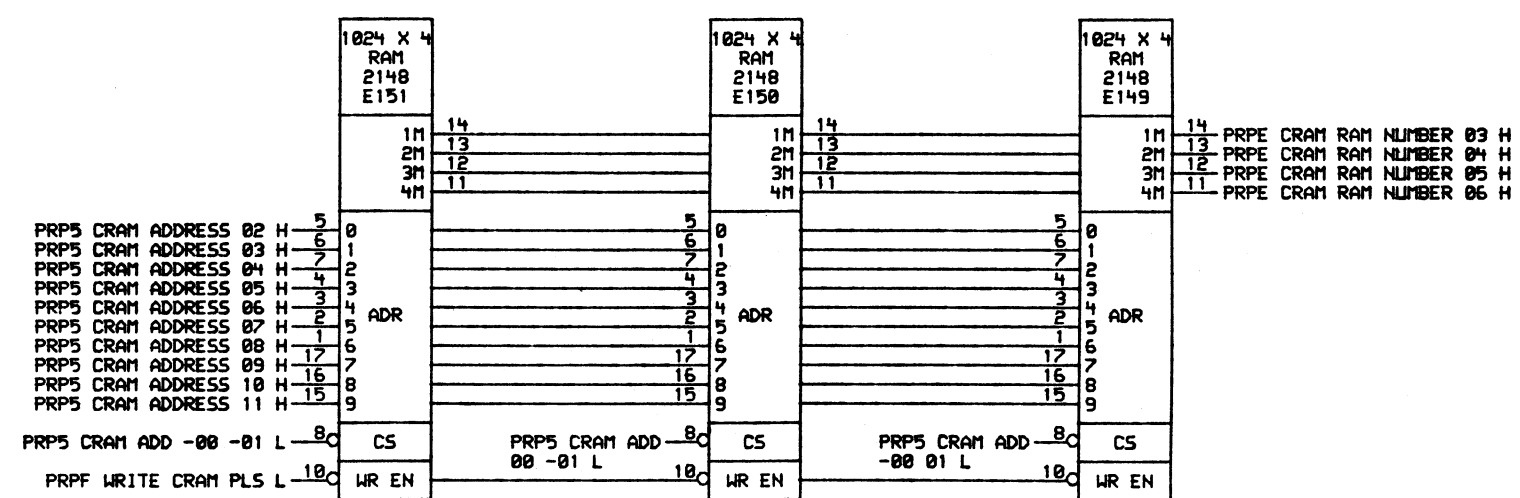
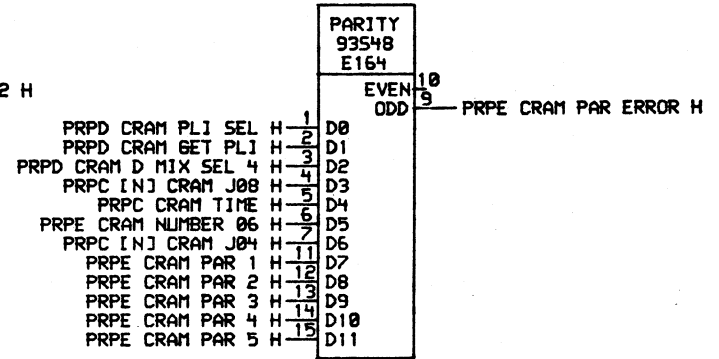
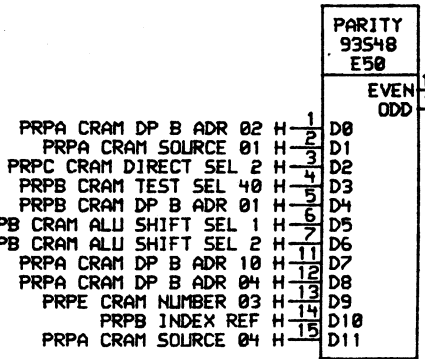
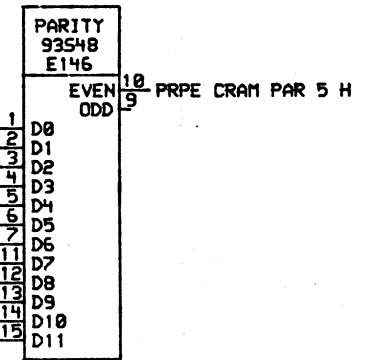
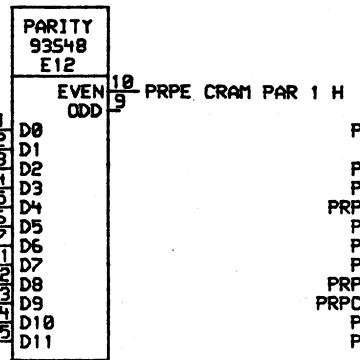
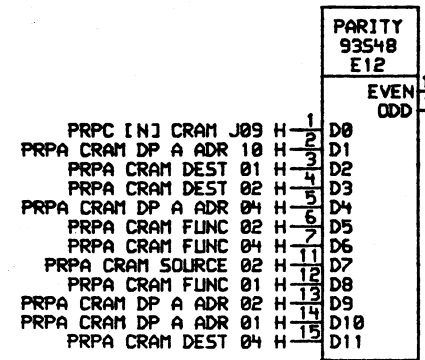
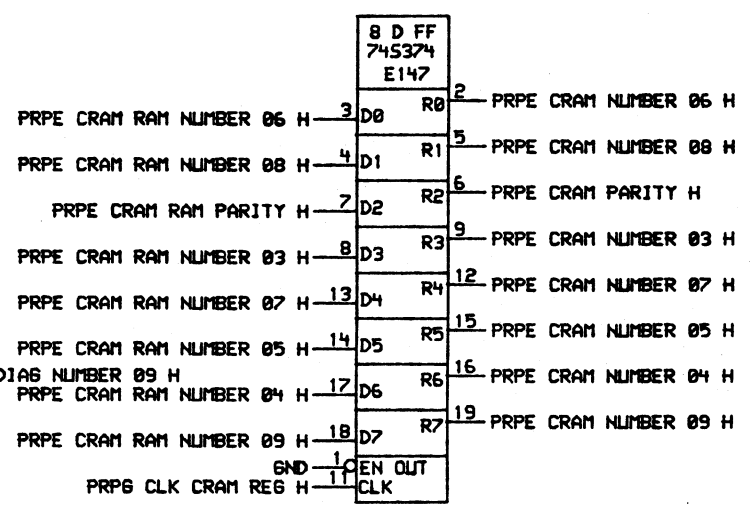
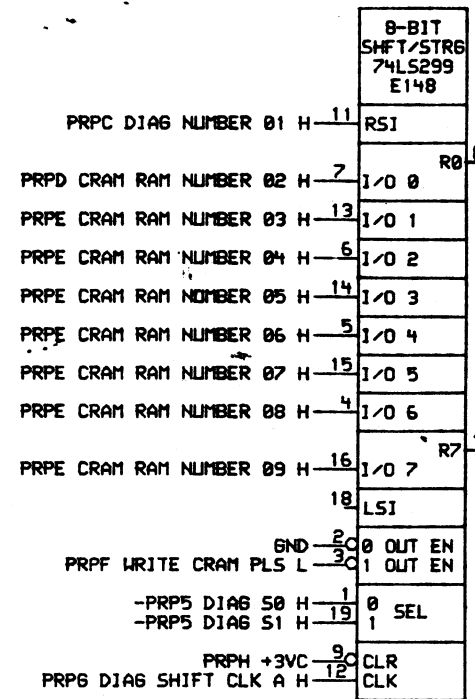


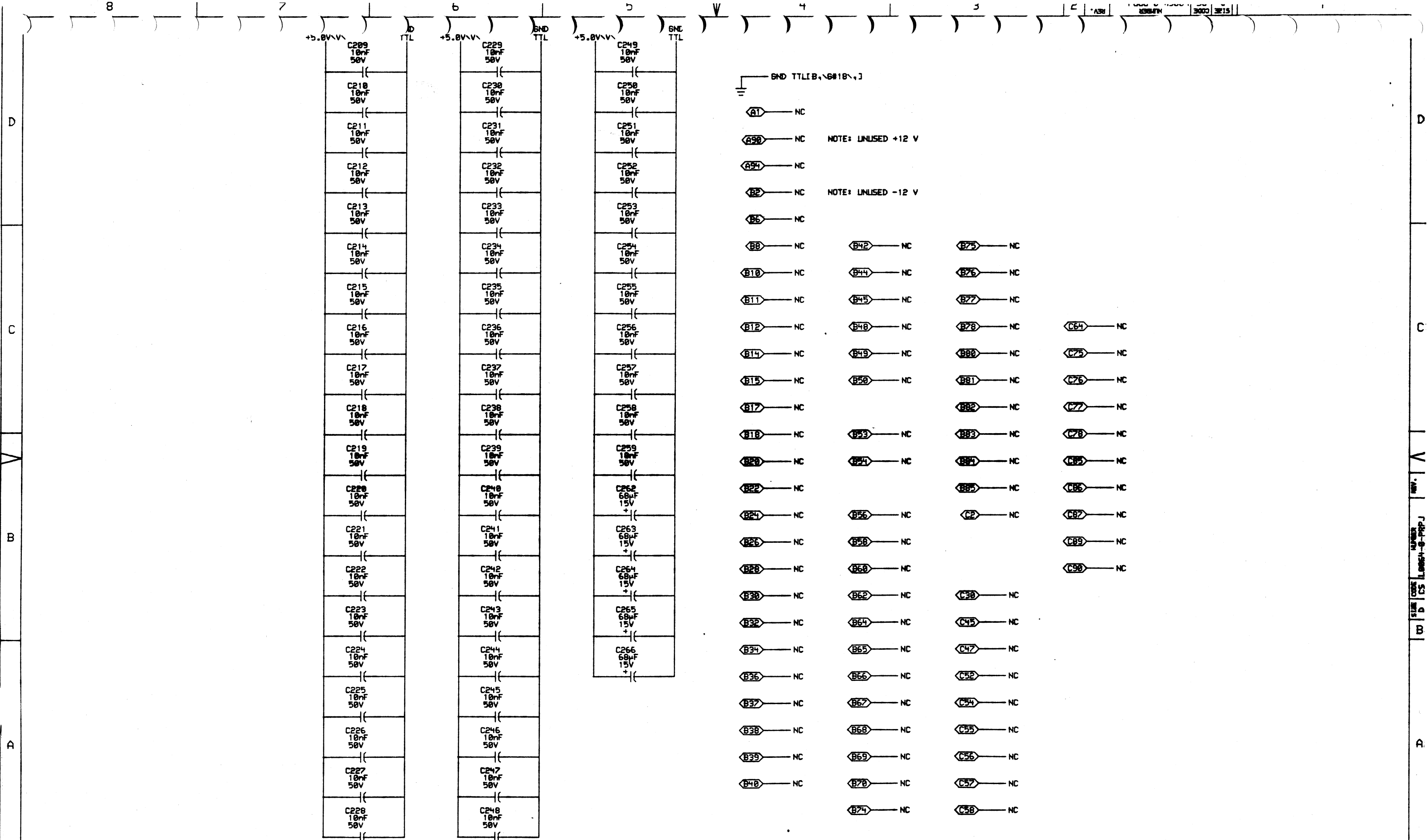


DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	1 OF 1	2080 PORT GRAM L
VILLANI (E800-L0064) 27PRPC-DW 104-DEC-81 12:56 NEXT HIGHER ASSEMBLY:				SIZE CODE NUMBER
FIRST USED ON OPTION/MODEL:				D CS L0064-0-PRPC
				REV.



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	12-JAN-82			2080 PORT
	DATE	BOARD LOCATION:		CRAM M
	12-JAN-82	SHEET	OF	
VULCAN: (2080-1-0064) 11 PRPD.DRW 27-OCT-81 13:35 NEXT HIGHER ASSEMBLY:				SIZE CODE NUMBER
FIRST USED ON OPTION/MODEL:				B-DD-L0064-0
				REV.



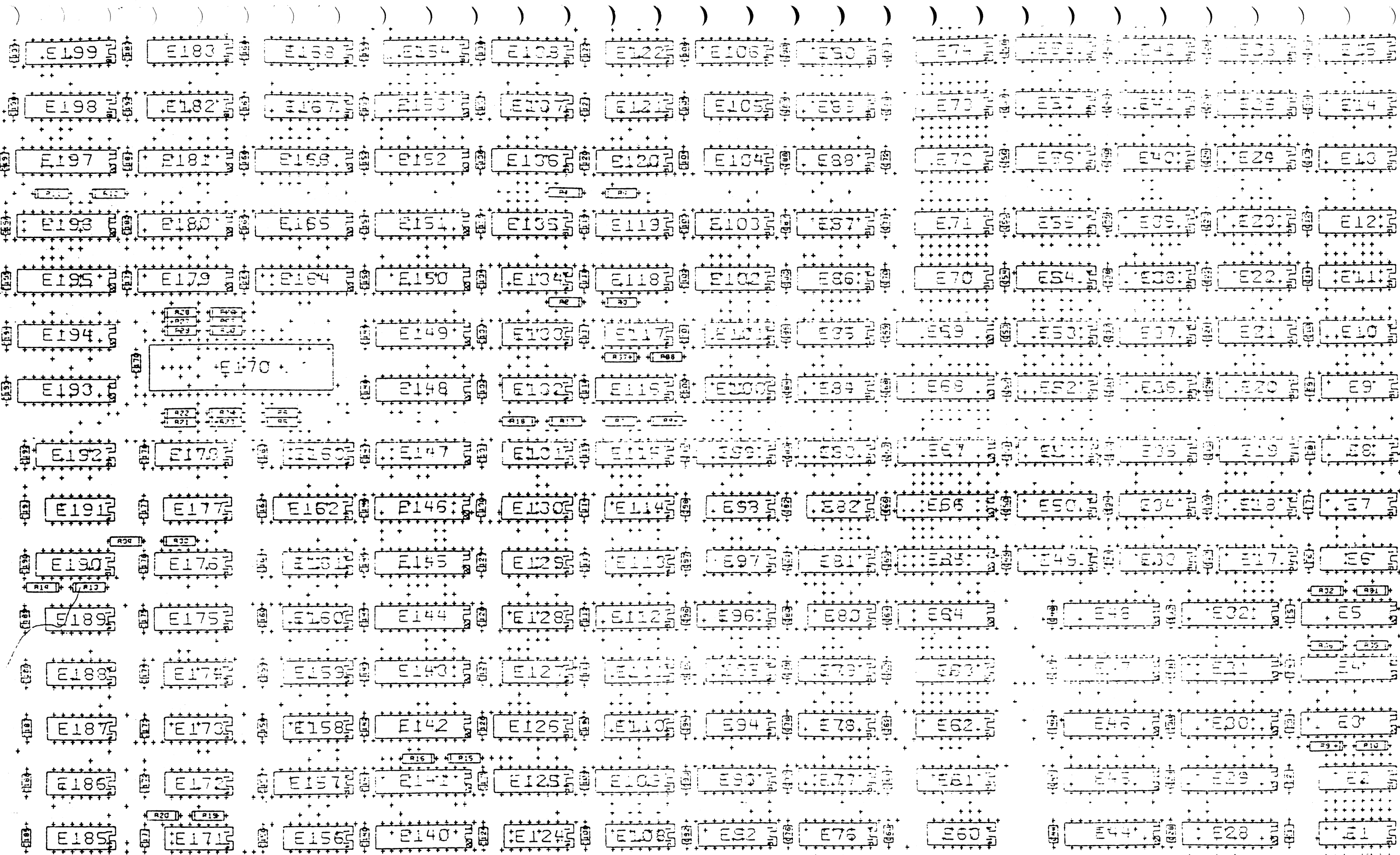


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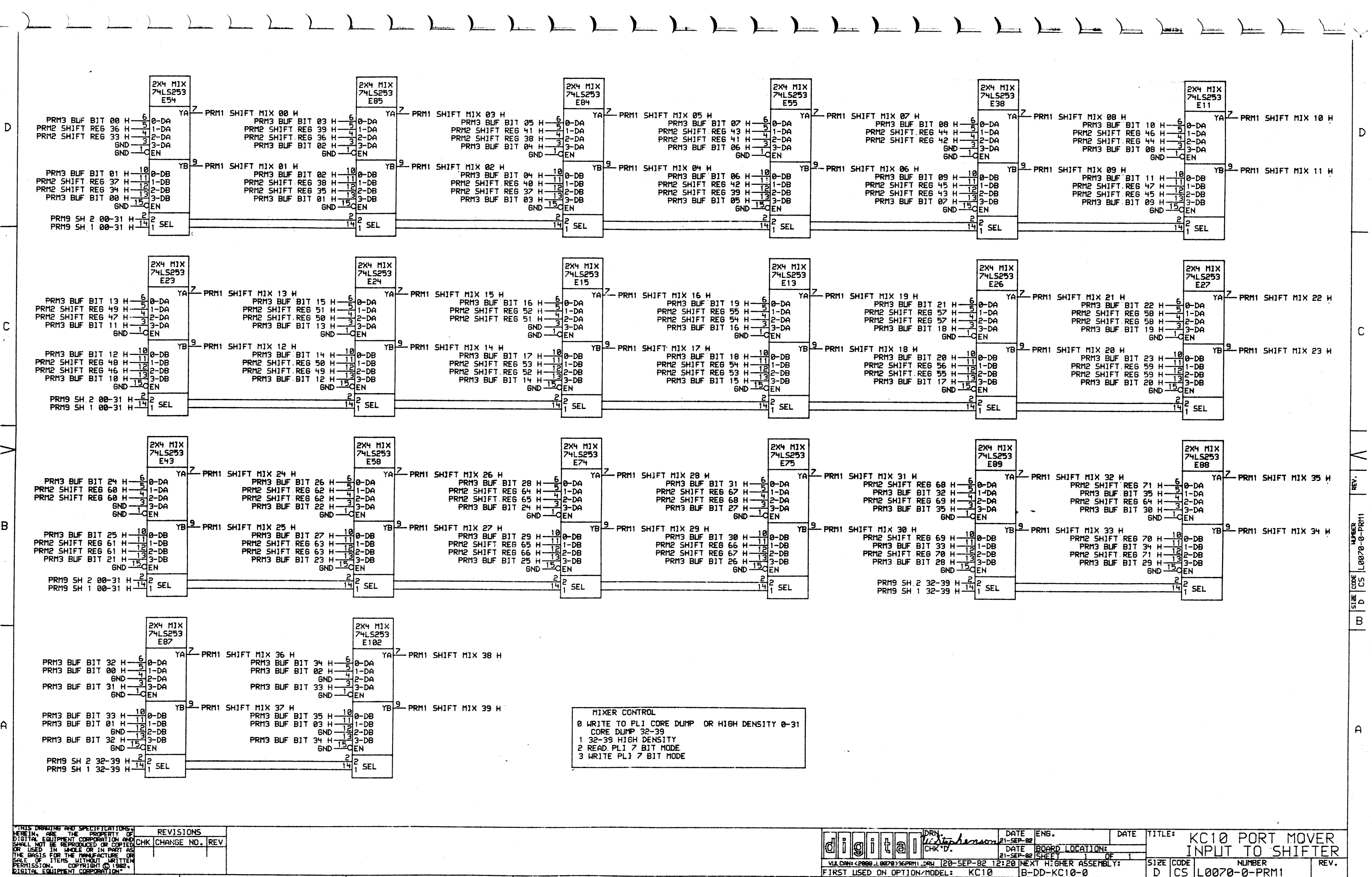
REVISIONS		
CHK	CHANGE NO.	REV

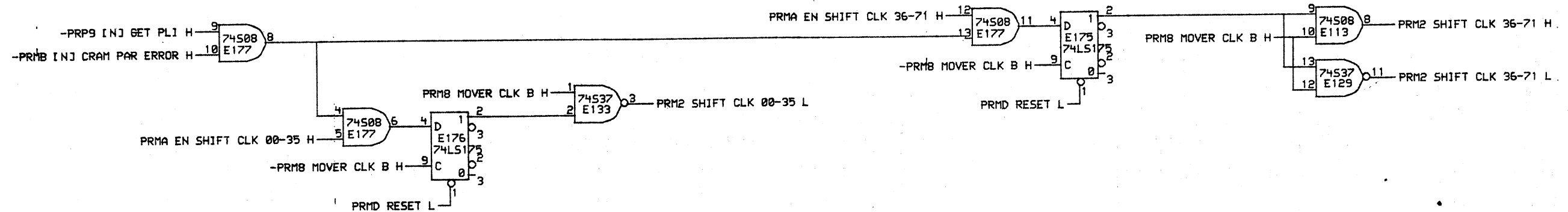
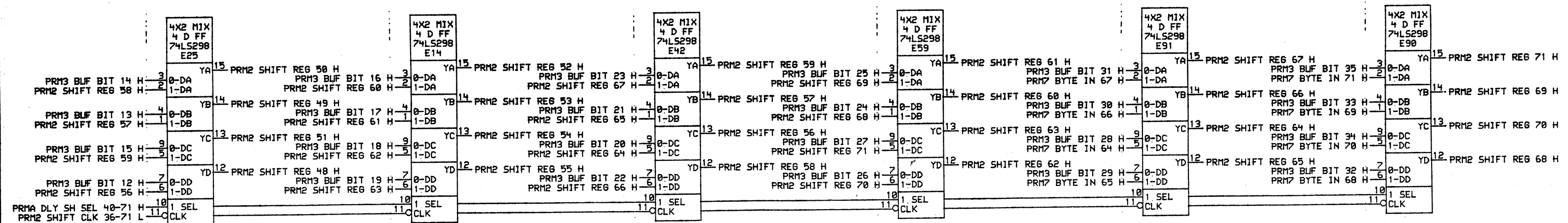
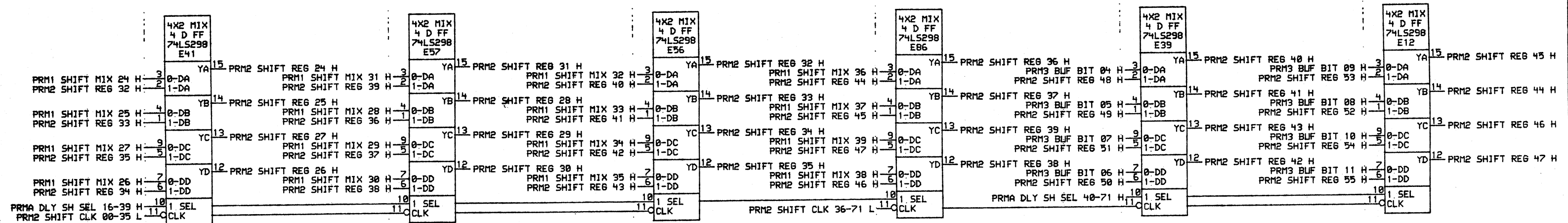
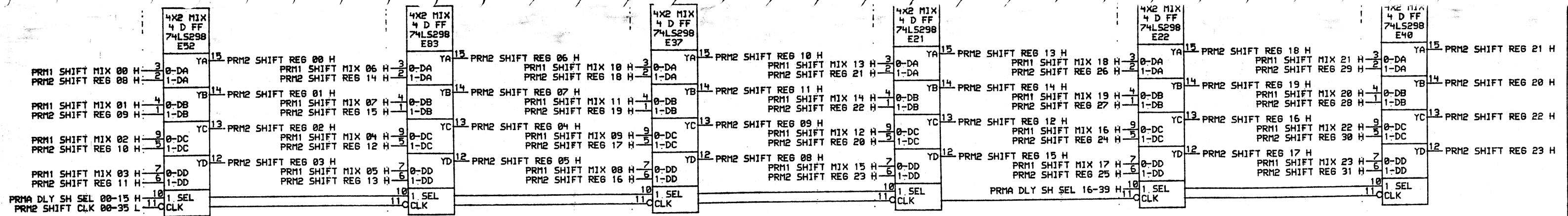
digital	DATE	ENG.	DATE	TITLE:
	12-MAY-82	12:35	12-MAY-82	2080 PORT PWR & GND 2
FIRST USED ON OPTION MODEL:		B-DO--0		REV.
		SIZE	CODE	NUMBER
		D	CS	L0064-0-PRPJ

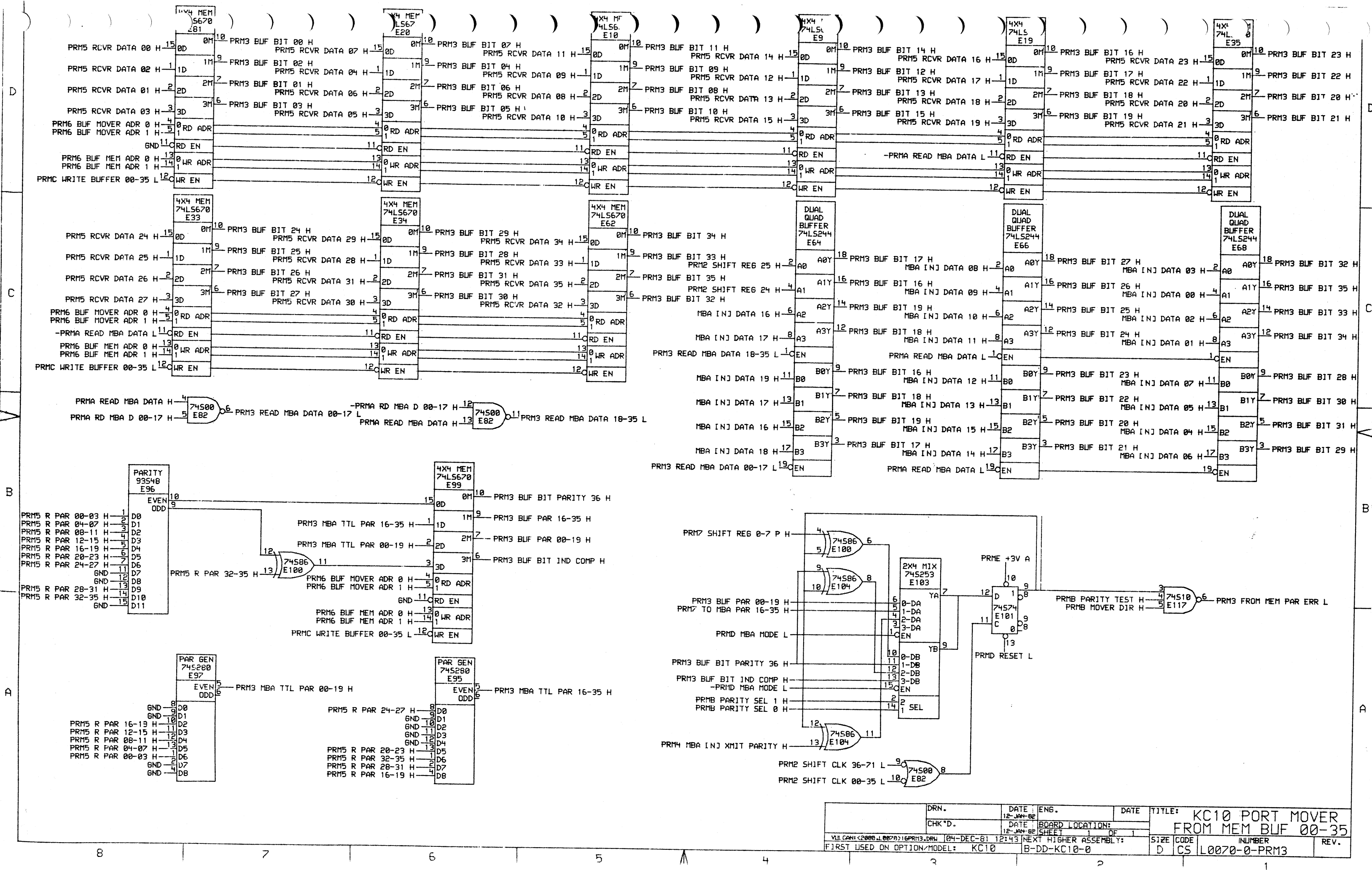
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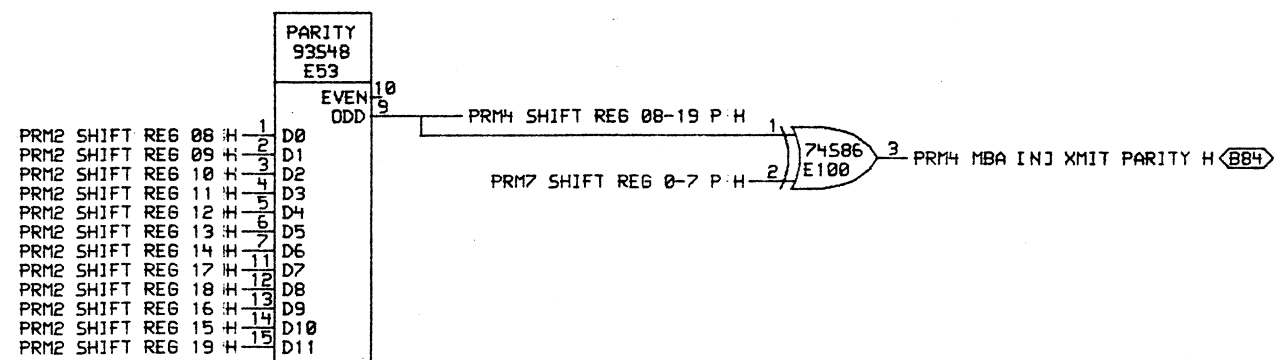
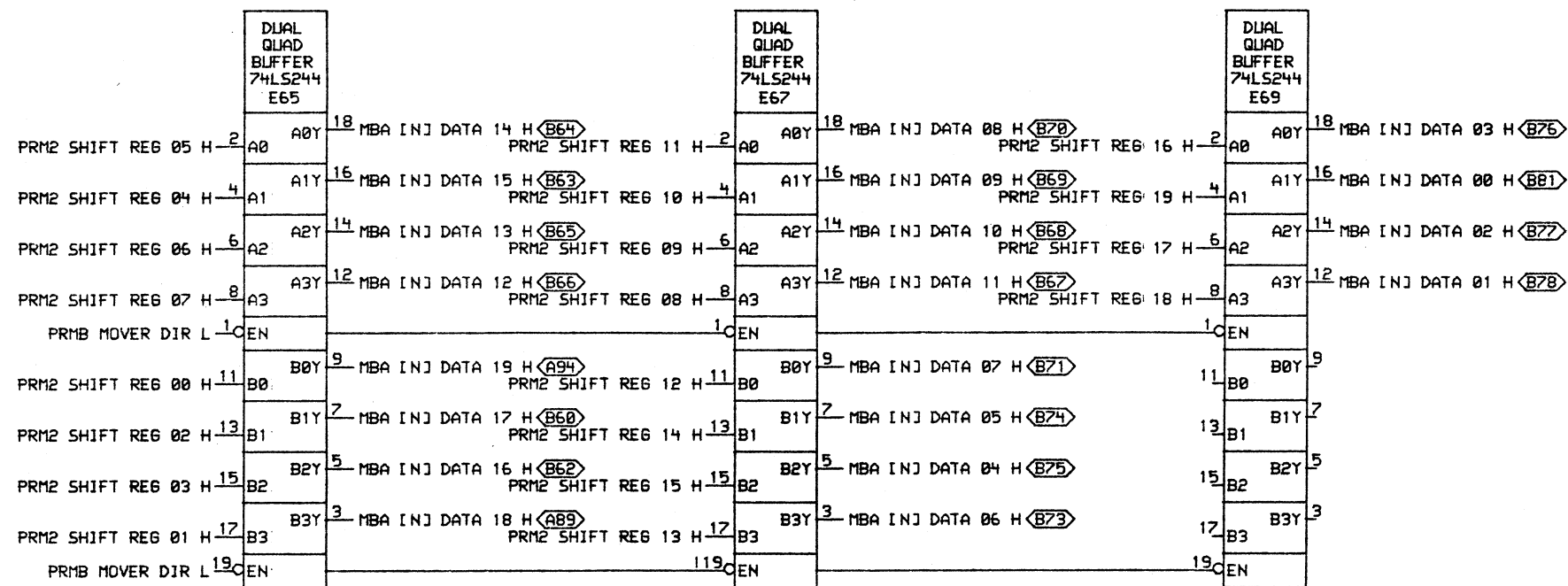


PRM

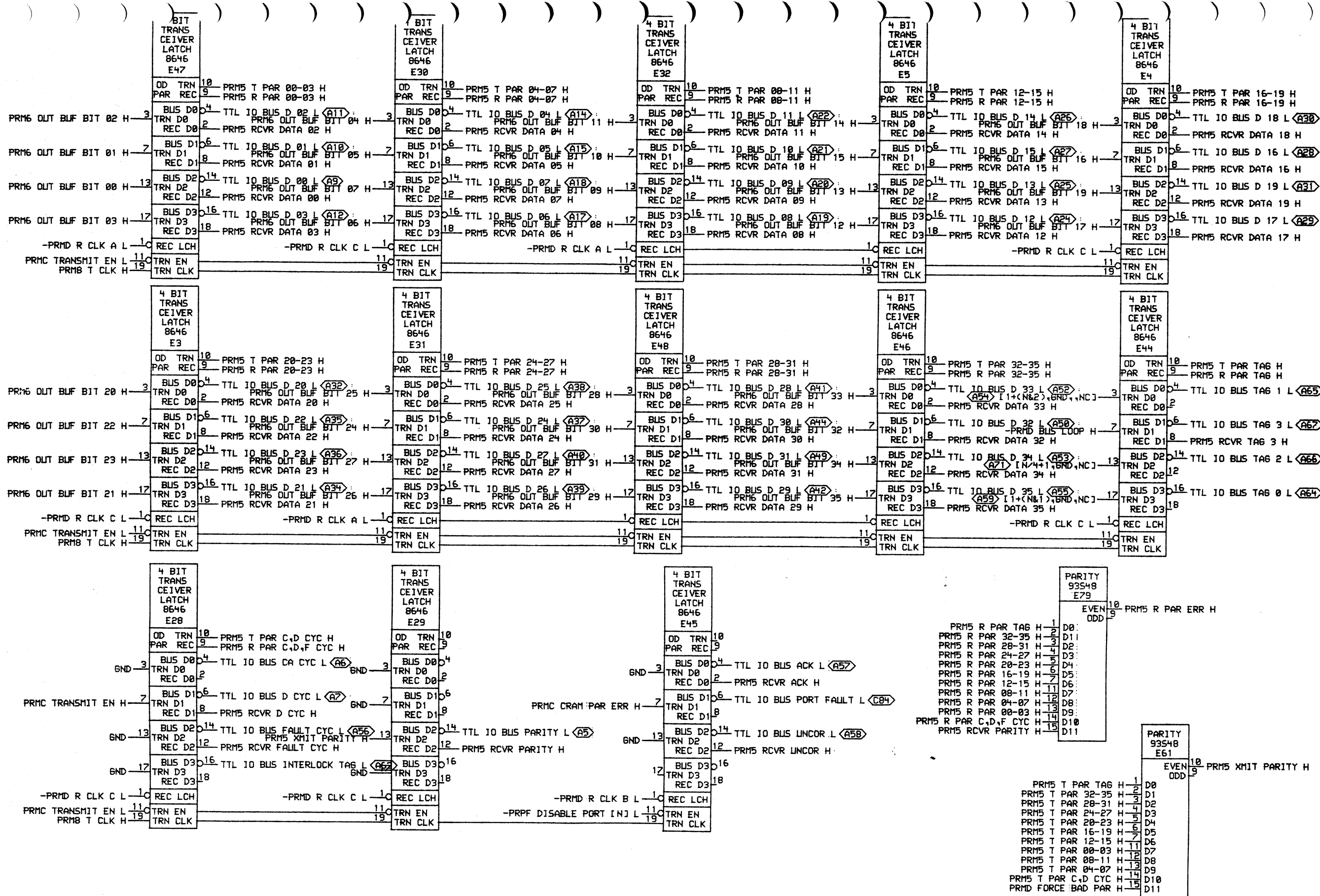


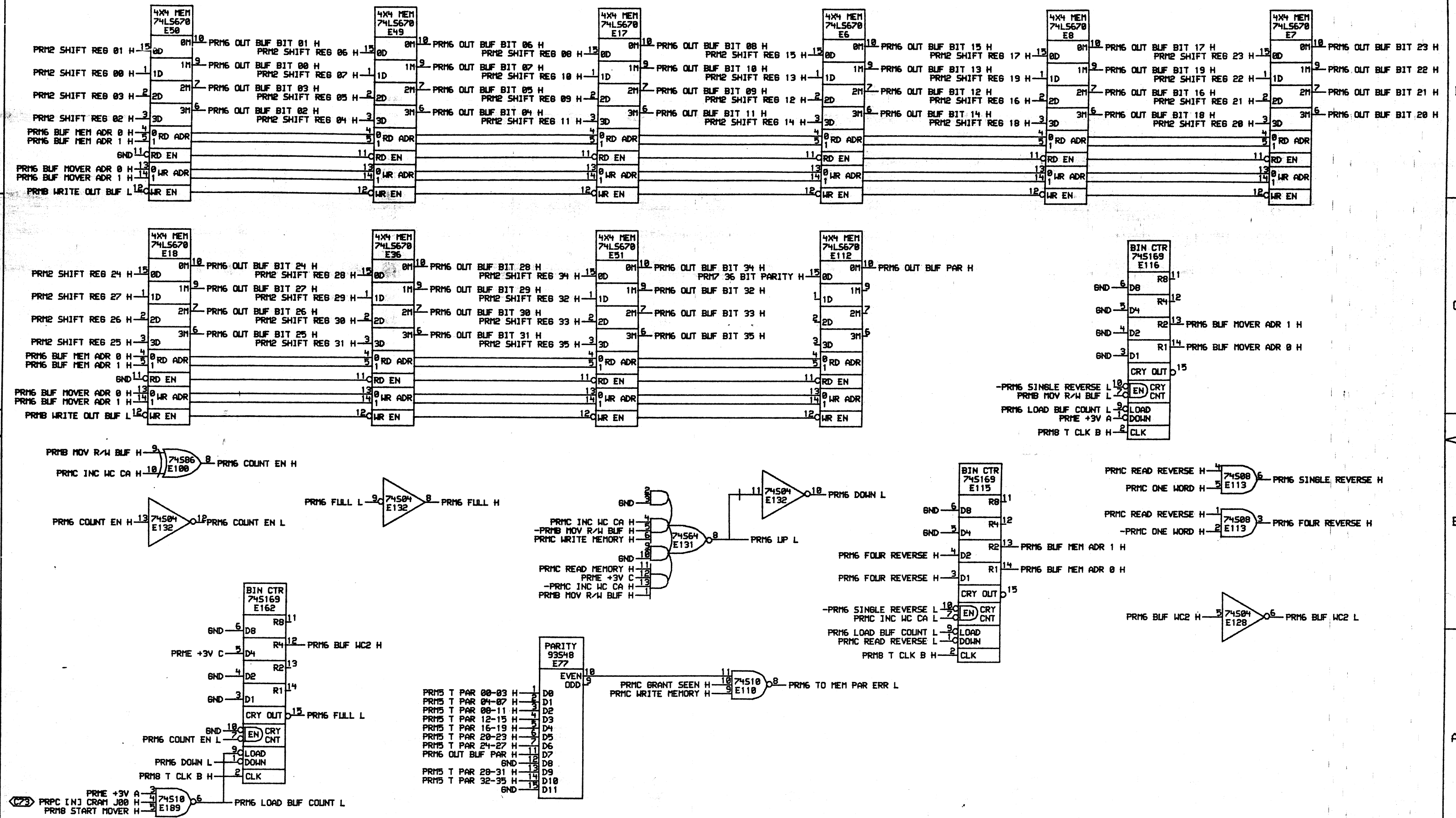


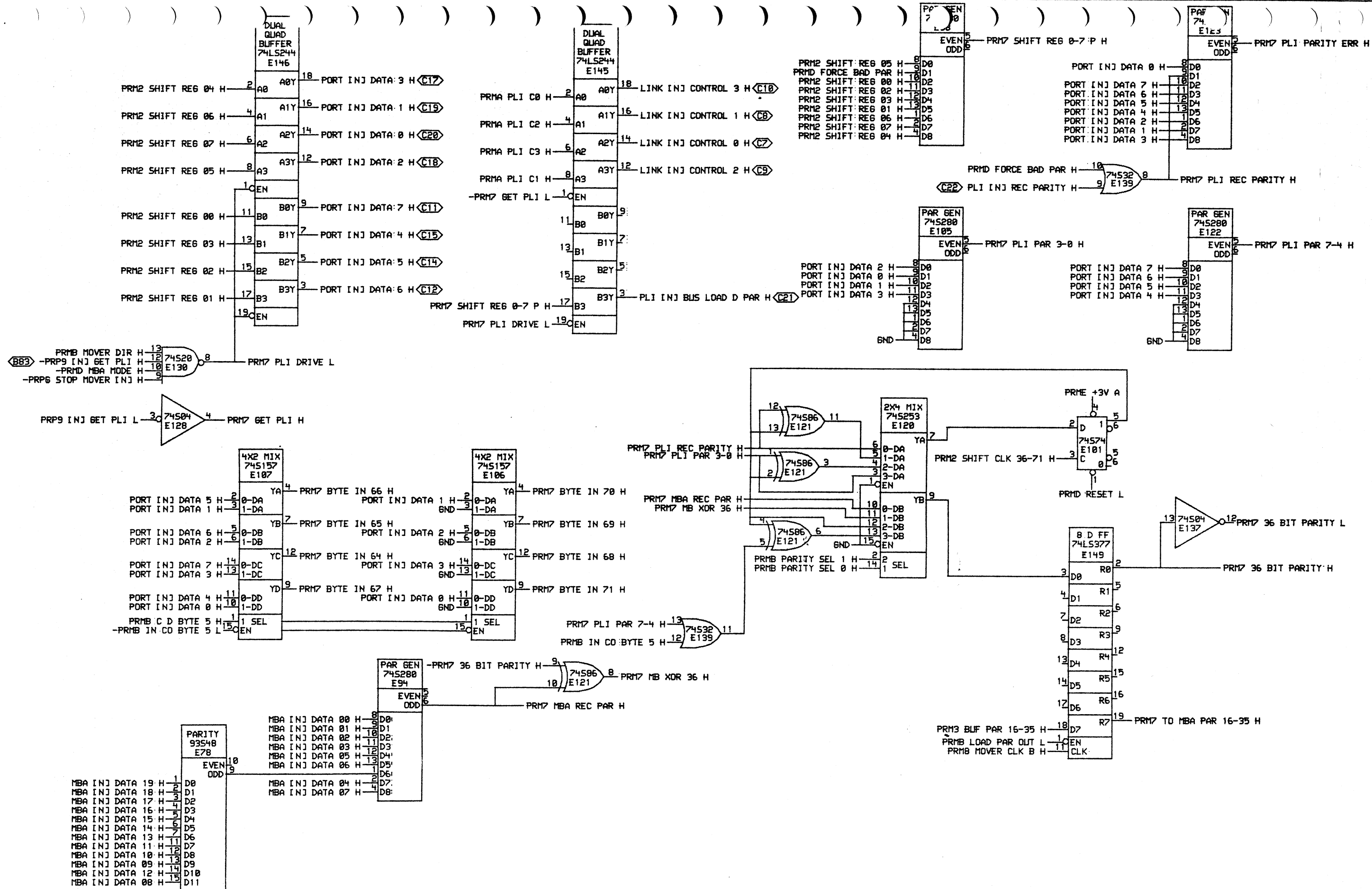


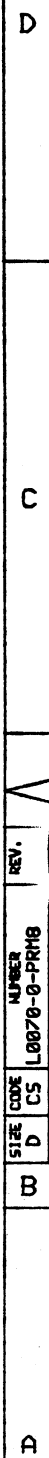


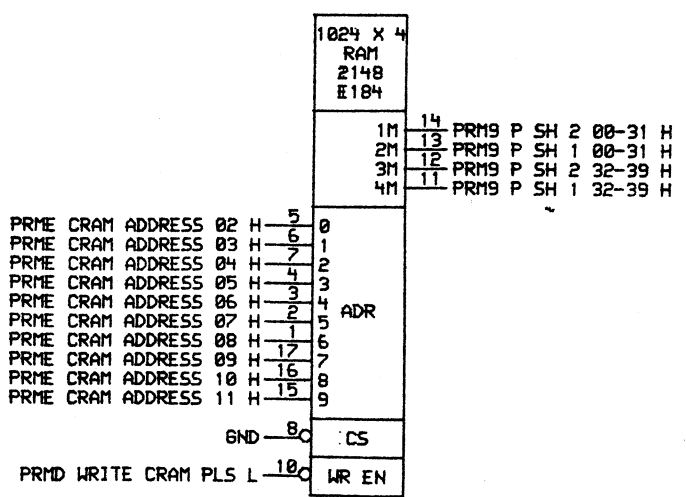
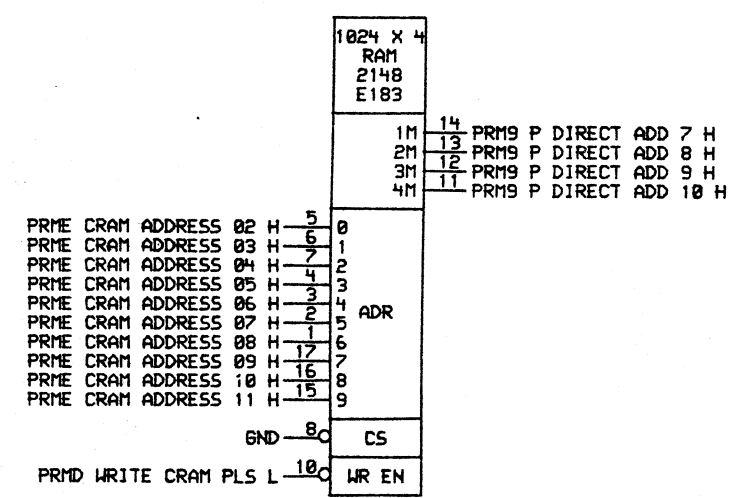
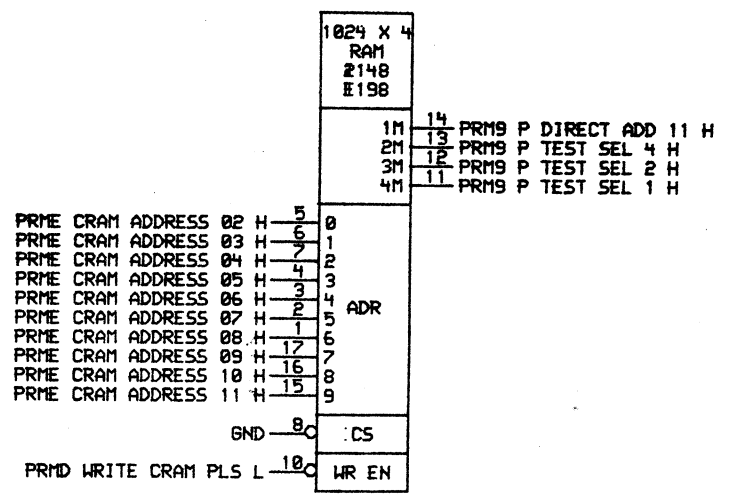
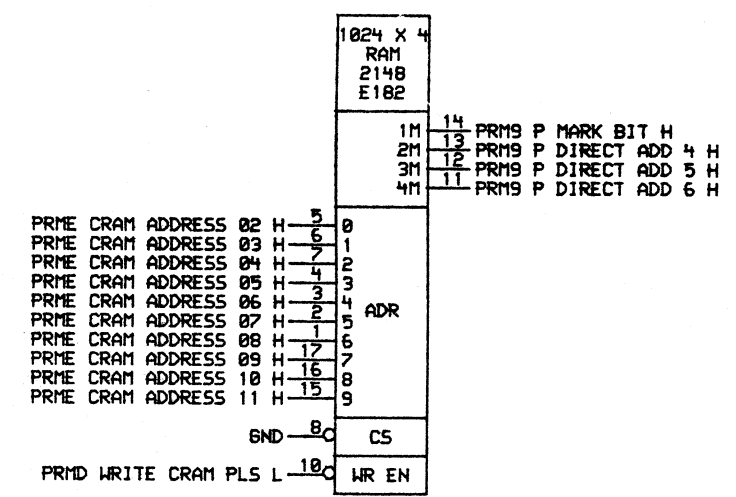
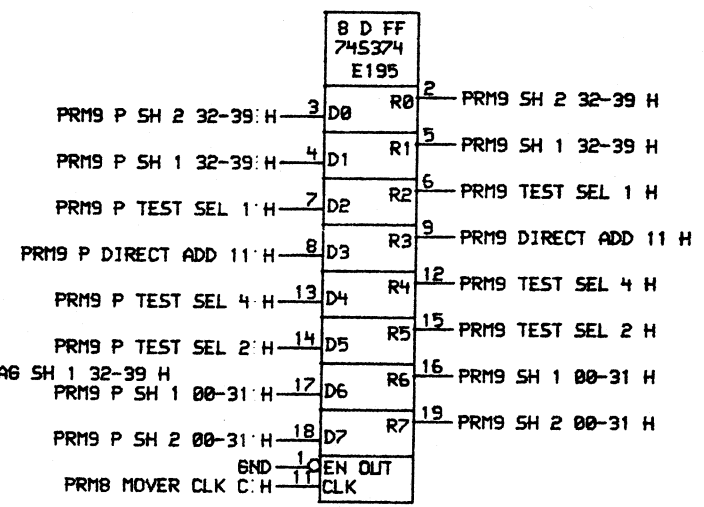
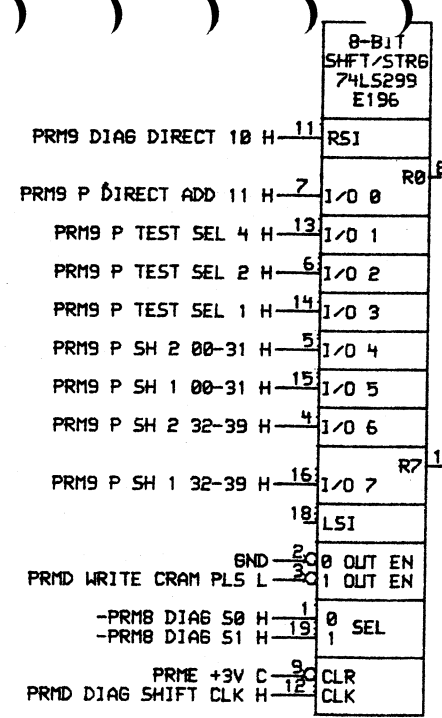
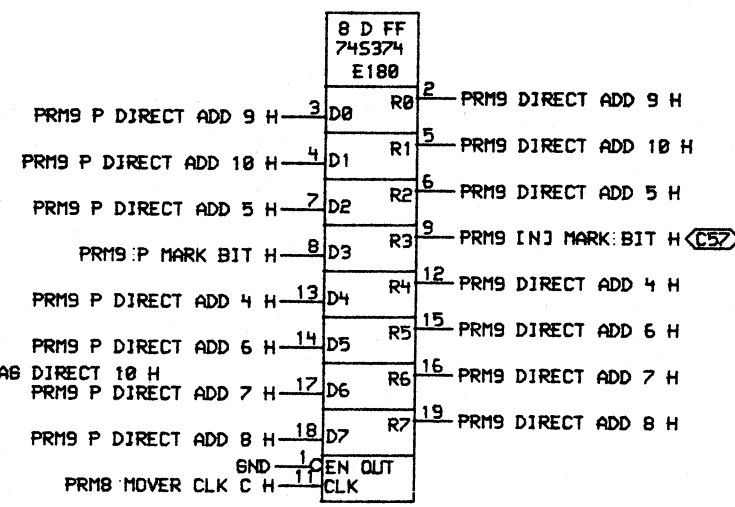
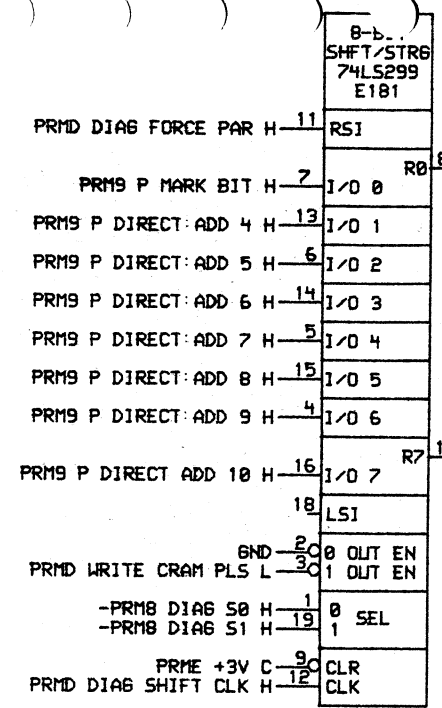
DRN.	DATE	ENG.	DATE	TITLE:	KC10 PORT MOVER
CHK'D.	DATE	BOARD LOCATION:		PORT TO MBA	
VULCAN: (2080-10070) 16PRM4.DRW	04-DEC-81 12:44	NEXT HIGHER ASSEMBLY:	1	OF 1	
FIRST USED ON OPTION/MODEL:	KC10	B-DD-KC10-0	SIZE	CODE	NUMBER
			D	CS	L0070-0-PRM4
					REV.

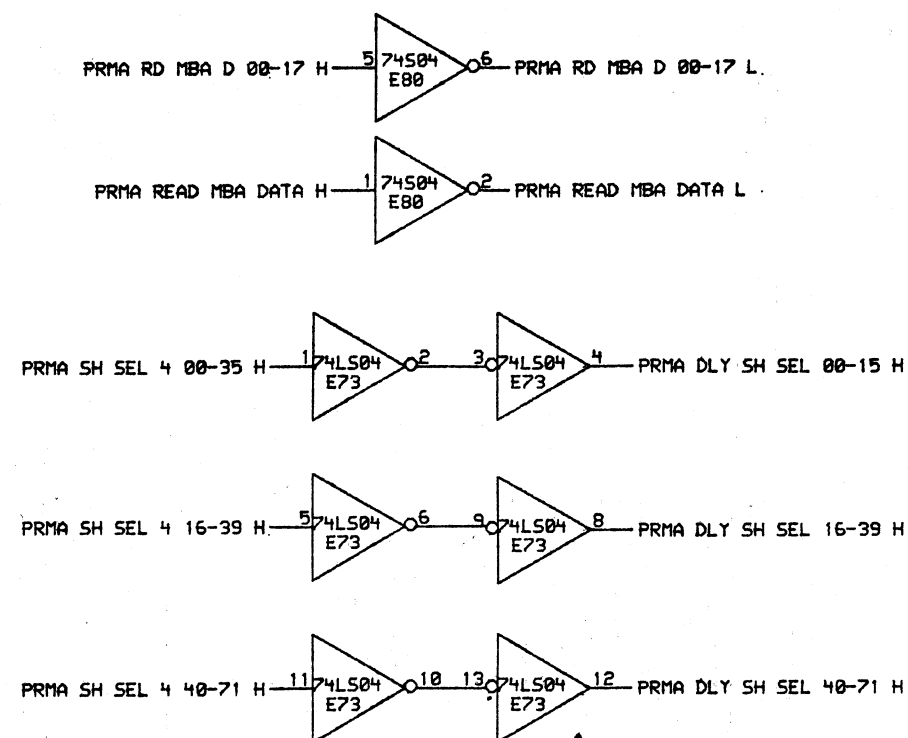
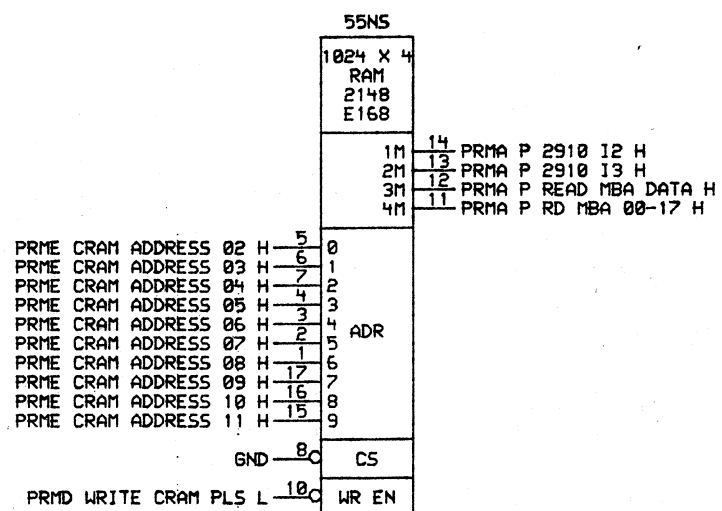
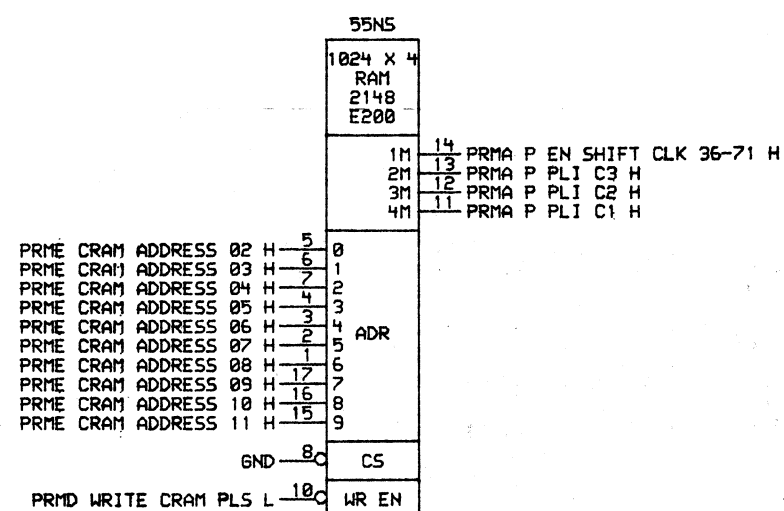
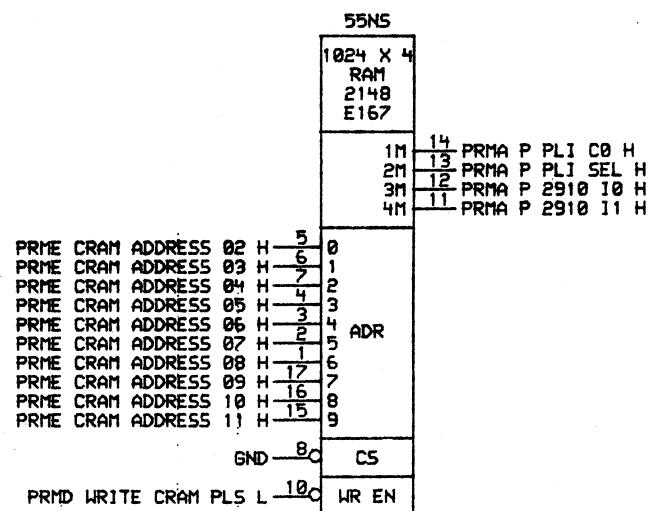
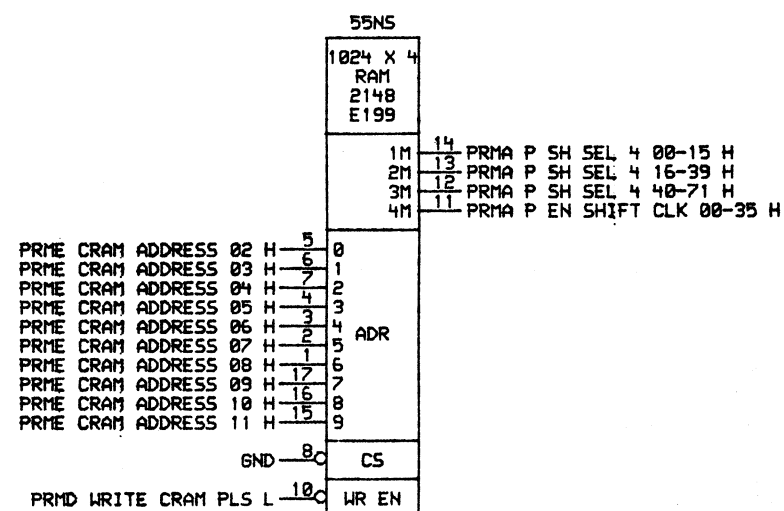
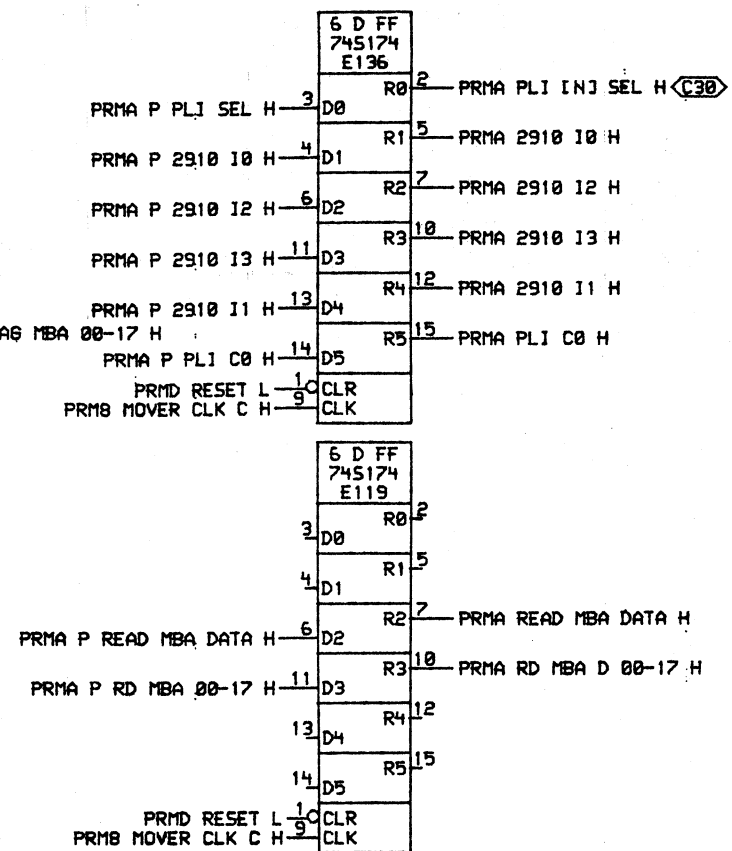
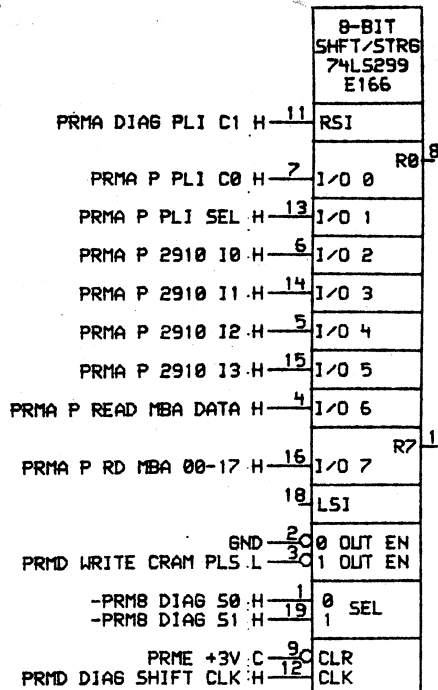
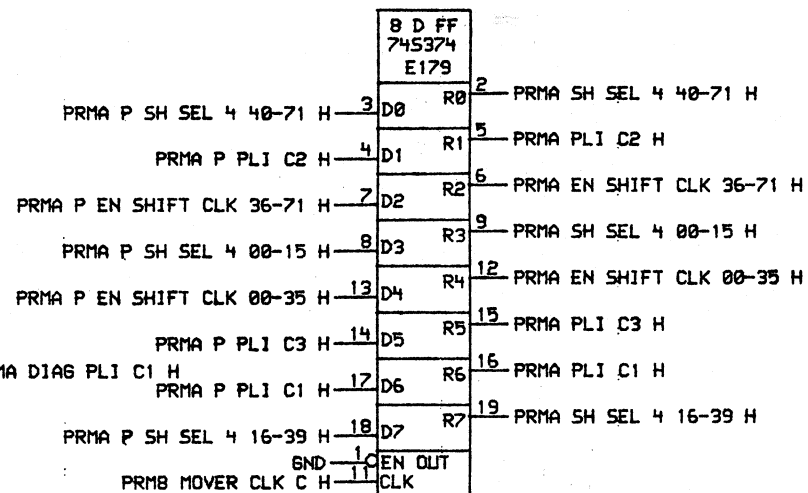
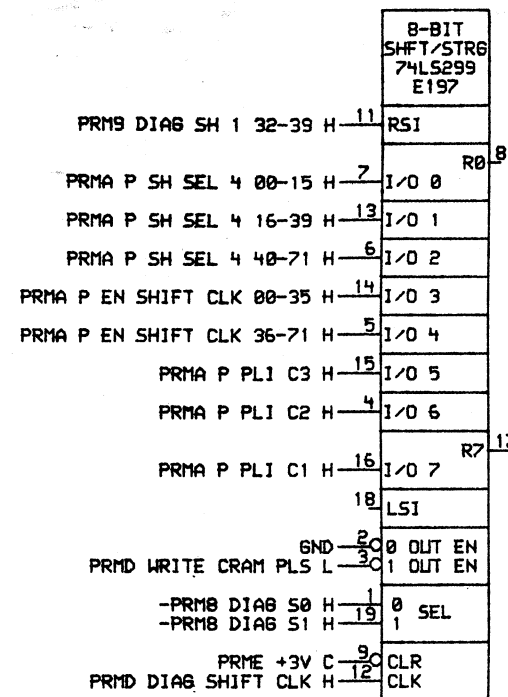












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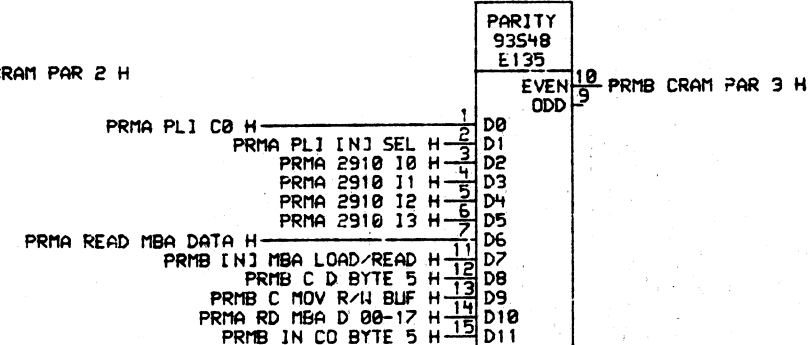
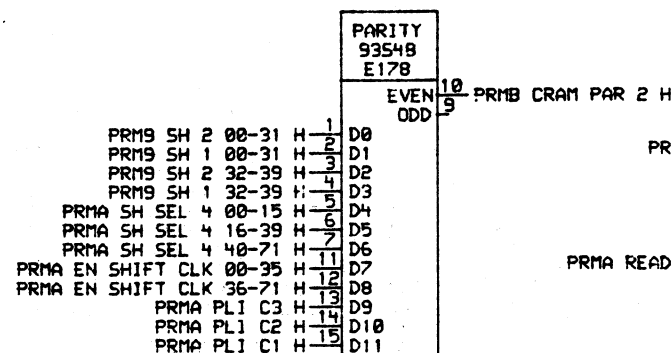
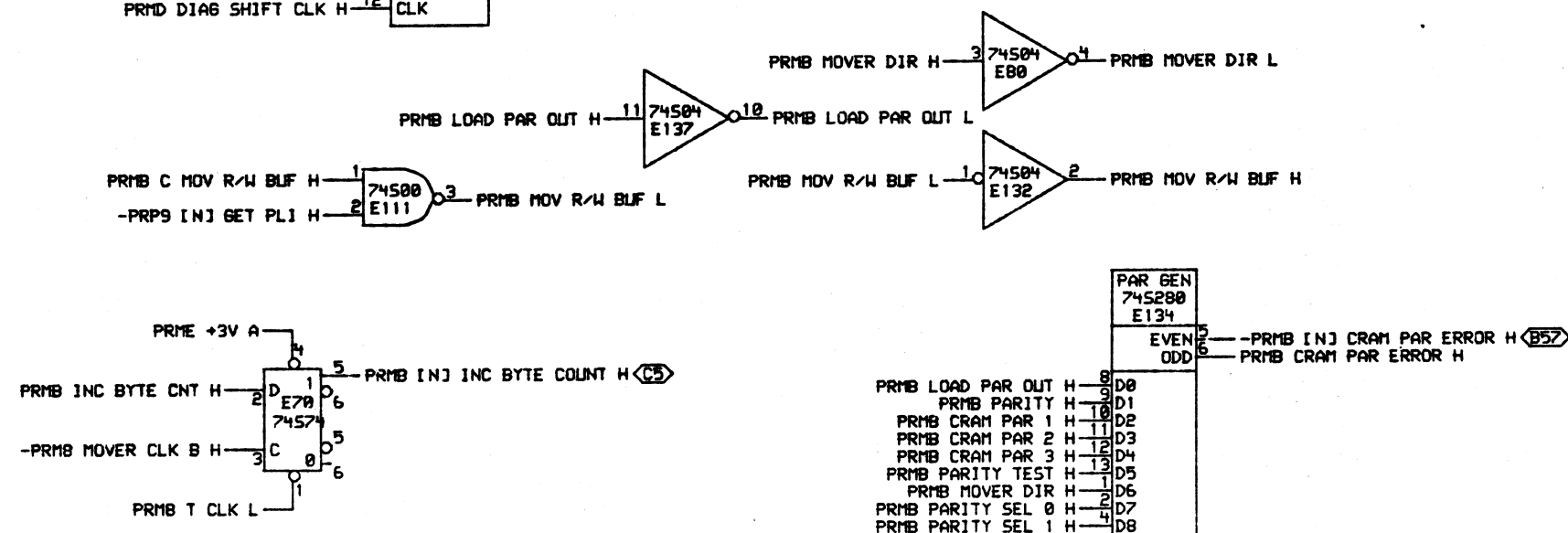
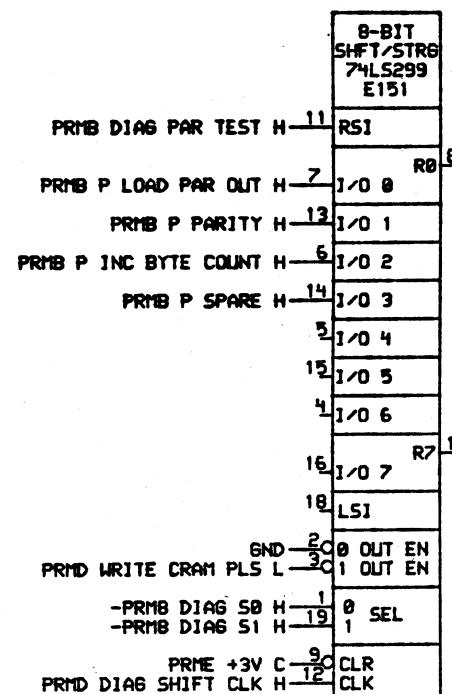
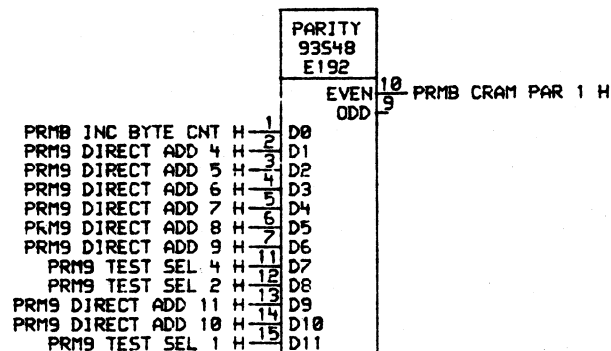
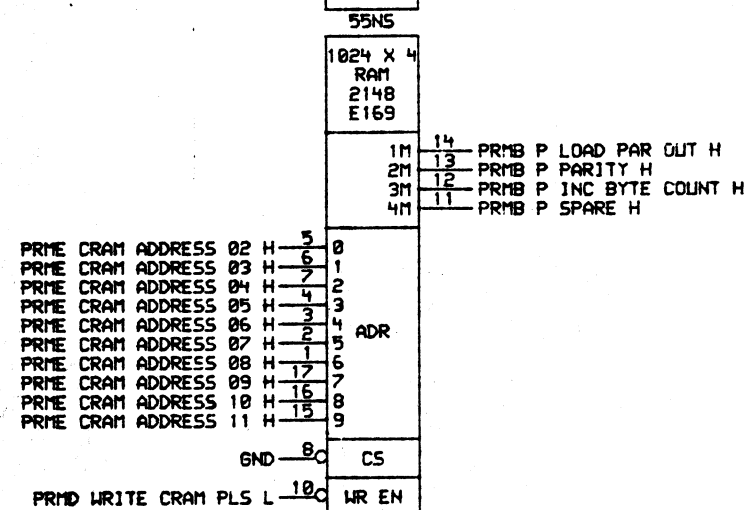
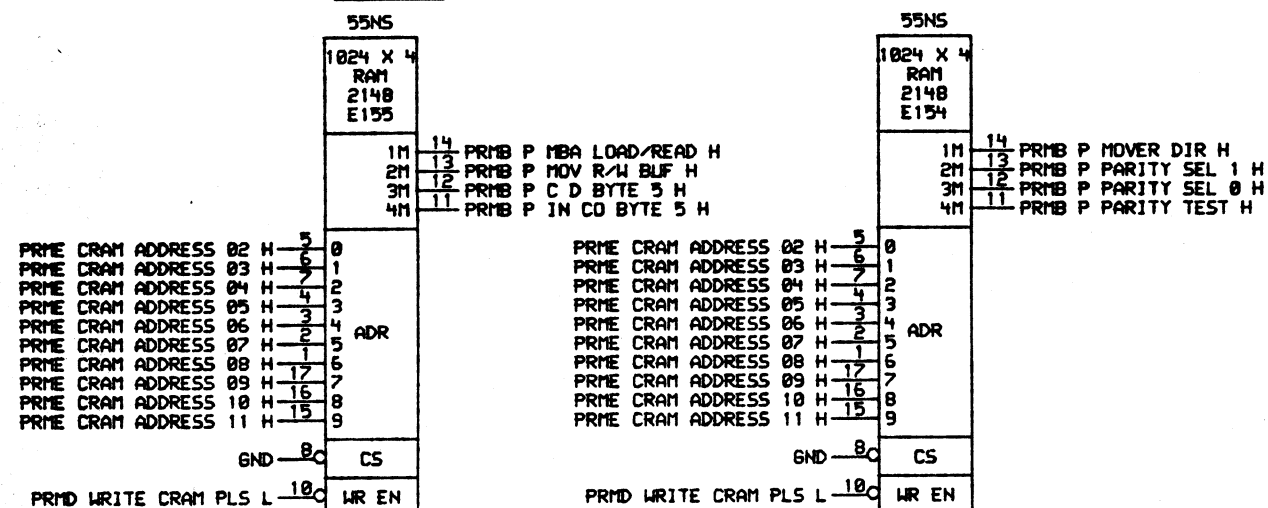
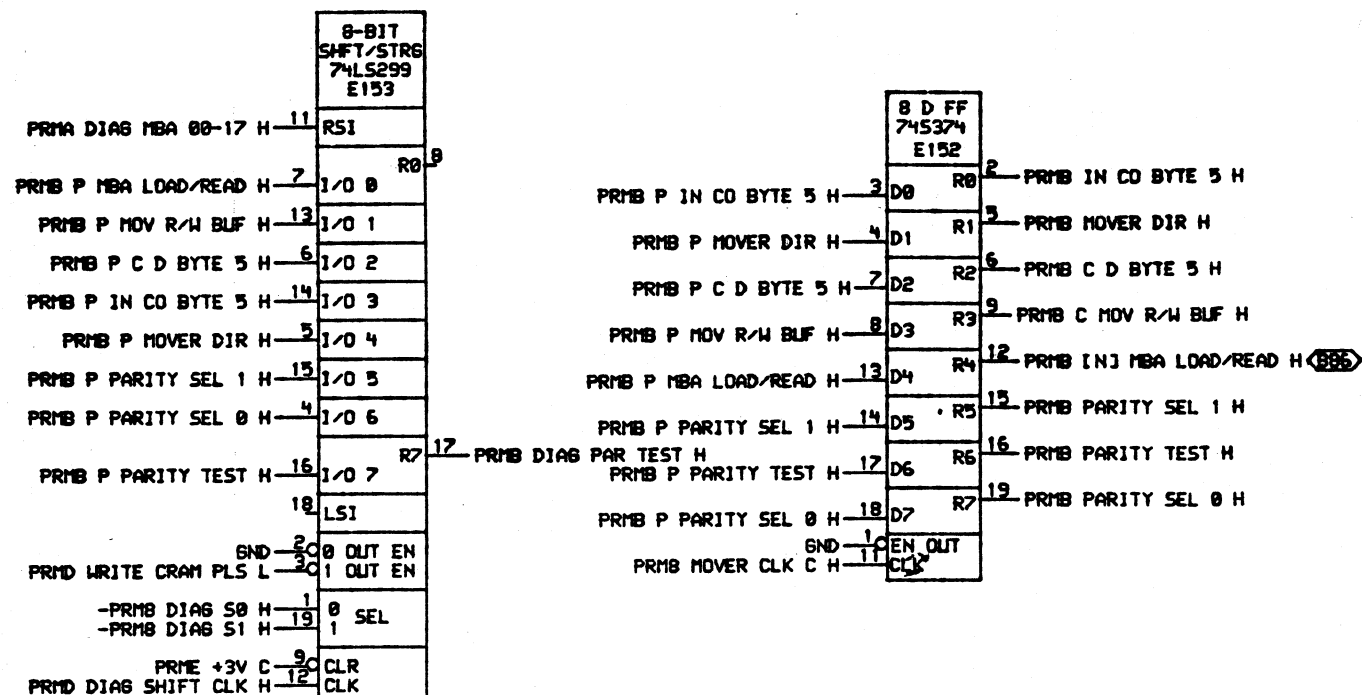
NUMBER

L0070-0-PRMB

SIZE

D

REV.



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REVISIONS
CHK CHANGE NO. REV

digital

DRN
CHK'D

DATE ENG.
31-JUL-82

DATE BOARD LOCATION
31-JUL-82 SHEET 1 OF 1

DATE

TITLE: KC10 PORT MOVER
CRAM N

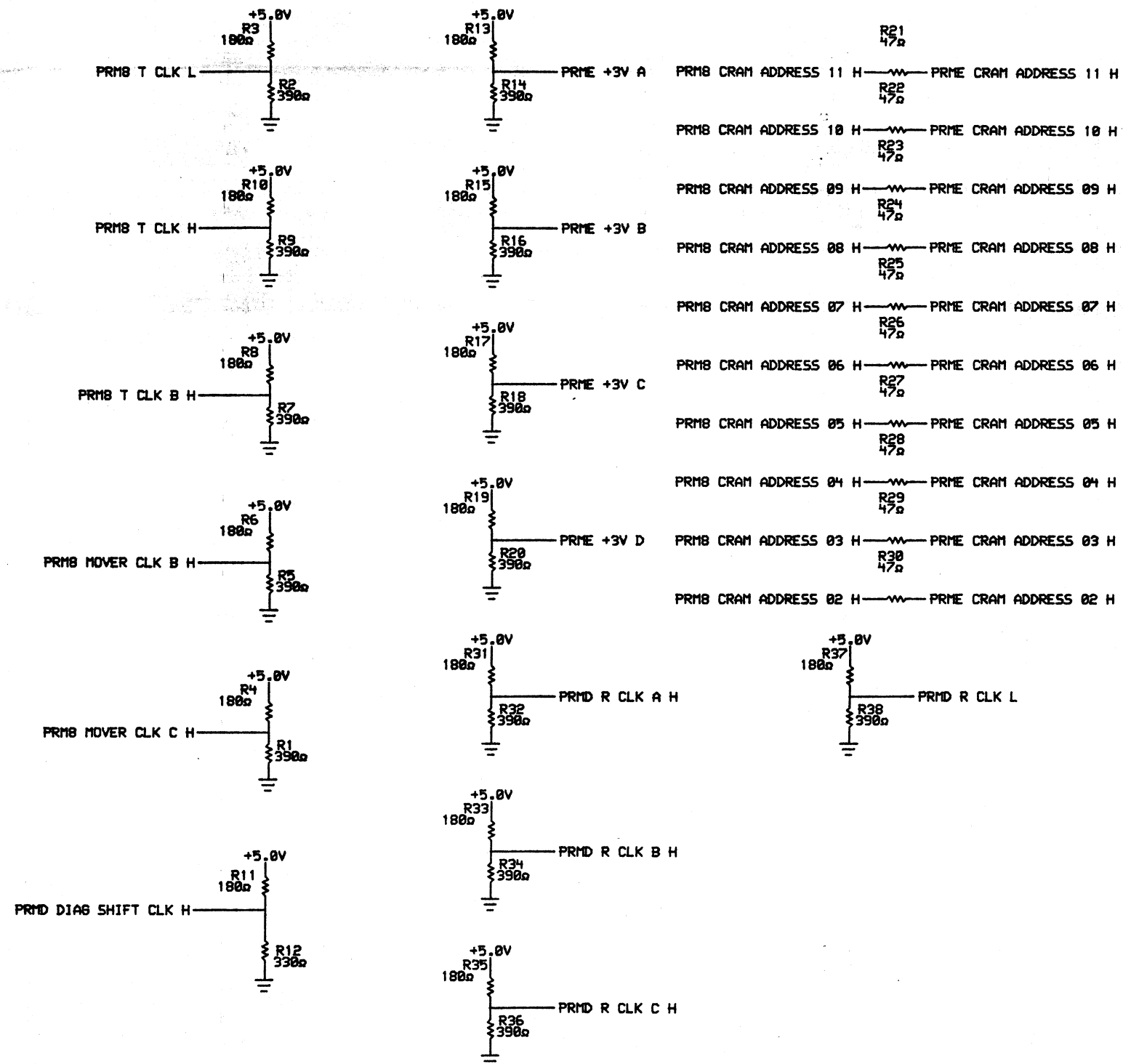
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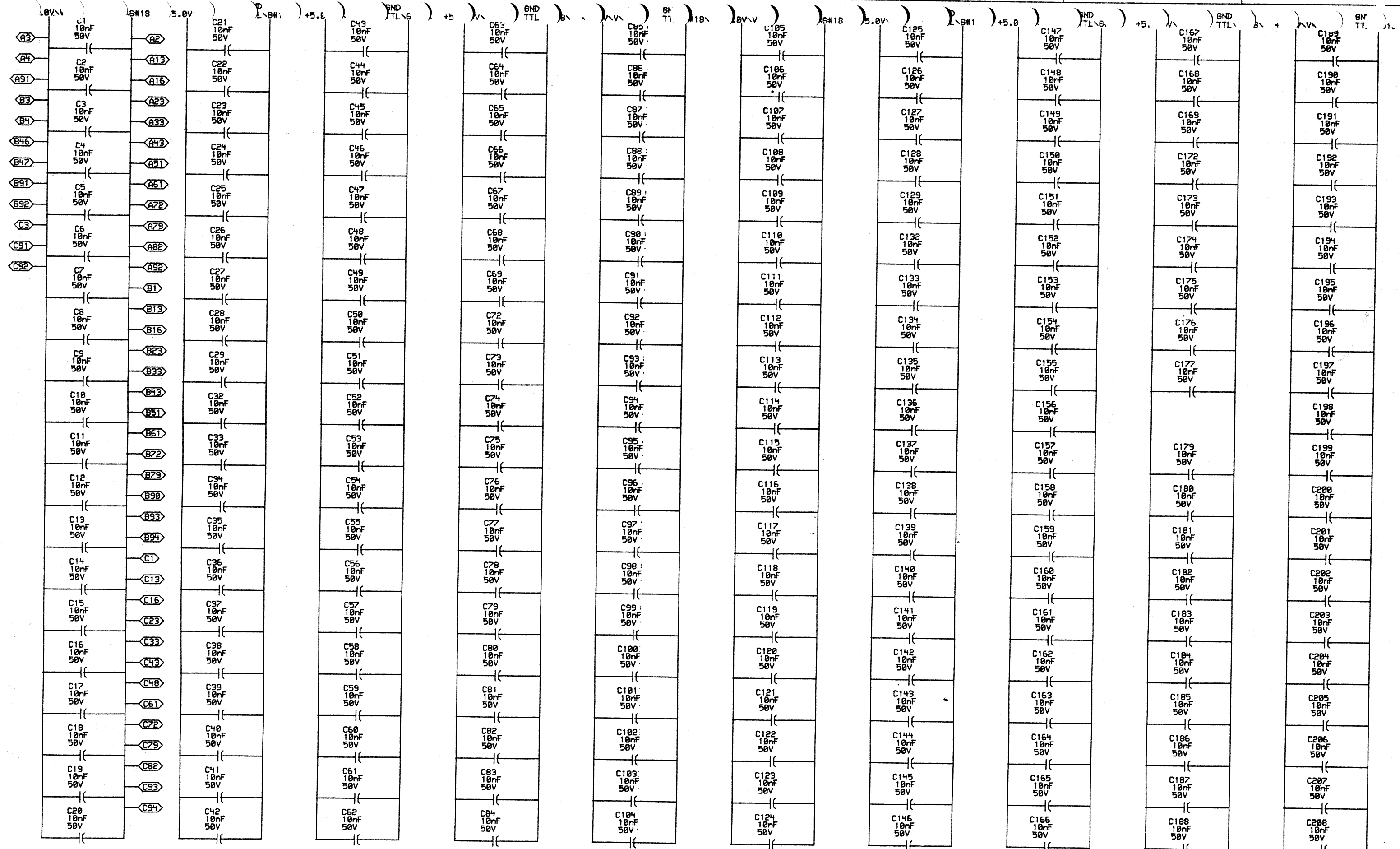
REV.

D CS L0070-0-PRMB

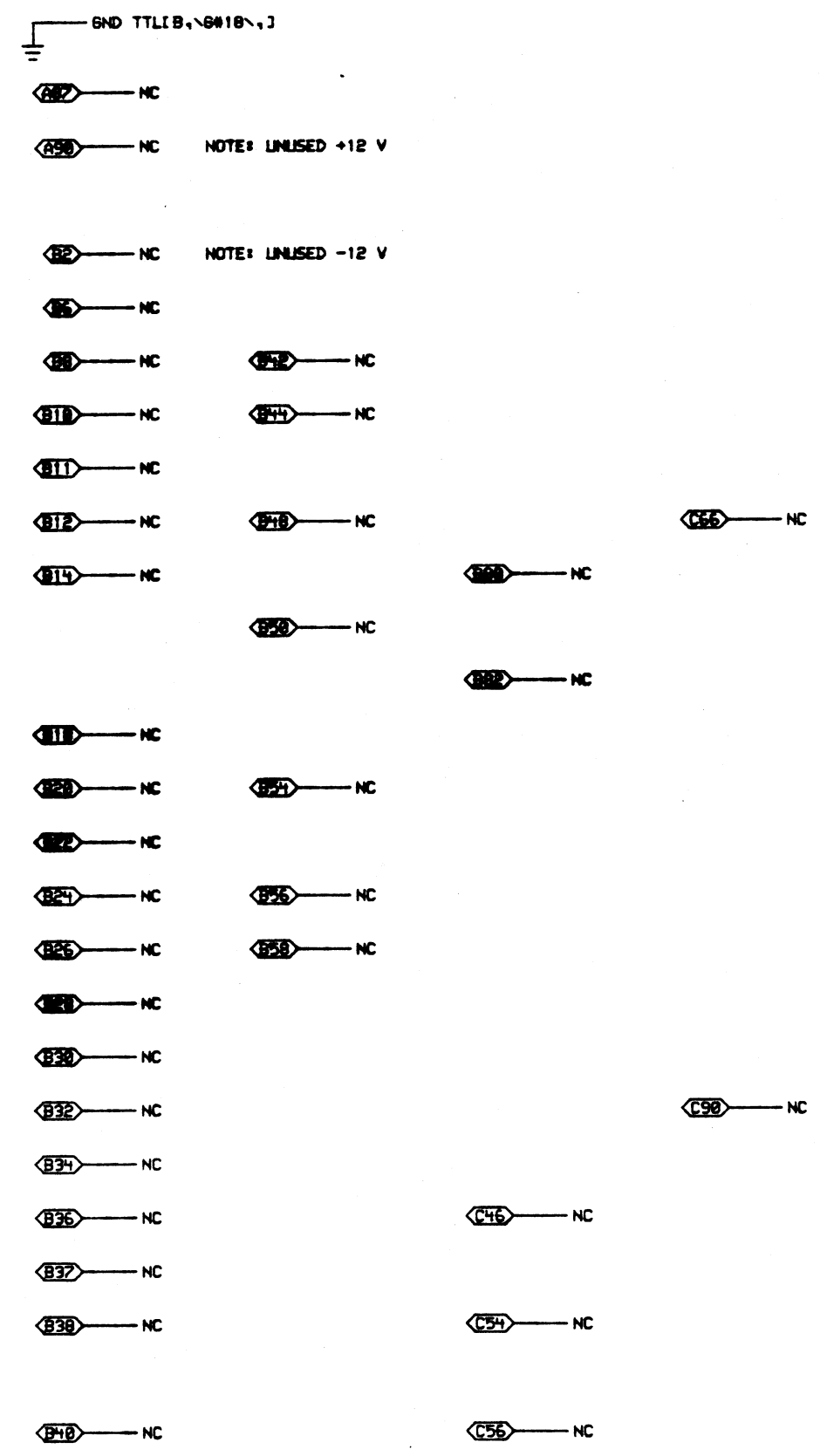
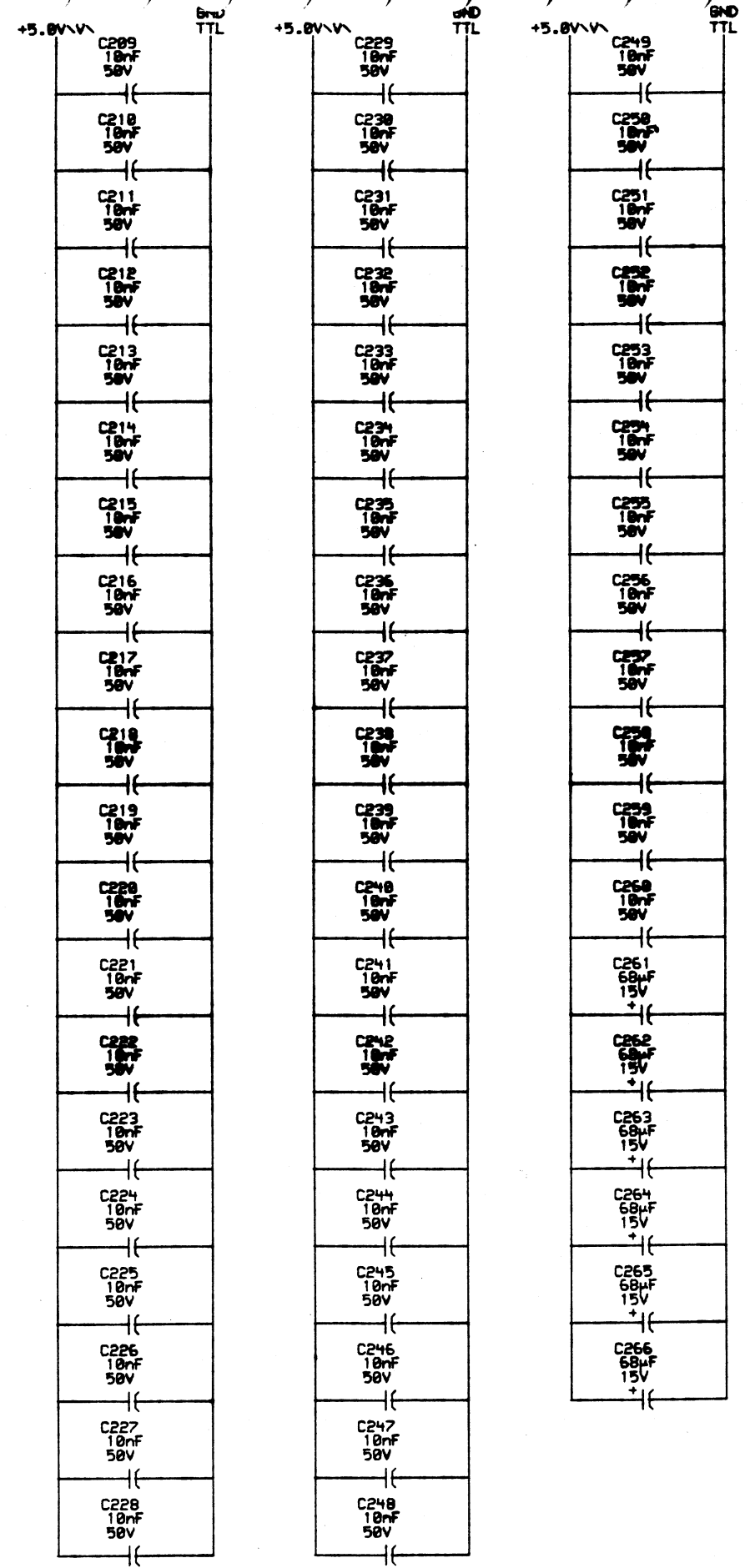
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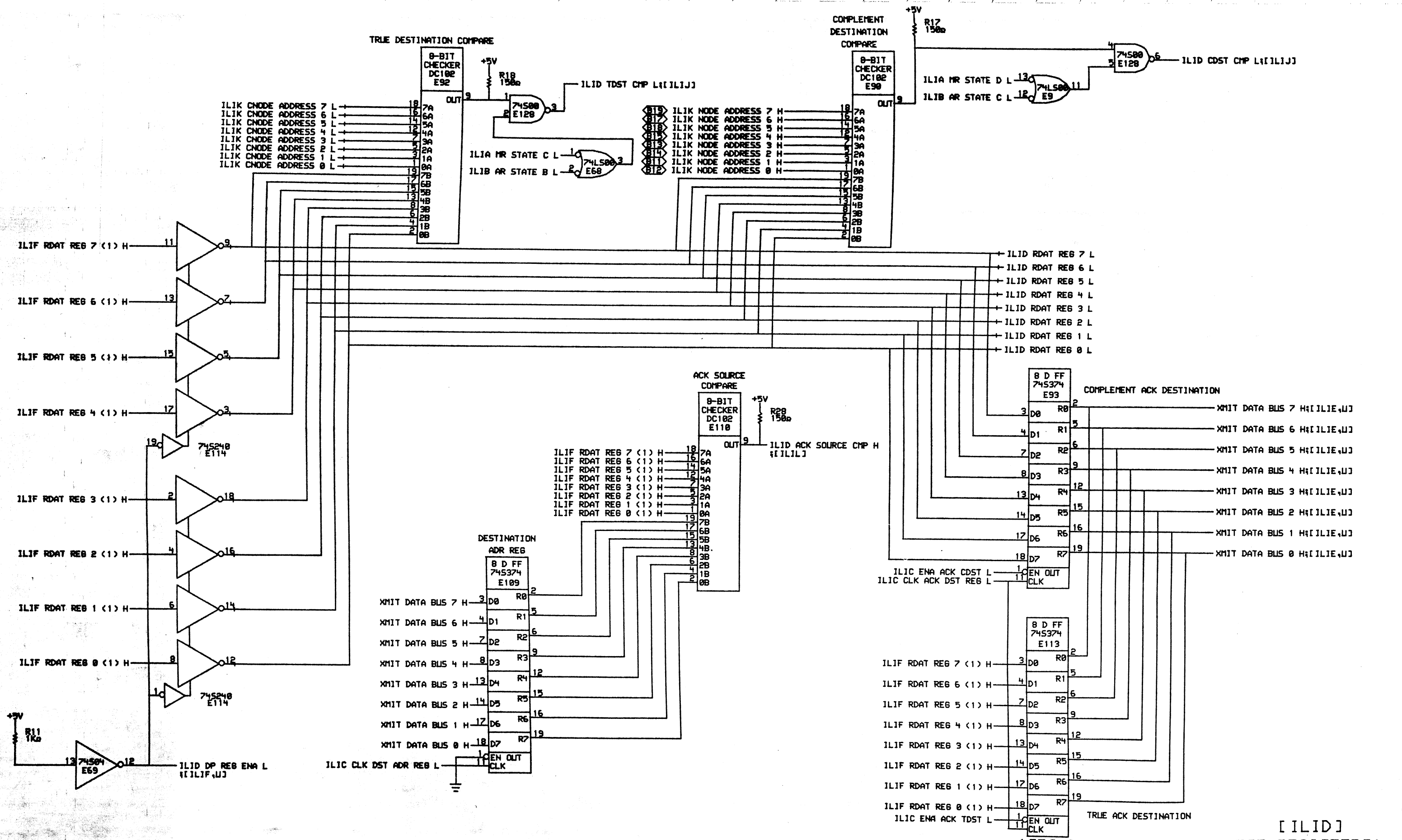
DRN.	DATE	ENG.	DATE	TITLE: KC10 PORT MOVER RESISTORS
CHK'D.	DATE	BOARD LOCATION:	DATE	
VULCAN (2000-10070) 105-MAR-82 16:26	09-MAR-82	09-MAR-82	09-MAR-82	
FIRST USED ON OPTION/MODEL: KC10	NEXT HIGHER ASSEMBLY: B-DD-KC10-0	SIZE CODE D CS	NUMBER L0070-0-PRME	REV.

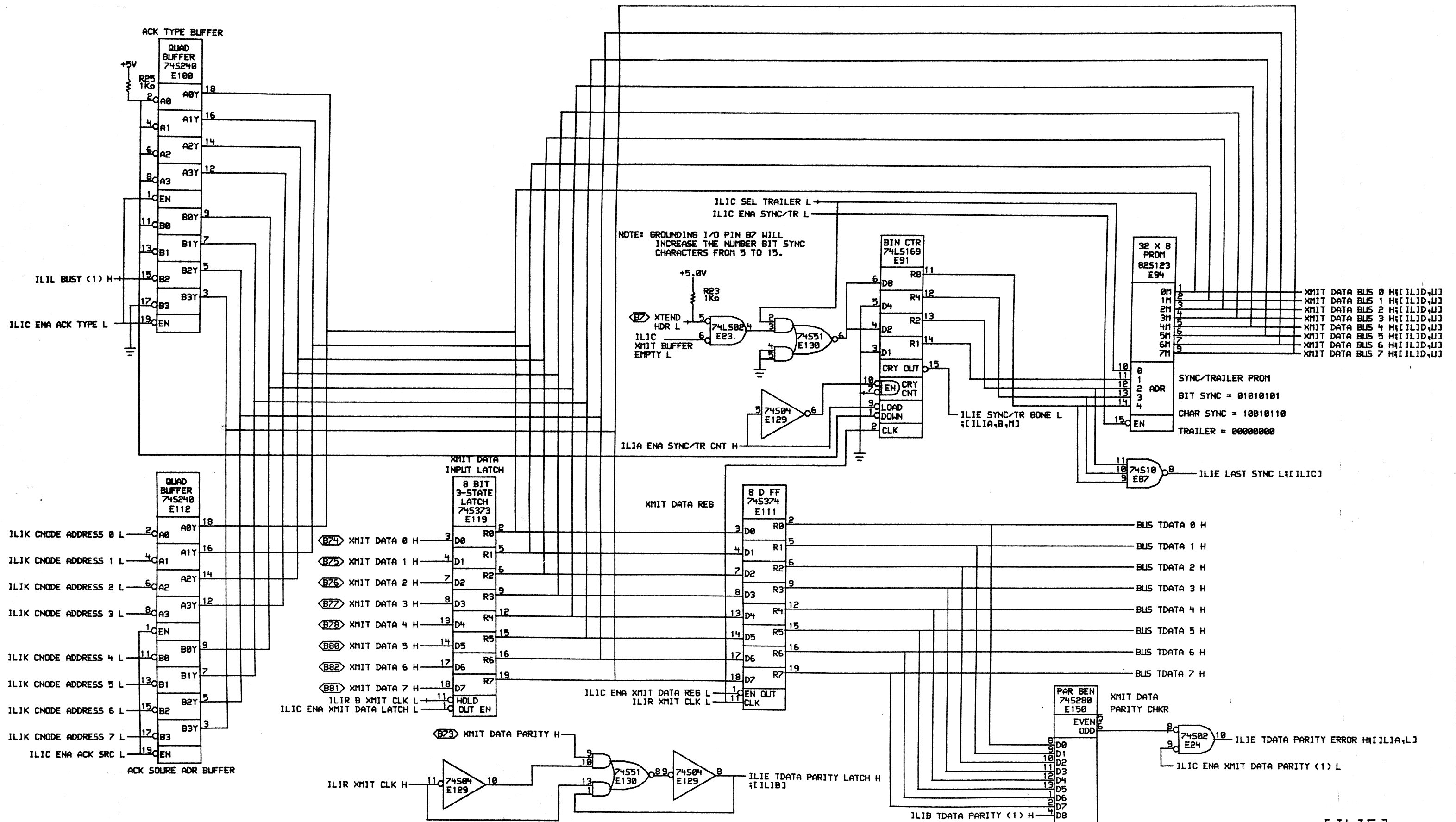


DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	12-JAN-82			2080 PORT
VULCAN (2080-10070) PRM DRN 109-DEC-81 19:15 NEXT HIGHER ASSEMBLY:				SIZE CODE
FIRST USED ON OPTION/MODEL: B-DD--0				D CS
				NUMBER
				L0070-0-PRMF
				REV.



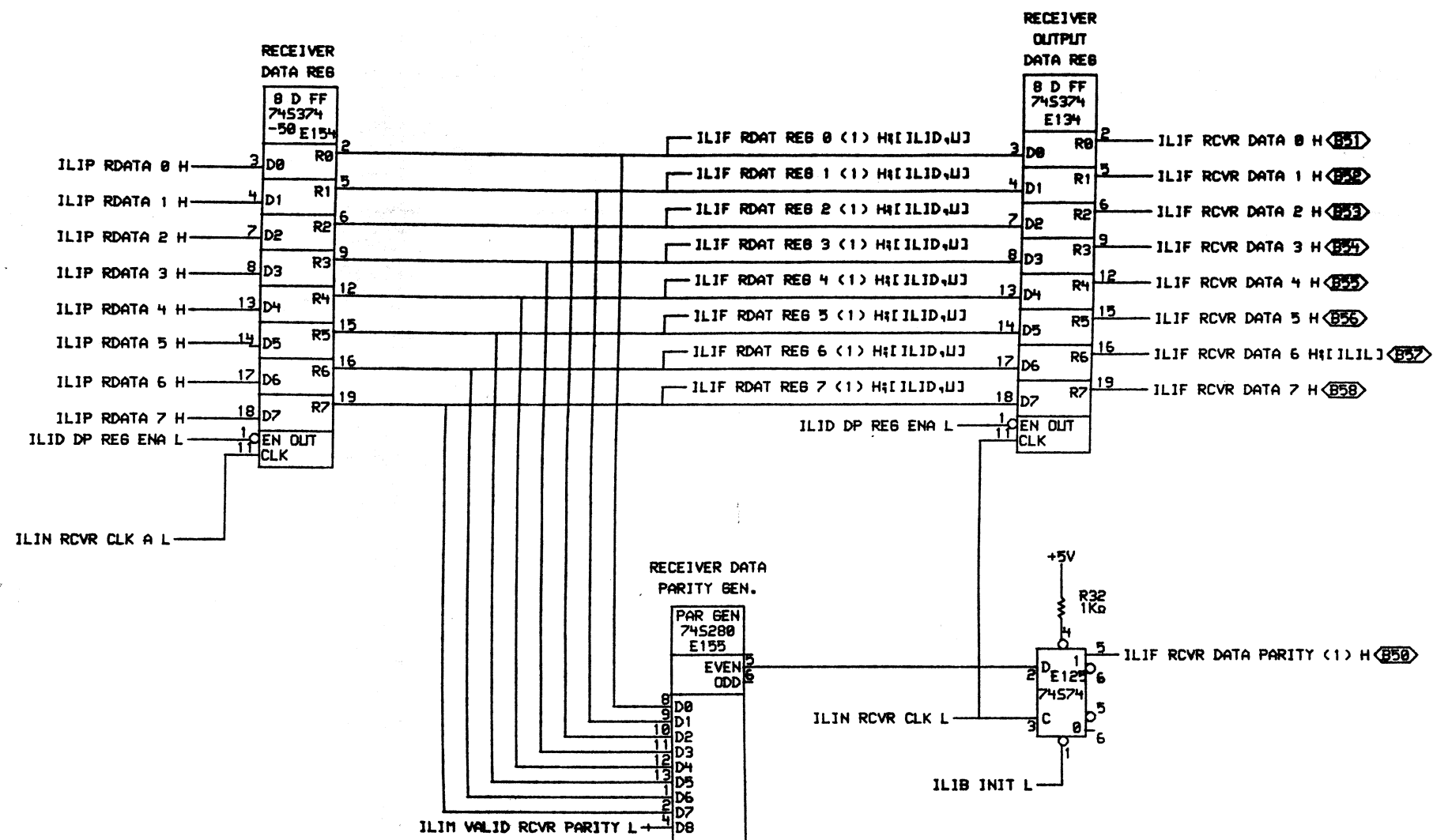
ILI1
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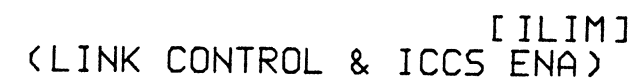


[ILIE]
(TRANSMIT DATA PATH)

DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION	SHEET	OF 20
INTERPROCESSOR LINK INTERFACE (ILI)				
FIRST USED ON OPTION/MODEL: C1780 D-UA-L0100-0-1				
SIZE	CODE	NUMBER	REV.	
D	C5	L0100-0-ILIE	A	

[ILIF]
(RCVR DATA REGISTERS)

	DIN.	DATE	ENS.	DATE	TITLE				
	CAR'D.	DATE	BOARD LOCATION		INTERPROCESSOR LINK INTERFACE(ILI)				
WALCON(L0100)PILS.DIODE-PAR-02 UNIT5 NEXT HIGHEN ASSEMBLY						SIZE	CODE	NUMBER	REV.
FIRST USED ON OPTION MODEL: C1700						D	C5	L0100-0-ILIF	A

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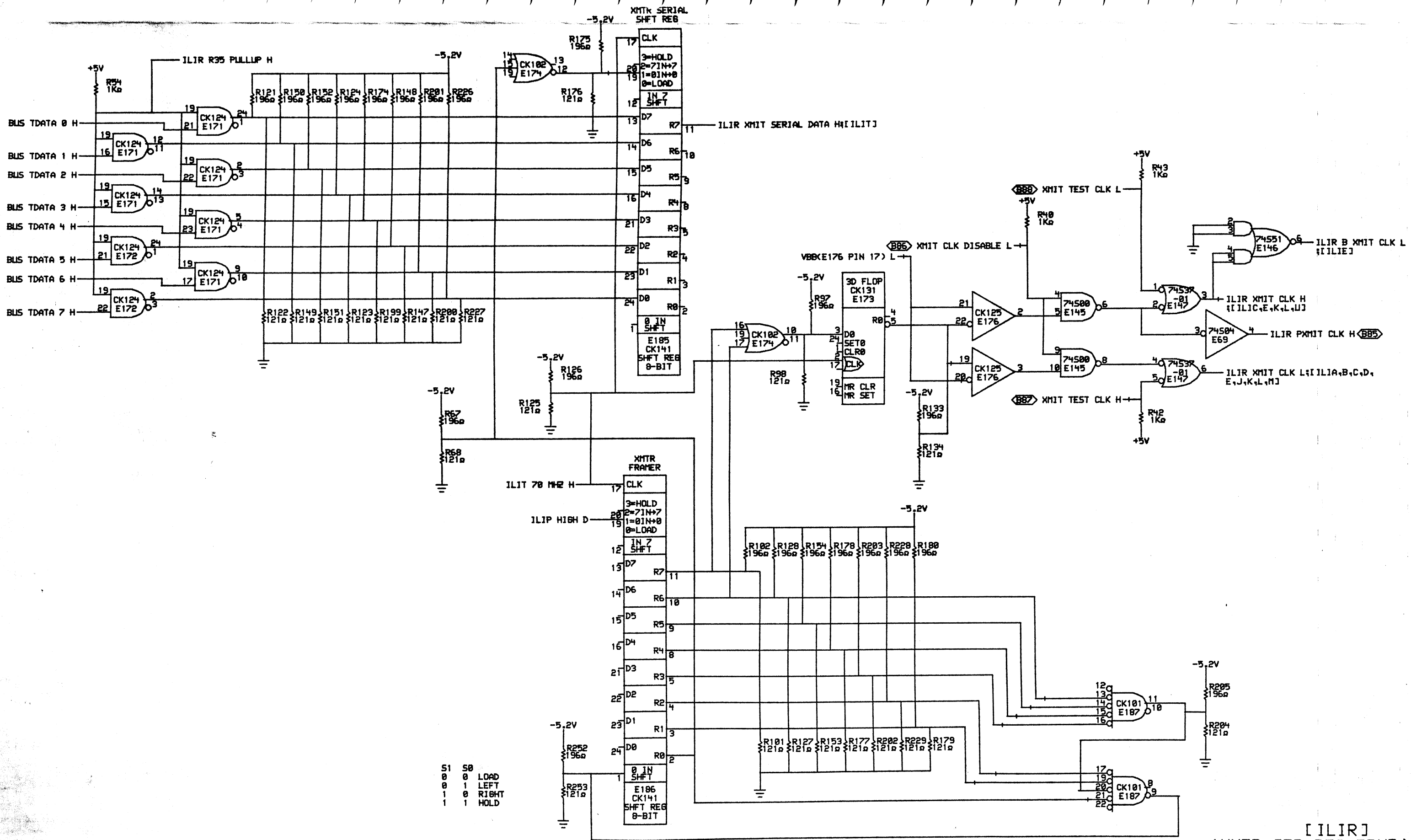
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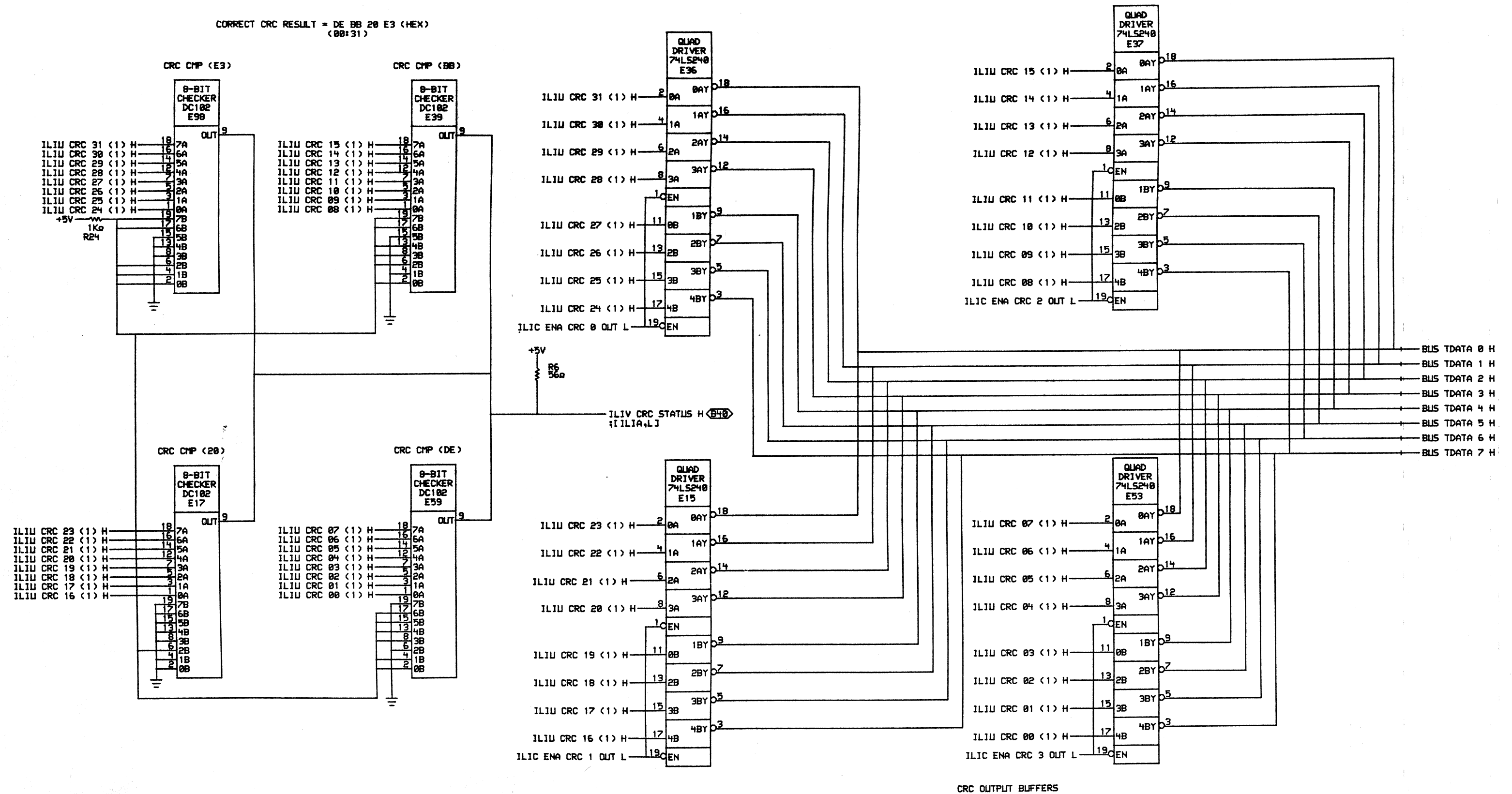


[ILIR]
(XMIT SER REG FRMR)

DATE	ENG.	DATE	TITLE
DATE	DATE	DATE	INTERPROCESSOR
DATE	DATE	DATE	LINK INTERFACE (ILI)
DATE	DATE	DATE	SIZE CODE
DATE	DATE	DATE	NUMBER
DATE	DATE	DATE	REV.
DATE	DATE	DATE	A

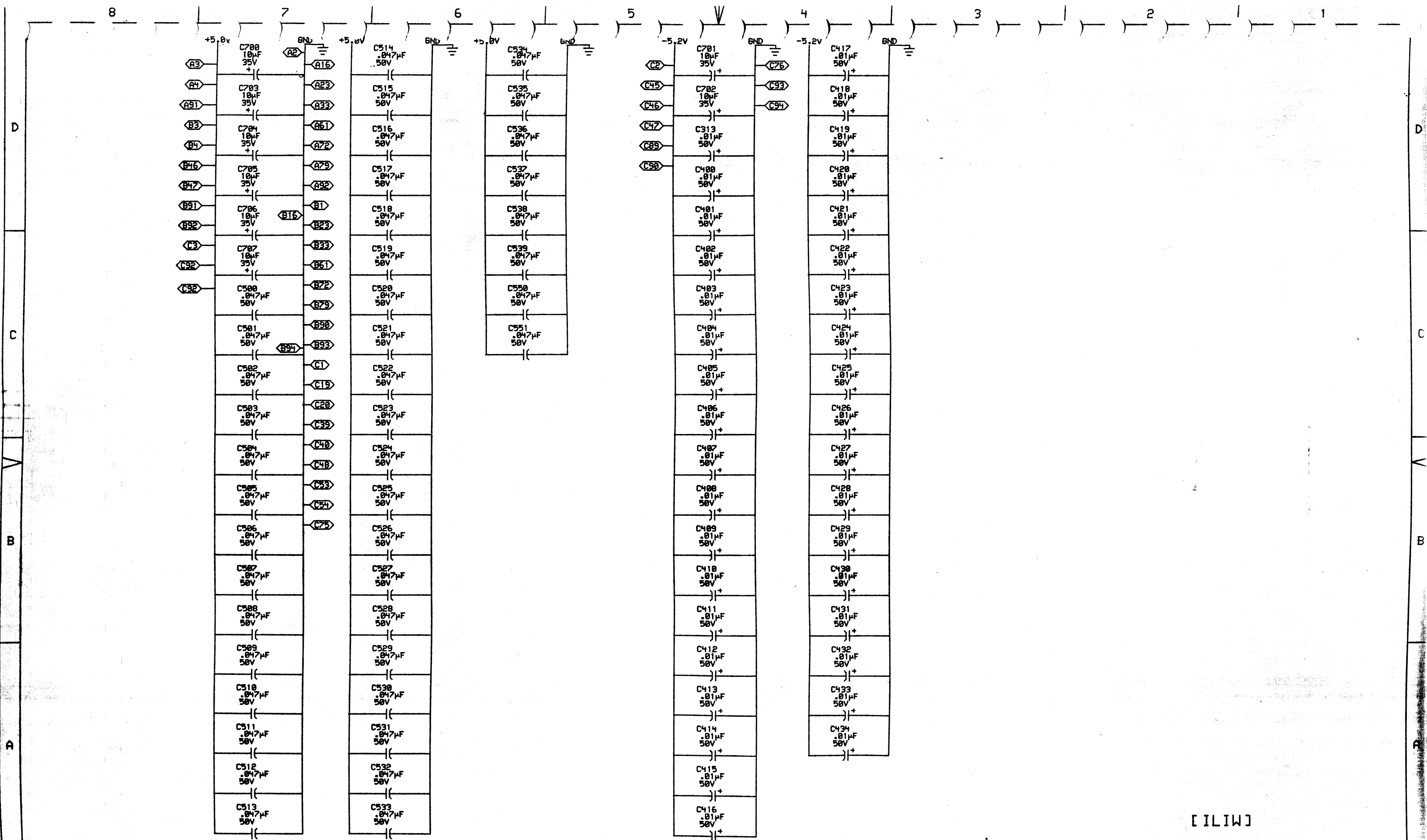
FIRST USED ON OPTION MODEL: C1780 D-UA-L0100-0-1

CORRECT CRC RESULT = DE BB 20 E3 (HEX)
(00:31)

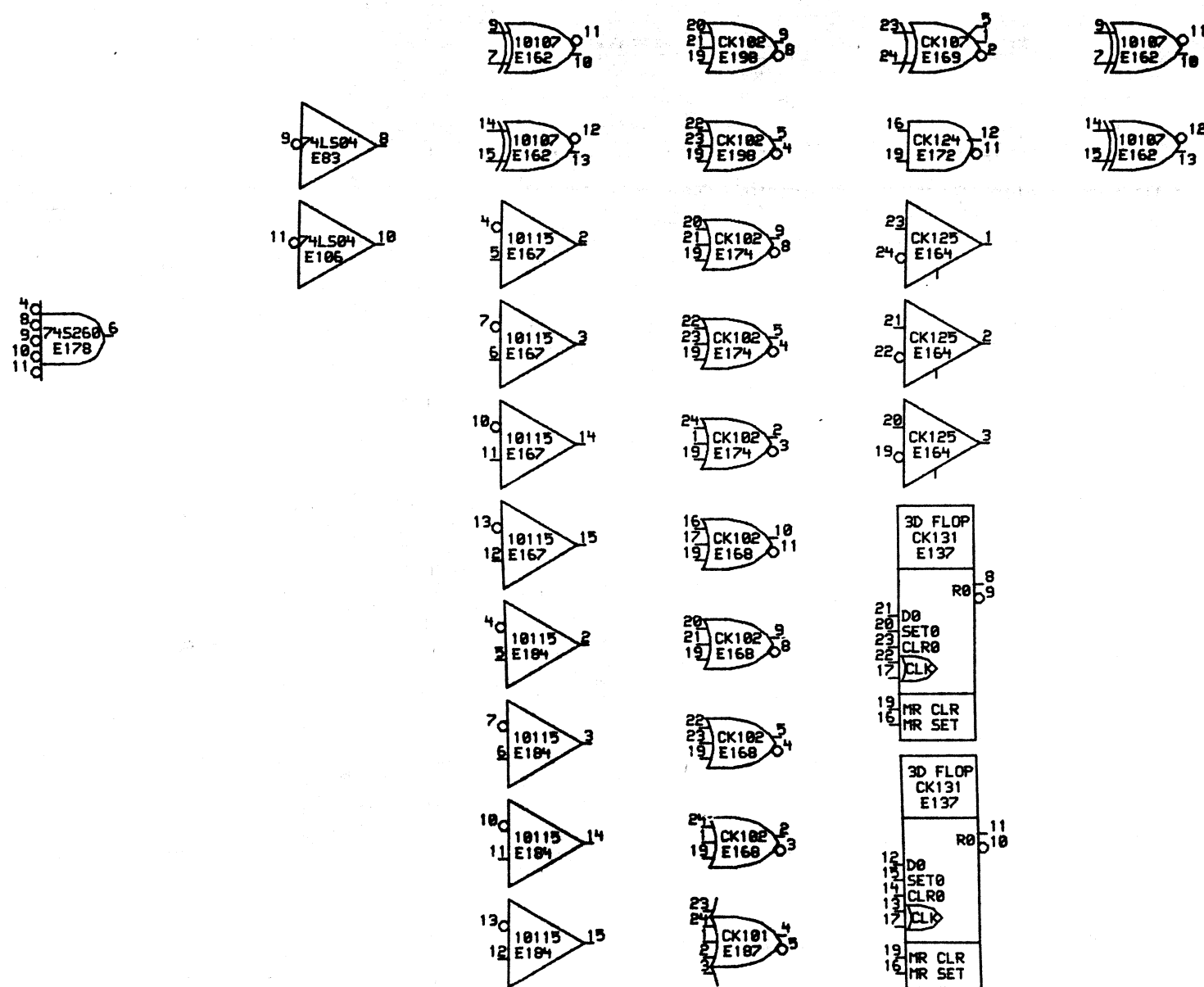


[ILIV]
(CRC COMPARE & OUTPUT)

DWN.		DATE		ENG.		DATE		TITLE: INTERPROCESSOR					
CHK'D.		DATE		BOARD LOCATION				LINK INTERFACE(ILI)					
				SHEET		18 OF 20							
VULCAN (2000-L0100)XPL18.DRW				102-MAR-82 09:21				NEXT HIGHER ASSEMBLY:		SIZE	CODE	NUMBER	REV.
FIRST USED ON OPTION/MODEL: CI780				D-LA-L0100-0-1				D	CS	L0100-0-ILIV		A	



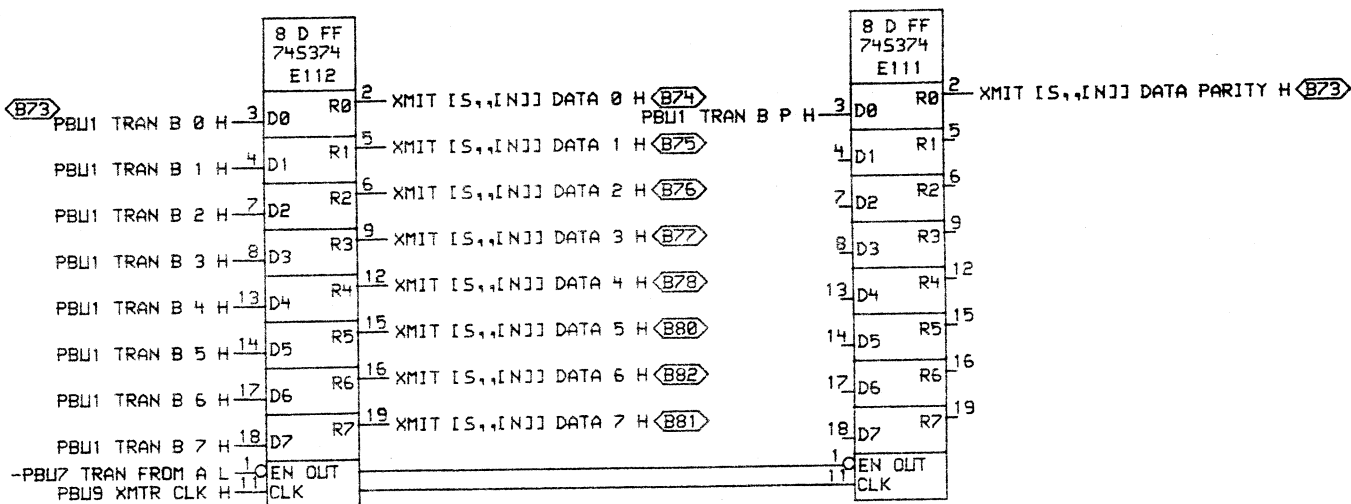
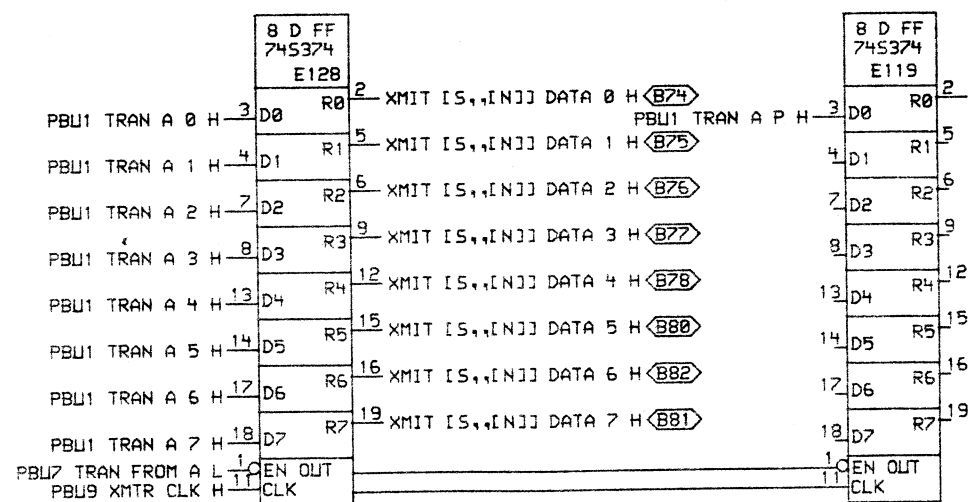
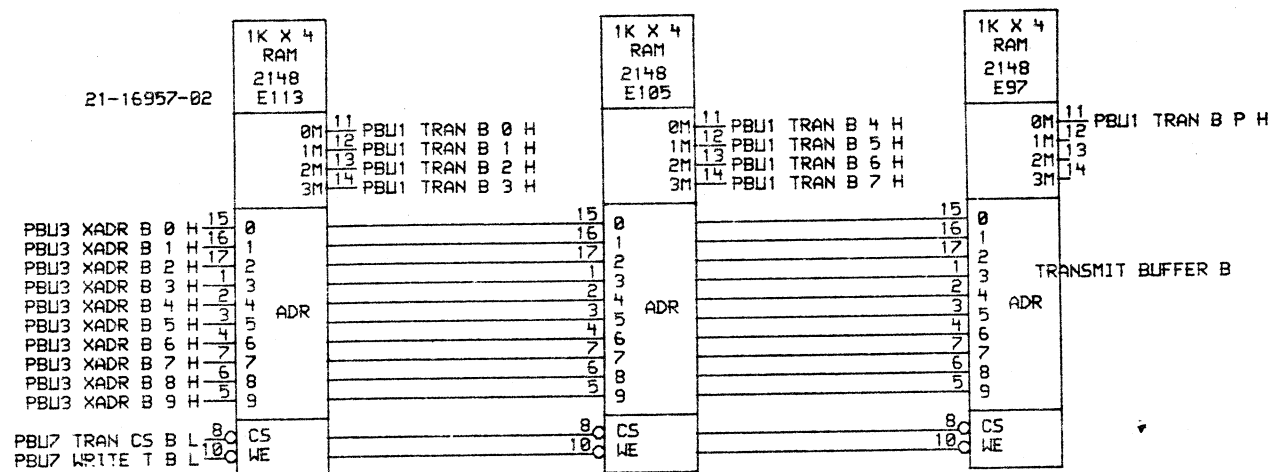
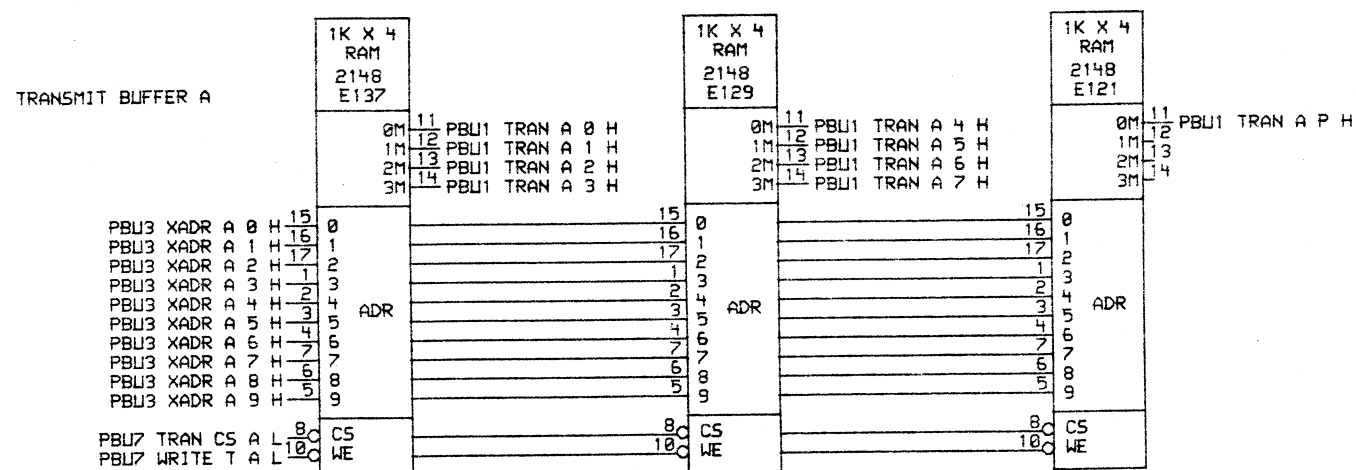
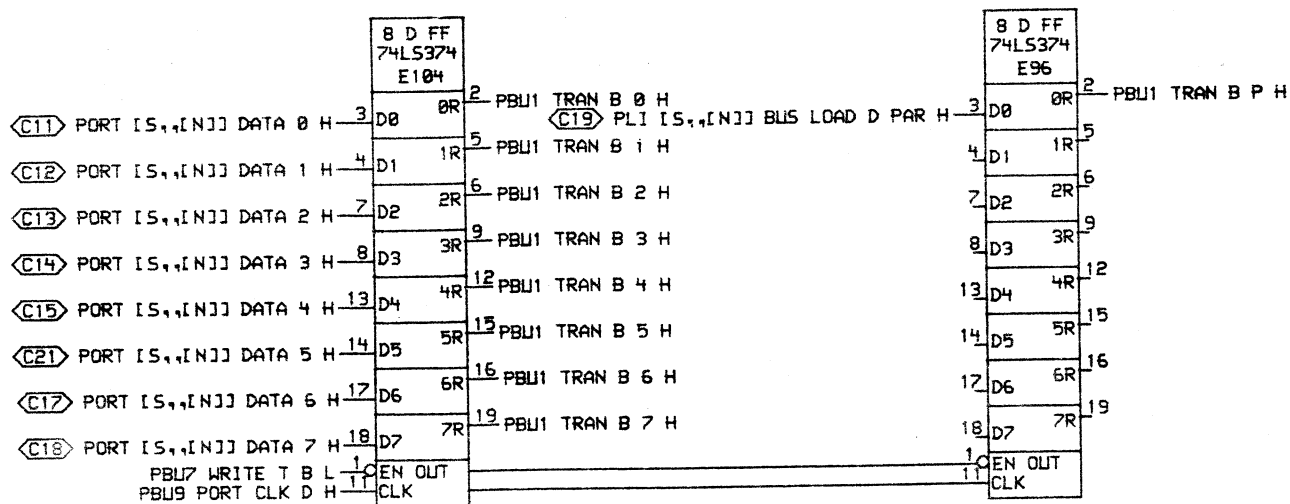
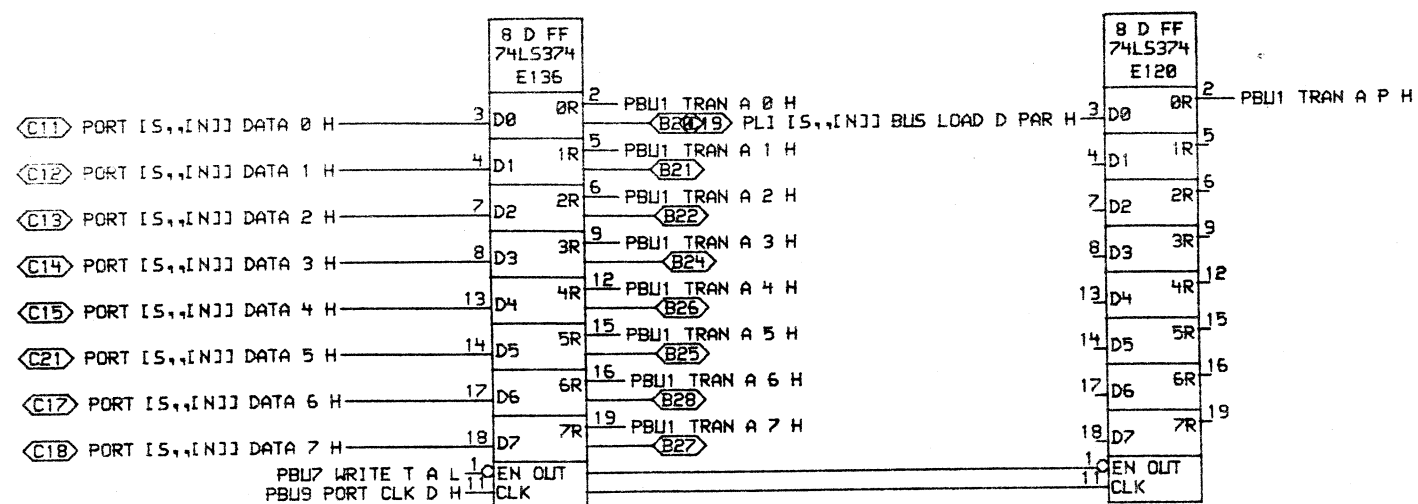
SPARE GATES AND I/O PINS

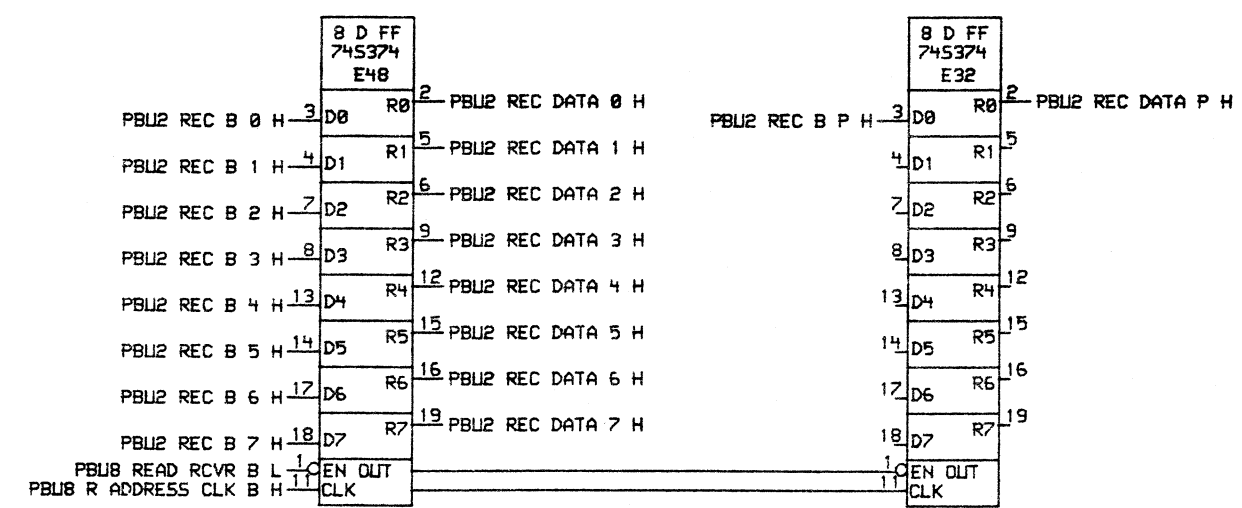
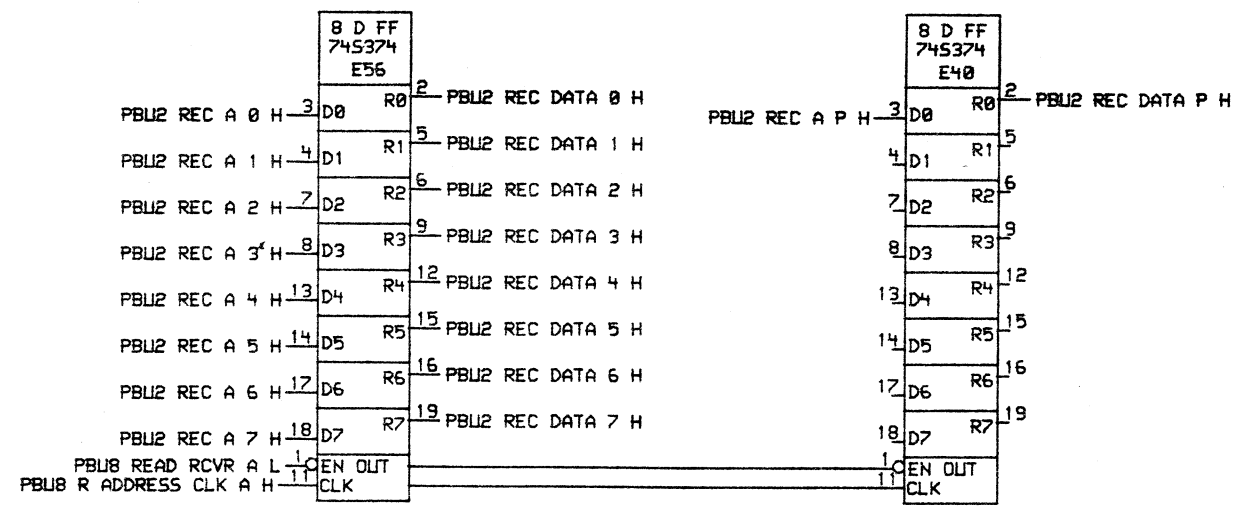
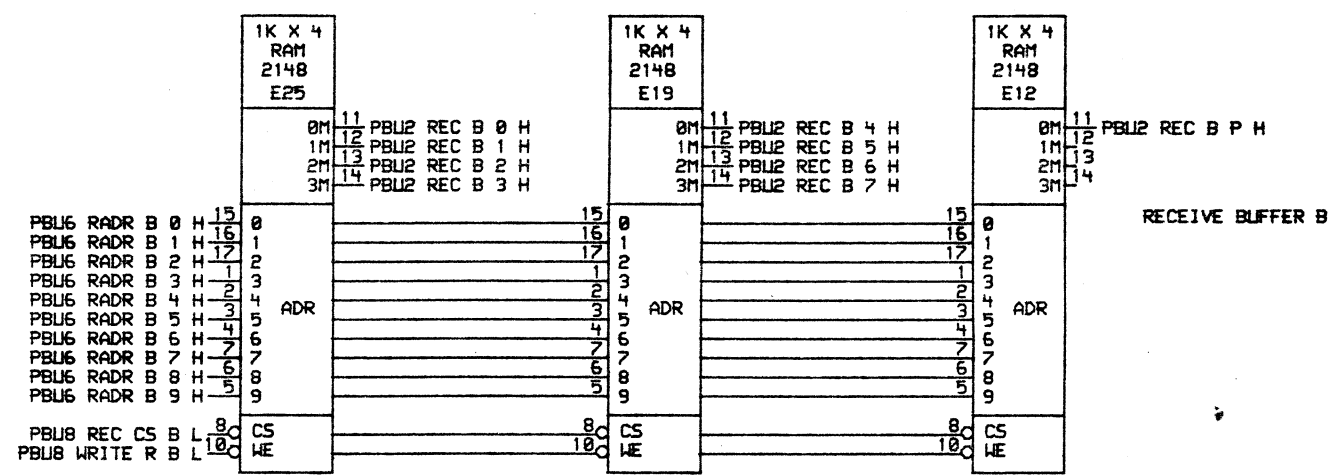
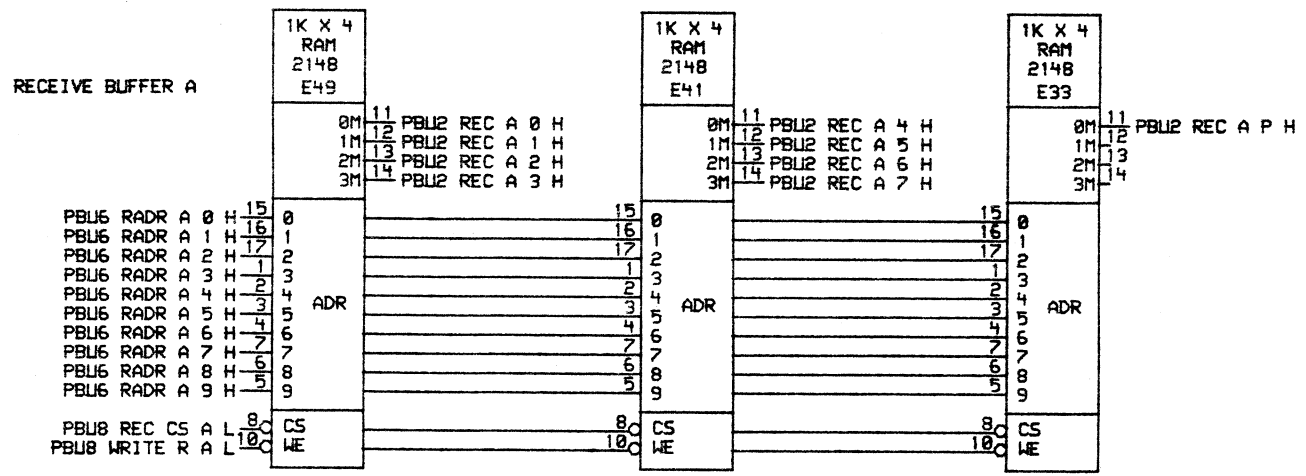
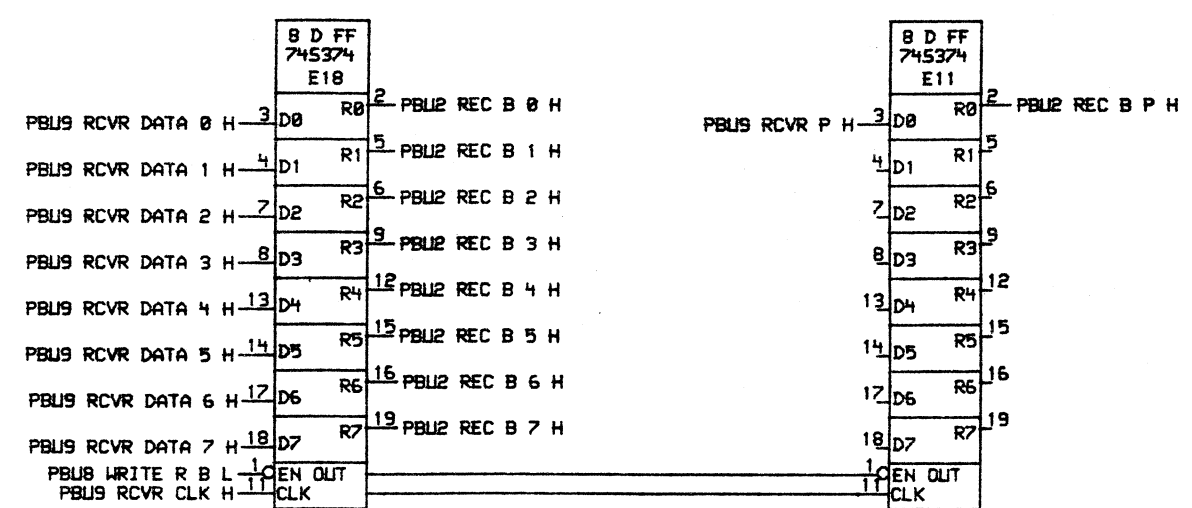
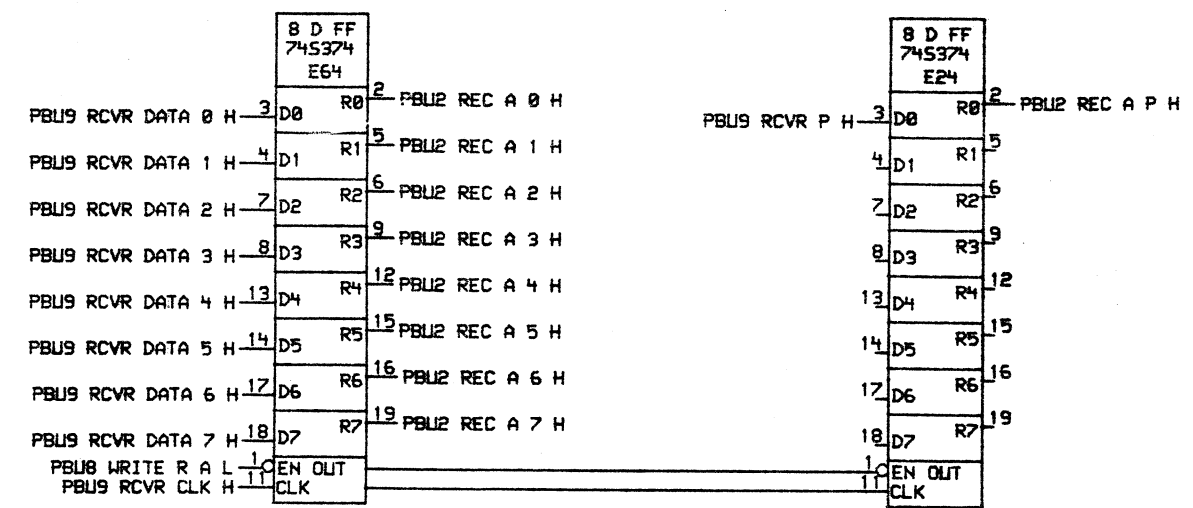


[ILIX]

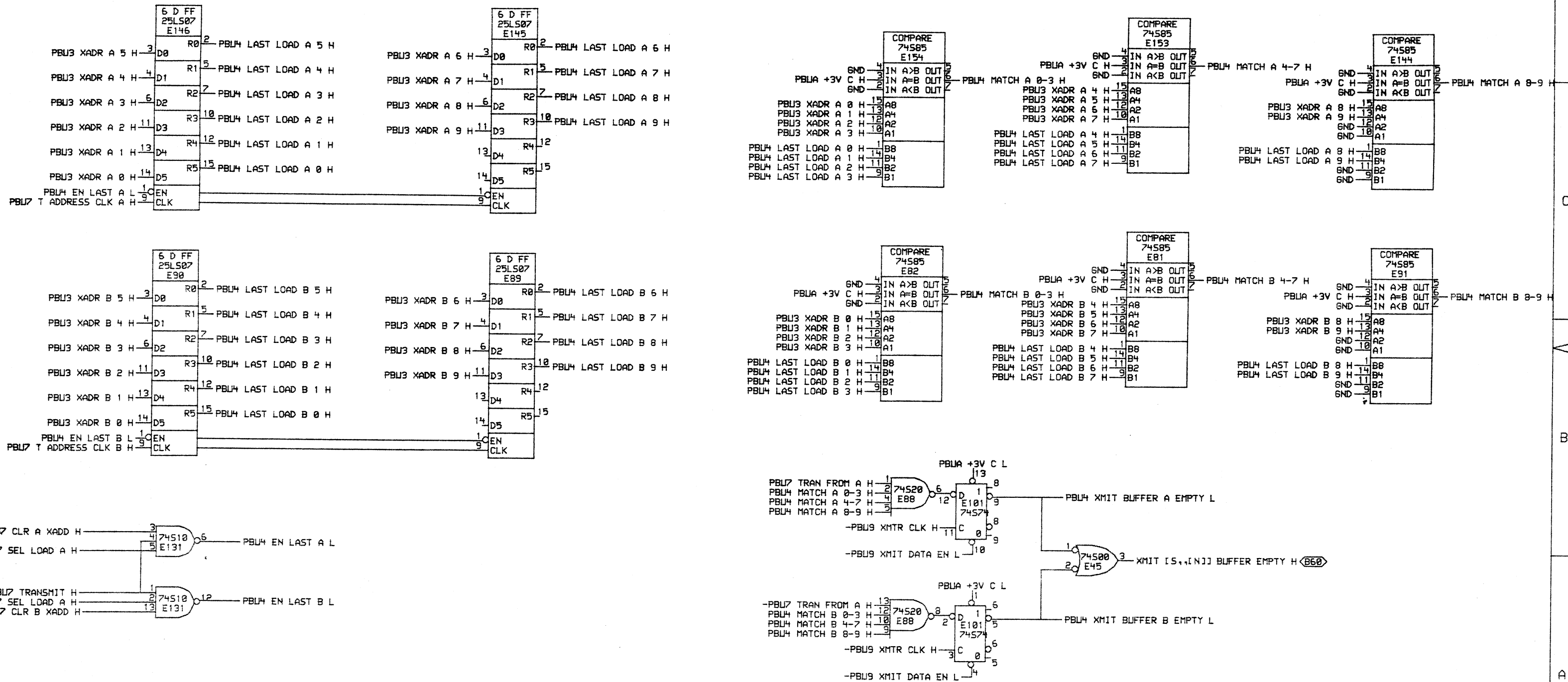
DATE	ENG.	DATE	ENG.	DATE	ENG.	TITLE
DATE	ENG.	DATE	ENG.	DATE	ENG.	INTERPROCESSOR
DATE	ENG.	DATE	ENG.	DATE	ENG.	LINK INTERFACE (ILIX)
DATE	ENG.	DATE	ENG.	DATE	ENG.	REV.
DATE	ENG.	DATE	ENG.	DATE	ENG.	A
DATE	ENG.	DATE	ENG.	DATE	ENG.	1
DATE	ENG.	DATE	ENG.	DATE	ENG.	2
DATE	ENG.	DATE	ENG.	DATE	ENG.	3
DATE	ENG.	DATE	ENG.	DATE	ENG.	4
DATE	ENG.	DATE	ENG.	DATE	ENG.	5
DATE	ENG.	DATE	ENG.	DATE	ENG.	6
DATE	ENG.	DATE	ENG.	DATE	ENG.	7
DATE	ENG.	DATE	ENG.	DATE	ENG.	8

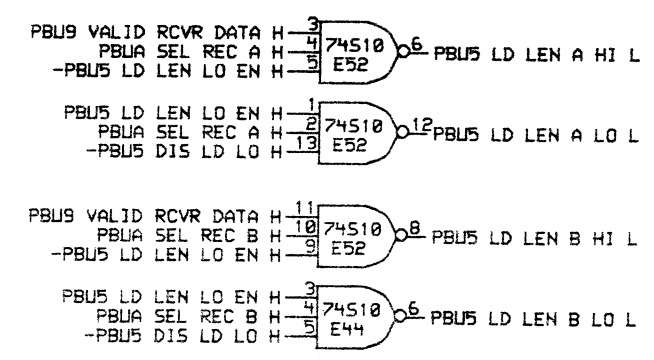
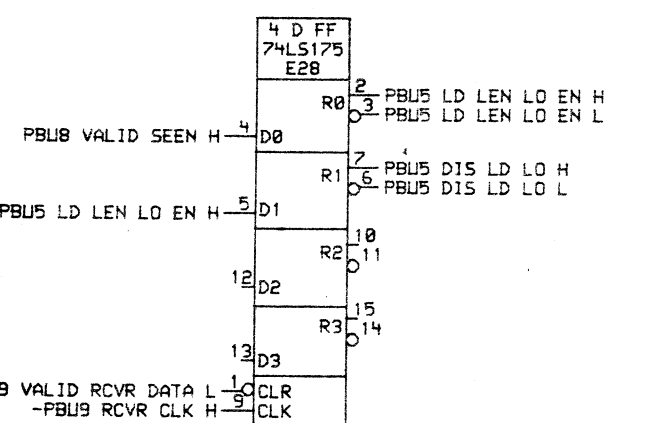
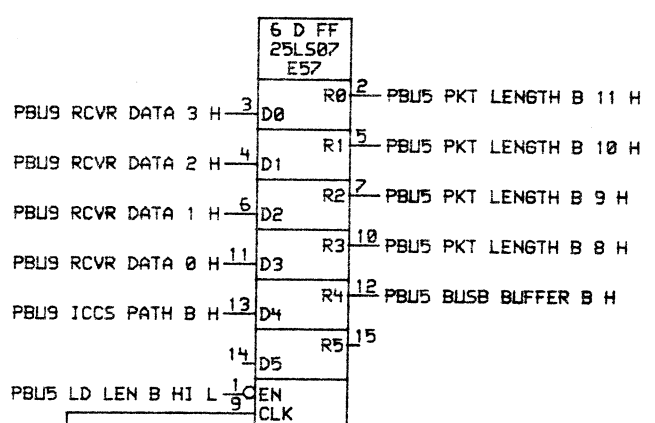
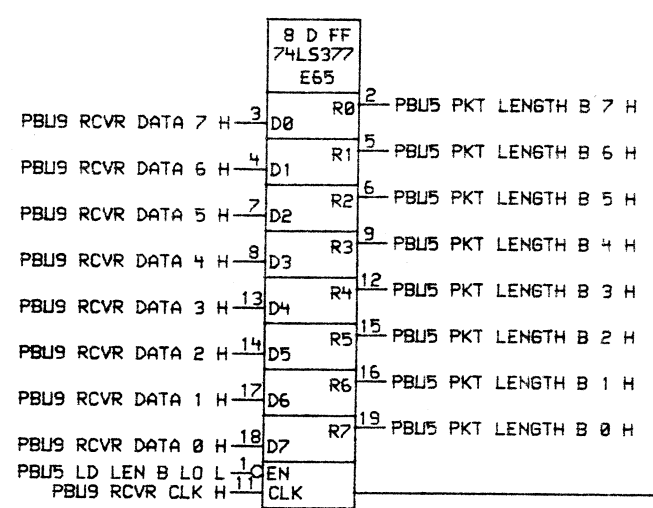
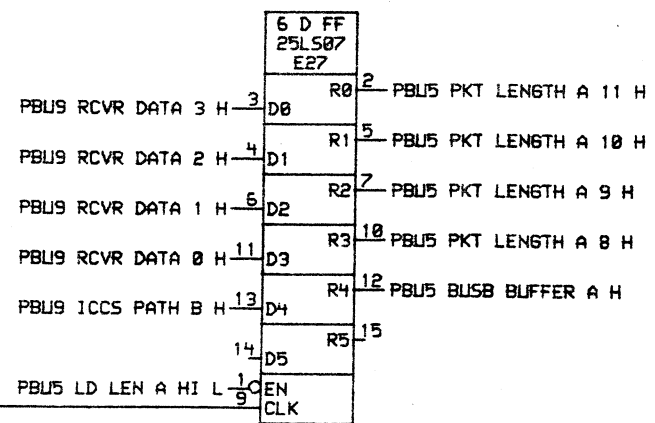
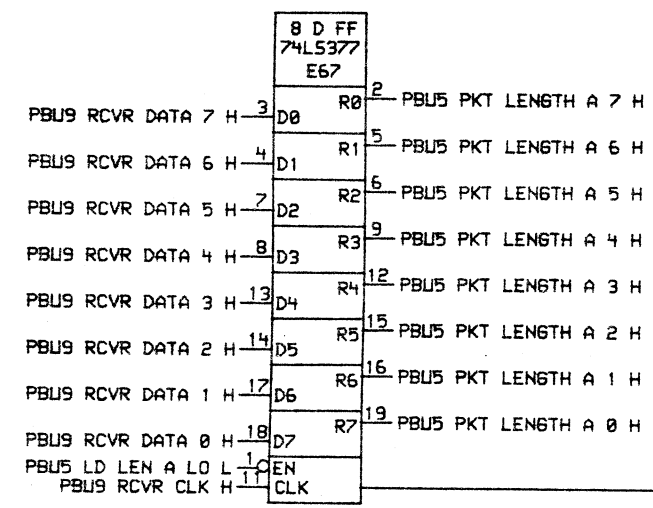
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<2080.L0067>PBU2
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<2080.L0067>PBU9



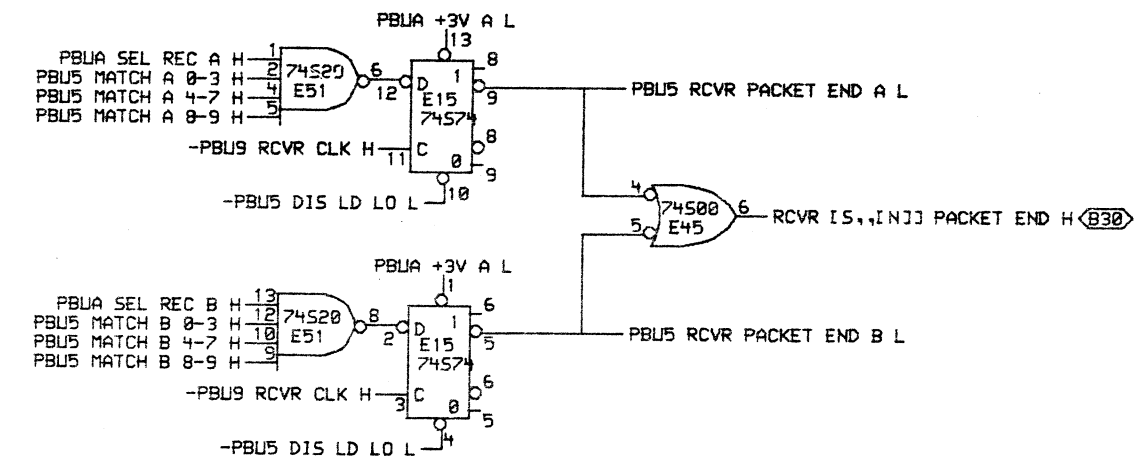
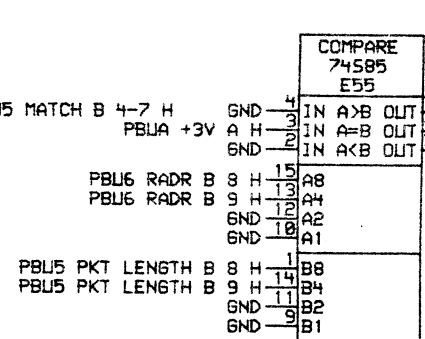
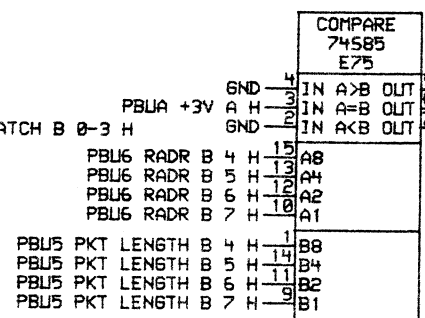
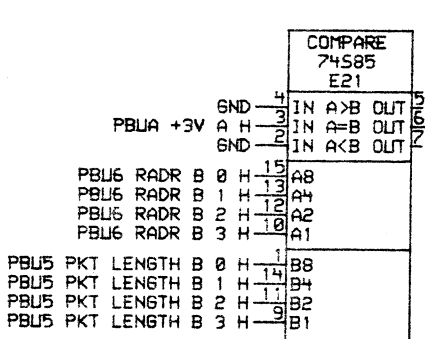
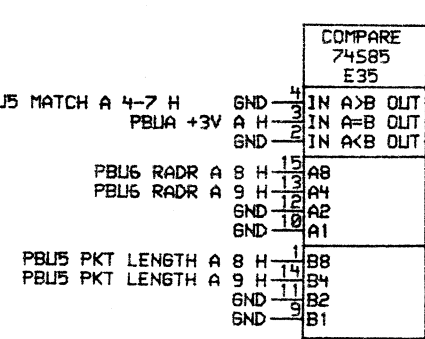
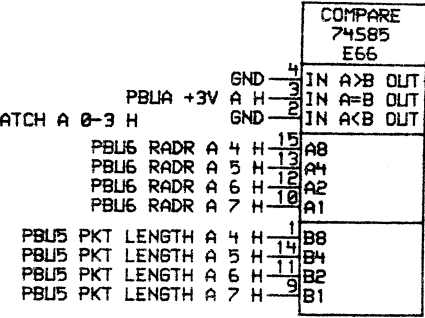
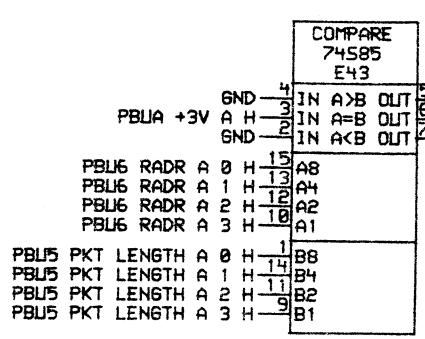


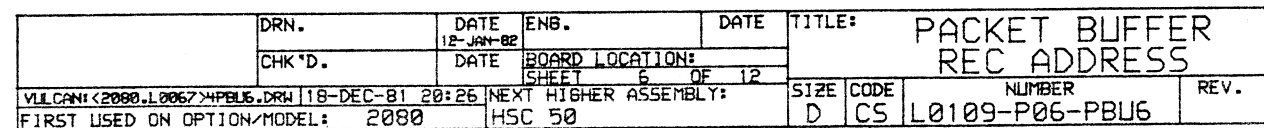
TRANSMIT PACKET SIZE COMPARATORS

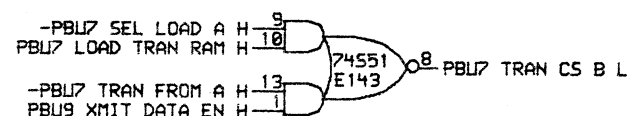
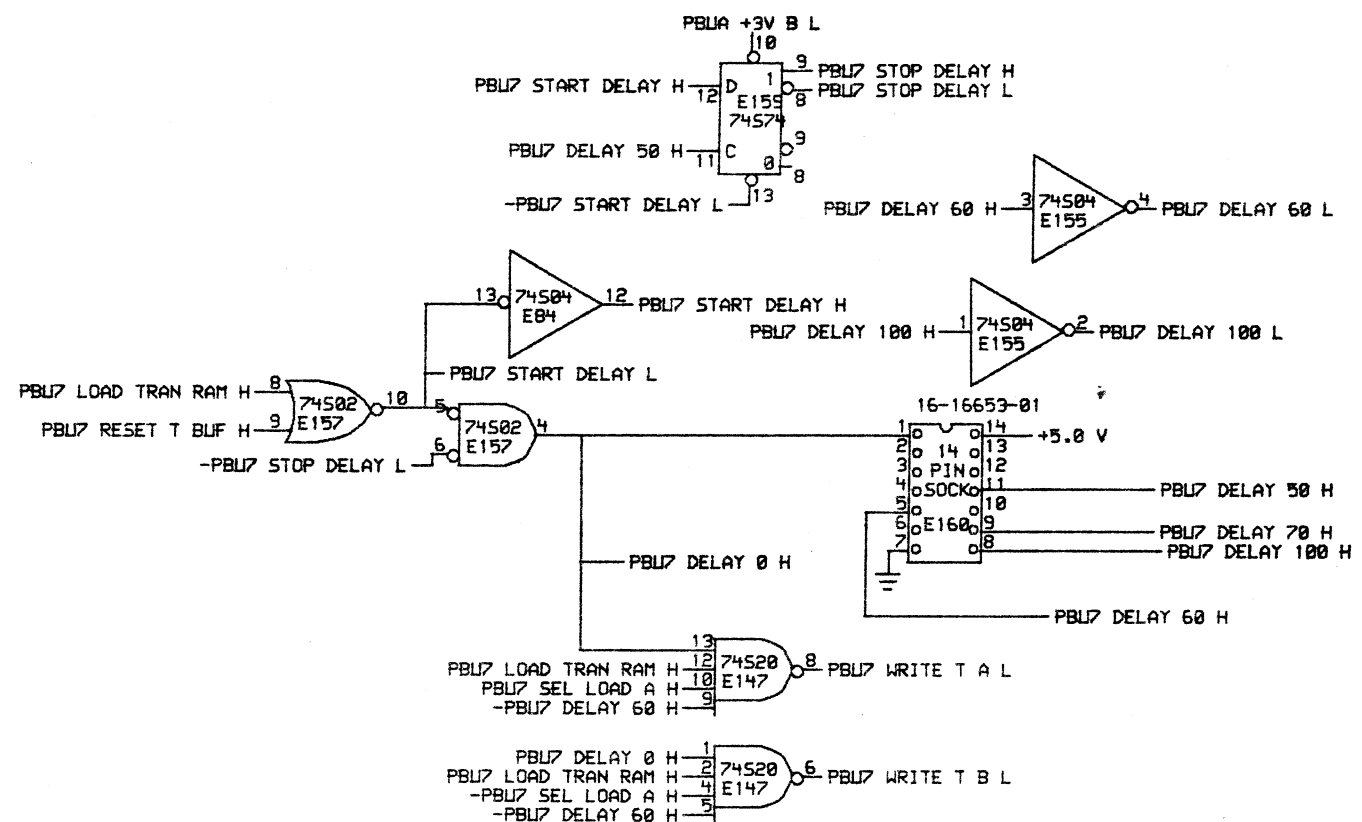
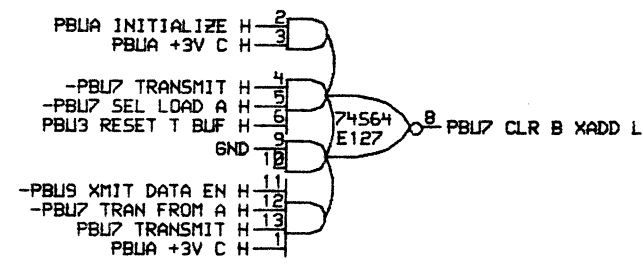
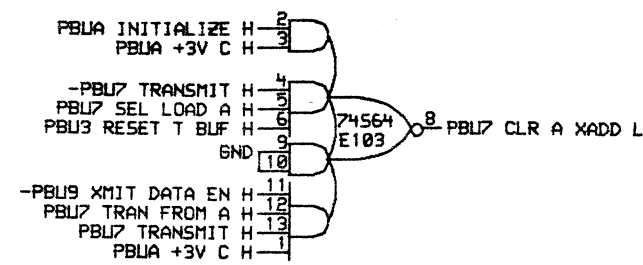
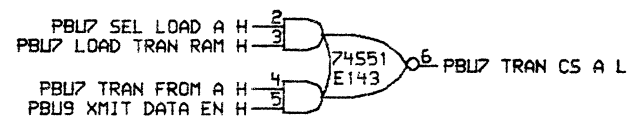
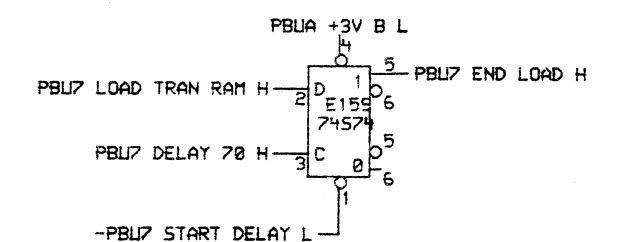
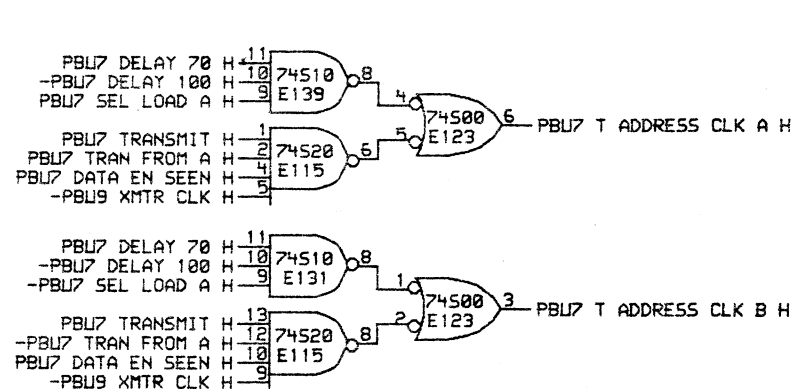
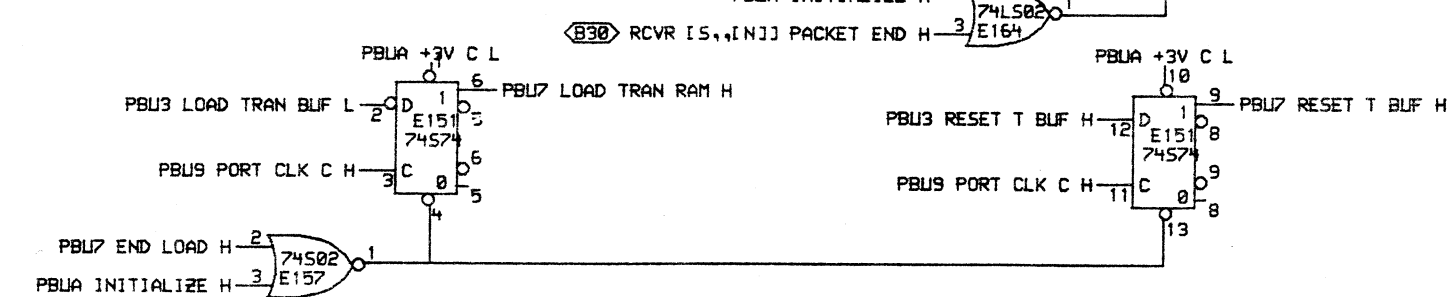
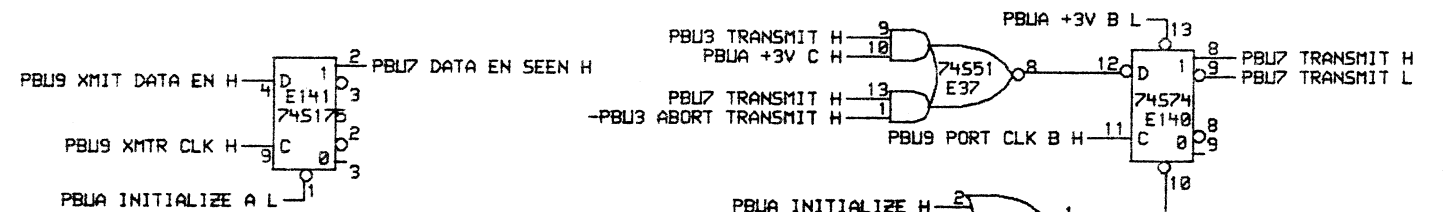
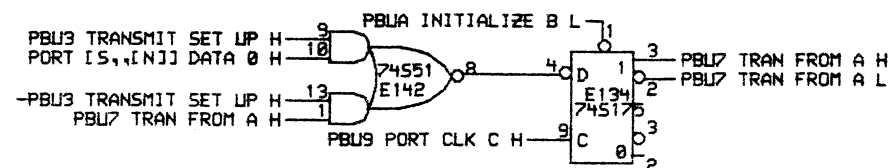
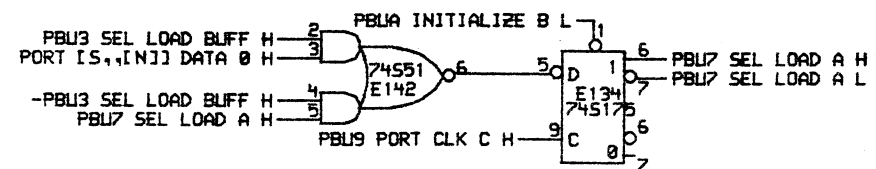
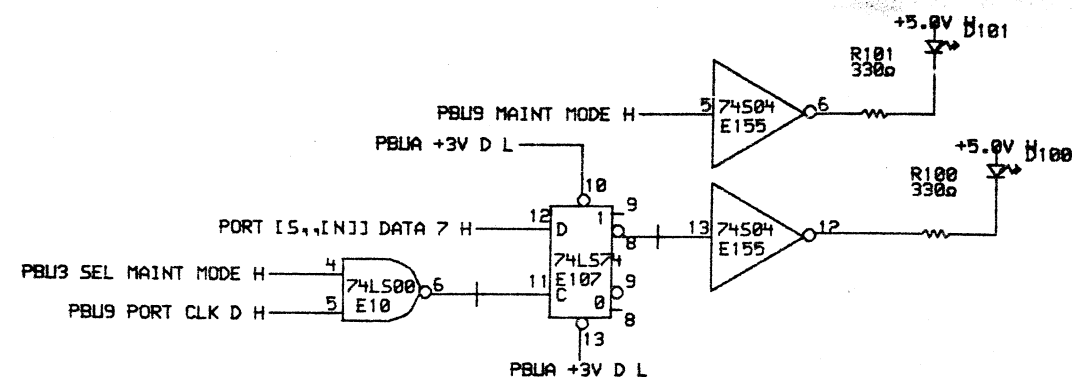




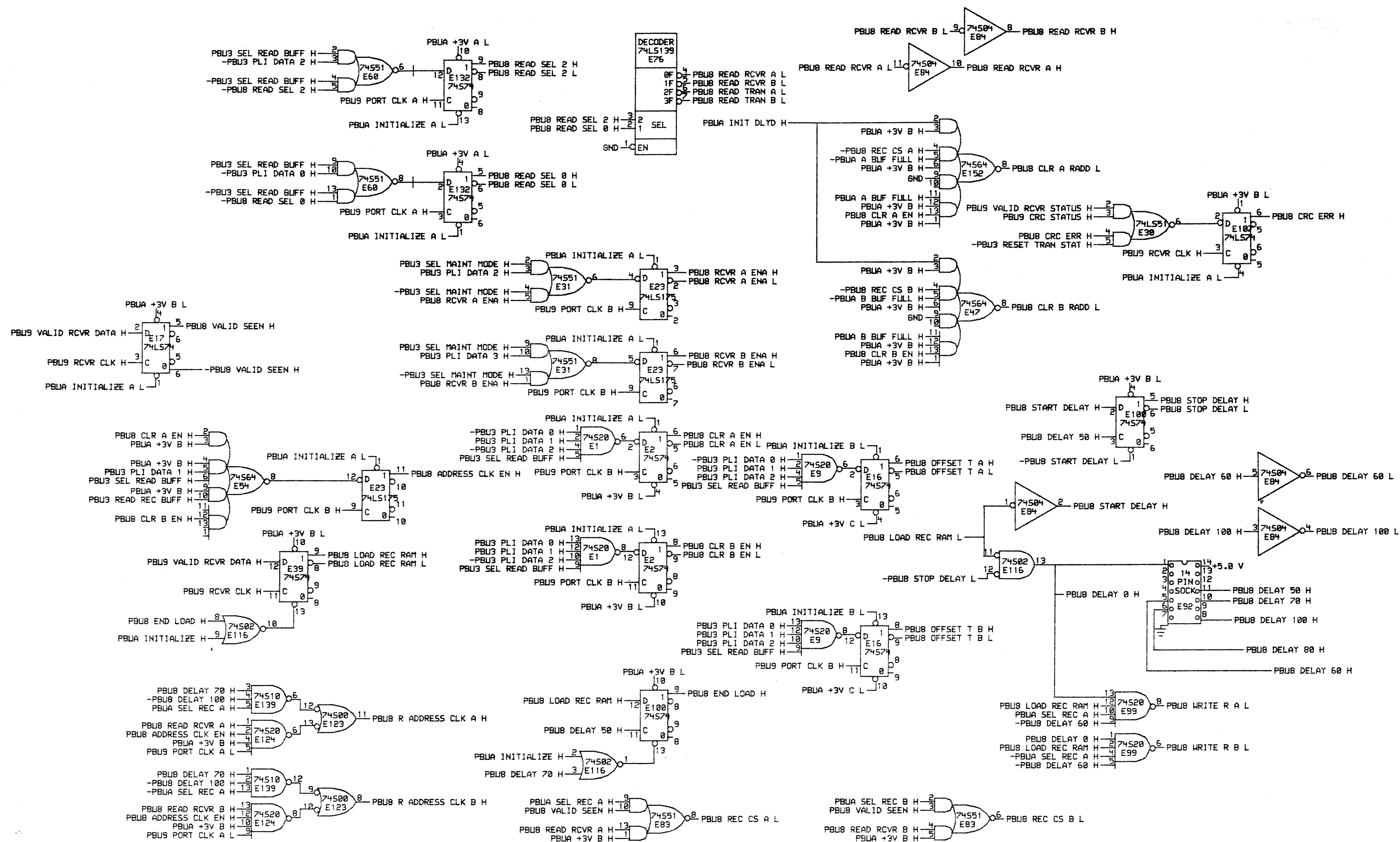
RECEIVE PACKET COMPARATORS



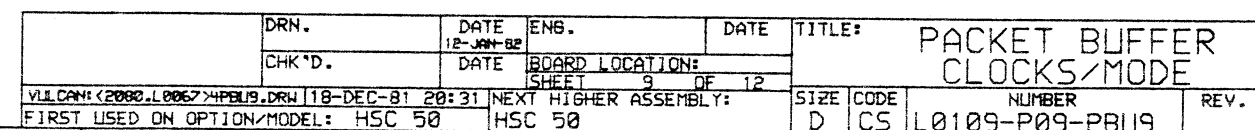
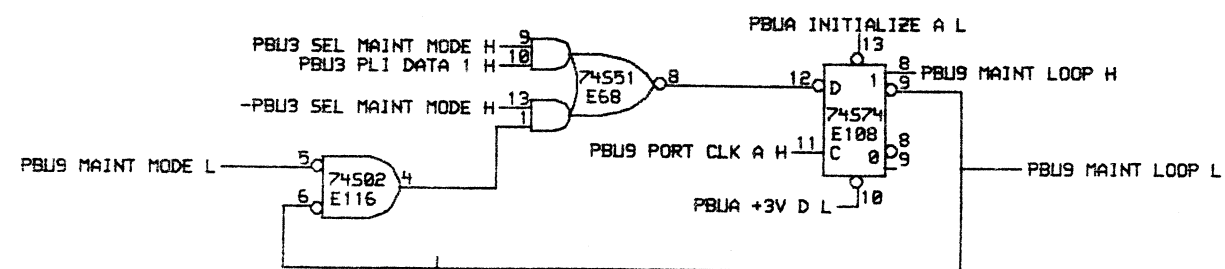
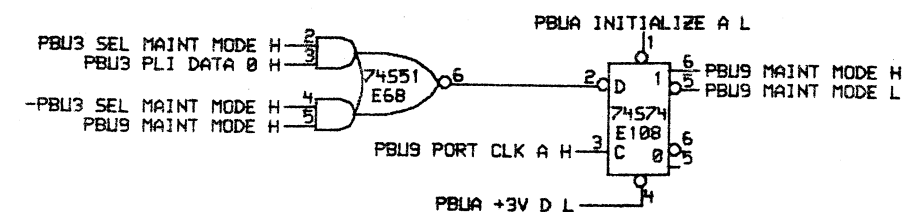


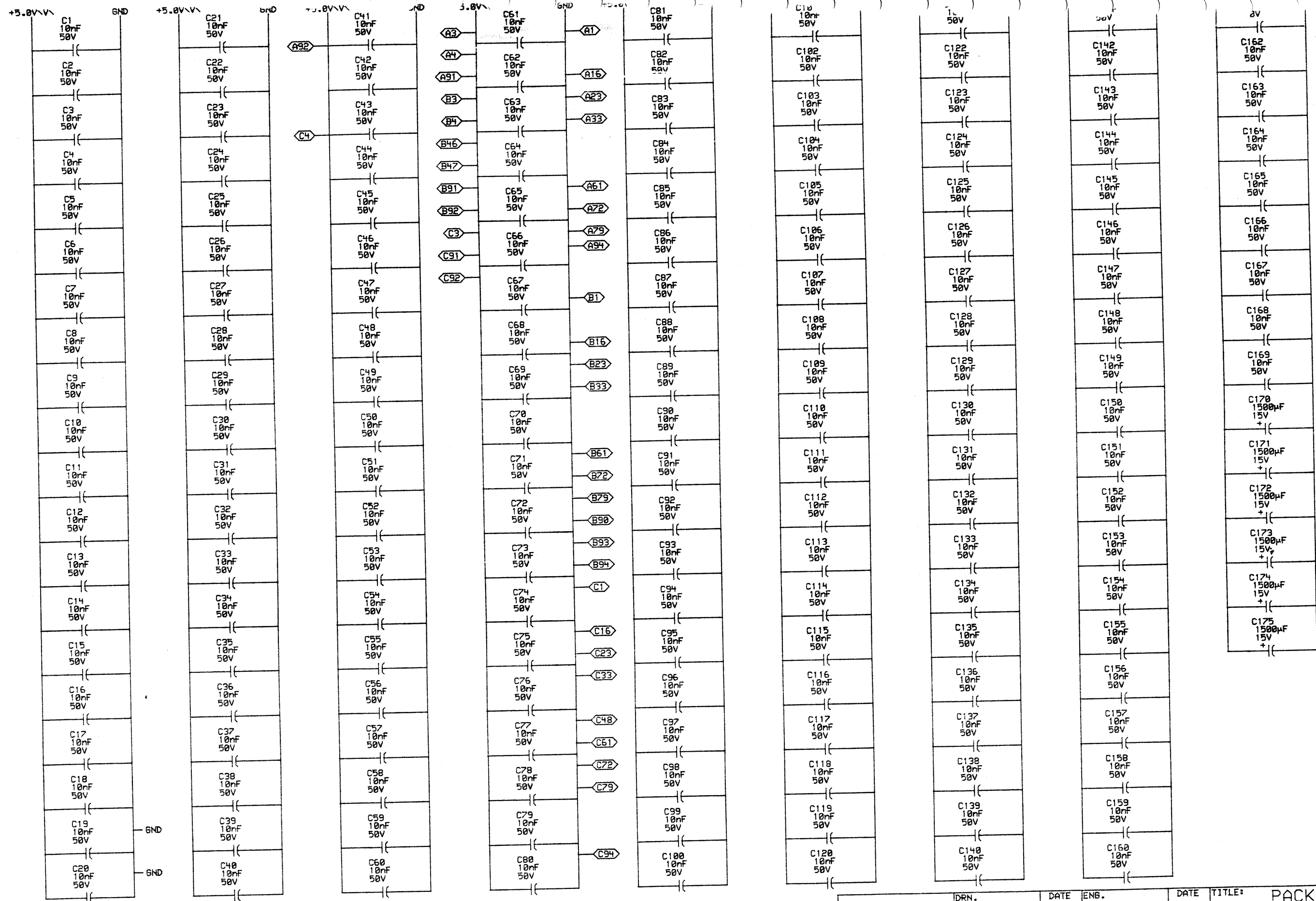


DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		PACKET BUFFER
VLCAN: C2000.L0067-HPBU7.DRW	18-DEC-81	20:27	NEXT HIGHER ASSEMBLY:	TRANSMIT CONTROL
FIRST USED ON OPTION/MODEL:	HSC 50	HSC 50	SIZE	CODE
			D	CS
			NUMBER	REV.
			L0109-P07-PBU7	



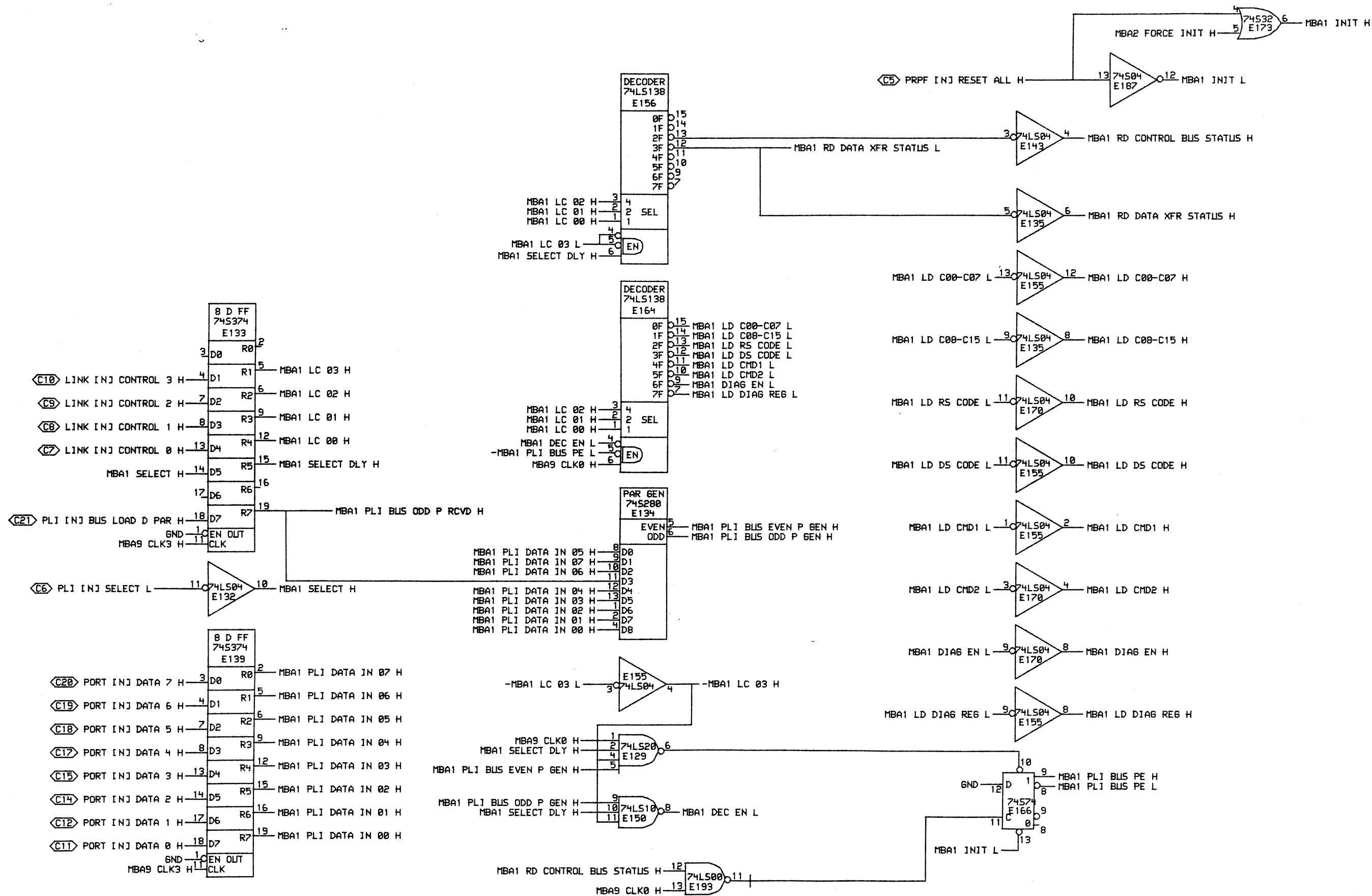
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		PACKET BUFFER
VULCAN (2000.L0067) PBU8.DRW 105-AUG-81 11:36	NEXT HIGHER ASSEMBLY:	SIZE	CODE	NUMBER
FIRST USED ON OPTION/MODEL: HSC 50	HSC 50	D	CS	L0109-P08-PBU8
				REV.

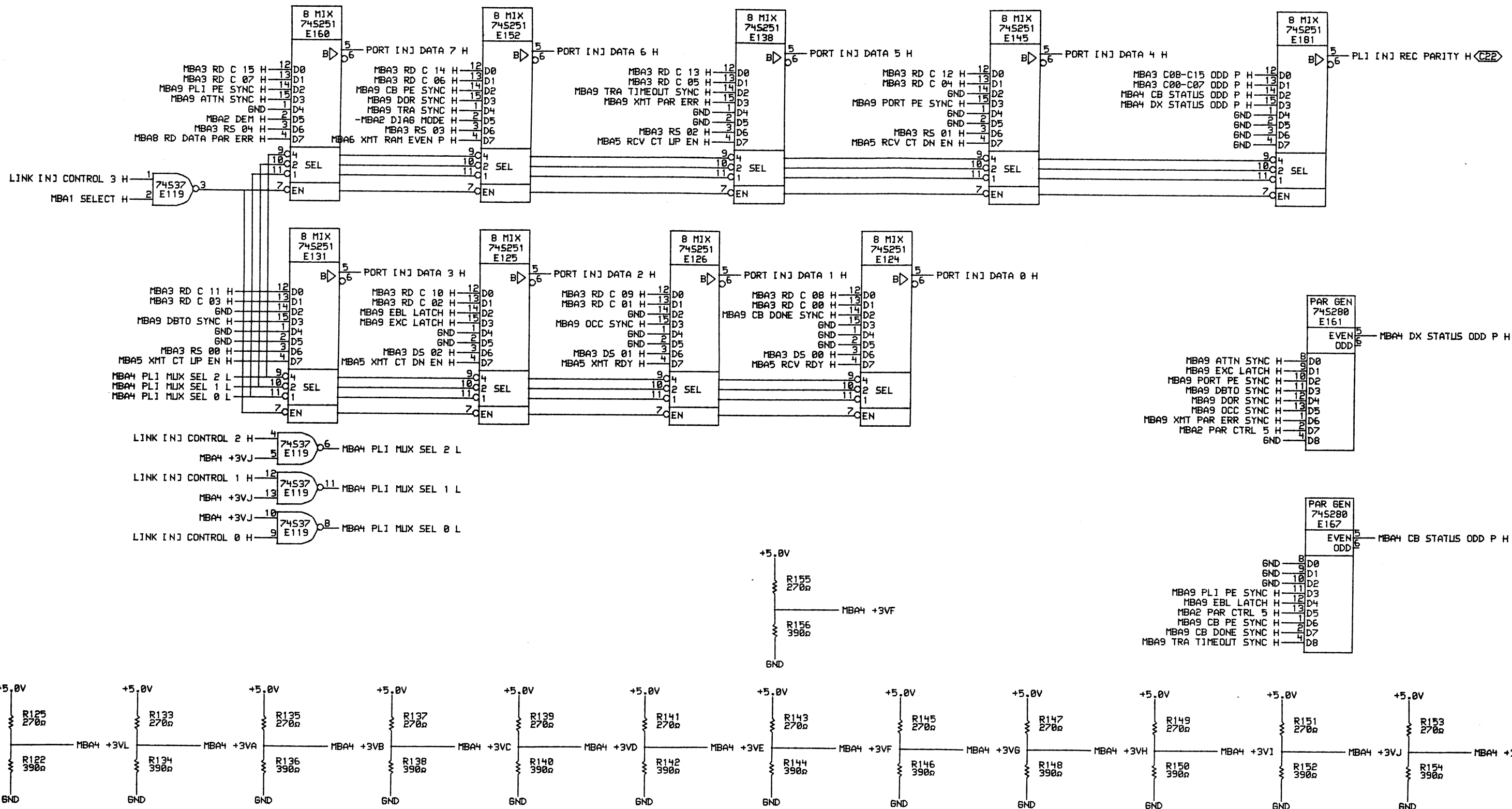




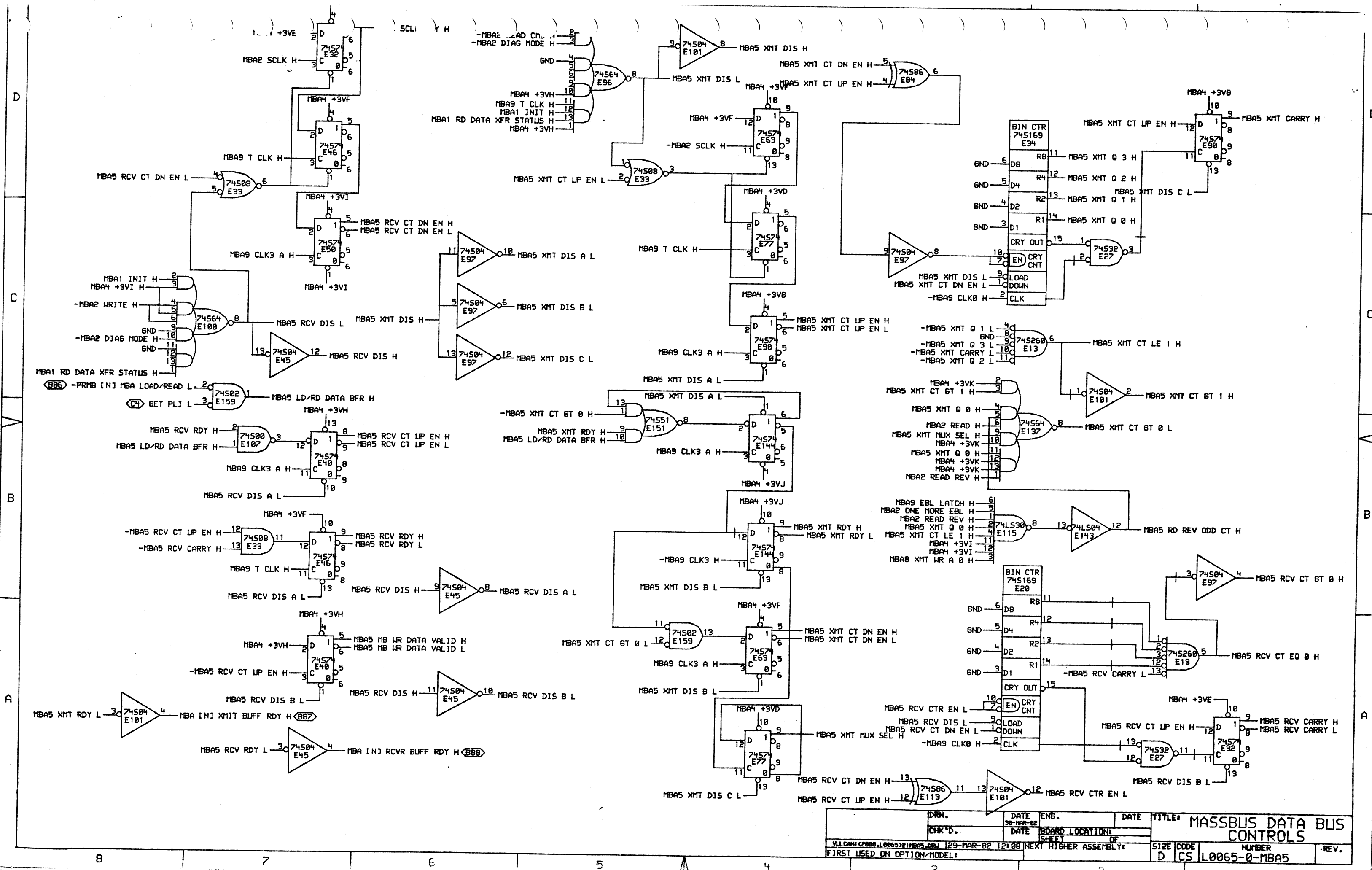
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF 12
VULCAN (2000.L0067)PBLJ.DRW 05-AUG-81 11:38 NEXT HIGHER ASSEMBLY:				SIZE CODE NUMBER REV.
FIRST USED ON OPTION/MODEL: HSC 50 HSC 50				D CS L0109-P11-PBLJ

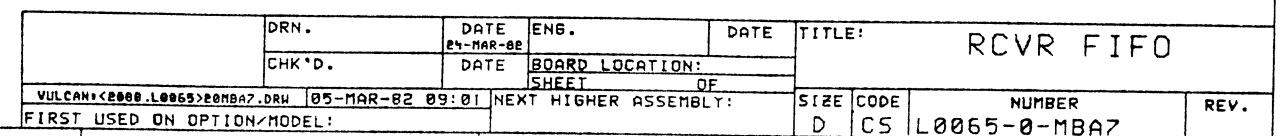
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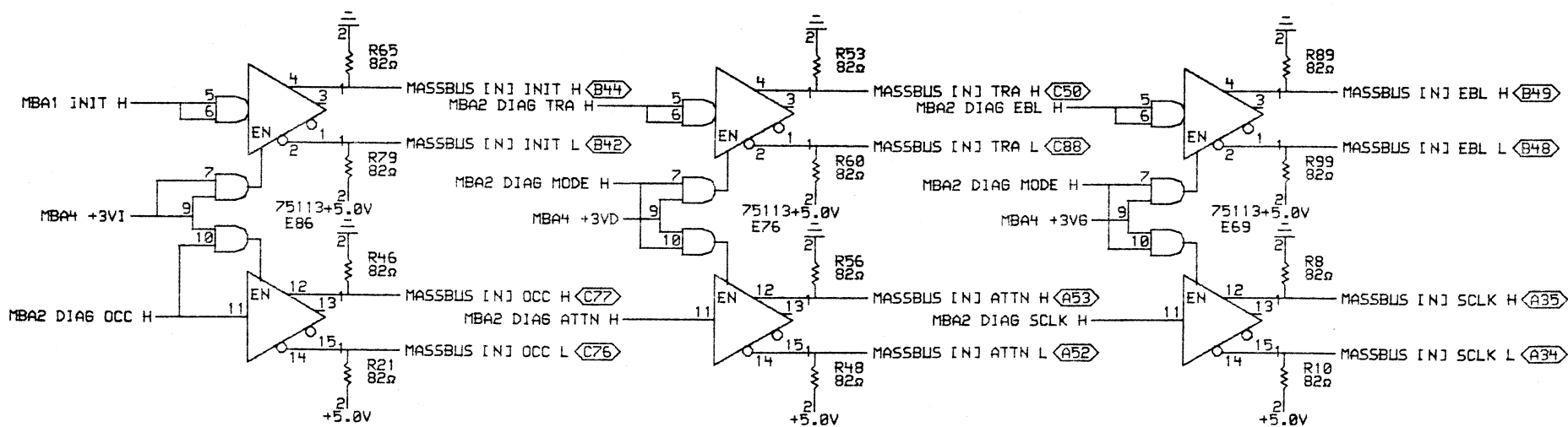
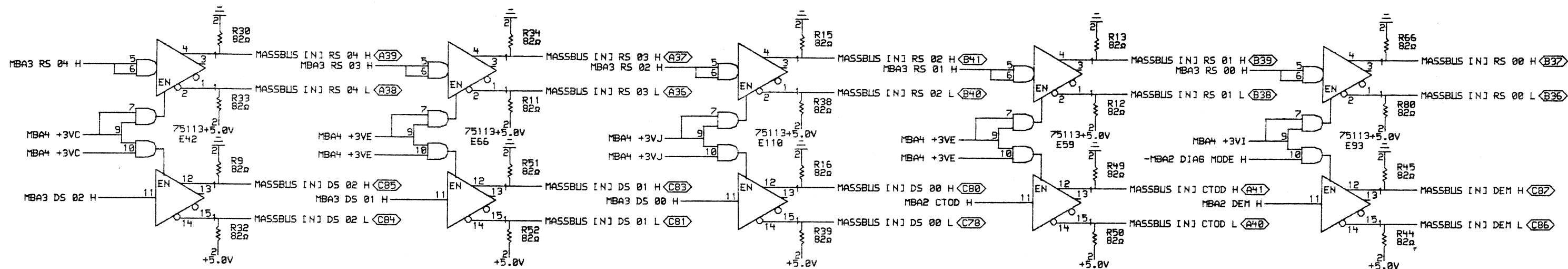
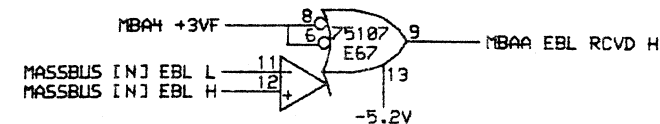
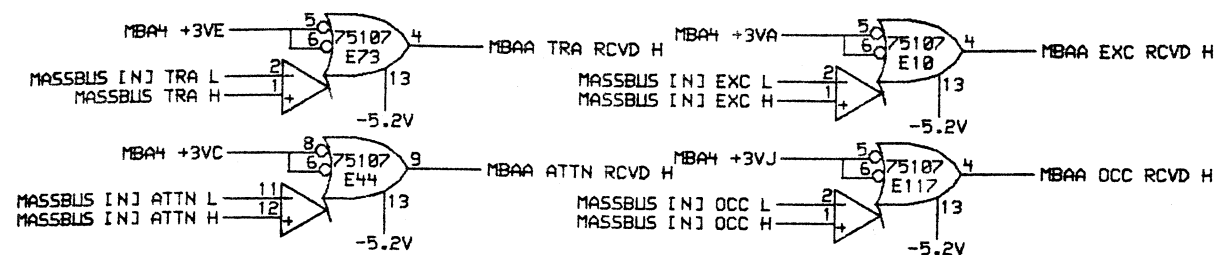




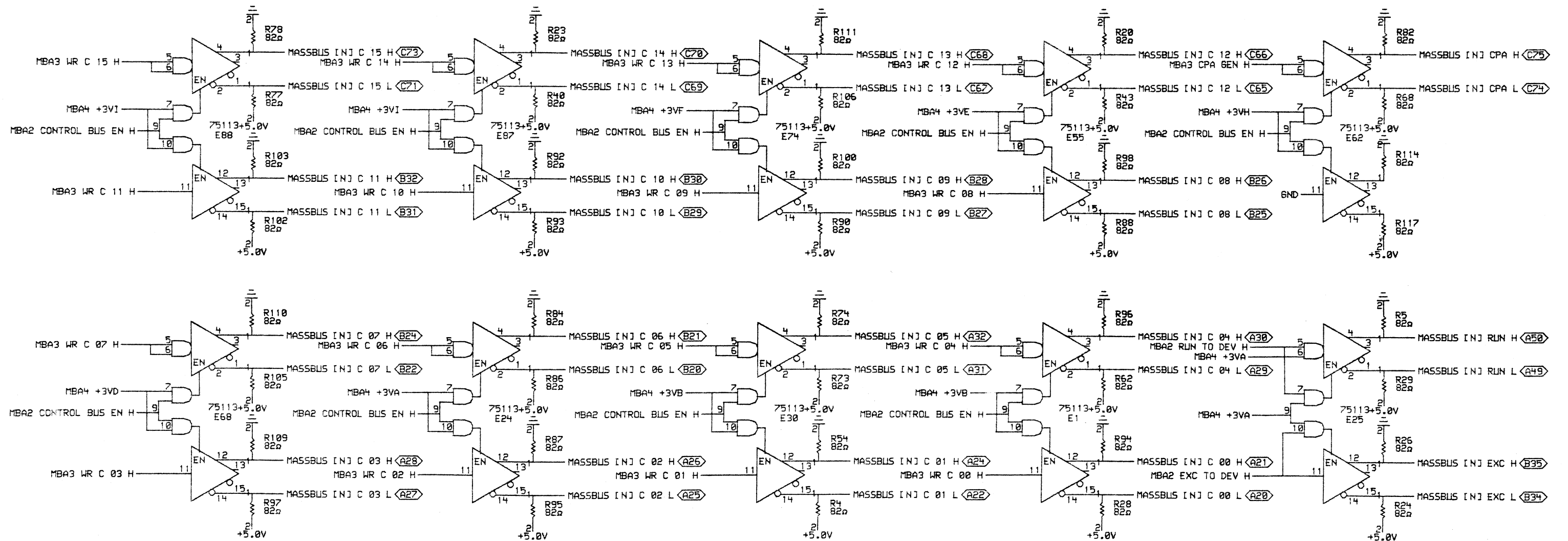
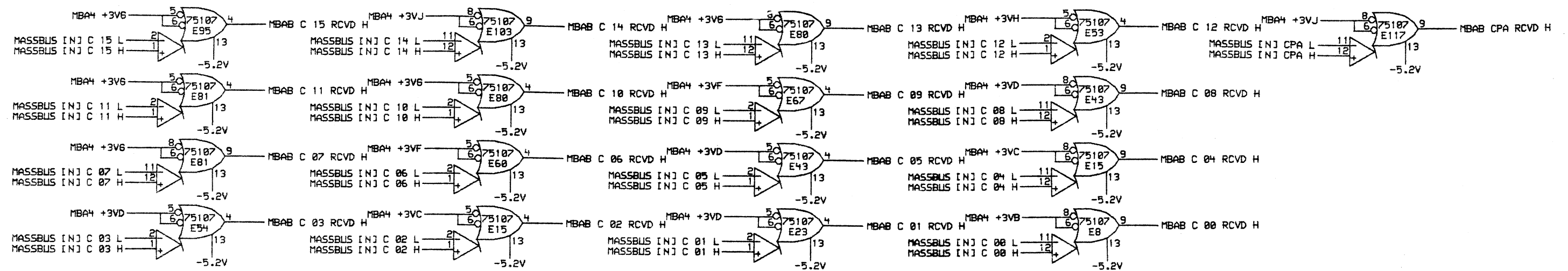
DRN.	DATE	ENG.	DATE	TITLE:	PLI BUS MUX		
CHR'D.	DATE	BOARD LOCATION:	DATE	SIZE	CODE	NUMBER	REV.
VLCAN (P000-L0065) 17MAY-01	112-FEB-82	16:31	NEXT HIGHER ASSEMBLY:	D	C5	L0065-0-MBA4	
FIRST USED ON OPTION/MODEL:							



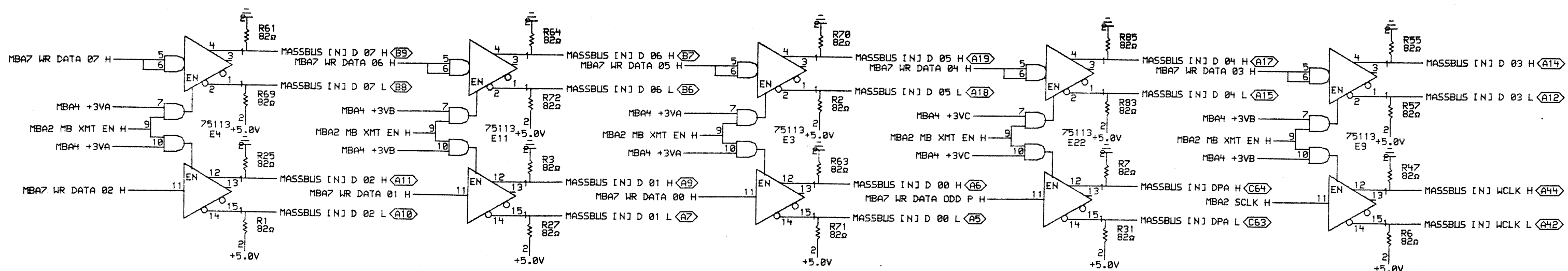
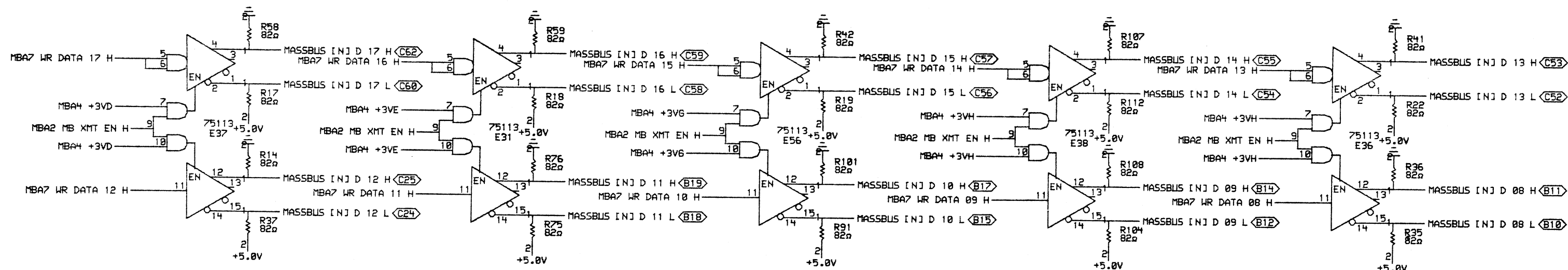
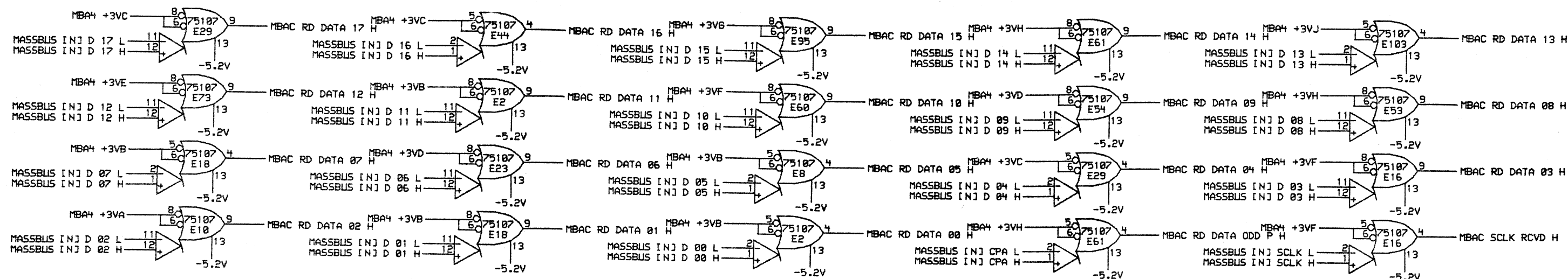




DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		RS, DS DRIVERS
		SHEET	OF	DIAG DRIVERS
VLCAN: (2000-L0065) 12MBAA.DRW	28-DEC-81 11:05	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL:			D	C5
			NUMBER	REV.
			L0065-0-MBAA	

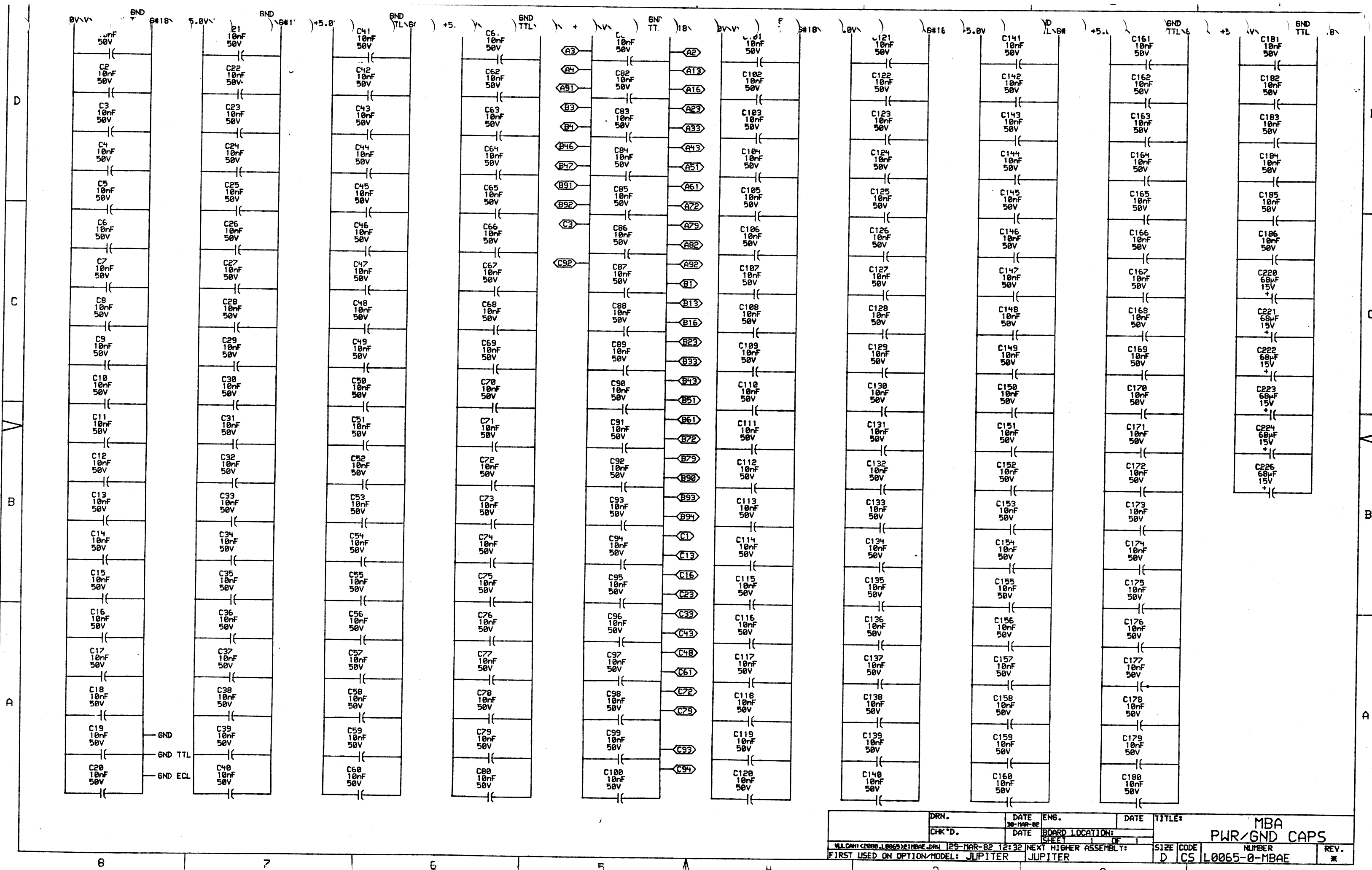


DRN.	DATE	ENG.	DATE	TITLE:	
CHK'D.	DATE	BOARD LOCATION:		CONTROL BUS	
VUL CAN: ZP000.L0065/121000.DRW		SHEET OF		SIZE	CODE
FIRST USED ON OPTION/MODEL:		NEXT HIGHER ASSEMBLY:		D	C5
28-DEC-81 11:09				NUMBER	
				L0065-0-MBAB	
				REV.	



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		DATA BUS
		SHEET	OF	DRIVERS/RECEIVERS
VULCANI (2000.L0065)121MBAC.DRW	28-DEC-81 11:14	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL:			D	CS
			NUMBER	REV.
			L0065-0-MBAC	

	DRN.	DATE	ENG.	DATE	TITLE:				
	CHK'D.	DATE	BOARD LOCATION:						
			SHEET OF						
VULCAN# 2000.L0065*MBAD.DRW			08-DEC-81 21:07		SIZE		CODE	NUMBER	REV.
FIRST USED ON OPTION/MODEL:			NEXT HIGHER ASSEMBLY:		D		CS	L0065-0-MBAD	



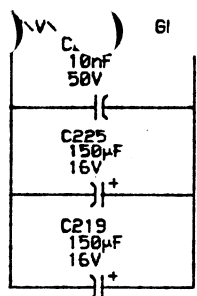
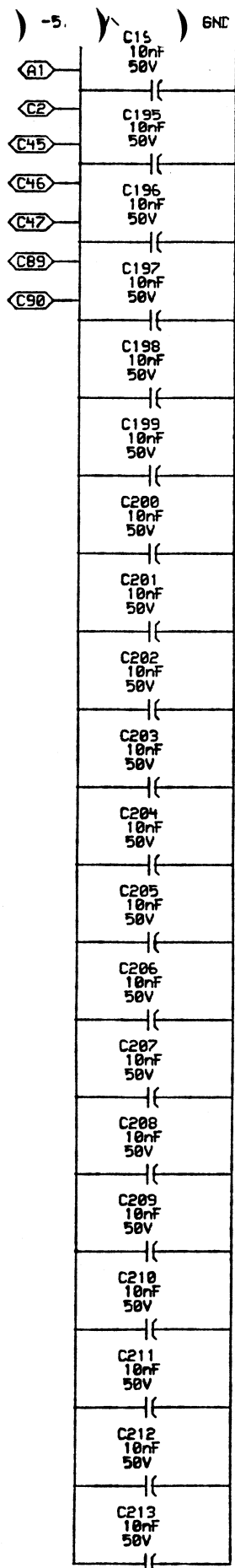
D

C

B

A

A45-NC	A88-NC	C40-NC
A46-NC	A89-NC	C41-NC
A47-NC	A90-NC	C42-NC
A48-NC	A93-NC	C44-NC
A54-NC	A94-NC	C49-NC
A55-NC	B2-NC	
A56-NC	B5-NC	
A57-NC	B45-NC	
A58-NC	B50-NC	
A59-NC	B52-NC	
A60-NC	B53-NC	
A63-NC	B54-NC	
A64-NC	B55-NC	
A65-NC	B56-NC	
A66-NC	B57-NC	
A67-NC	B58-NC	
A68-NC	B59-NC	
A69-NC	B80-NC	
A70-NC	B82-NC	
A71-NC		
A73-NC		
A74-NC	C28-NC	
A75-NC	C29-NC	
A76-NC	C30-NC	
A77-NC	C31-NC	
A78-NC	C32-NC	
A80-NC	C34-NC	
A81-NC	C35-NC	
A83-NC	C36-NC	
A84-NC	C37-NC	
A85-NC	C38-NC	
A87-NC	C39-NC	



GND ECLIB, 6#19, J

DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		MBA
FIRST USED ON OPTION/MODEL: JUPITER				PWR/GND CAPS
NEXT HIGHER ASSEMBLY: JUPITER				NUMBER
SIZE	CODE	NUMBER		REV.
D	CS	L0065-0-MBAF		*