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VULCAN:<2080.L0057>24MAP2
VULCAN:<2080.L0057>24MAP3
VULCAN:<2080.L0057>24MAP4
VULCAN:<2080.L0057>43MAP5
VULCAN:<2080.L0057>24MAP6
VULCAN:<2080.L0057>54MAP7
VULCAN:<2080.L0057>24MAP8
VULCAN:<2080.L0057>24MAP9
VULCAN:<2080.L0057>24MAPA
VULCAN:<2080.L0057>24MAPB
VULCAN:<2080.L0057>53MAPC
VULCAN:<2080.L0057>54MAPD
VULCAN:<2080.L0057>24MAPE
VULCAN:<2080.L0057>24MAPF
VULCAN:<2080.L0057>24MAPG
VULCAN:<2080.L0057>51MAPH
VULCAN:<2080.L0057>52MAPI
VULCAN:<2080.L0057>50MAPJ
VULCAN:<2080.L0057>49MAPK
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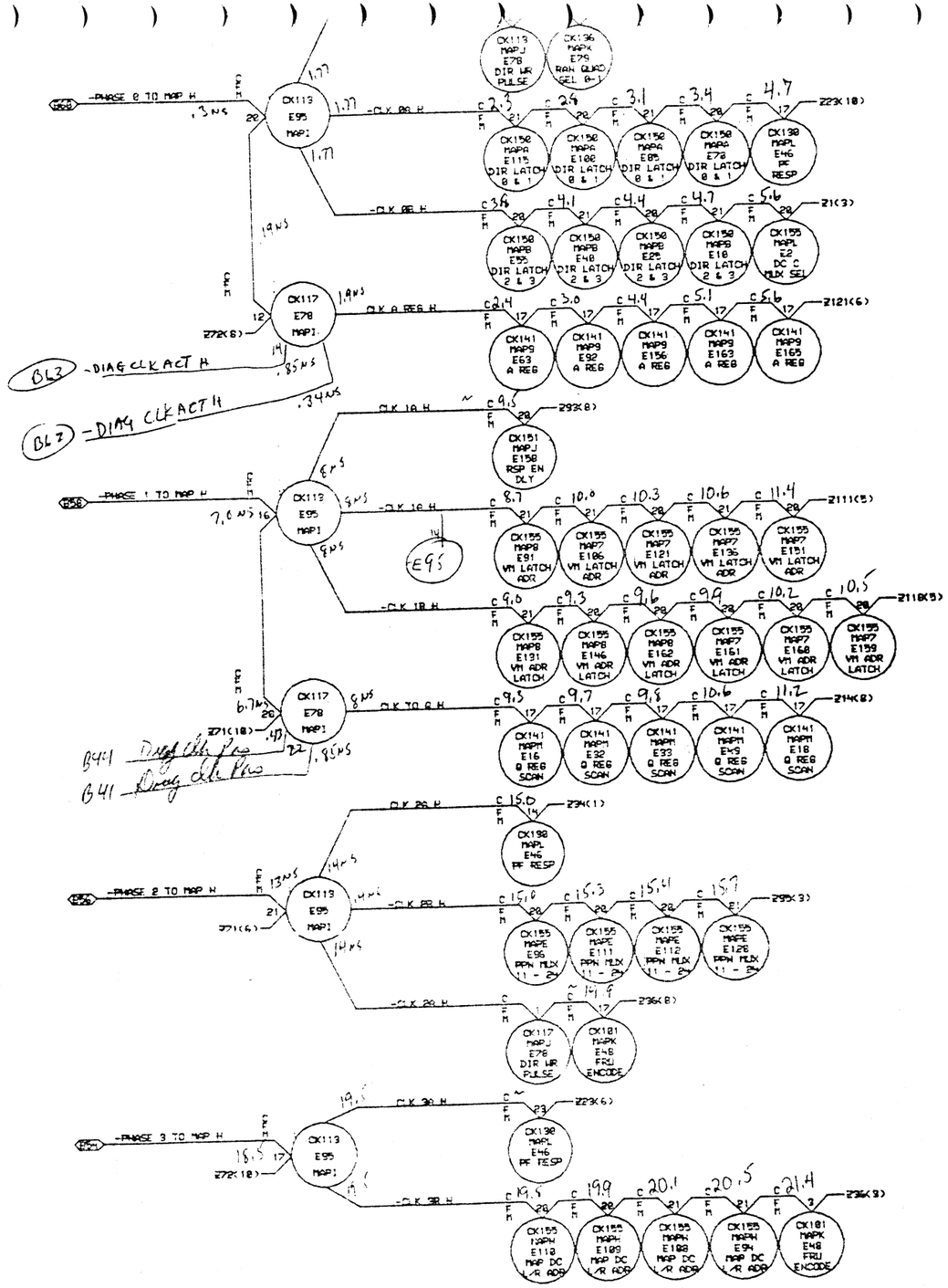
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E162 E147 E132 E117 E102 E087 E072 E057 E042 E027 E012
E161 E146 E131 E116 E101 E086 E071 E056 E041 E026 E011
E160 E145 E130 E115 E100 E085 E070 E055 E040 E025 E010
E159 E144 E129 E114 E099 E084 E069 E054 E039 E024 E009
E158 E143 E128 E113 E098 E083 E068 E053 E038 E023 E008
E157 E142 E127 E112 E097 E082 E067 E052 E037 E022 E007
E156 E141 E126 E111 E096 E081 E066 E051 E036 E021 E006
E155 E140 E125 E110 E095 E080 E065 E050 E035 E020 E005
E154 E139 E124 E109 E094 E079 E064 E049 E034 E019 E004
E153 E138 E123 E108 E093 E078 E063 E048 E033 E018 E003
E152 E137 E122 E107 E092 E077 E062 E047 E032 E017 E002
E151 E136 E121 E106 E091 E076 E061 E046 E031 E016 E001

C122 C121 C120 C119 C118 C117

"165"

MAP 6

Rick
Here we are able to
pull together
Rocky

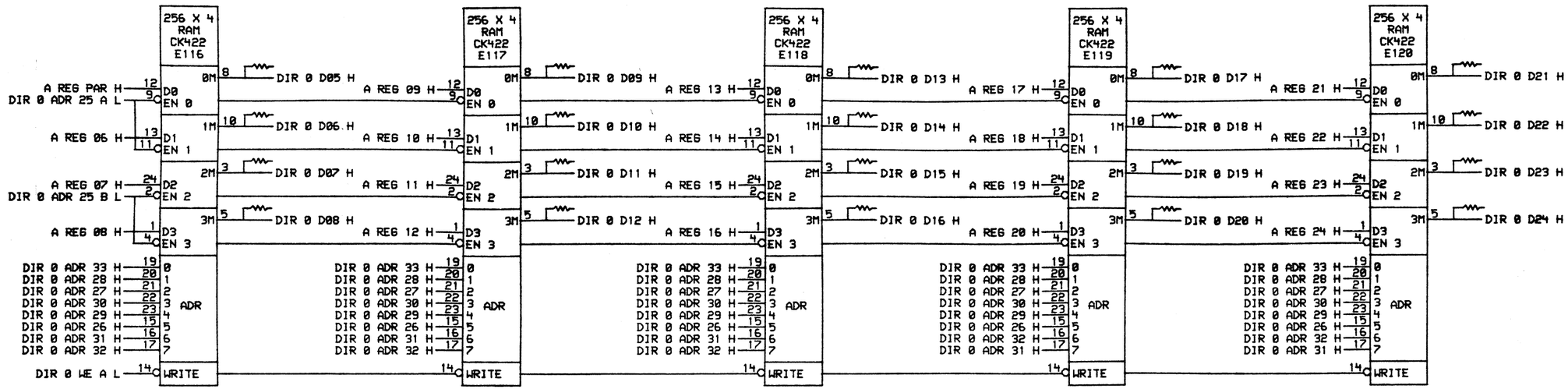


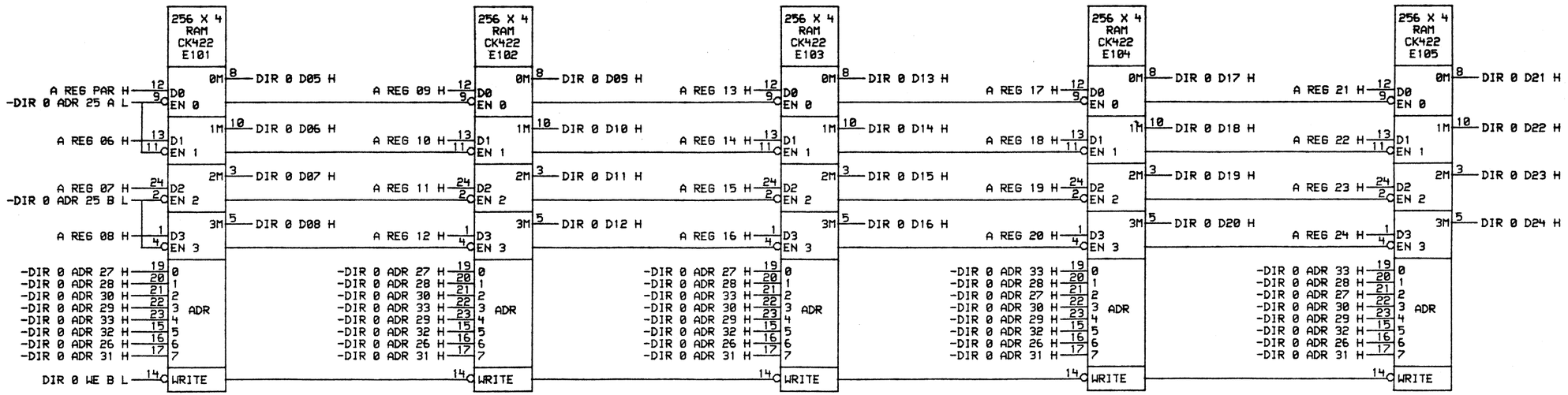
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	CHK'D.
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FIRST USED ON OPTION/MODEL:	

ENG.	DATE	TITLE: MAP	
		SIZE	CODE
		D	05

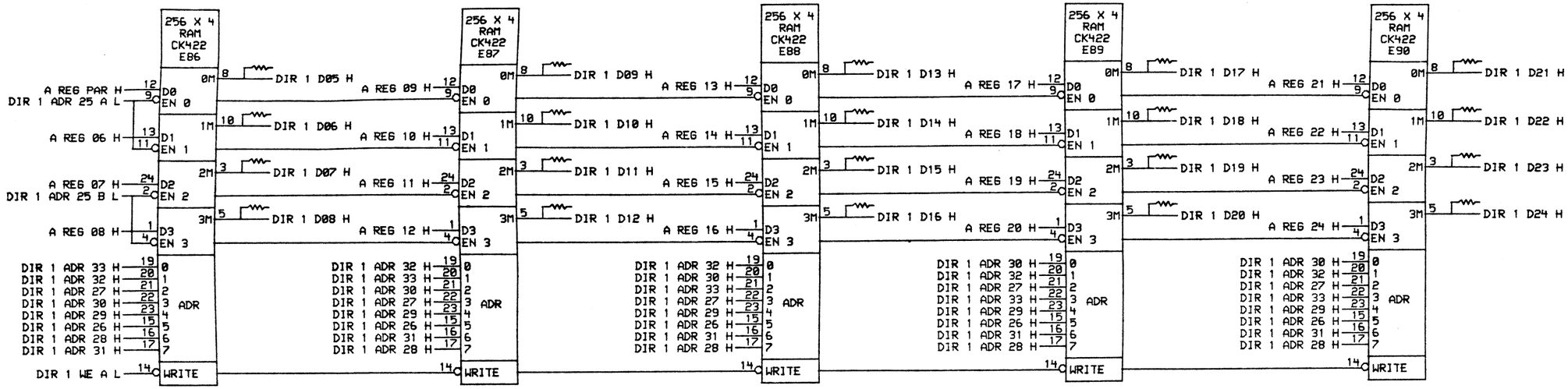
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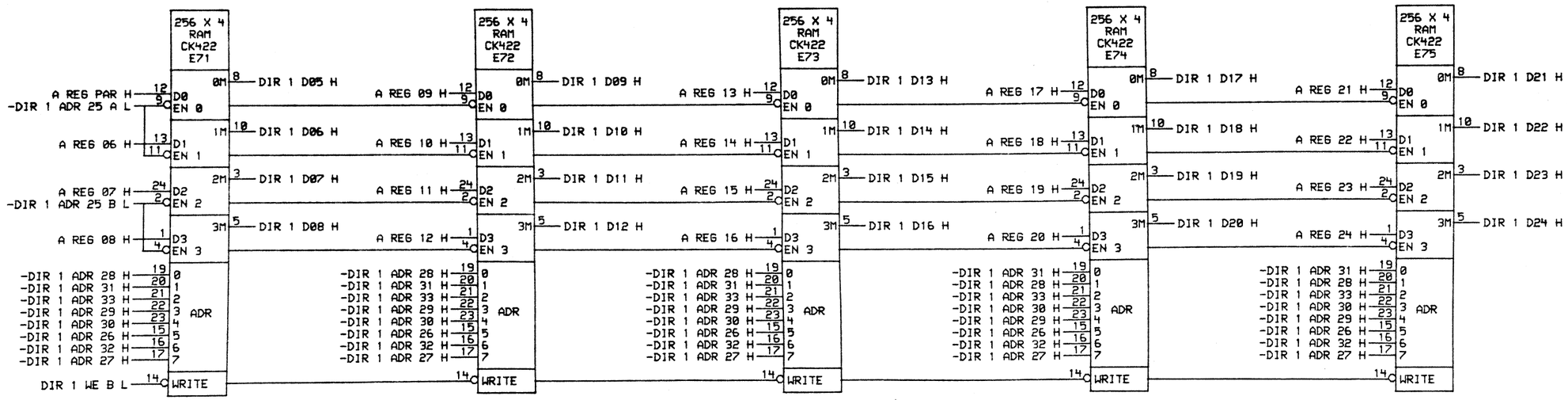
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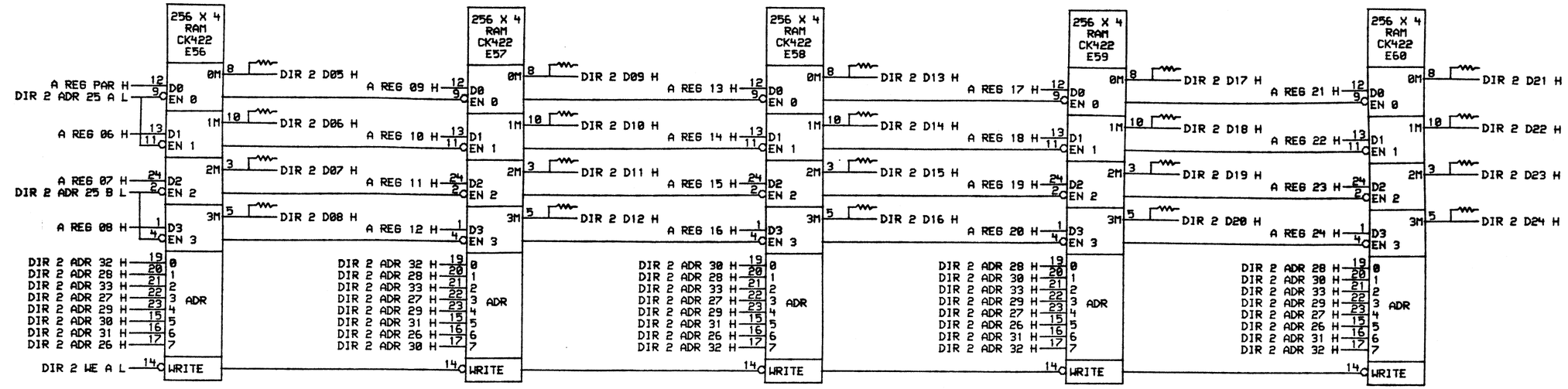


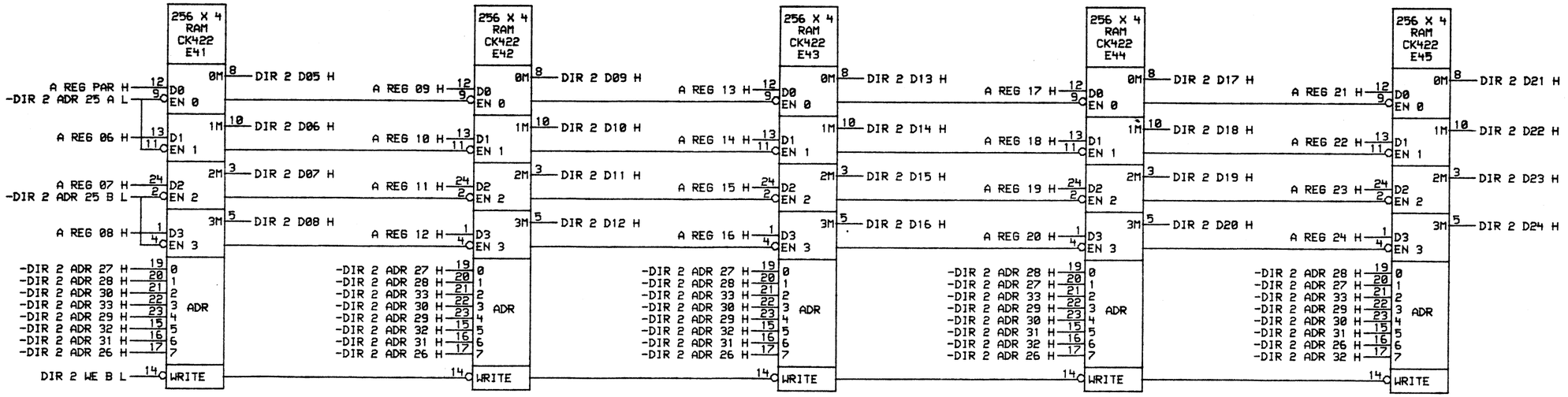
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	CHK'D.	DATE	BOARD LOCATION:		
			SHEET 1 OF 1		
VLI CAN: <2000.L0057>24MAP1.DRW	09-MAR-81 09:00		NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0057-0-MAP1 / REV.
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0057-0		



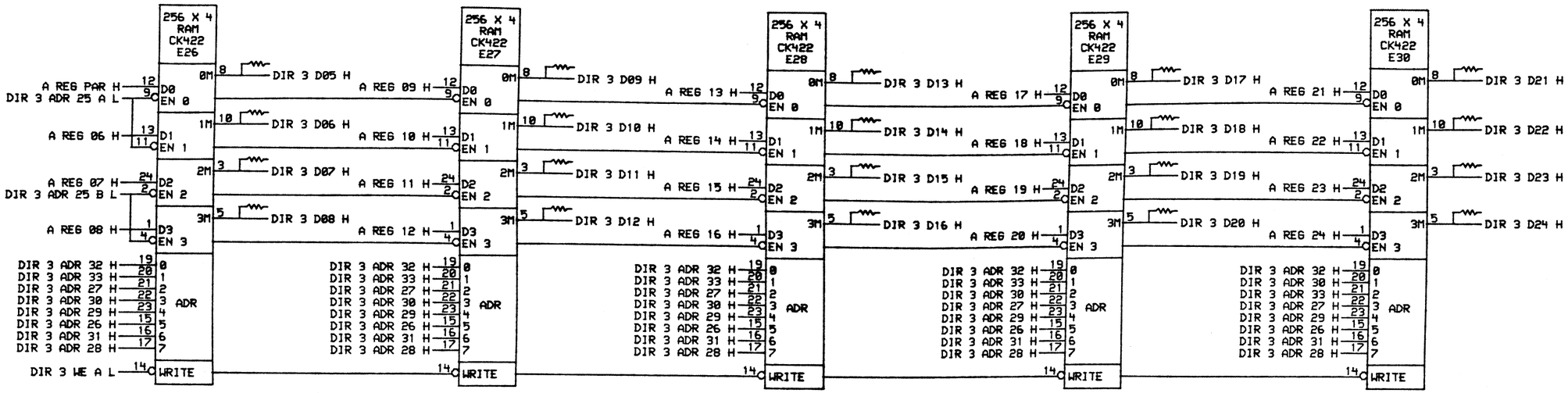


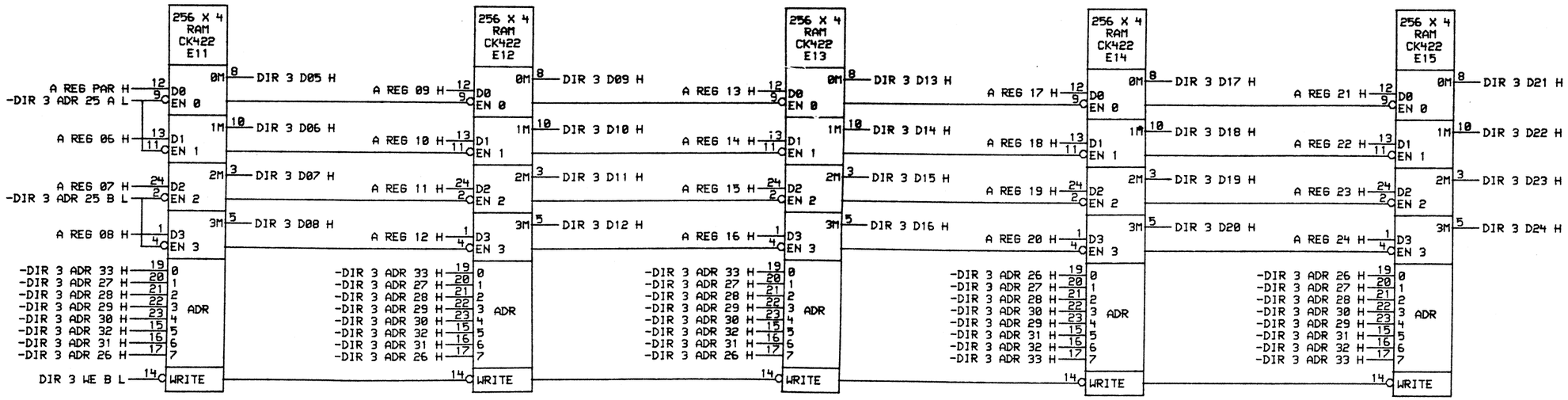
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	CHK'D.	DATE	BOARD LOCATION:		
			SHEET 1 OF 1		
VULCAN:<2000.L0057>24MAP2.DRW 09-MAR-81 09:02			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0057-0-MAP2 / REV.
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0057-0		



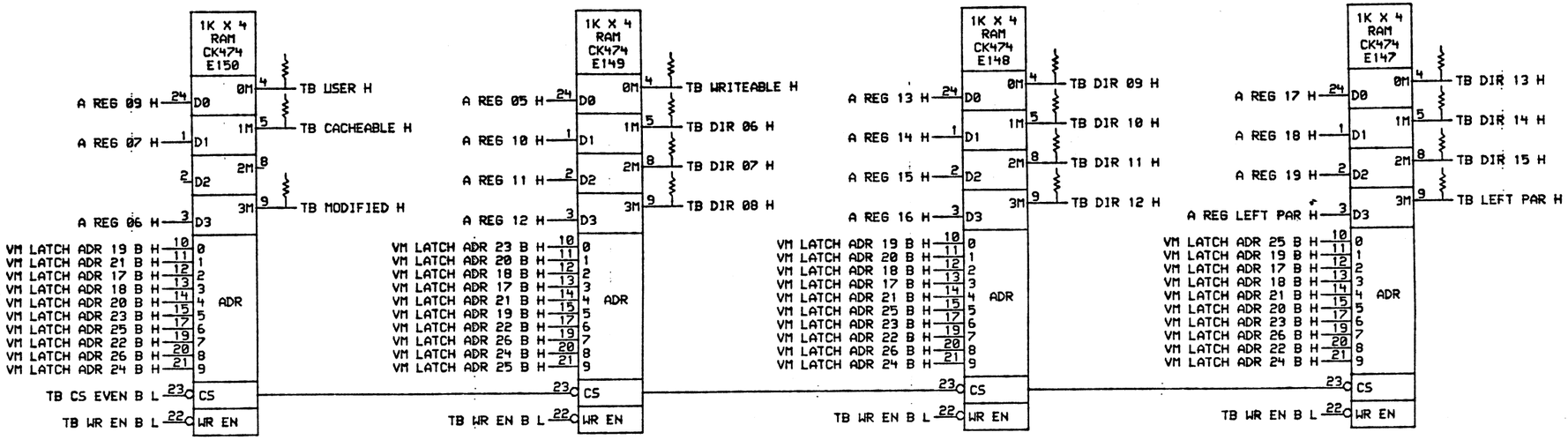


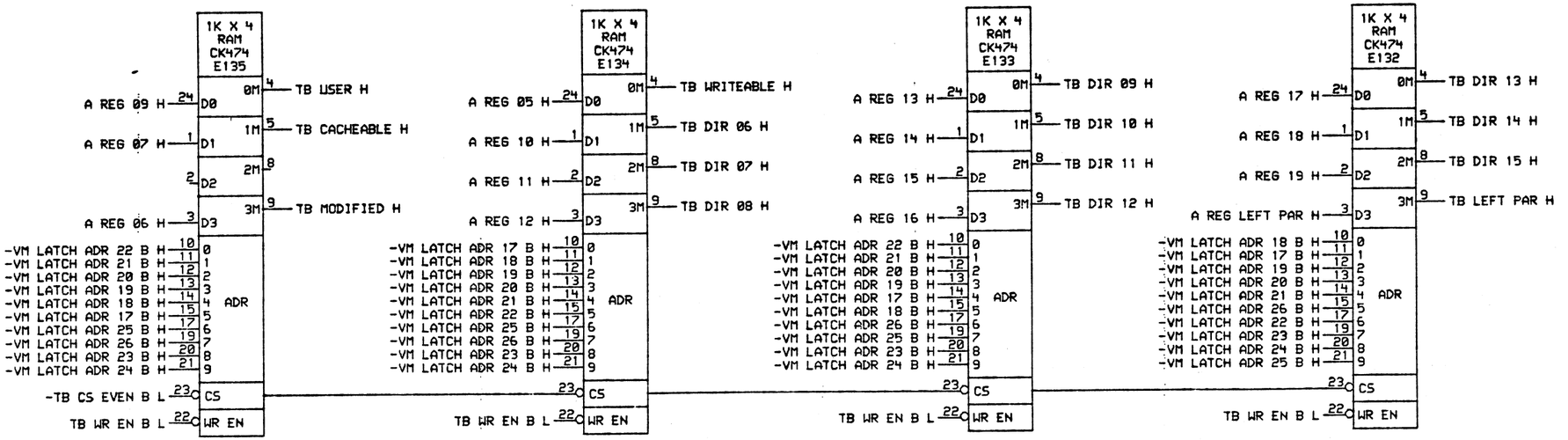
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	CHK'D.	DATE	BOARD LOCATION:		
VULCAN:<2080.L0057>2MMAP3.DRW	09-MAR-81 09:05		SHEET 1 OF 1		
FIRST USED ON OPTION/MODEL: 2080		NEXT HIGHER ASSEMBLY: B-DD-L0057-0		SIZE D	CODE CS
				NUMBER L0057-0-MAP3	REV.





	DRN.	DATE 01-FEB-82	ENG.	DATE	TITLE: CACHE DIRECTORY 3 00-23
	CHK'D.	DATE	BOARD LOCATION: SHEET 1 OF 1		
VULCAN:<2080.L0057>24MAP4.DRW 09-MAR-81 09:06			NEXT HIGHER ASSEMBLY:	SIZE D	CODE CS
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0057-0	NUMBER L0057-0-MAP4	REV.





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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. S. Toomey — DATE 21-JUL-82 — ENG. — DATE
CHK'D. — DATE — BOARD LOCATION: — SHEET 1 OF 1

VULCAN:<2080,L0057>43MAP5.DRW 20-JUL-82 16:21 — NEXT HIGHER ASSEMBLY: B-DD-L0057-0
FIRST USED ON OPTION/MODEL: 2080

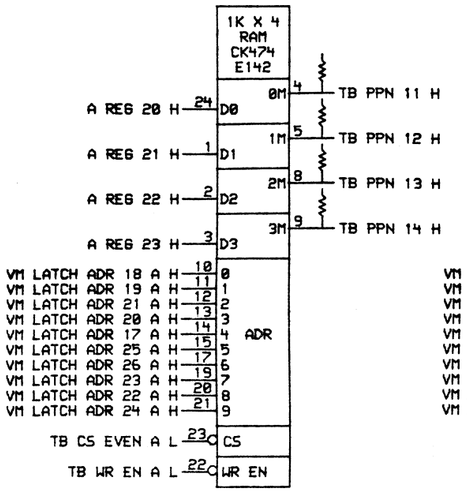
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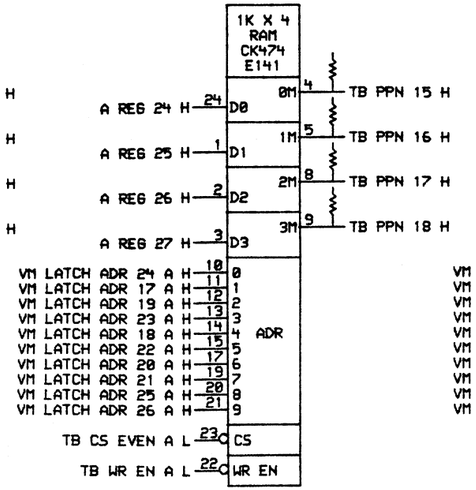
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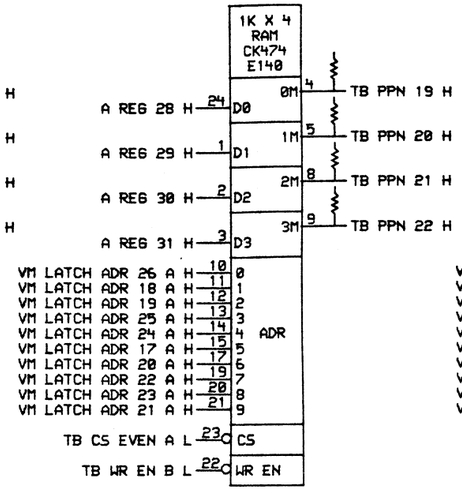
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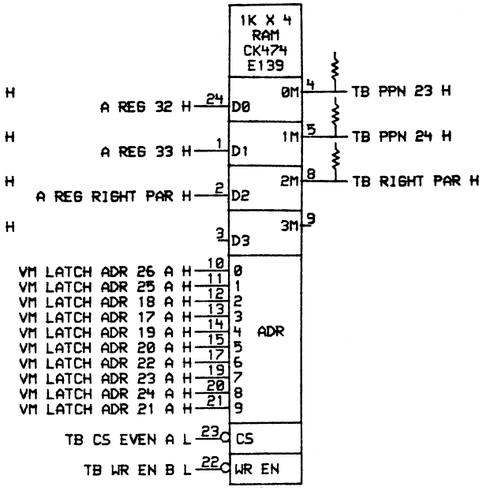
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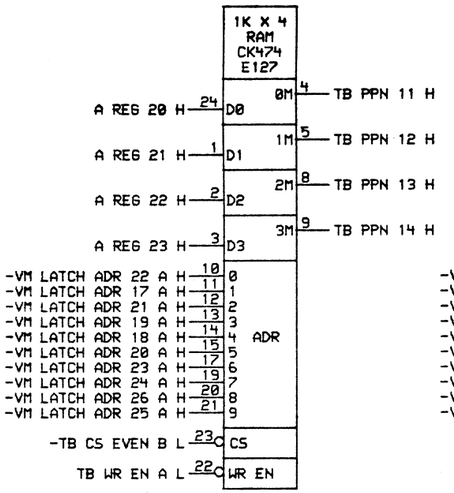
D C B A

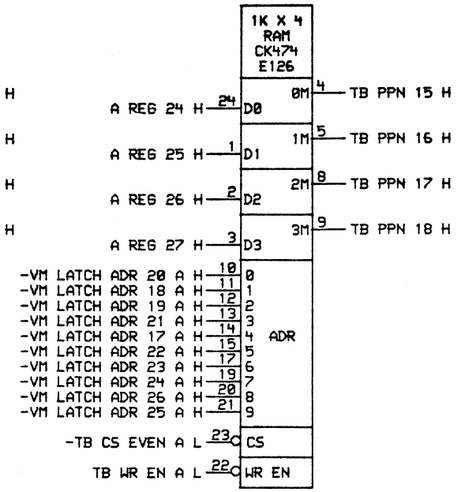


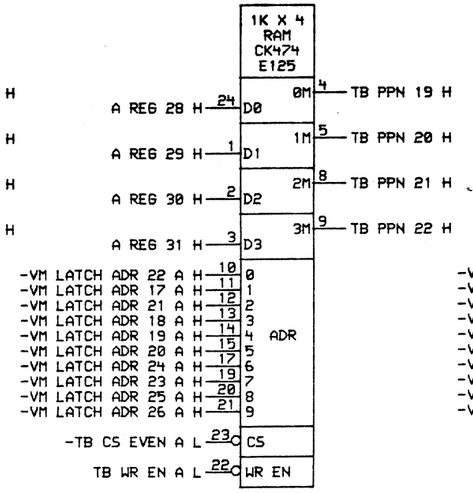


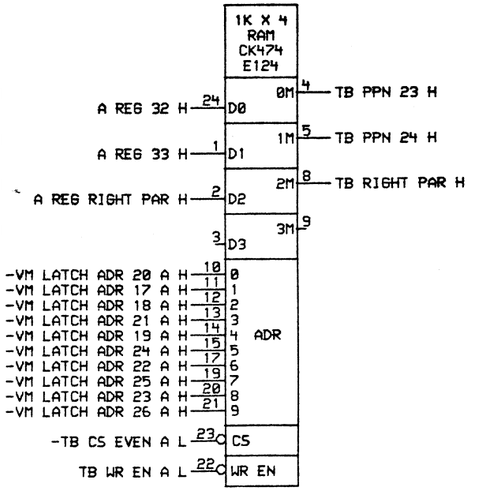








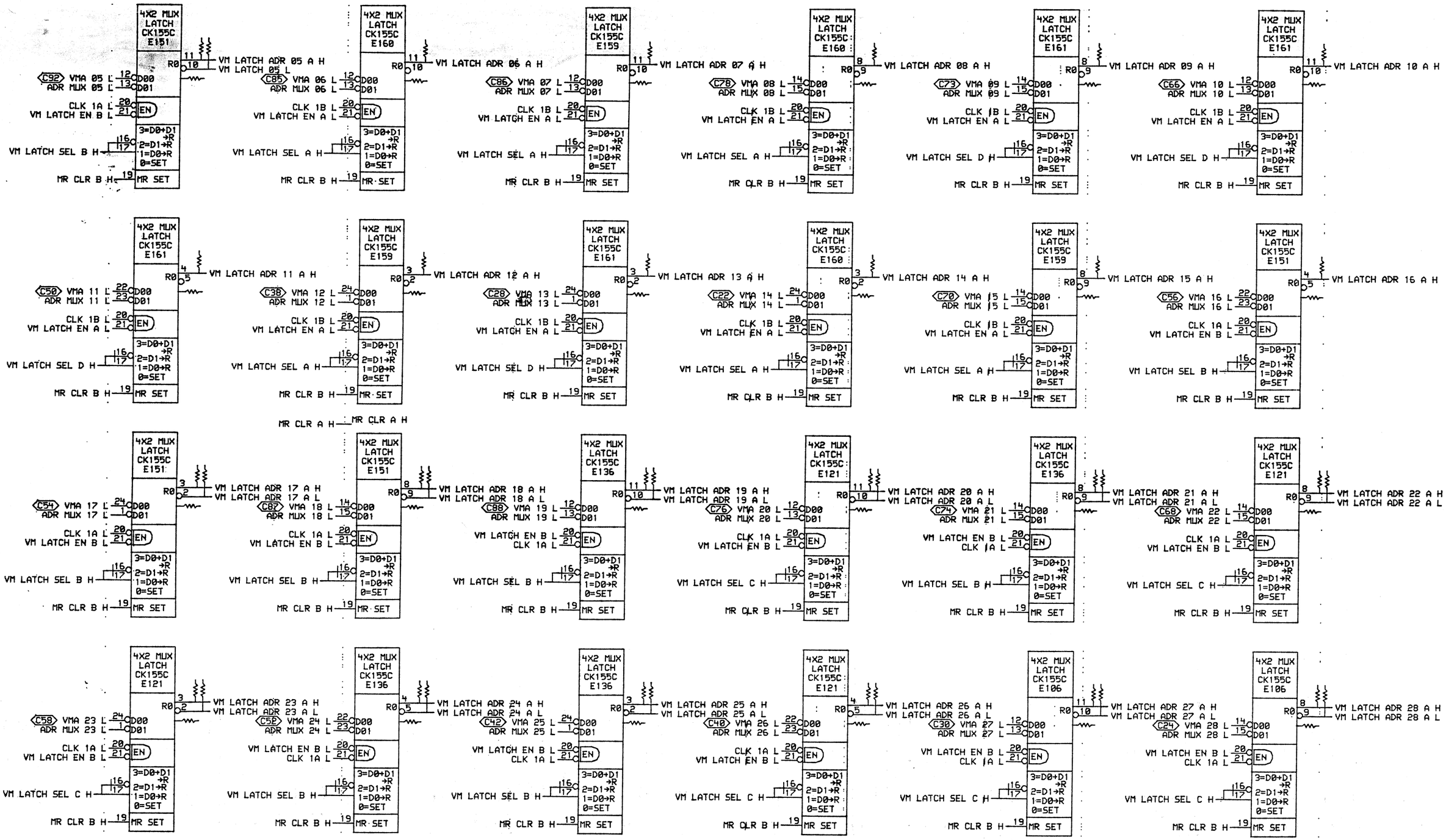




	DRN.	DATE 01-FEB-82	ENG.	DATE	TITLE: TB PPN 11-24			
	CHK'D.	DATE	BOARD LOCATION: SHEET 1 OF 1					
VULCAN:<2080,L0057>2M-MAP6.DRW 09-MAR-81 09:13			NEXT HIGHER ASSEMBLY:		SIZE D	CODE CS	NUMBER L0057-0-MAP6	REV.
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0057-0					

8 7 6 5 4 3 2 1

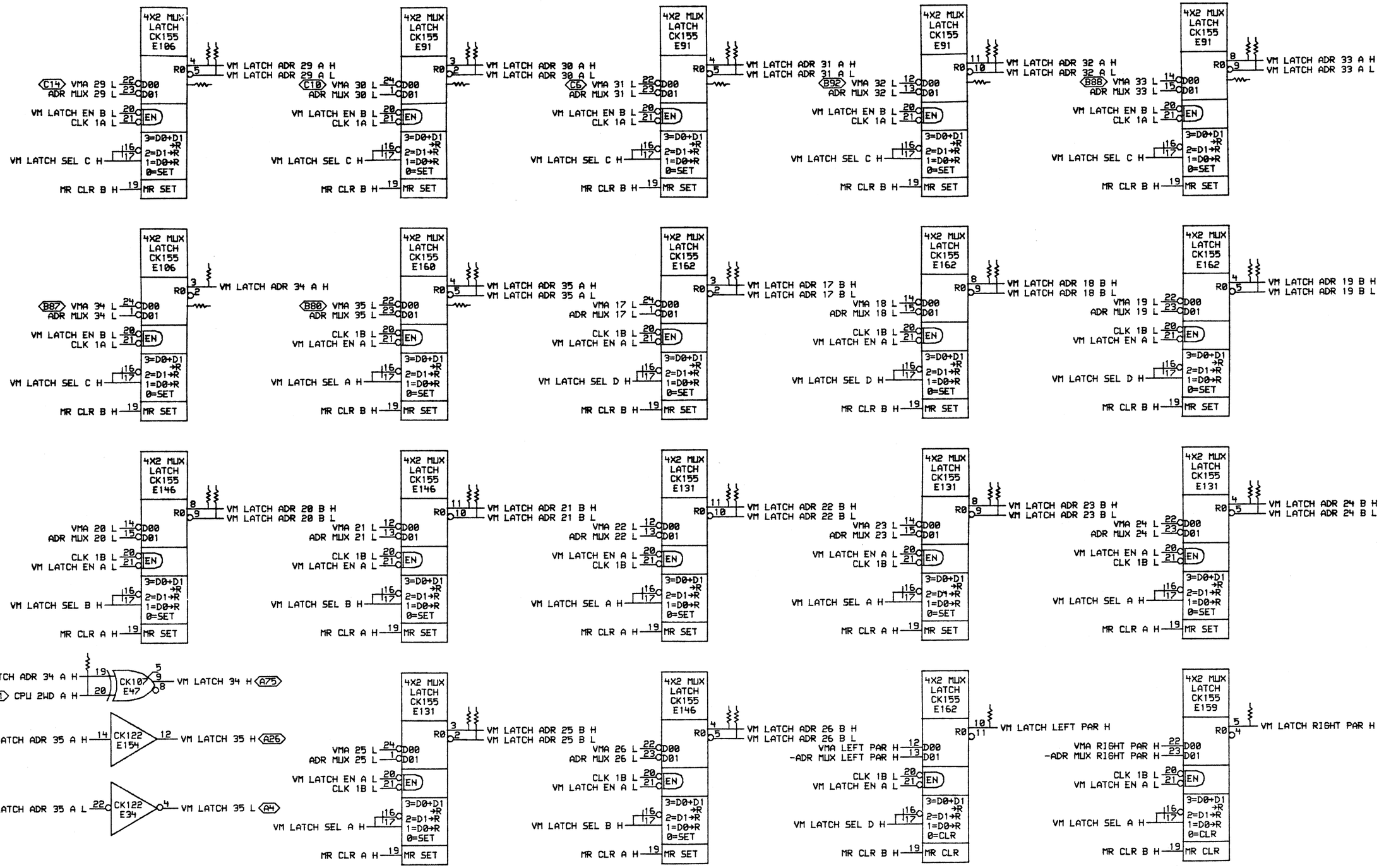
D C B A



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	CHK'D.	DATE	BOARD LOCATION:			
ARCH:<UHLER.PLOTS>54MAP7.DRW 22-NOV-82 14:27			SHEET 1 OF 2			
FIRST USED ON OPTION/MODEL: 2080:		NEXT HIGHER ASSEMBLY: B-DD-L0057-0			SIZE D / CODE CS / NUMBER L0057-0-MAP7	REV.

8 7 6 5 4 3 2 1

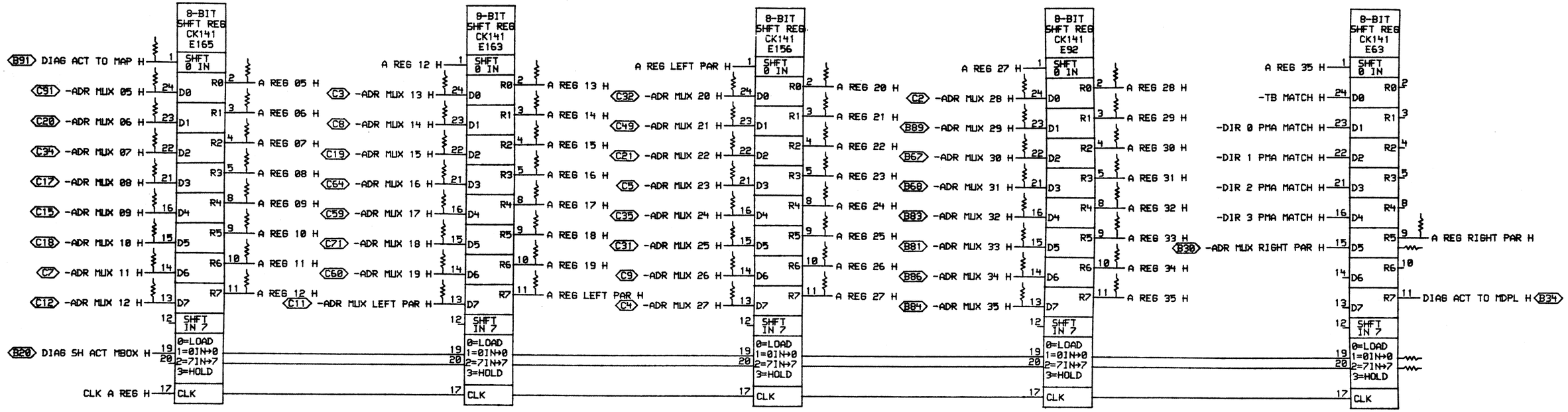
D C B A



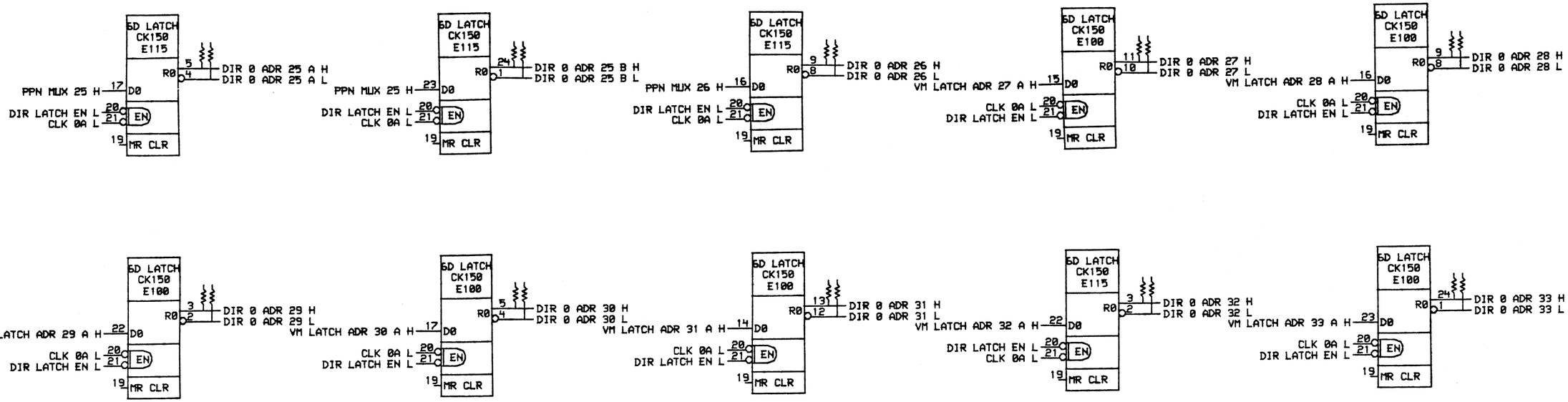
	DRN.	DATE 01-FEB-82	ENG.	DATE	TITLE: VM LTC 29A-35A 17B-26B + PAR
	CHK'D.	DATE	BOARD LOCATION: SHEET 2 OF 2		
VLI CAN: <2080.L0057>24MAP8.DRW	27-JAN-82 15:27		NEXT HIGHER ASSEMBLY:	SIZE D	CODE CS NUMBER L0057-0-MAP8 REV.
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0057-0		

8 7 6 5 4 3 2 1

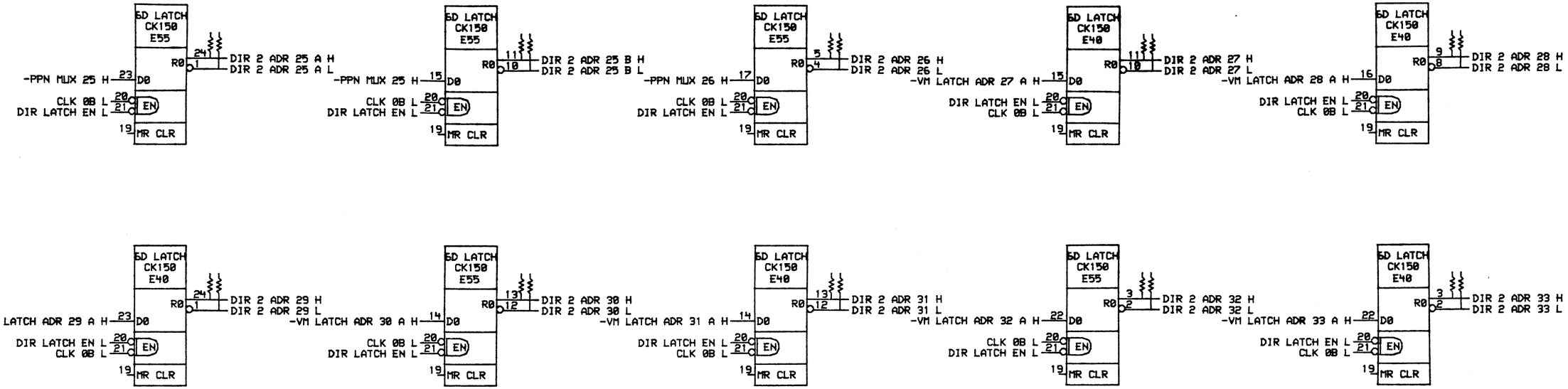
D C B A



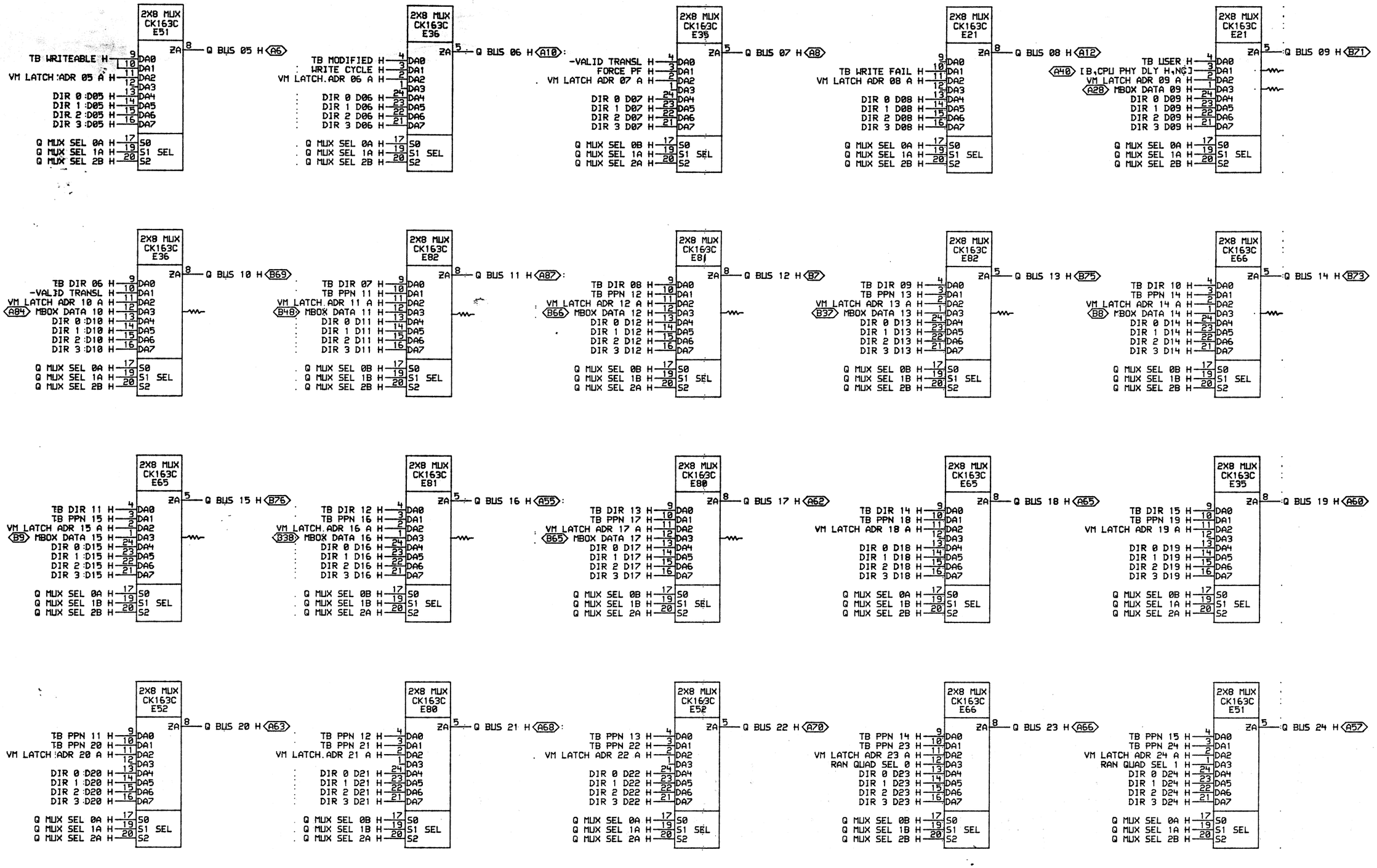
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	CHK'D.	DATE	BOARD LOCATION:		
			SHEET 1 OF 1		
VULCAN:<2080.L0057>2=MAP9.DRW 27-JAN-82 15:36			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0057-0-MAP9 / REV.
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0057-0		



	DRN.	DATE 01-FEB-82	ENG.	DATE	TITLE: DIR LATH 0 & 1
	CHK'D.	DATE	BOARD LOCATION:		SHEET 1 OF 2
VULCAN:<2000,L0057>E4MAPA.DRW 01-FEB-82 08:34			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0057-0-MAPA / REV.
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0057-0		



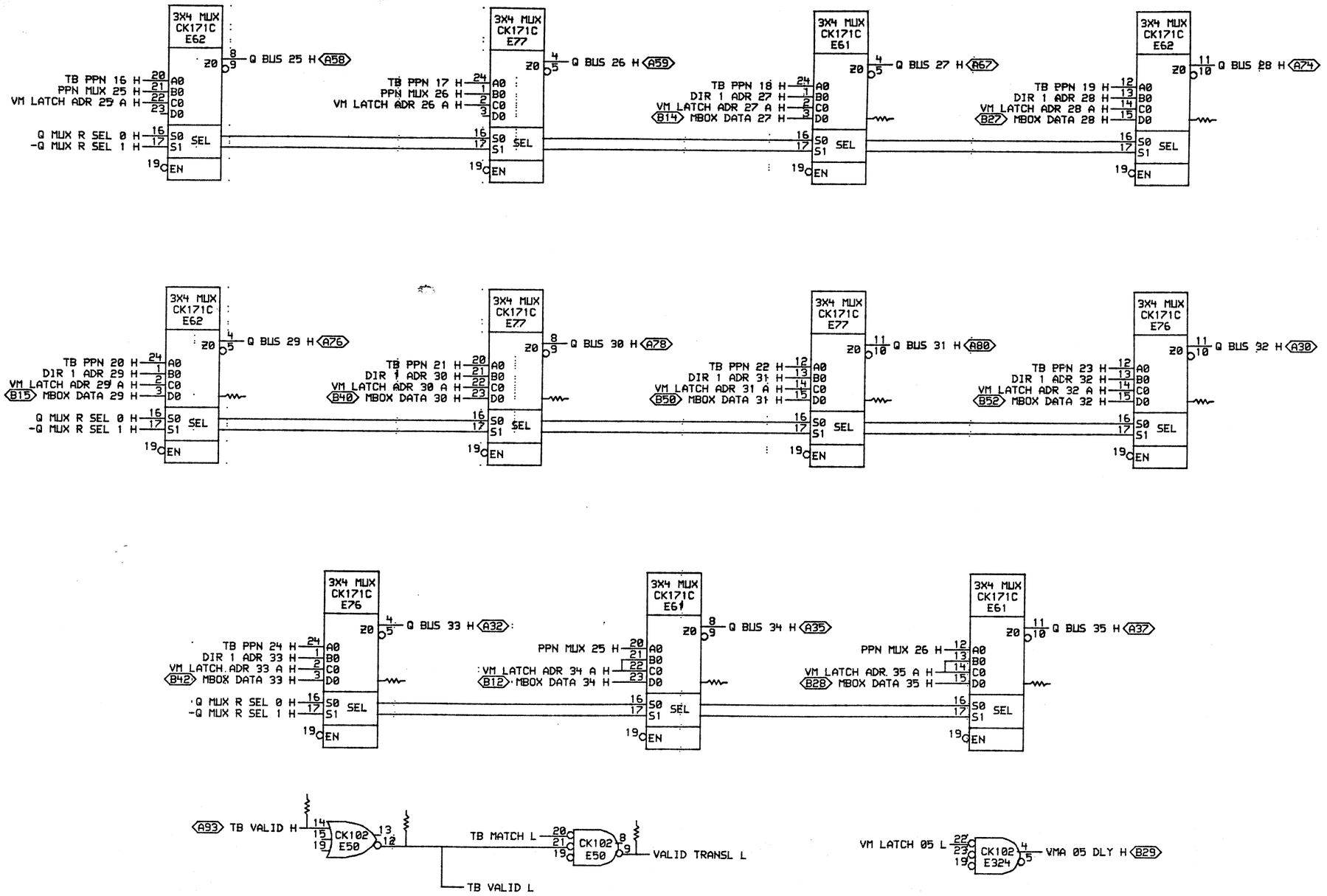
	DRN.	DATE 01-FEB-82	ENG.	DATE	TITLE: DIR LATCH 2 & 3
	CHK'D.	DATE	BOARD LOCATION: SHEET 2 OF 2		
VULCAN:<2080.L0057>E=MAPB.DRW 01-FEB-82 08:21			NEXT HIGHER ASSEMBLY:	SIZE D	CODE CS
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0057-0	NUMBER L0057-0-MAPB	REV. *



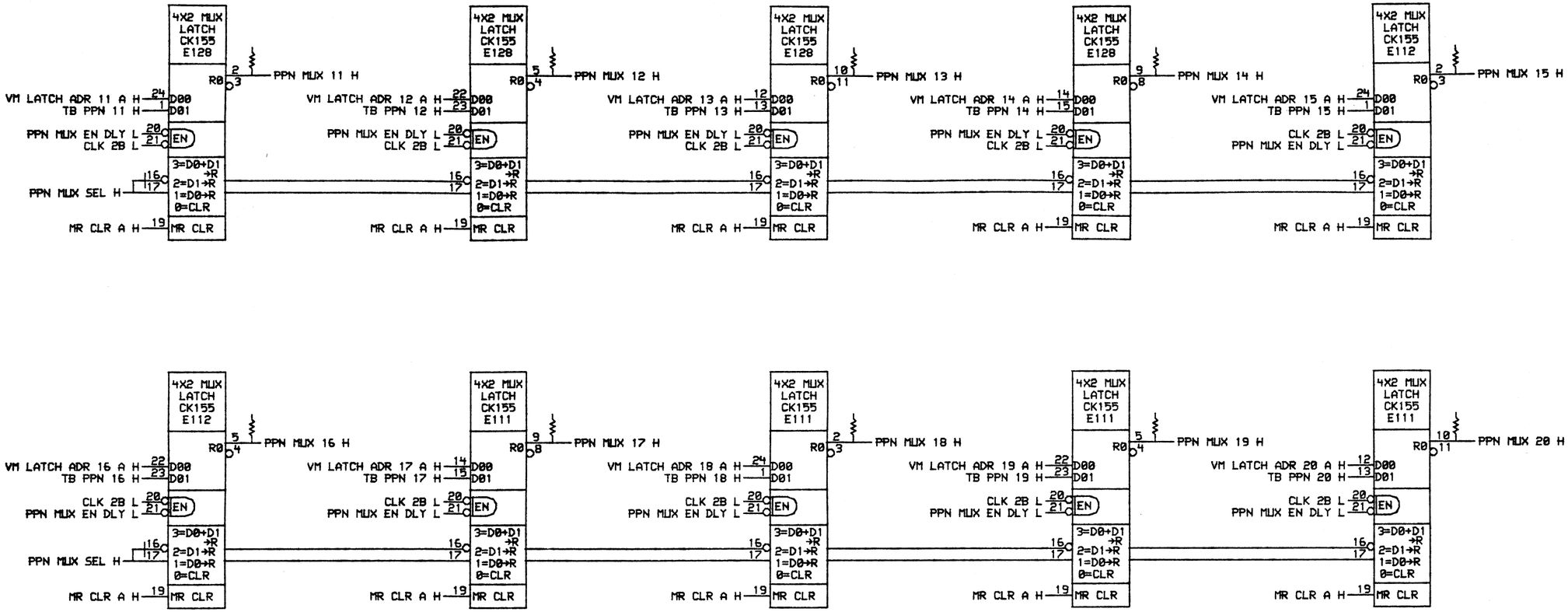
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	CHK'D.	DATE	BOARD LOCATION: SHEET 1 OF 2		
ARCH:<UHLER.PLOTS>55MAPC.DRW 15-NOV-82 14:01			NEXT HIGHER ASSEMBLY: B-DD-L0057-0	SIZE D	CODE CS
FIRST USED ON OPTION/MODEL: 2080				NUMBER L0057-0-MAPC	REV. *

8 7 6 5 4 3 2 1

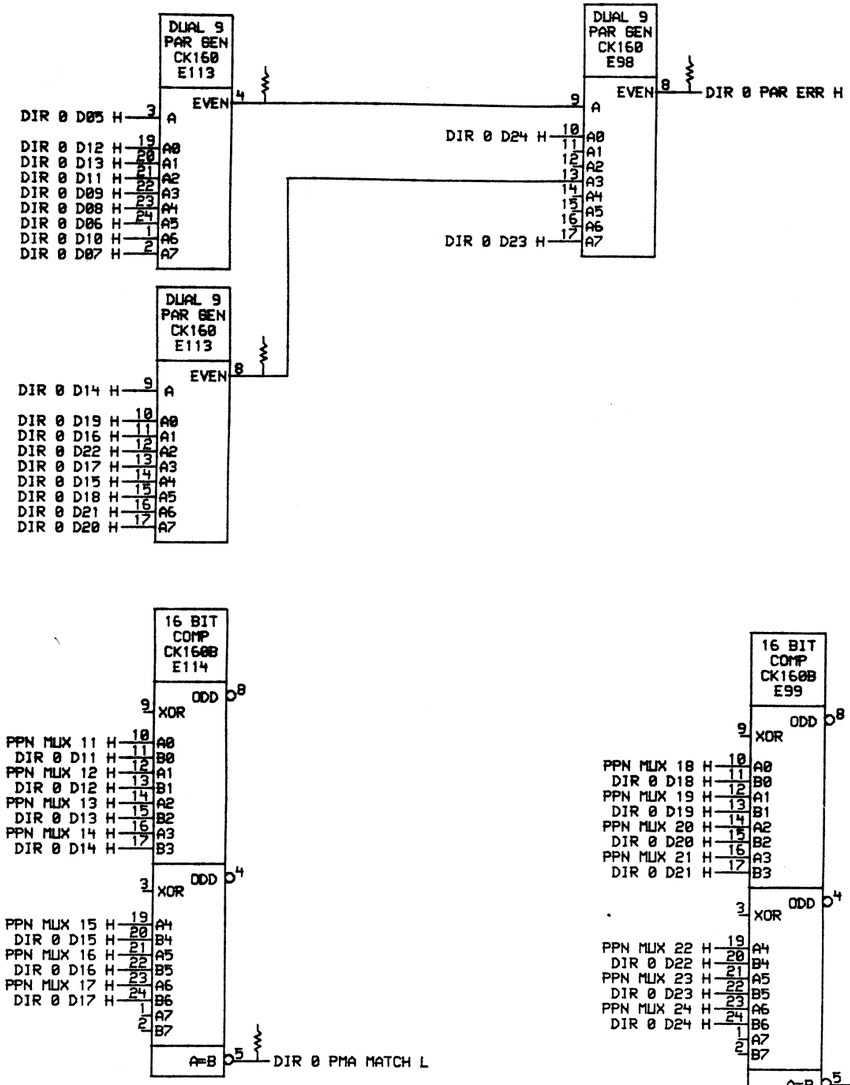
D C B A



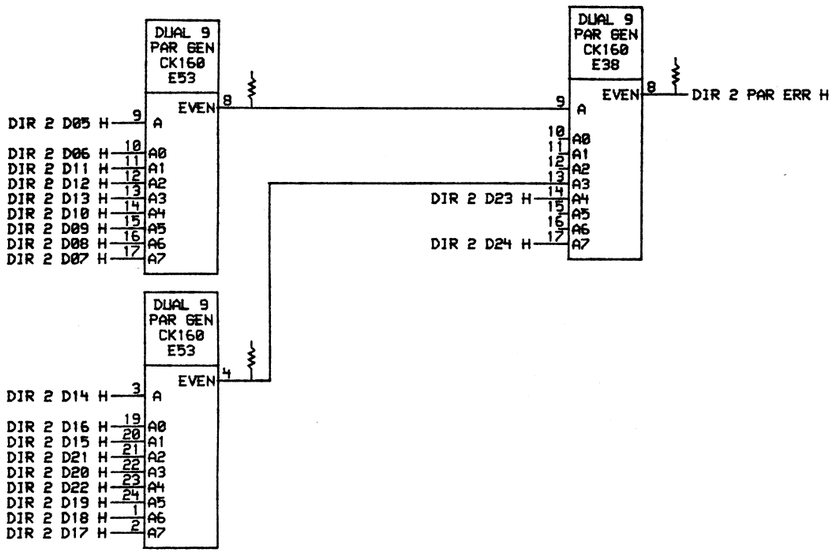
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	CHK'D.	DATE	BOARD LOCATIONS: SHEET 2 OF 2		
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FIRST USED ON OPTION/MODEL: 2080			B-DD-L0057-0	NUMBER L0057-0-MAPD	REV. *

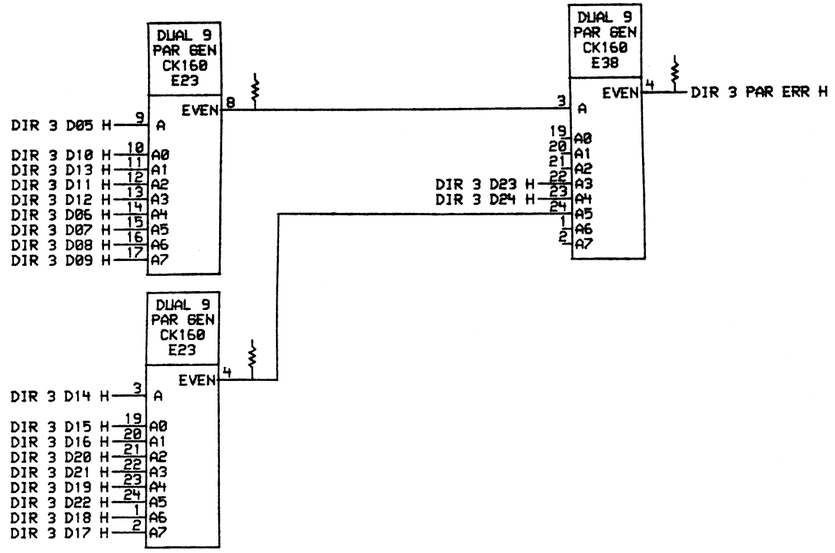


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	CHK'D.	DATE	BOARD LOCATION: SHEET 1 OF 1		
VULCAN:<2080.L0057>2:MAPE.DRW 01-FEB-82 08:05			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0057-0-MAPE / REV. *
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0057-0		

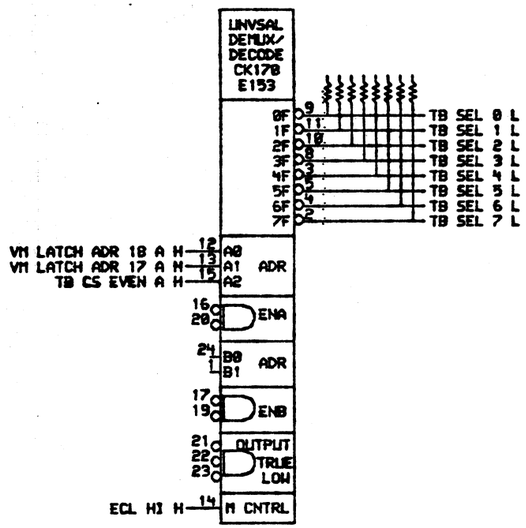


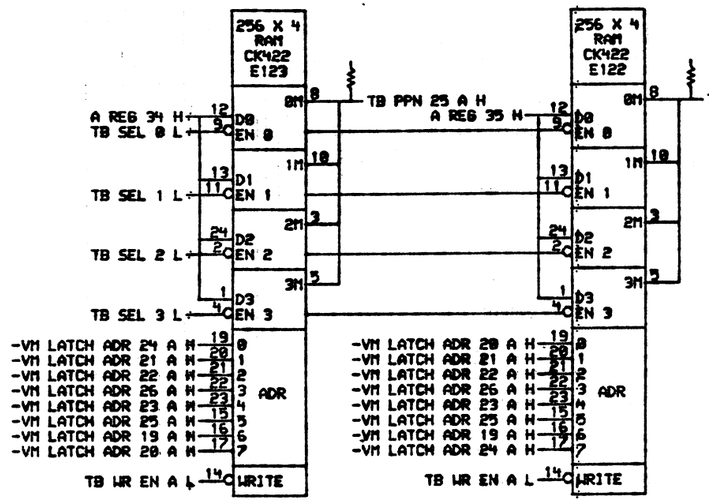
	DRN.	DATE 01-FEB-82	ENG.	DATE	TITLE: DIR MATCH 0 & 1
	CHK'D.	DATE	BOARD LOCATION:		
			SHEET 1 OF 2		
VULCAN:<P080.L0057>2*MAPF.DRW	01-FEB-82 08:51	NEXT HIGHER ASSEMBLY:		SIZE D	CODE CS	NUMBER L0057-0-MAPF	REV. *
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0057-0					

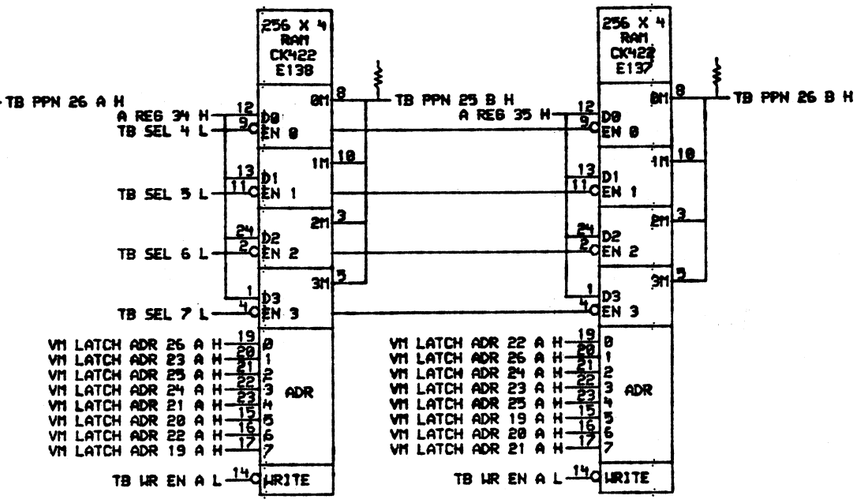


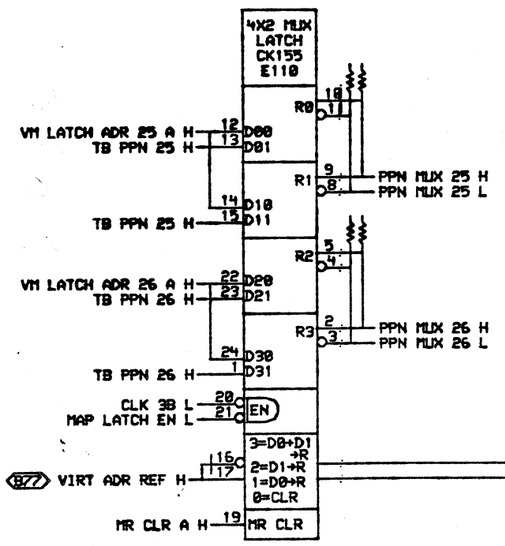


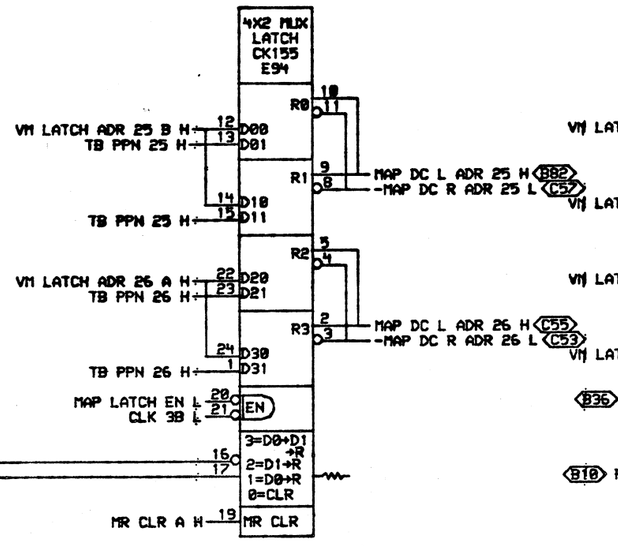
	DRN.	DATE 01-FEB-82	ENG.	DATE	TITLE:	DIR MATCH 0-3
	CHK'D.	DATE	BOARD LOCATION: SHEET 1 OF 1			
VULCAN:<2080.L0057>L*MAPG.DRW 01-FEB-82 09:02			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0057-0-MAPG	REV. *
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0057-0			

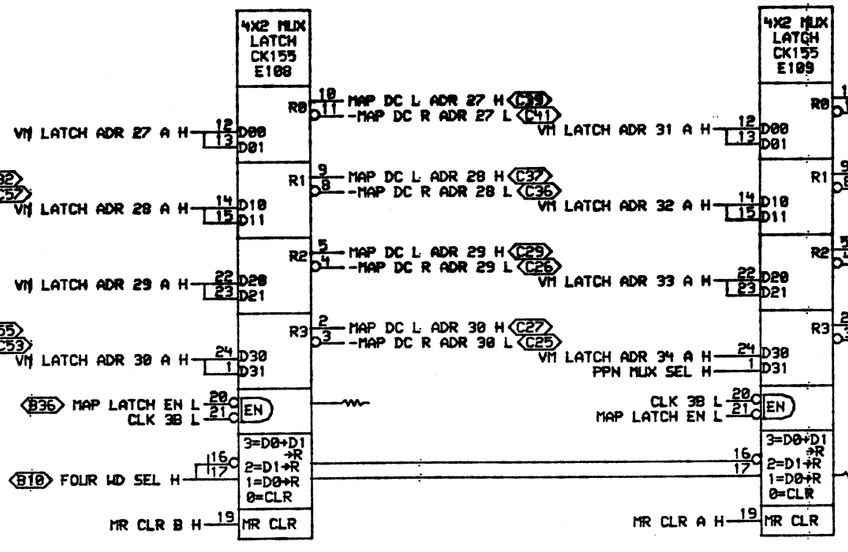














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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. J.Landy 07-NOV-82 13:21
CHK'D.
VULCAN (2080_L0057)5(MAPH.DRW
FIRST USED ON OPTION/MODEL: 2080

DATE 08-NOV-82 | ENG. | DATE
DATE | BOARD LOCATION:
SHEET 1 OF 1
NEXT HIGHER ASSEMBLY: B-DD-L0057-0

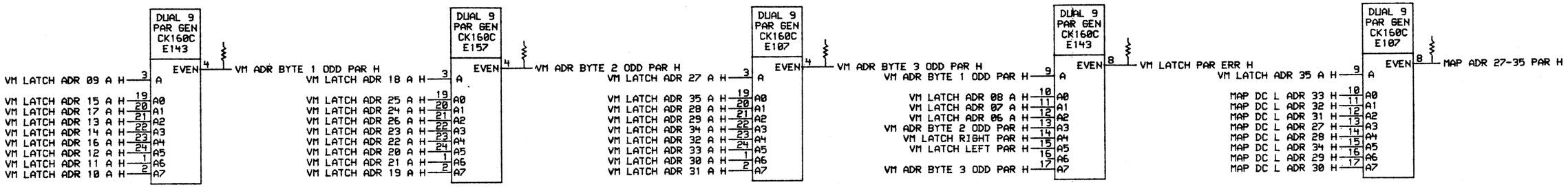
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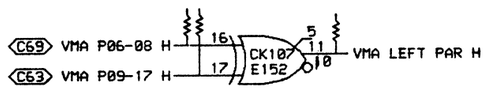
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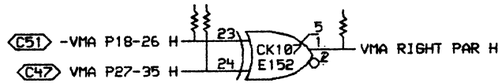
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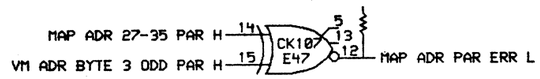
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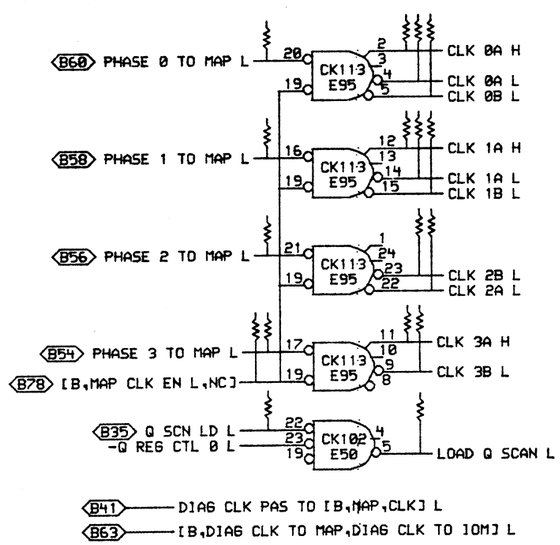
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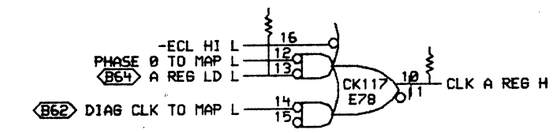


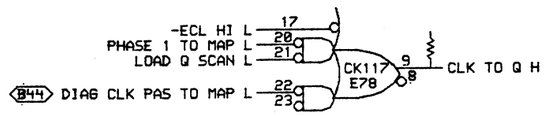


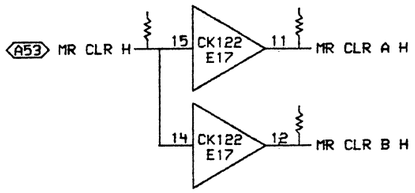


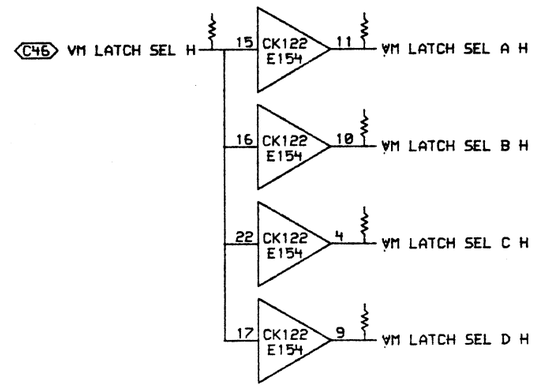


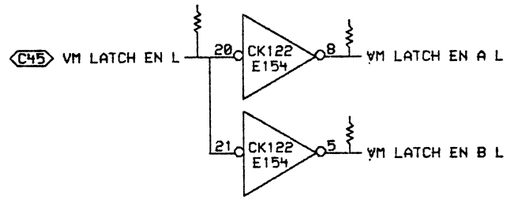












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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. T. Toomey | DATE 10-NOV-82 | ENG. | DATE

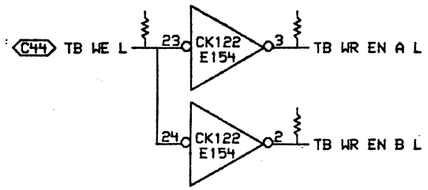
CHK "D. | DATE | BOARD LOCATION: | SHEET 1 OF 1

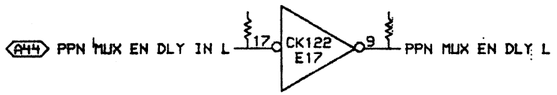
VULCAN: <2080.L0057>*MAP1.DRW | 09-NOV-82 10:05 | NEXT HIGHER ASSEMBLY: B-DD-L0057-0

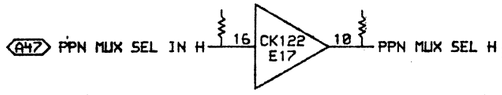
FIRST USED ON OPTION/MODEL: 2080

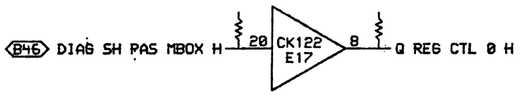
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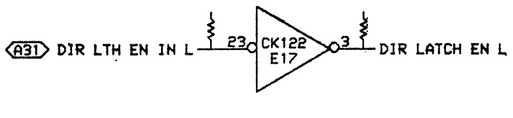
SIZE	CODE	NUMBER	REV.
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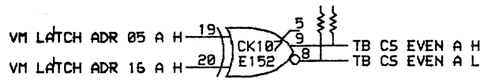


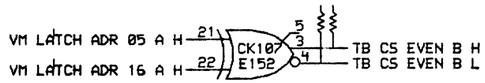


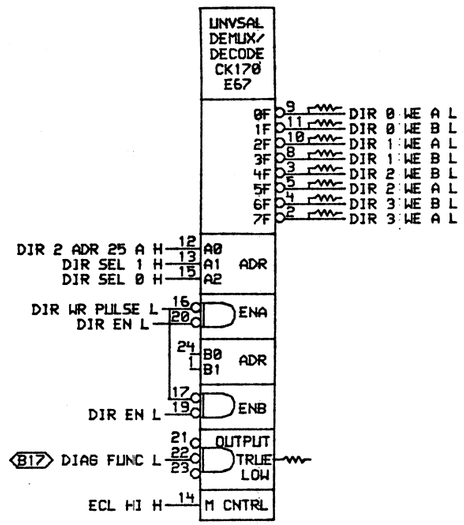


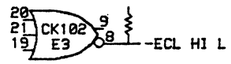


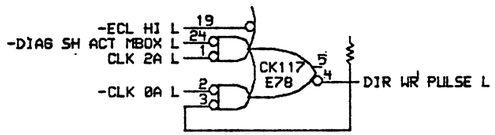


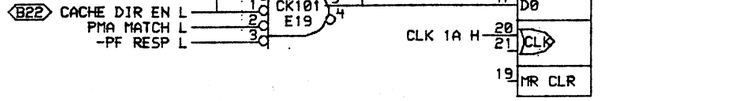


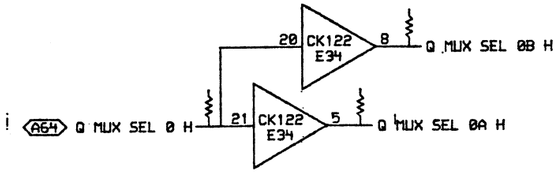


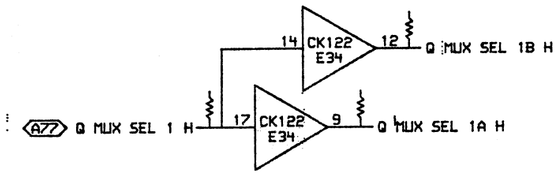


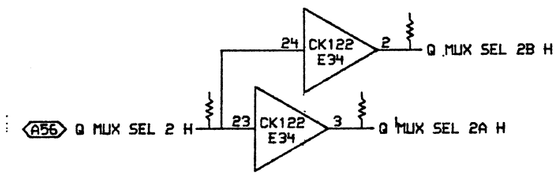


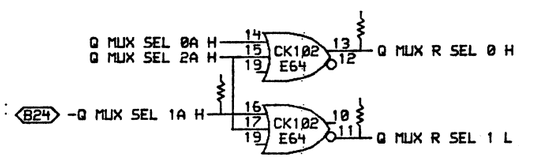


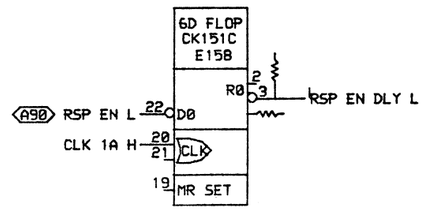


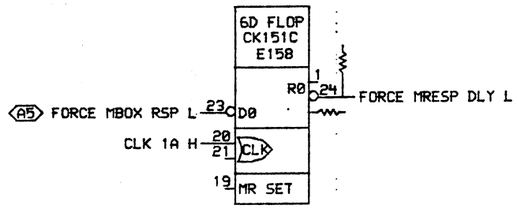












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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. S. Toomey	DATE 27-OCT-82	ENG.	DATE
	CHK D.	DATE	BOARD LOCATION:	
			SHEET 1 OF 1	
VULCAN:<2080.L0057>50MAPJ.DRW	26-OCT-82 08:01	NEXT HIGHER ASSEMBLY:		
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0057-0		

TITLE: INPUT BUFFERS

SIZE	CODE	NUMBER	REV.
D	CS	L0057-0-MAPJ	*

UNVSAL PRIO ENCODER CK165 E4
ANY 5 — IB,MAP INPUT ERR H,NC] A39
Q0 2 — MAP FRU 3 H
Q1 4 — IB,MAP FRU 2 H,NC] H
Q2 9 — MAP FRU 1 H
Q3 10 12 11
GS2 8
D0 HI 24
D1 23
D2 22
D3 21
D4 16
D5 15
D6 14
D7 LOW 13
EN IN 19
B MODE 20
EN OUT 17
MAP ADR PAR ERR L 24
-FOUR WD SEL L 1 19
CK102 E64 3 2
VM LATCH PAR ERR H 24
A REG PAR ERR H 23
MBOX R DATA ERR H 21
ECL HI H 20
A86 IB,EN INPUT ERR L,NC] 23 24 1 2 3
CK101 E48 5 4
CLK 3B L
4-STAGE CTR/SHFT CK136 E79
TC 1
SHFT IN 3 12
R3 11 10
D3 13
R2 8 9
D2 14
R1 5 4 — RAN QUAD SEL 0 H
D1 15
R0 2 3 — RAN QUAD SEL 1 H
D0 16
SHFT IN 0 CET 24
INCR QUAD L 23 CEP
S0 20
S1 21 SEL
B85 LD QUAD L 22 S2
CLK 0A H 17 CP
MR CLR A H 19 MR CLR
C62 INCR QUAD H 24 1 19 CK102 E58 2 3 INCR QUAD L
3X4 MUX CK171 E22
Z0 11 10 — DIR M WR B L B11
DIR 0 D10 H 12 A0
DIR 1 D10 H 13 B0
DIR 2 D10 H 14 C0
DIR 3 D10 H 15 D0
Z1 8 9 — DIR M WR L A58
DIR 0 D10 H 20 A1
DIR 1 D10 H 21 B1
DIR 2 D10 H 22 C1
DIR 3 D10 H 23 D1
Z2 4 5 — WD 0 VAL IN H / MAP WD 0 VAL L A25
DIR 0 D06 H 24 A2
DIR 1 D06 H 1 B2
DIR 2 D06 H 2 C2
DIR 3 D06 H 3 D2
DIR SEL 1 H 16
DIR SEL 0 H 17 S0 S1 SEL
19 EN
UNVSAL PRIO ENCODER CK165 E5
ANY 5 — IB,MAP RAM ERR H,NC] A48
Q0 2 — MAP FRU 3 H A29
Q1 4 — IB,MAP FRU 2 H,NC] H A34
Q2 9 — MAP FRU 1 H A36
Q3 10 12 11
GS2 8
TB PAR ERR H 21
D0 HI
D1 23
D2 25
D3 21
DIR 0 PAR ERR H 16 D4
DIR 1 PAR ERR H 15 D5
DIR 2 PAR ERR H 14 D6
DIR 3 PAR ERR H 13 D7 LOW
19 EN IN
ECL HI H 20 B MODE
-MAP INPUT ERR L 17 EN OUT
CLK 2A L 17 19
A89 IB,EN RAM ERR L,NC] 20 21 22
CK101 E48 9 8
ANY VALID MASK L 20
B39 -I/O 4WD RD L 21 19
CK102 E64 8 9 — -I/O 4WD READ L
3X4 MUX CK171 E20
Z0 11 10 — WD 1 VAL IN H / MAP WD 1 VAL L A73
DIR 0 D07 H 12 A0
DIR 1 D07 H 13 B0
DIR 2 D07 H 14 C0
DIR 3 D07 H 15 D0
Z1 8 9 — WD 2 VAL IN H / MAP WD 2 VAL L A71
DIR 0 D08 H 20 A1
DIR 1 D08 H 21 B1
DIR 2 D08 H 22 C1
DIR 3 D08 H 23 D1
Z2 4 5 — WD 3 VAL IN H / MAP WD 3 VAL L A69
DIR 0 D09 H 24 A2
DIR 1 D09 H 1 B2
DIR 2 D09 H 2 C2
DIR 3 D09 H 3 D2
DIR SEL 1 H 16
DIR SEL 0 H 17 S0 S1 SEL
19 EN
4X2 MUX LATCH CK155C E155
R0 9 8 — WD 1 VAL H A54
WD1 VALID MASK H 14 D00
WD 1 VAL IN H 15 D01
20 21 EN
3=D0+D1→R
2=D1→R
1=D0→R
0=CLR
I/O 4WD READ H 16 17
19 MR CLR
4X2 MUX LATCH CK155C E155
R0 10 11 — WD 0 VAL H A41
WD0 VALID MASK H 12 D00
WD 0 VAL IN H 13 D01
20 21 EN
3=D0+D1→R
2=D1→R
1=D0→R
0=CLR
I/O 4WD READ H 16 17
19 MR CLR
4X2 MUX LATCH CK155C E155
R0 5 4 — WD 2 VAL H A45
WD2 VALID MASK H 22 D00
WD 2 VAL IN H 23 D01
20 21 EN
3=D0+D1→R
2=D1→R
1=D0→R
0=CLR
I/O 4WD READ H 16 17
19 MR CLR
4X2 MUX LATCH CK155C E155
R0 2 3 — WD 3 VAL H A42
WD3 VALID MASK H 24 D00
WD 3 VAL IN H 1 D01
20 21 EN
3=D0+D1→R
2=D1→R
1=D0→R
0=CLR
I/O 4WD READ H 16 17
19 MR CLR

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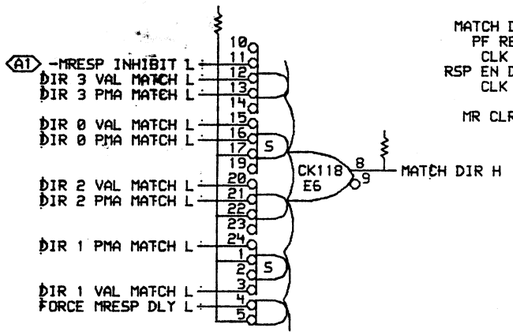
REVISIONS		
CHK	CHANGE NO.	REV

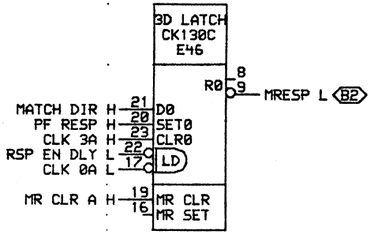
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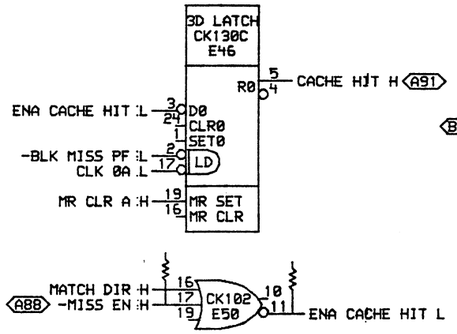
DRN. S. Toomey — DATE 13-OCT-82 — ENG. — DATE
CHK "D." — DATE — BOARD LOCATION: — SHEET 1 OF 1
VULCAN:<2080.L0057.49MAPK.DRW> 12-OCT-82 07:30 — NEXT HIGHER ASSEMBLY: B-DD-L0057-0
FIRST USED ON OPTION/MODEL: '2080

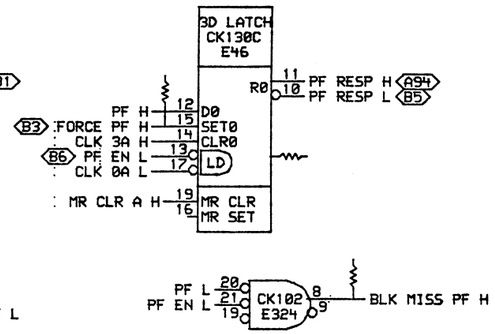
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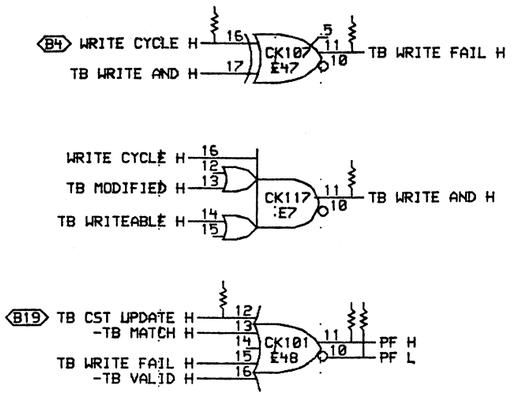
SIZE	CODE	NUMBER	REV.
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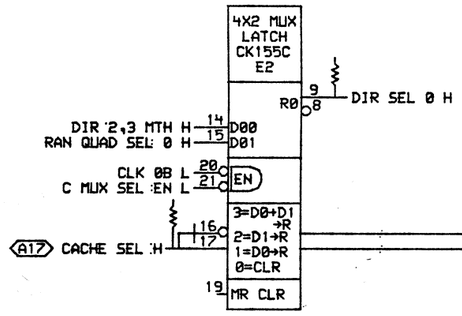


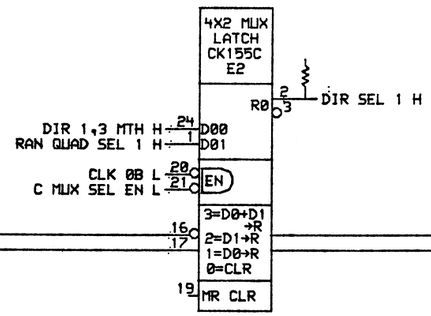


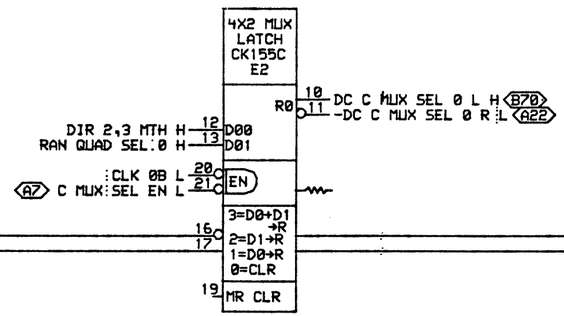


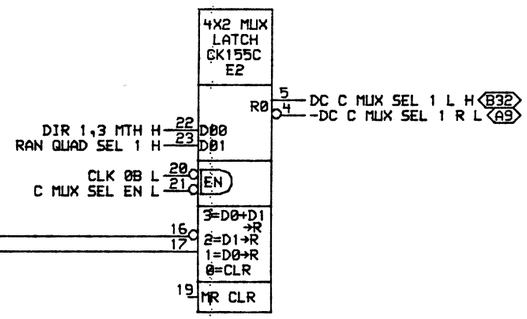


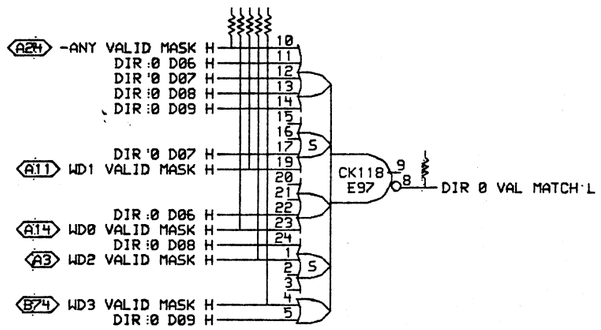


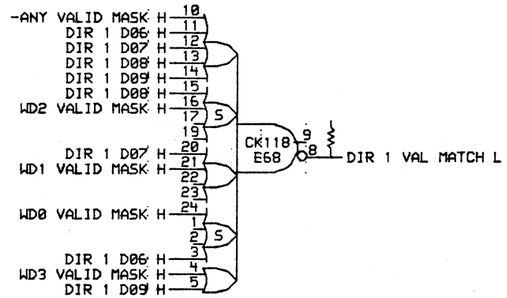


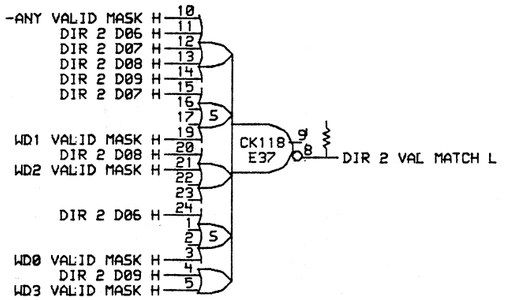


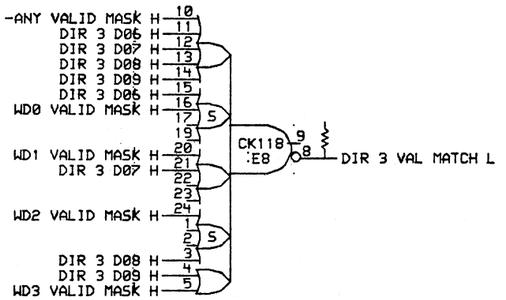




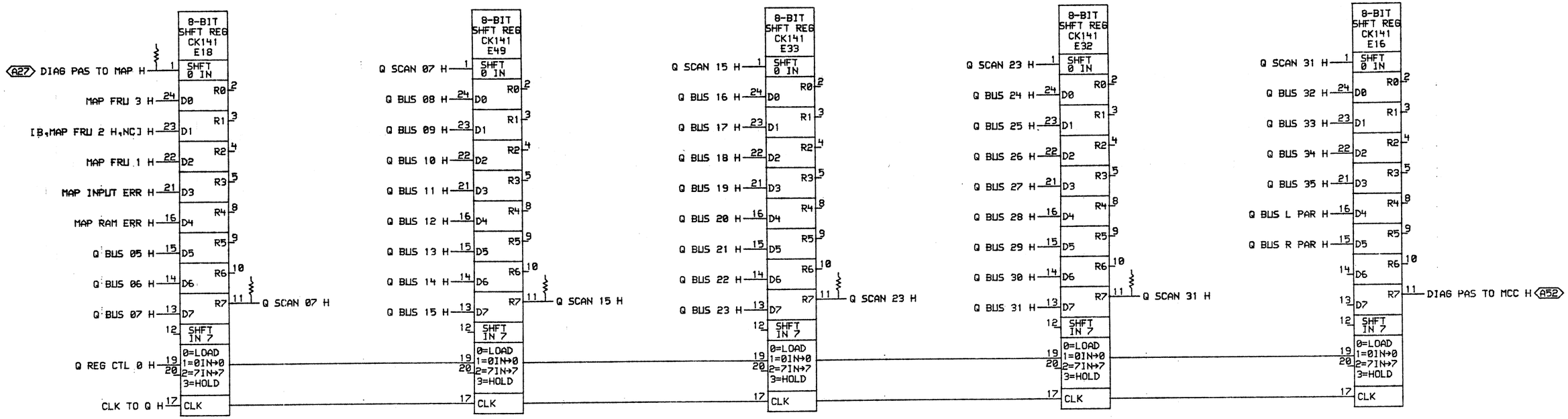








	DRN.	DATE 22-NOV-82	ENG.	DATE	TITLE: VALID MATCHES RSP,SEL CTL
	CHK'D.	DATE	BOARD LOCATION:		SHEET 1 OF 1
ARCH: <UHLER.PLOTS>53MAPL.DRW 15-NOV-82 16:59			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0057-0-MAPL / REV. *
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0057-0		



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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. S.Toomey 08-SEP-82 | ENG. | DATE

CHK'D. | DATE | BOARD LOCATION: | SHEET 1 OF 1

VULCAN:(2080.L0057)47MAPM.DRW 07-SEP-82 10:07 | NEXT HIGHER ASSEMBLY: B-DD-L0057-0

FIRST USED ON OPTION/MODEL: 2080

TITLE: Q SCAN REG Q DATA PAR

SIZE	CODE	NUMBER	REV.
D	CS	L0057-0-MAPM	*

REV. *
NUMBER L0057-0-MAPM
CODE CS
SIZE D

8 7 6 5 4 3 2 1

-4.5V\V\ GND ECL\G#19\

C1 10nF 50V — A2
C2 10nF 50V — A13
C3 10nF 50V — A16
C4 10nF 50V — A23
C11 10nF 50V — A33
C12 10nF 50V — A43
C13 10nF 50V — A51
C14 10nF 50V — A61
C15 10nF 50V — A72
C22 10nF 50V — A79
C23 10nF 50V — A82
C24 10nF 50V — A92
C25 10nF 50V — B1
C26 10nF 50V — B13
C123 10nF 50V — B16
C33 10nF 50V — B23
C34 10nF 50V — B33
C35 10nF 50V — B43
C36 10nF 50V — B51
C43 10nF 50V — B61
B72
B79
B90
B93
B94
C1
C13
C16
C23
C33
C43
C48
C61
C72
C79

-4.5V\V\ GND ECL\G#19\

C44 10nF 50V — C82
C45 10nF 50V — C93
C46 10nF 50V — C94
C47 10nF 50V
C124 10nF 50V
C54 10nF 50V
C55 10nF 50V
C56 10nF 50V
C57 10nF 50V
C64 10nF 50V
C65 10nF 50V
C66 10nF 50V
C67 10nF 50V
C68 10nF 50V
C125 10nF 50V
C75 10nF 50V
C76 10nF 50V
C77 10nF 50V
C78 10nF 50V
C85 10nF 50V

-4.5V\V\ GND ECL\G#19\

C86 10nF 50V
C87 10nF 50V
C88 10nF 50V
C89 10nF 50V
C126 10nF 50V
C96 10nF 50V
C97 10nF 50V
C98 10nF 50V
C99 10nF 50V
C106 10nF 50V
C107 10nF 50V
C108 10nF 50V
C109 10nF 50V
C110 10nF 50V
C127 10nF 50V
C5 10nF 50V
C6 10nF 50V
C7 10nF 50V
C8 10nF 50V
C9 10nF 50V

-4.5V\V\ GND ECL\G#19\

C10 10nF 50V
C16 10nF 50V
C17 10nF 50V
C18 10nF 50V
C19 10nF 50V
C20 10nF 50V
C21 10nF 50V
C27 10nF 50V
C28 10nF 50V
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C30 10nF 50V
C31 10nF 50V
C32 10nF 50V
C37 10nF 50V
C38 10nF 50V
C39 10nF 50V
C40 10nF 50V
C41 10nF 50V
C42 10nF 50V
C48 10nF 50V

-4.5V\V\ GND ECL\G#19\

C49 10nF 50V
C50 10nF 50V
C51 10nF 50V
C52 10nF 50V
C53 10nF 50V
C58 10nF 50V
C59 10nF 50V
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C61 10nF 50V
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C71 10nF 50V
C72 10nF 50V
C73 10nF 50V
C74 10nF 50V
C79 10nF 50V
C80 10nF 50V
C81 10nF 50V

-4.5V\V\ GND ECL\G#19\

C82 10nF 50V
C83 10nF 50V
C84 10nF 50V
C90 10nF 50V
C91 10nF 50V
C92 10nF 50V
C93 10nF 50V
C94 10nF 50V
C95 10nF 50V
C100 10nF 50V
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C102 10nF 50V
C103 10nF 50V
C104 10nF 50V
C105 10nF 50V
C111 10nF 50V
C112 10nF 50V
C113 10nF 50V
C114 10nF 50V
C115 10nF 50V

-4.5V\V\ GND ECL\G#19\

C116 10nF 50V
C117 1500µF 16V
C122 1500µF 16V

-2.0V\V\ GND ECL\G#19\

C118 2200µF 6V
C119 2200µF 6V
C120 2200µF 6V
C121 2200µF 6V — GND, GND ECL

NC Z44 — NC Z66
NC Z83
NC Z91 — NC Z92
NC Z100

A15 — NC A15
A18 — NC A18
A20 — NC A20
A21 — NC A21
A38 — NC A38
B26 — NC B26
A85 — NC A85
B31 — NC B31
B18 — NC B18
B21 — NC B21
B25 — NC B25

B53 — [B,GND ECL, GND CLK\G#20\]
B55 — [B,GND ECL, GND CLK\G#20\]
B57 — [B,GND ECL, GND CLK\G#20\]
B59 — [B,GND ECL, GND CLK\G#20\]
C89 — [B,GND ECL, NC]

	DRN.	DATE 23-NOV-82	ENG.	DATE	TITLE: MAP POWER & GROUND
	CHK'D.	DATE	BOARD LOCATION: SHEET 1 OF 1		
ARCH:<LUHLER.PLOTS>54MAPN.DRW 22-NOV-82 14:39			NEXT HIGHER ASSEMBLY:	SIZE D	CODE CS	NUMBER L0057-0-MAPN	REV. *
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0057-0				

8 7 6 5 4 3 2 1

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VULCAN:<2080.L0060>19MDP1
VULCAN:<2080.L0060>19MDP2
VULCAN:<2080.L0060>19MDP3
VULCAN:<2080.L0060>19MDP4
VULCAN:<2080.L0060>19MDP5
VULCAN:<2080.L0060>19MDP6
VULCAN:<2080.L0060>19MDP7
VULCAN:<2080.L0060>19MDP8
VULCAN:<2080.L0060>26MDP9
VULCAN:<2080.L0060>22MDPA
VULCAN:<2080.L0060>22MDPB
VULCAN:<2080.L0060>19MDPC
VULCAN:<2080.L0060>19MDPD
VULCAN:<2080.L0060>19MDPE
VULCAN:<2080.L0060>19MDPF
VULCAN:<2080.L0060>19MDPG
VULCAN:<2080.L0060>19MDPH
VULCAN:<2080.L0060>26MDPI
VULCAN:<2080.L0060>19MDPJ
VULCAN:<2080.L0060>19MDPK
VULCAN:<2080.L0060>27MDPL
VULCAN:<2080.L0060>26MDPM
VULCAN:<2080.L0060>25MDPN
VULCAN:<2080.L0060>27MDPP
VULCAN:<2080.L0060>24MDPR
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E165 E150 E135 E120 E105 E090 E075 E060 E045 E030 E015

E164 E149 E134 E119 E104 E089 E074 E059 E044 E029 E014

E163 E148 E133 E118 E103 E088 E073 E058 E043 E028 E013

E162 E147 E132 E117 E102 E087 E072 E057 E042 E027 E012

E161 E146 E131 E116 E101 E086 E071 E056 E041 E026 E011

E160 E145 E130 E115 E100 E085 E070 E055 E040 E025 E010

E159 E144 E129 E114 E099 E084 E069 E054 E039 E024 E009

E158 E143 E128 E113 E098 E083 E068 E053 E038 E023 E008

E157 E142 E127 E112 E097 E082 E067 E052 E037 E022 E007

E156 E141 E126 E111 E096 E081 E066 E051 E036 E021 E006

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E154 E139 E124 E109 E094 E079 E064 E049 E034 E019 E004

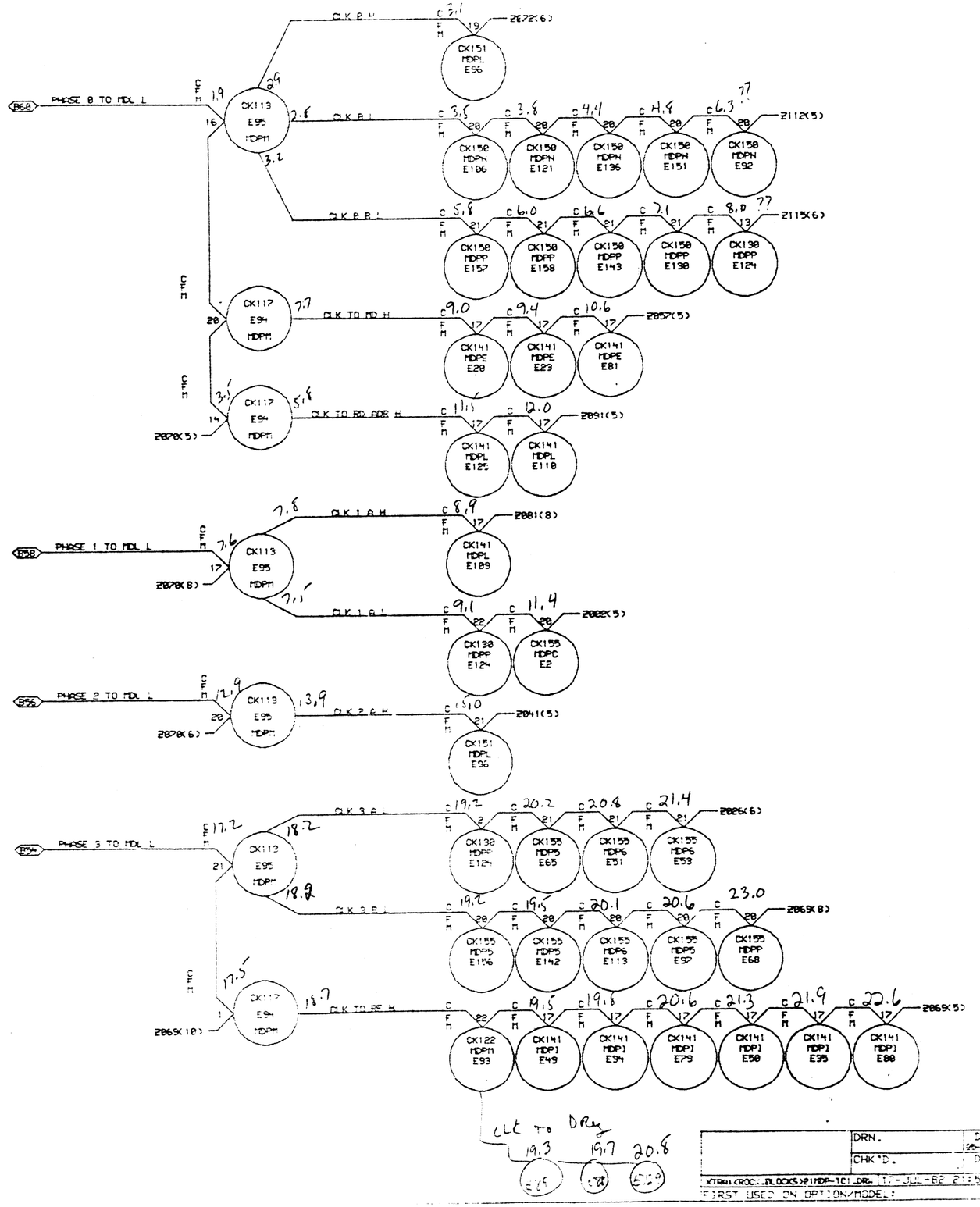
E153 E138 E123 E108 E093 E078 E063 E048 E033 E018 E003

E152 E137 E122 E107 E092 E077 E062 E047 E032 E017 E002

E151 E136 E121 E106 E091 E076 E061 E046 E031 E016 E001

+ C122 + + C121 + + C120 + + C119 + 165 + C118 + + C117 +

MDP



MDP

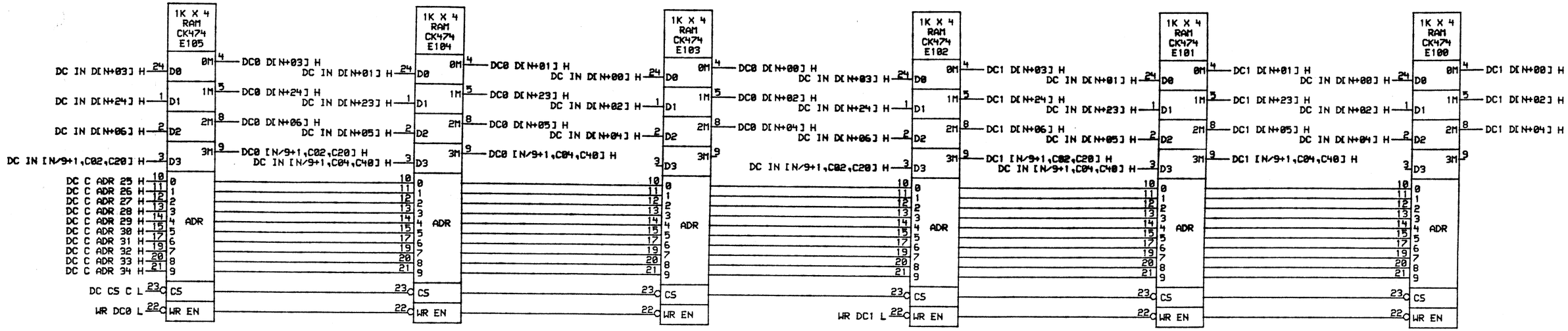
NEEDS LOOKING

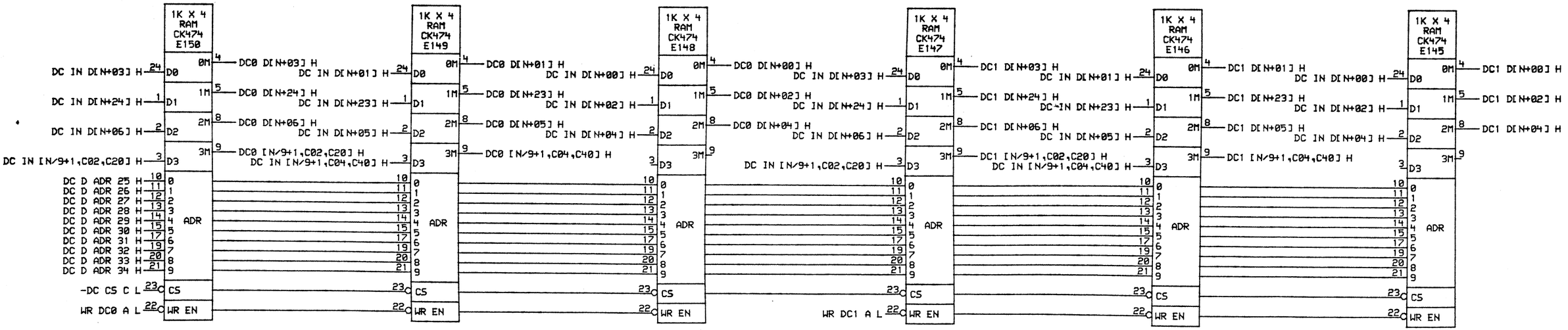
(1) E92 CLK 0 L
(2) E124 CLK 0 BL
E96 CLK 0 H

(3) CLK to RF
(4) CLK to D Reg

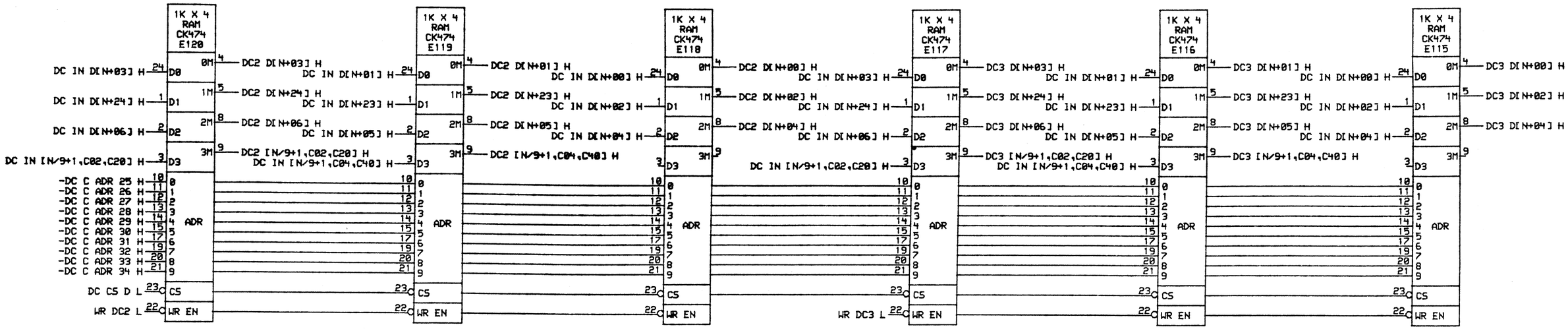
MDP

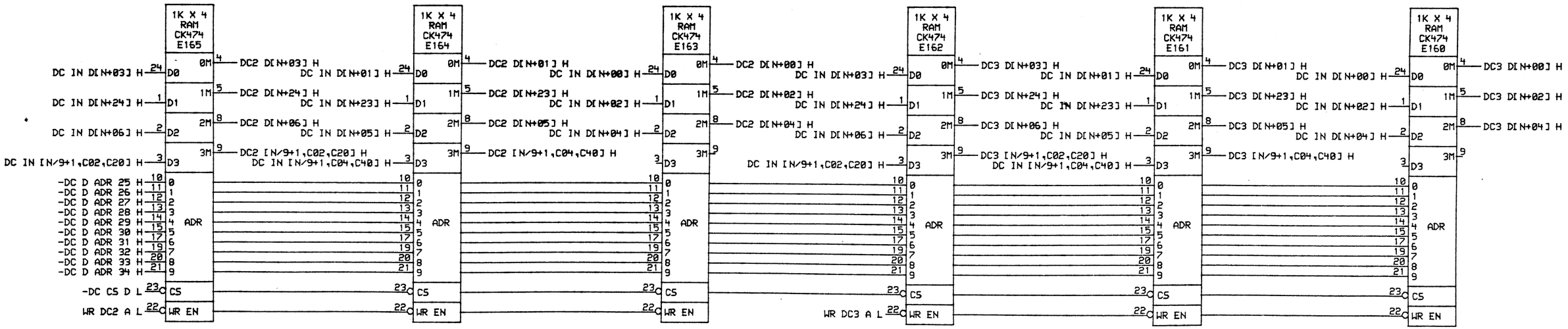
	DRN.	DATE	ENG.	DATE	TITLE:
	CHK'D.	DATE	BOARD LOCATION:		
XTRM:<ROC:..\BLOCKS>#1MDP-TC1.DRG 17-JUL-82 21:58			NEXT HIGHER ASSEMBLY:	SIZE D	CODE 05
FIRST USED ON OPTION/MODEL:					





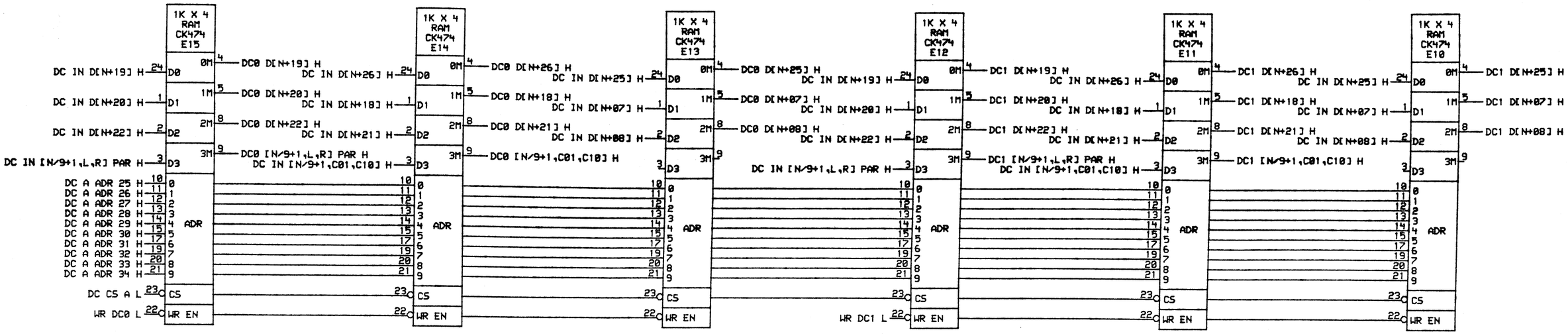
	DRN.	DATE 29-JAN-82	ENG.	DATE	TITLE: CACHE 0,1 RAM
	CHK'D.	DATE	BOARD LOCATION:		
			SHEET 1 OF 4		
VULCAN\<2080.L0060>\9MDP1.DRW	29-JAN-82 10:42		NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0060-0-MDP1 / REV. *
FIRST USED ON OPTION/MODEL:	2080		B-DD-L0060-0		

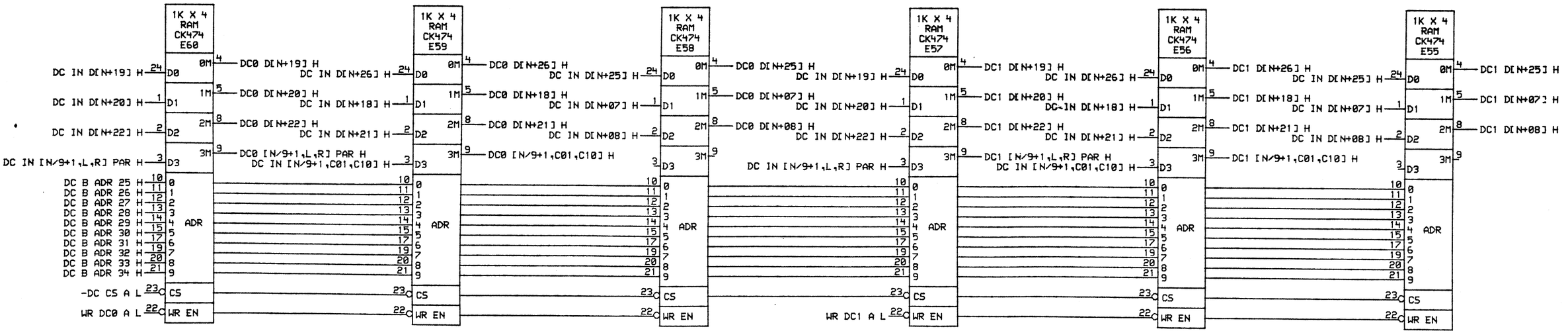




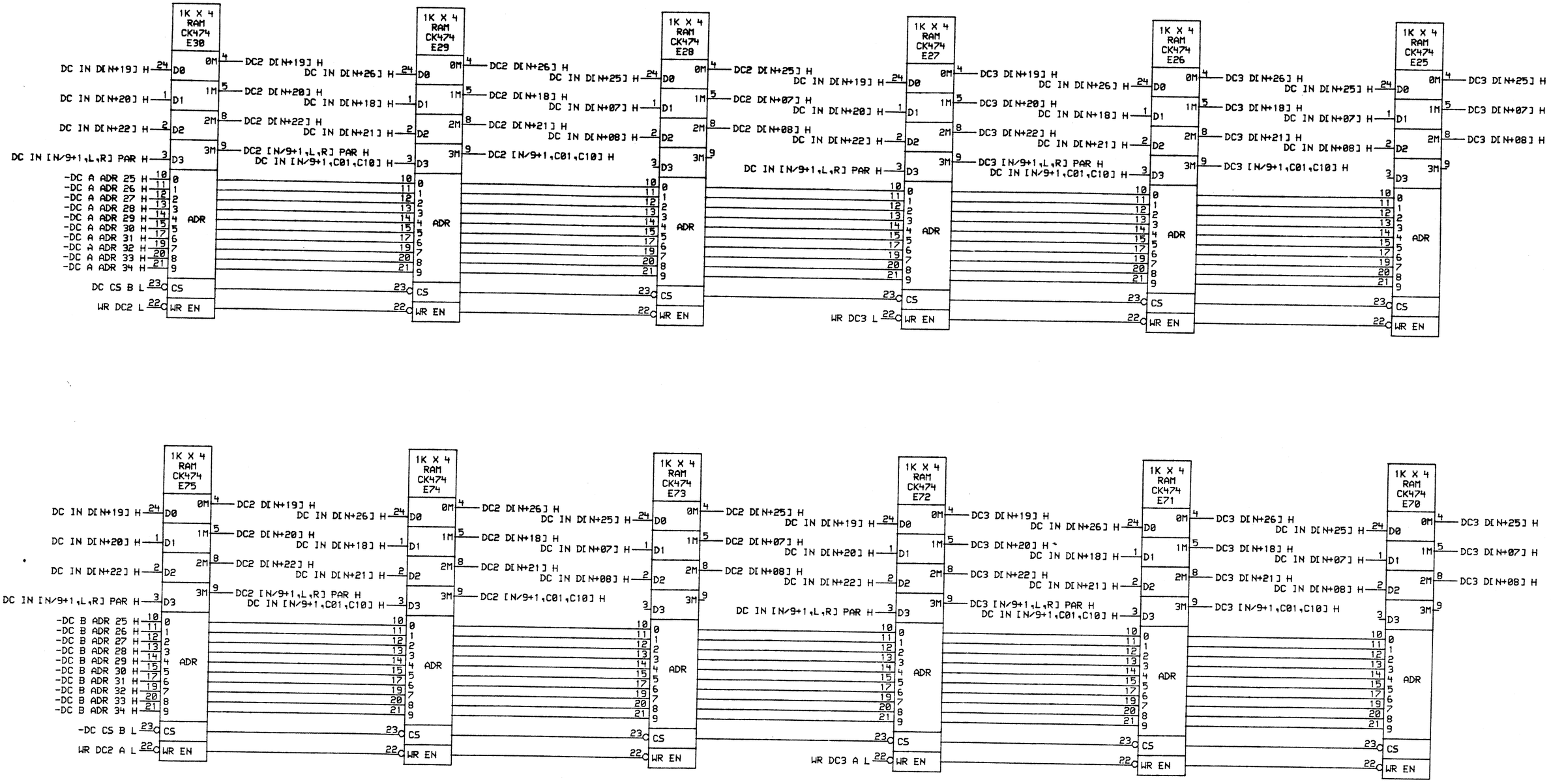
	DRN.	DATE 29-JAN-82	ENG.	DATE	TITLE: CACHE 2,3 RAM
	CHK'D.	DATE	BOARD LOCATION: SHEET 2 OF 4		
VLI CAN:<2080.L0060>19MDP2.DRW	29-JAN-82 10:42	NEXT HIGHER ASSEMBLY:		SIZE D	CODE CS NUMBER L0060-0-MDP2 REV. *
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0060-0			

8 7 6 5 4 3 2 1

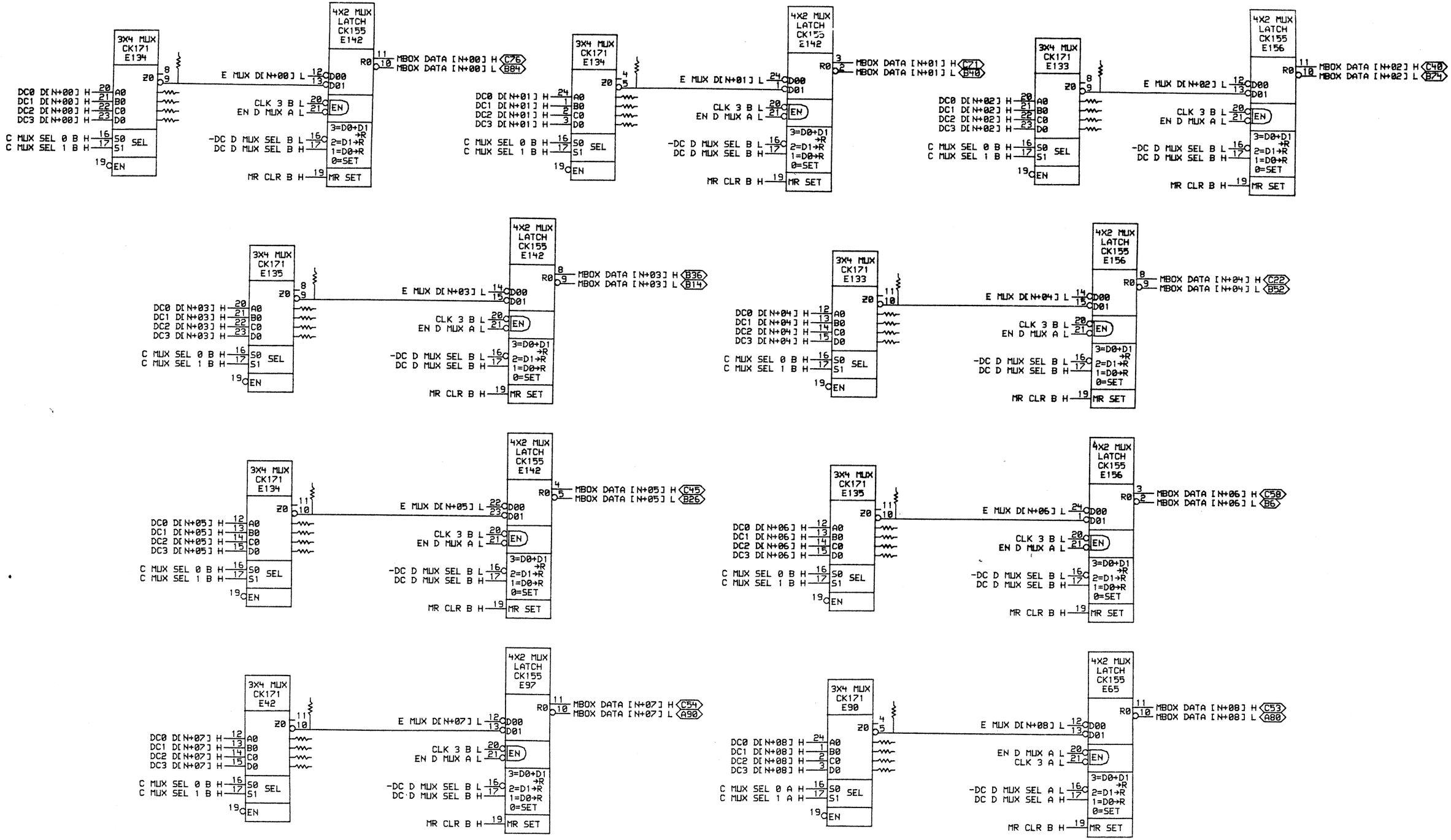




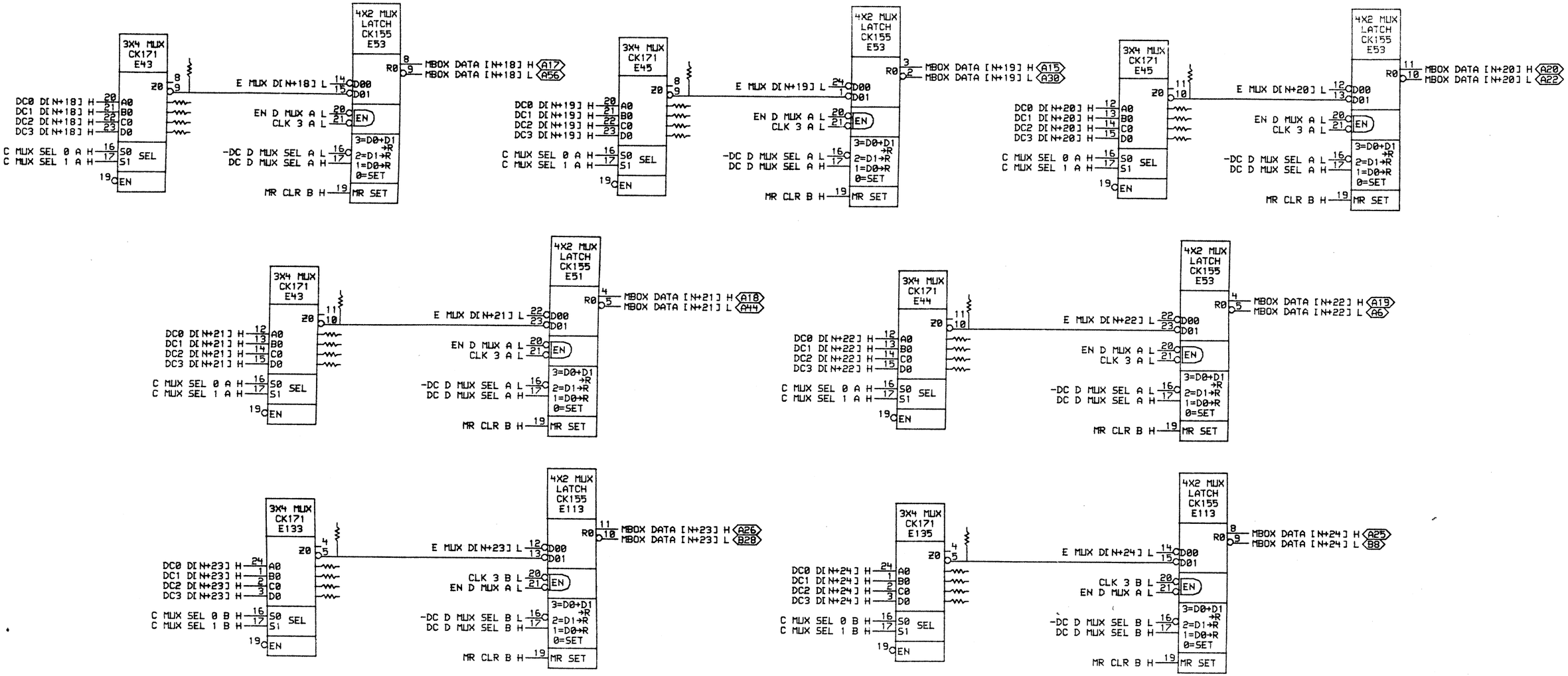
	DRN.	DATE 29-JAN-82	ENG.	DATE	TITLE: CACHE 0,1 RAM
	CHK'D.	DATE	BOARD LOCATION: SHEET 3 OF 4		
VULCAN:<2080.L0060>19MDP3.DRW 29-JAN-82 10:43			NEXT HIGHER ASSEMBLY:	SIZE D	CODE CS NUMBER L0060-0-MDP3 REV. *
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0060-0		



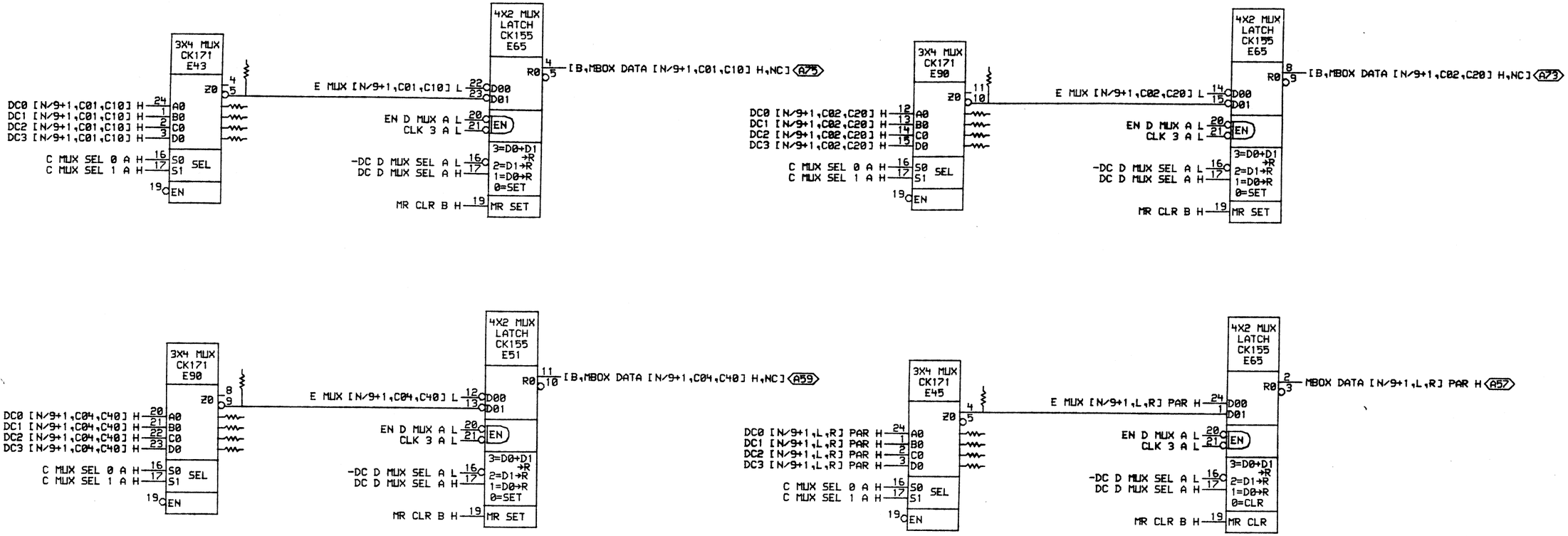
	DRN.	DATE 29-JAN-82	ENG.	DATE	TITLE: CACHE 2,3 RAM
	CHK'D.	DATE	BOARD LOCATION:		SHEET 4 OF 4
VULCAN:<2080.L0060>19MDP4.DRW	29-JAN-82 10:43	NEXT HIGHER ASSEMBLY:		SIZE D	CODE CS NUMBER L0060-0-MDP4 REV. *
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0060-0			



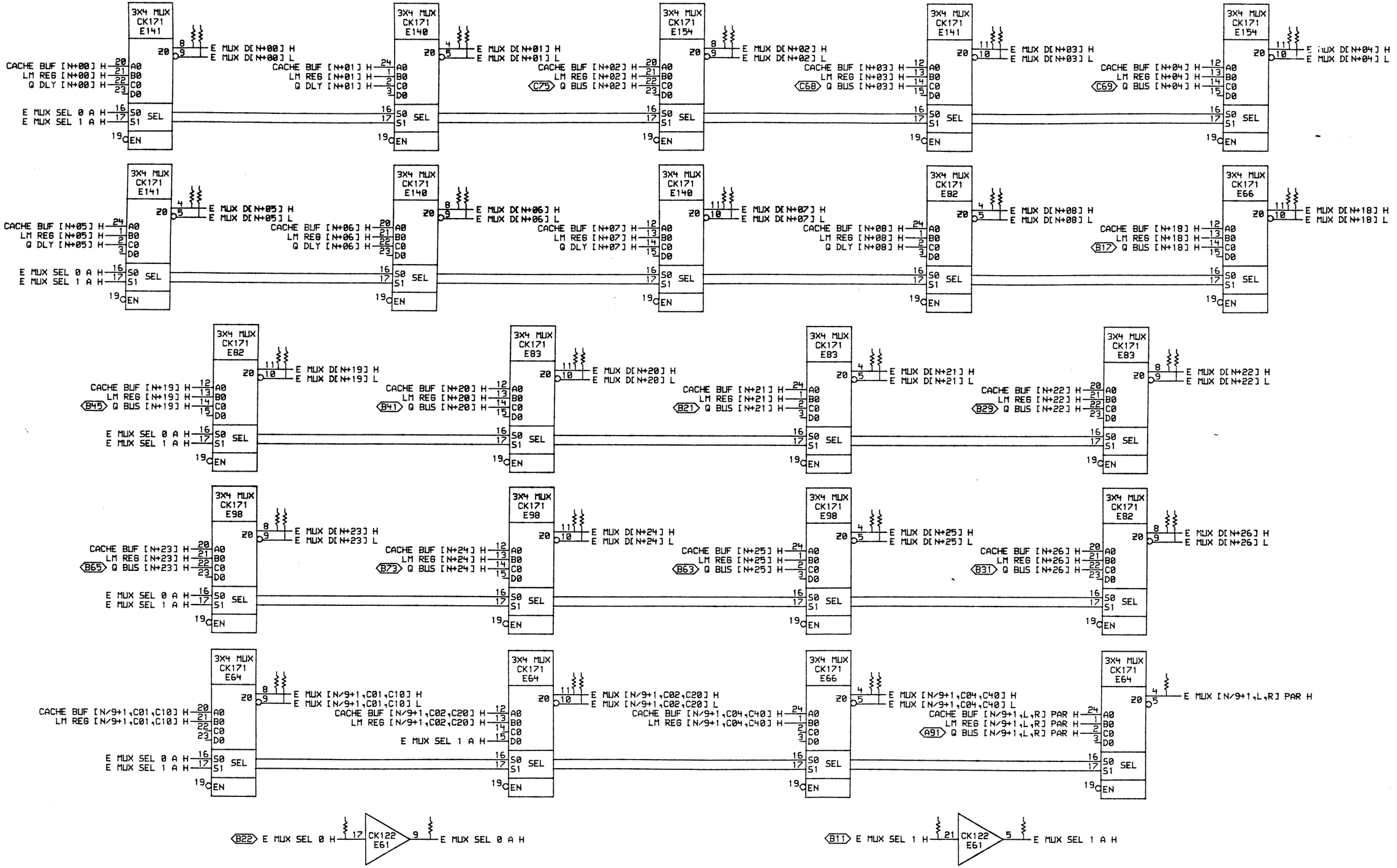
	DRN.	DATE 29-JAN-82	ENG.	DATE	TITLE: MBOX DATA 00-08 / 09-17
	CHK'D.	DATE	BOARD LOCATION:	SHEET 1 OF 3	
VULCAN:<P080,L0060>19MDP5.DRW 29-JAN-82 10:43			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0060-0-MDP5 / REV. *
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0060-0		



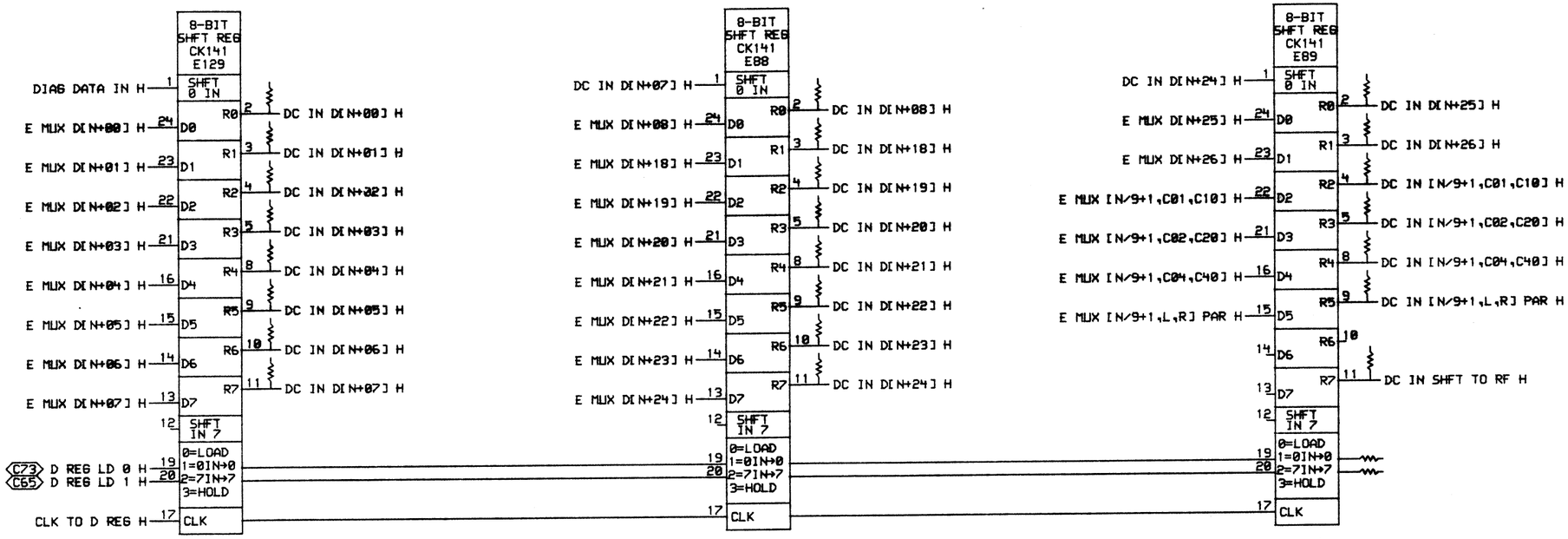
	DRN.	DATE 29-JAN-82	ENG.	DATE	TITLE: MBOX DATA 18-26 / 27-35
	CHK'D.	DATE	BOARD LOCATION: SHEET 2 OF 3		
VLICAN1<2080.L0060>1:MDP6.DRW 29-JAN-82 10:43			NEXT HIGHER ASSEMBLY: B-DD-L0060-0		SIZE D / CODE CS / NUMBER L0060-0-MDP6 / REV. *
FIRST USED ON OPTION/MODEL: 2080					



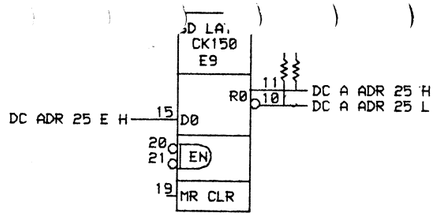
	DRN.	DATE 29-JAN-82	ENG.	DATE	TITLE: MBOX DATA C BITS & PAR
	CHK'D.	DATE	BOARD LOCATION:	SHEET 3 OF 3	
VULCAN:<2080,L0060>19MDP7.DRW 29-JAN-82 10:43			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0060-0-MDP7 / REV. *
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0060-0		

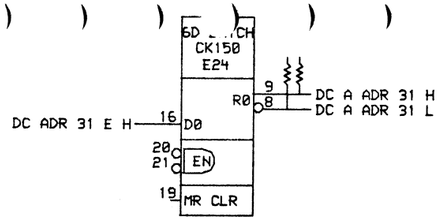


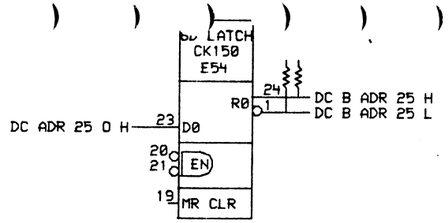
	DRN.	DATE 29-JAN-82	ENG.	DATE	TITLE: E MUX		
	CHK'D.	DATE	BOARD LOCATION:				
VULCAN:<2080.L0060>19MDP8.DRW 29-JAN-82 10:43			SHEET 1 OF 1				
FIRST USED ON OPTION/MODEL: 2080			NEXT HIGHER ASSEMBLY: B-DD-L0060-0		SIZE D	CODE CS	NUMBER L0060-0-MDP8	REV. *

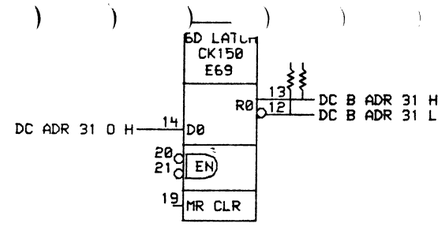


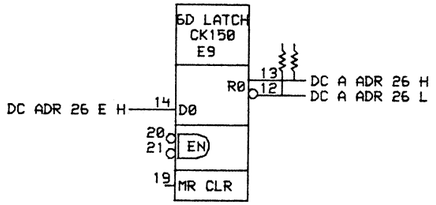
	DRN.	DATE 02-AUG-82	ENG.	DATE	TITLE: DC IN
	CHK'D.	DATE	BOARD LOCATION:		
			SHEET 1 OF 1		
VULCAN:<2000.L0060>26MDP9.DRW 02-AUG-82 11:42			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0060-0-MDP9 / REV. *
FIRST USED ON OPTION/MODEL:			B-DD-L0060-0		

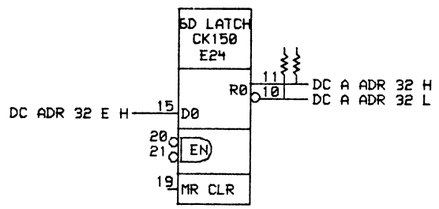


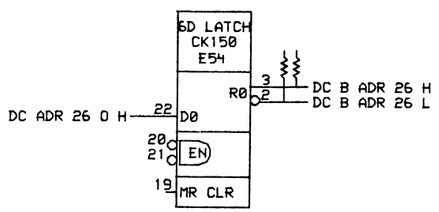


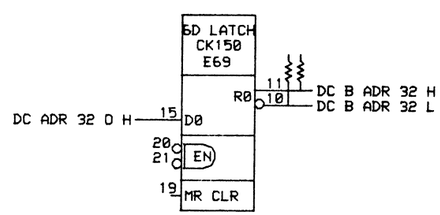


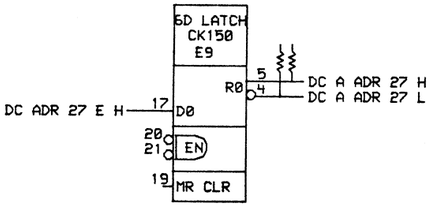


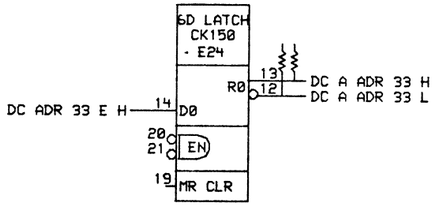


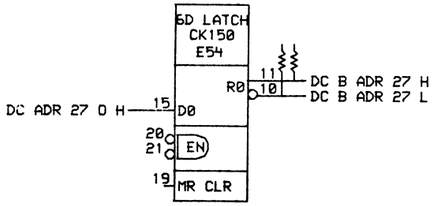


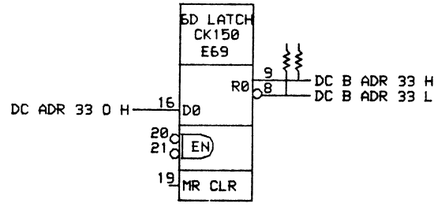


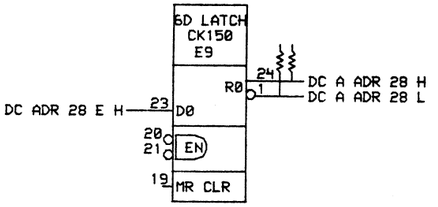


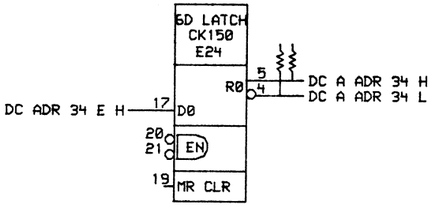


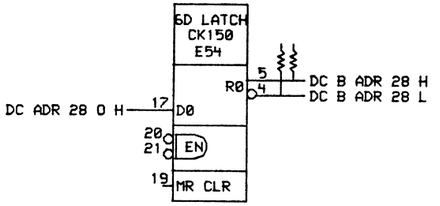


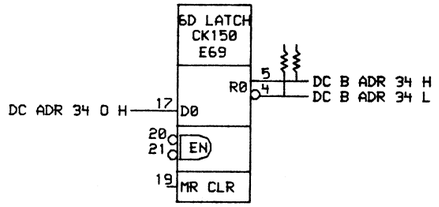


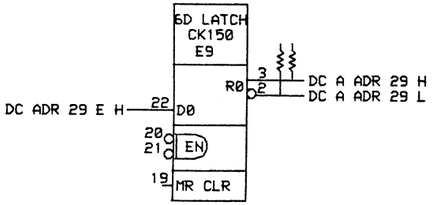


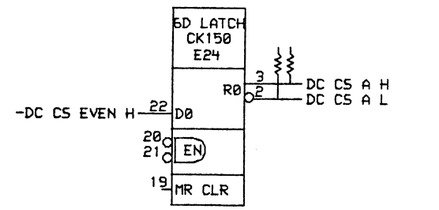


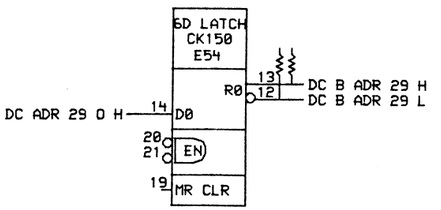


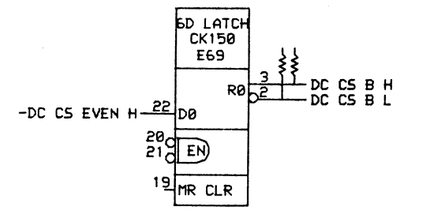


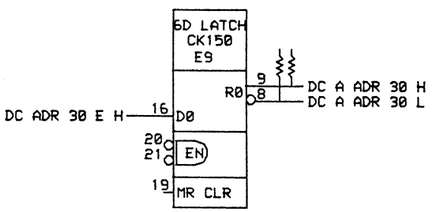


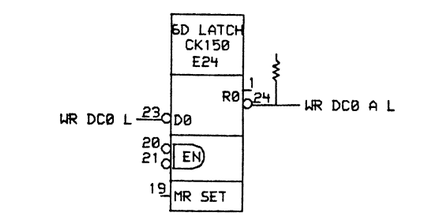


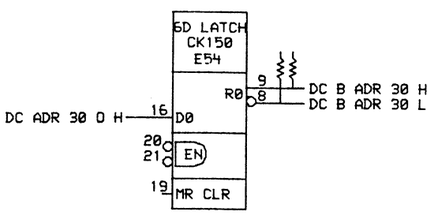


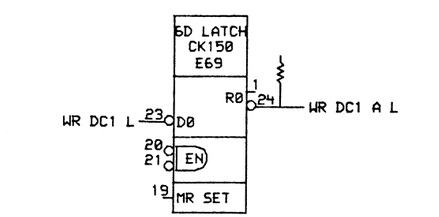












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REVISIONS		
CHK	CHANGE NO.	REV

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DRN. E.Toomey | DATE 09-APR-82 | ENG. | DATE

CHK D. | DATE | BOARD LOCATION:

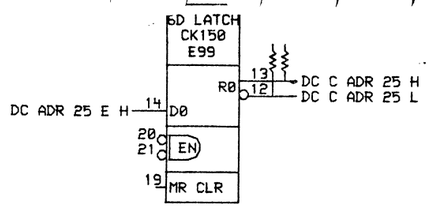
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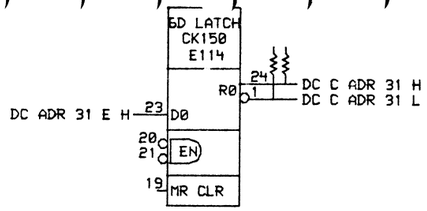
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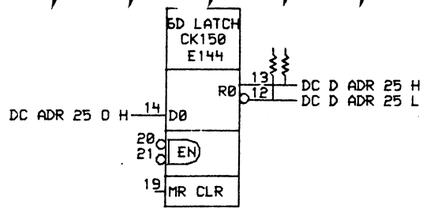
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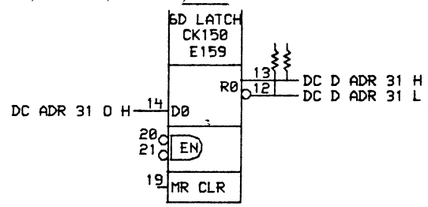
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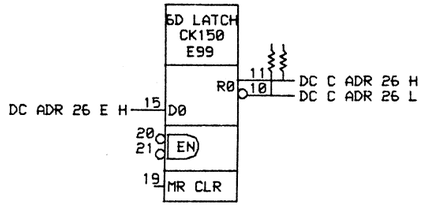
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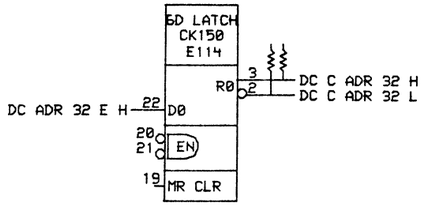


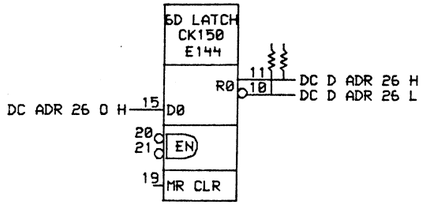


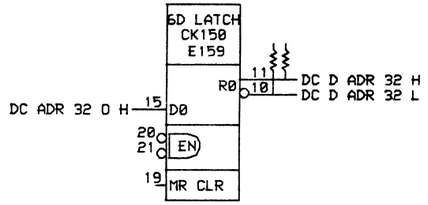


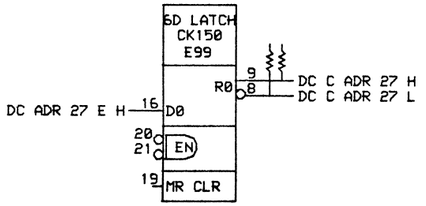


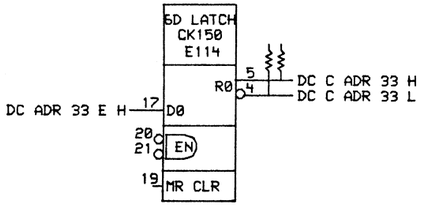


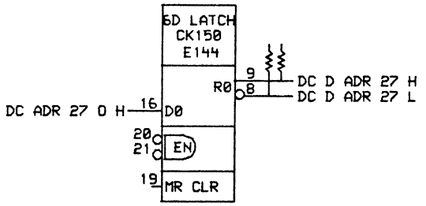


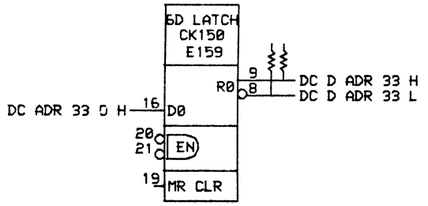


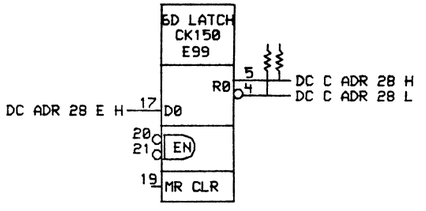


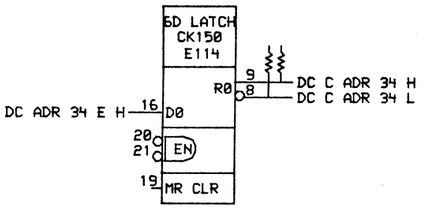


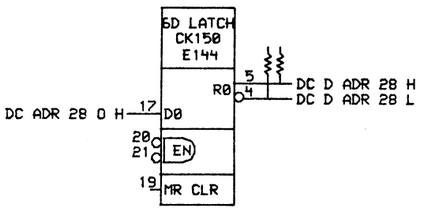


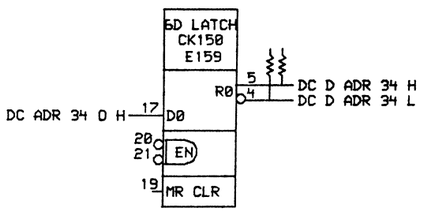


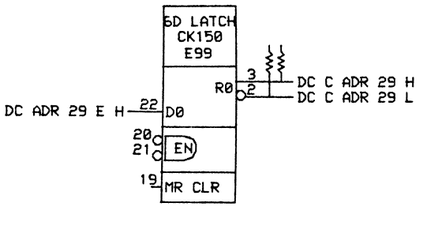


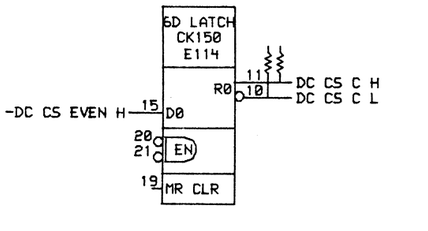


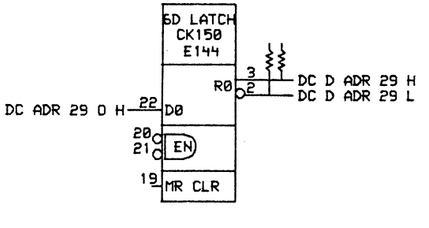


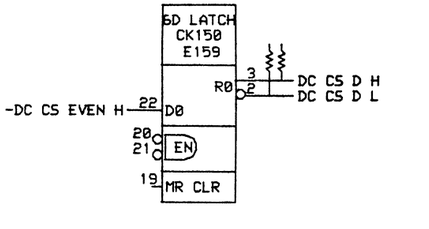


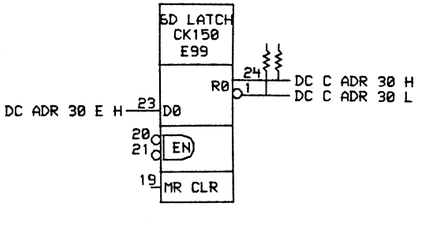


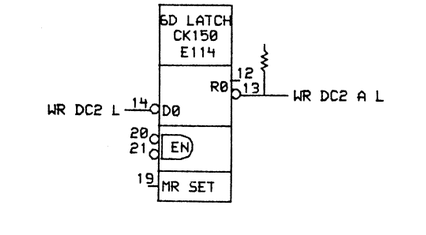


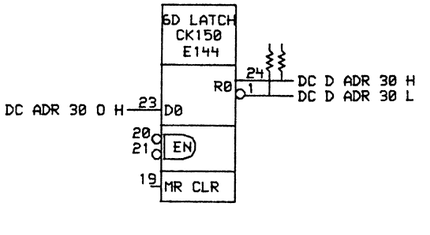


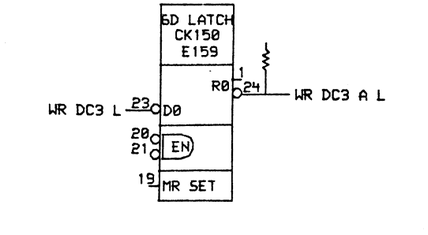












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REVISIONS		
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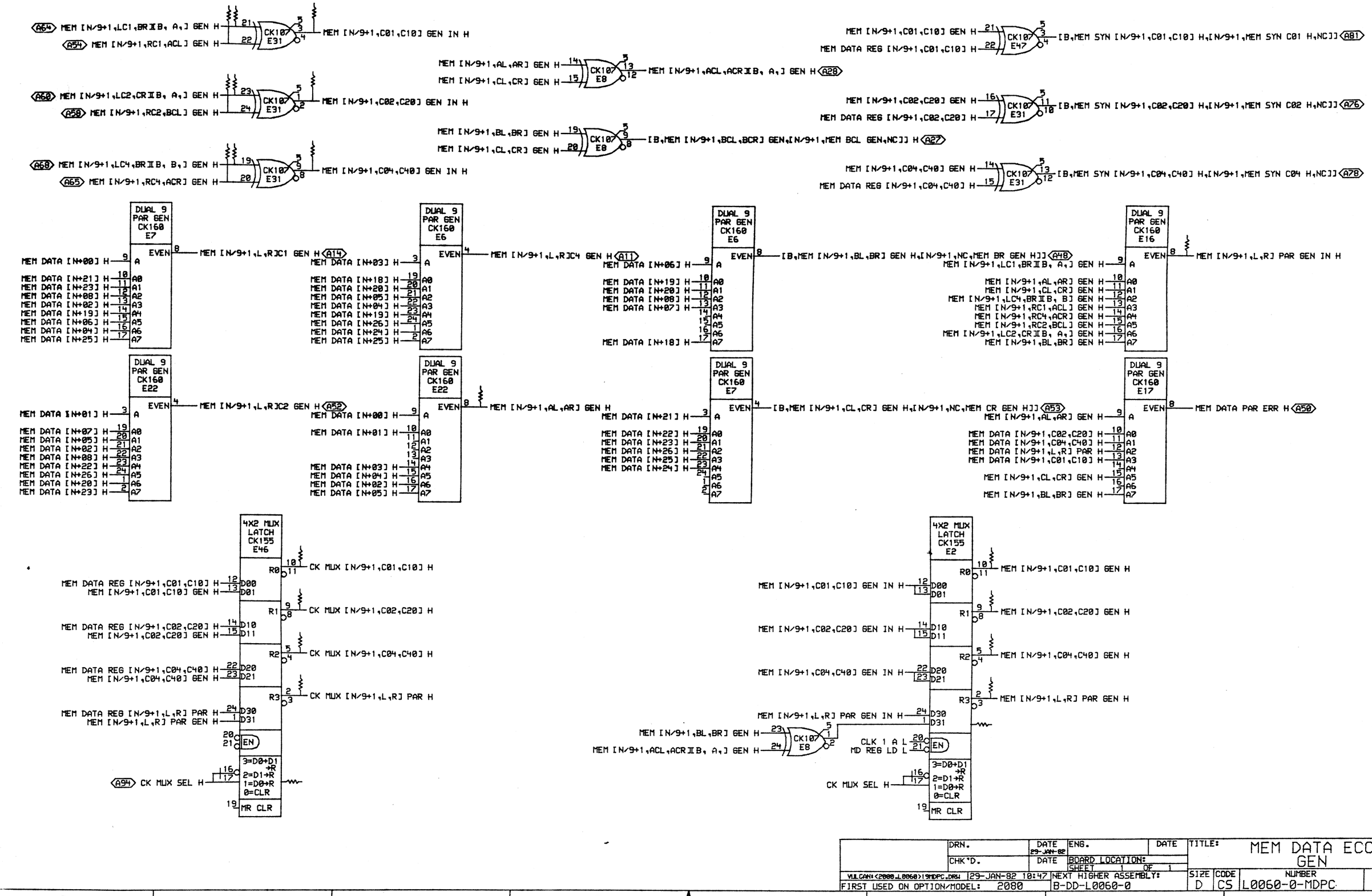
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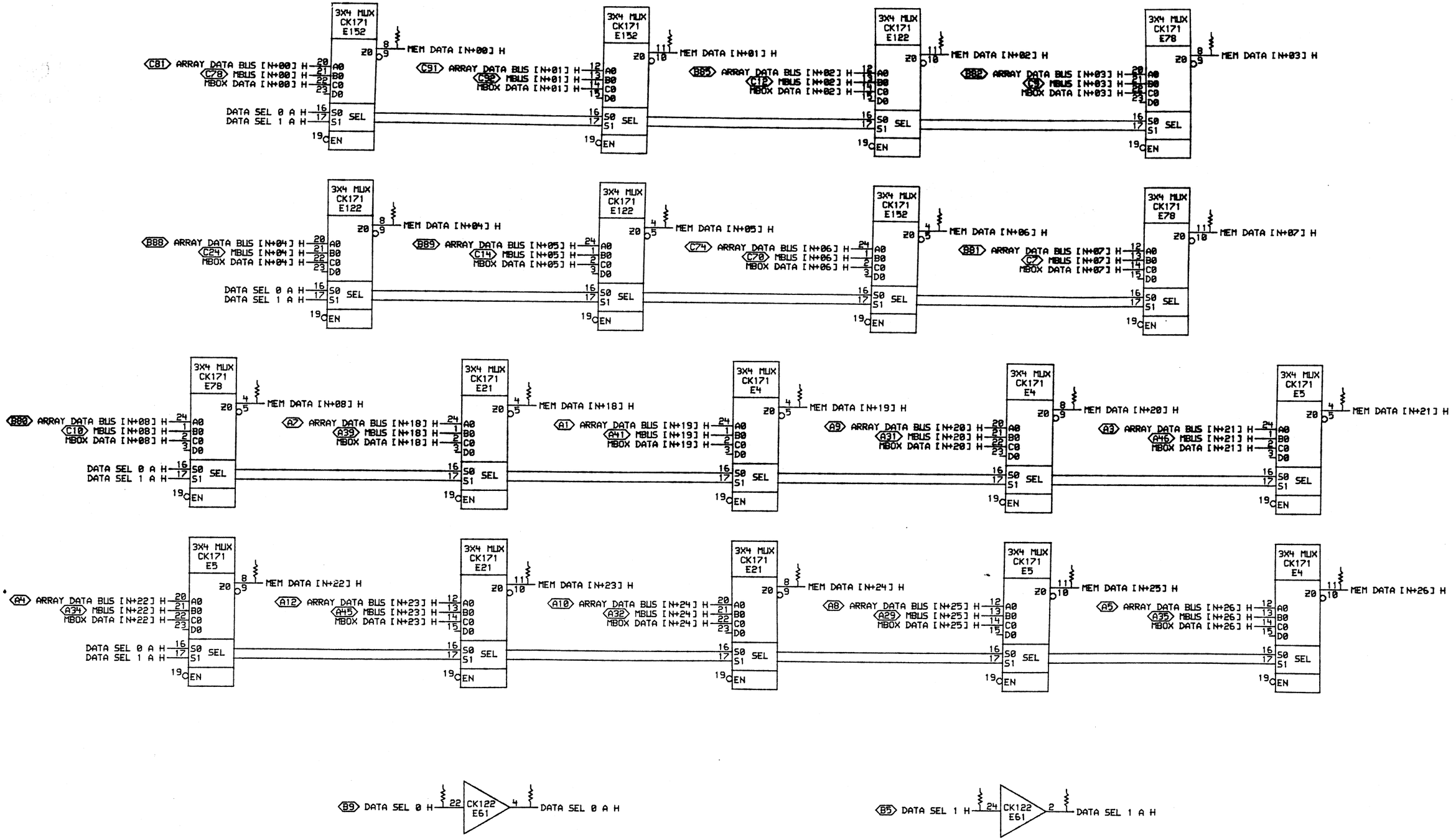
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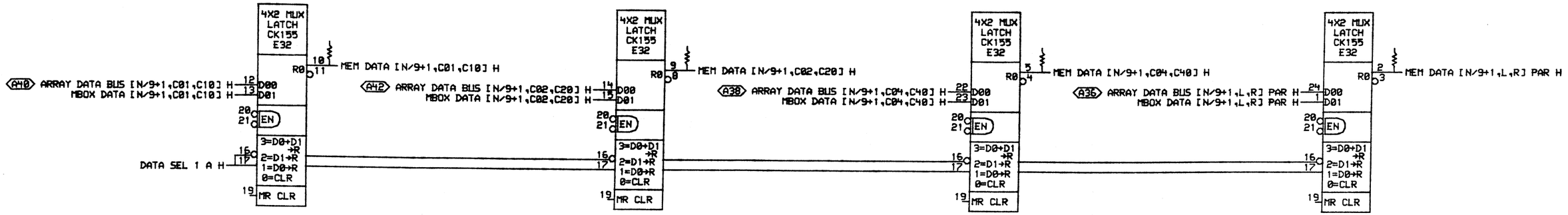
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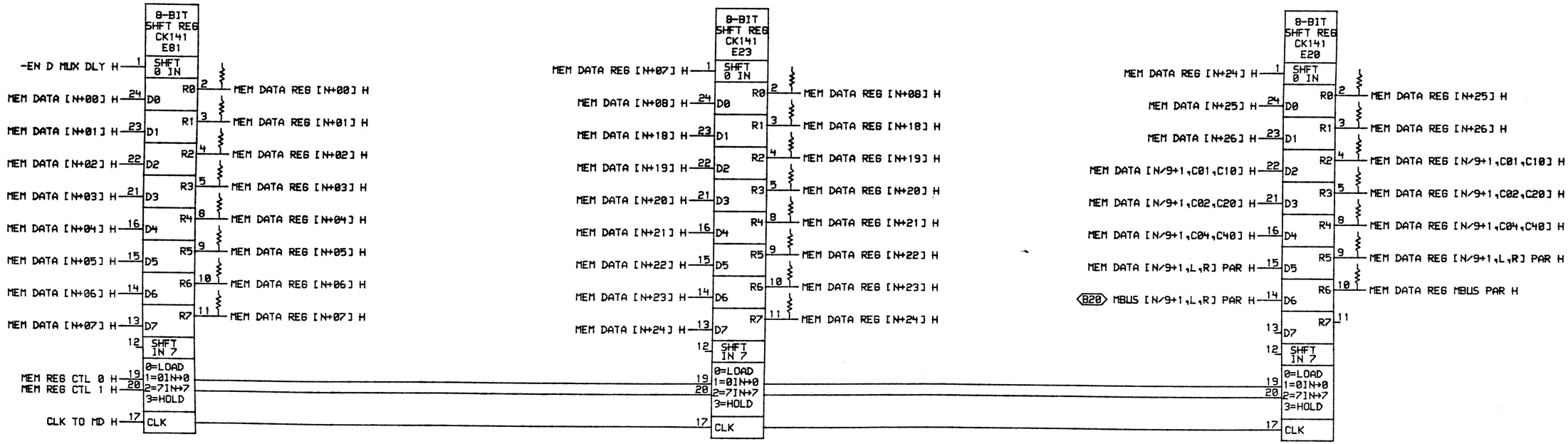


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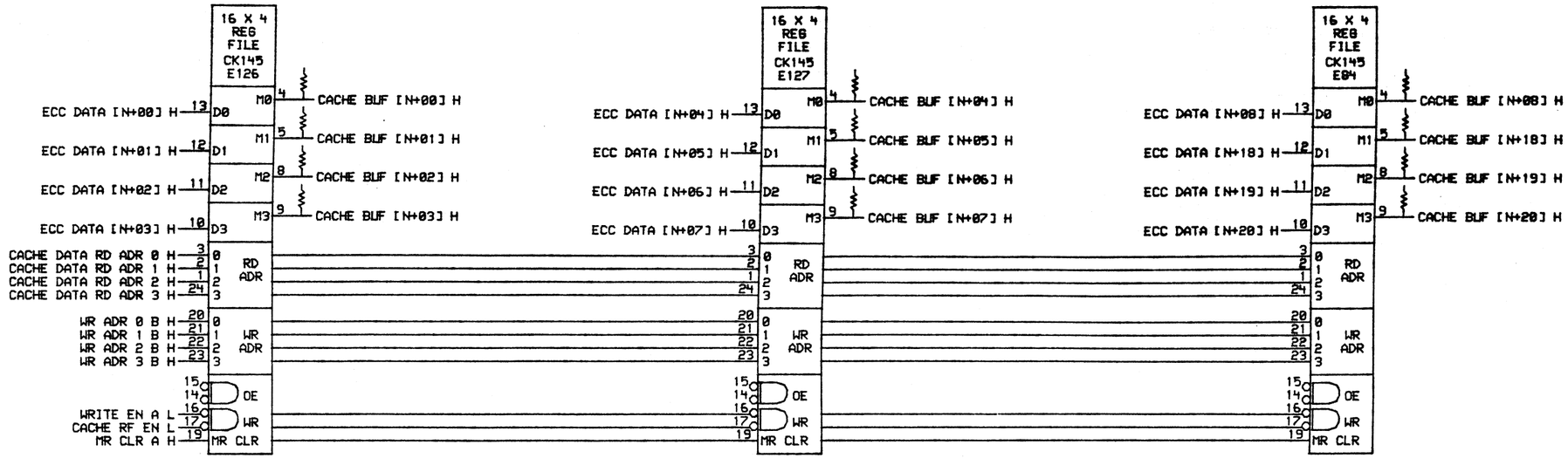


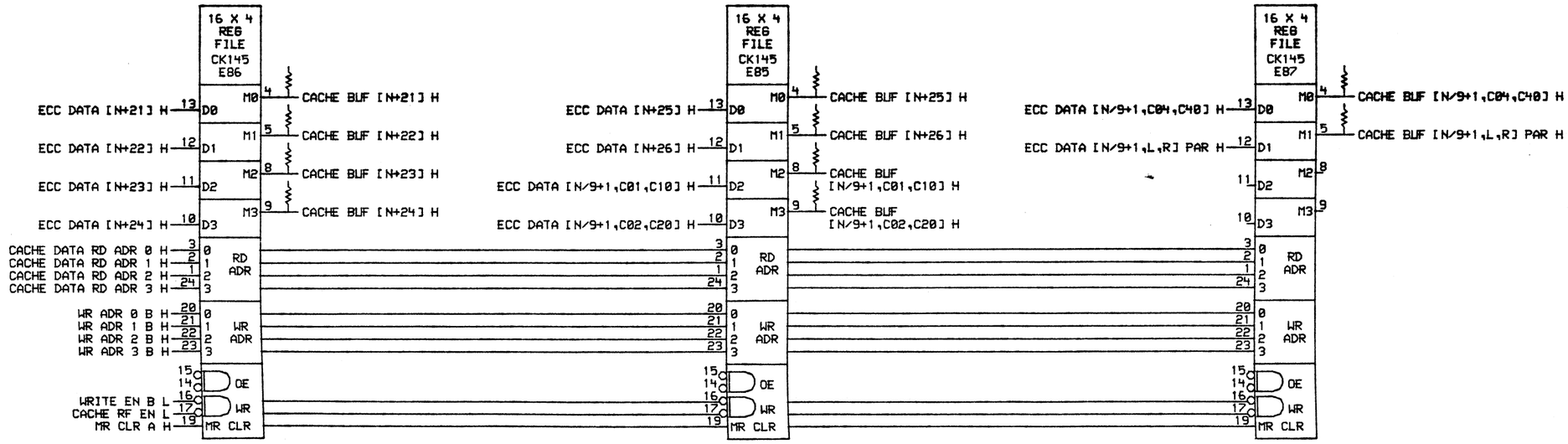
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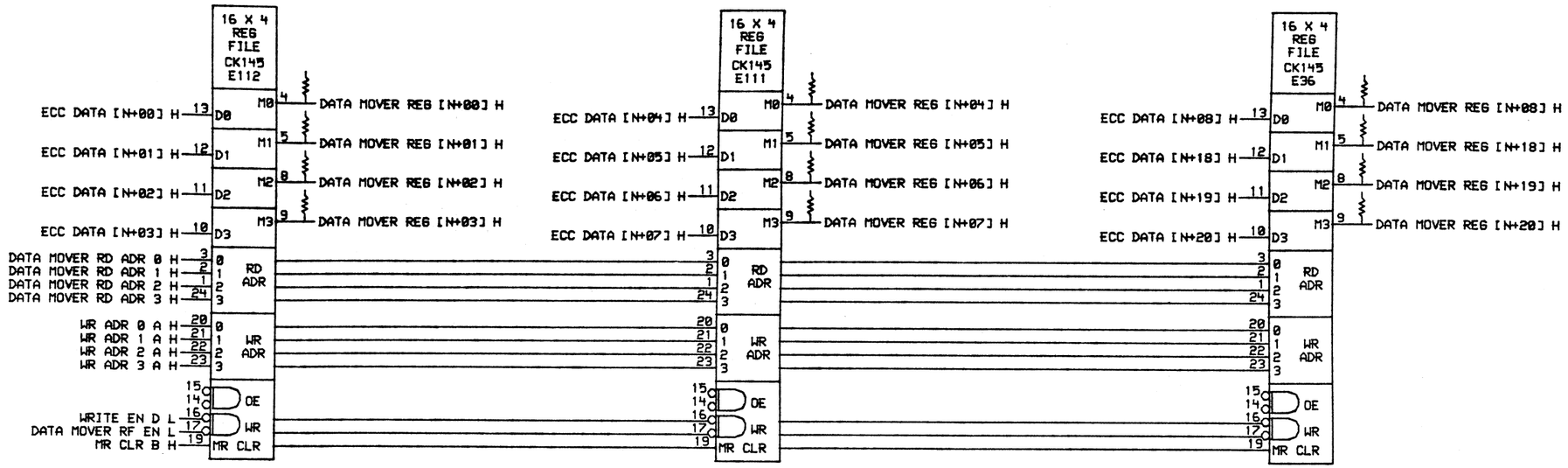


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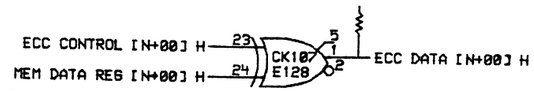


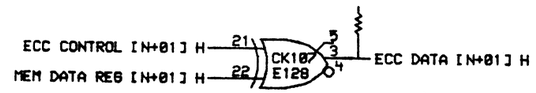


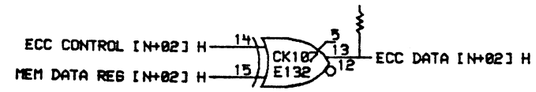
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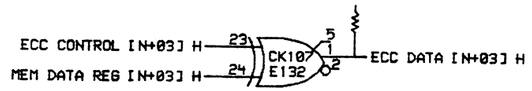


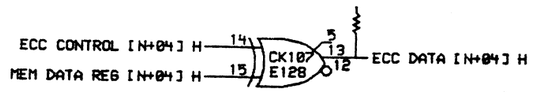
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	CHK'D.	DATE	BOARD LOCATION:		
			SHEET 1 OF 1		
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FIRST USED ON OPTION/MODEL:	2080	B-DD-L0060-0					

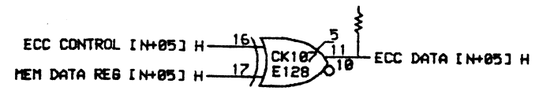


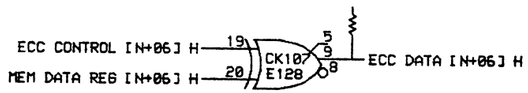


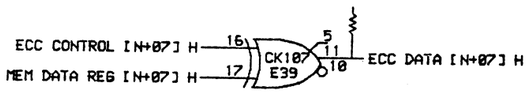


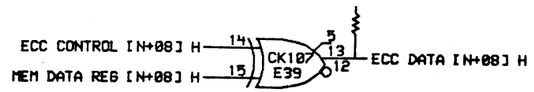


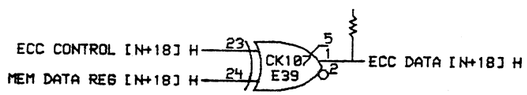


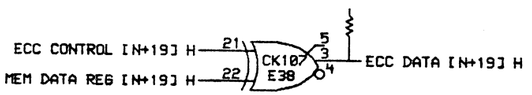


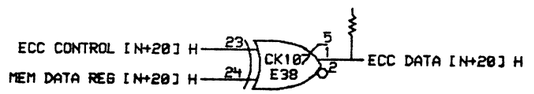


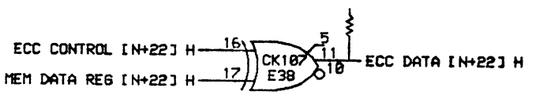


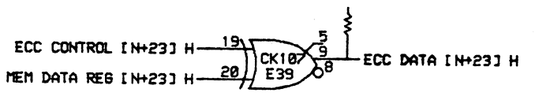


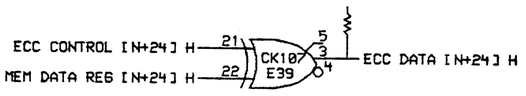


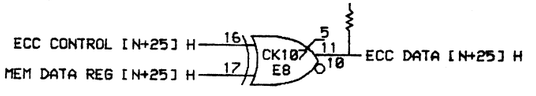


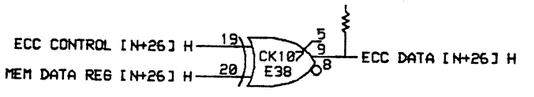


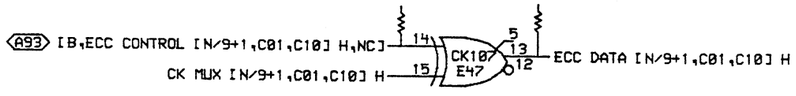


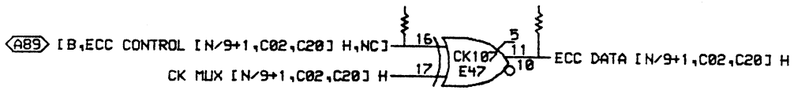


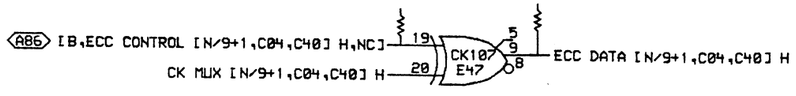


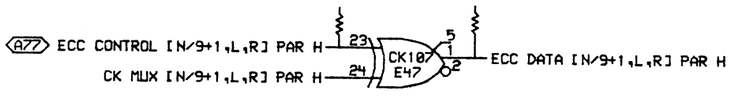




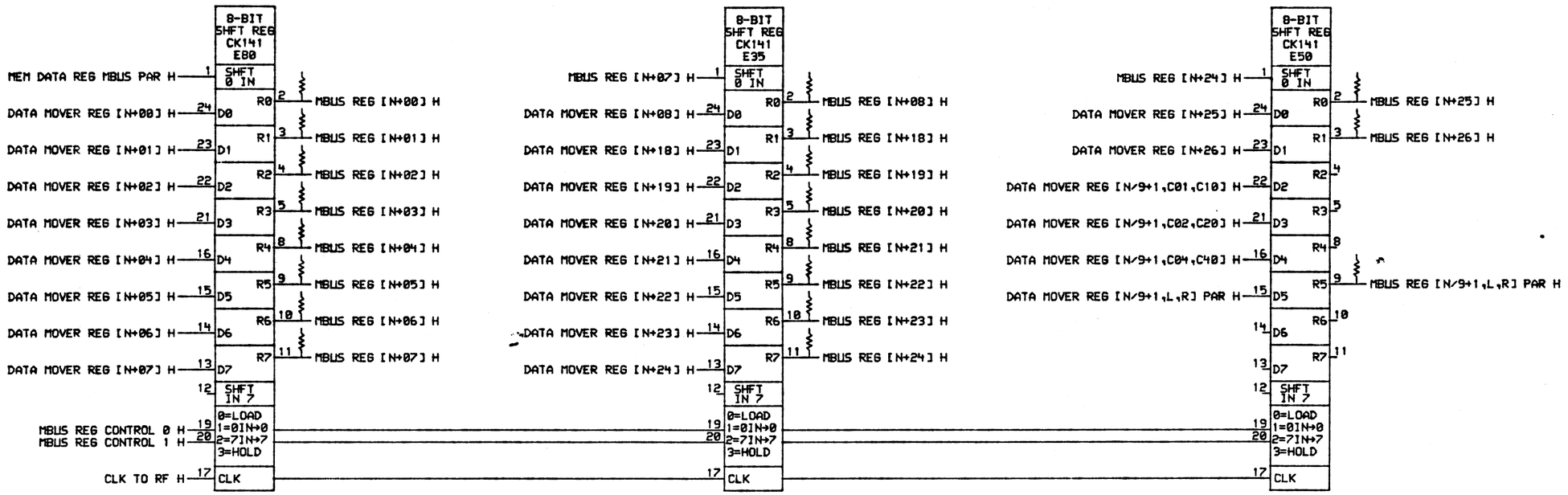


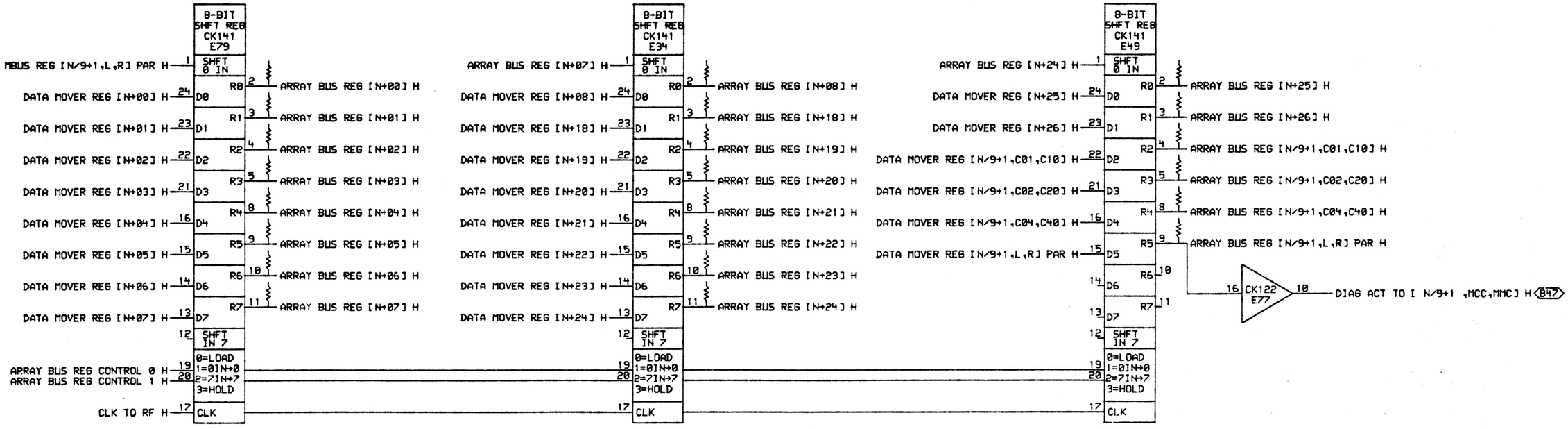




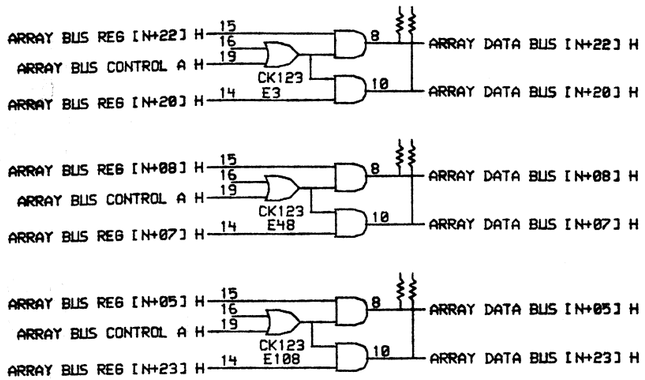


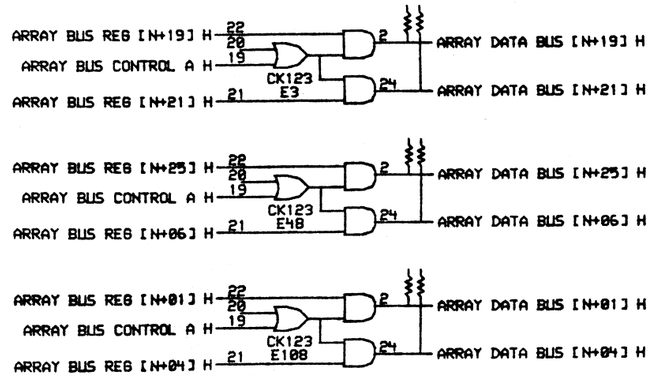
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	CHK'D.	DATE	BOARD LOCATION: SHEET 1 OF 1		
VULCAN:<2000.L0060>19MDPH.DRW 29-JAN-82 10:48			NEXT HIGHER ASSEMBLY:	SIZE D	CODE CS
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0060-0	NUMBER L0060-0-MDPH	REV. *

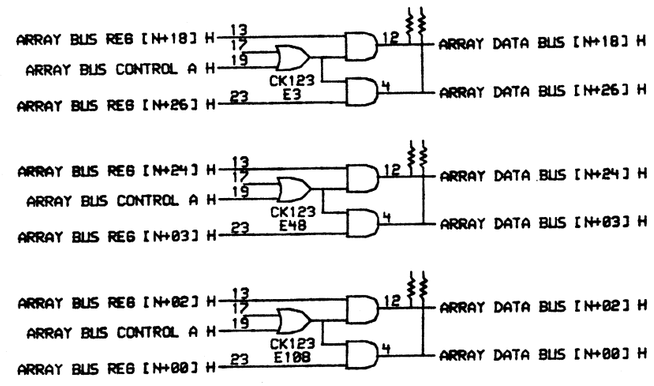


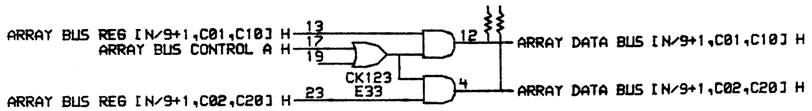


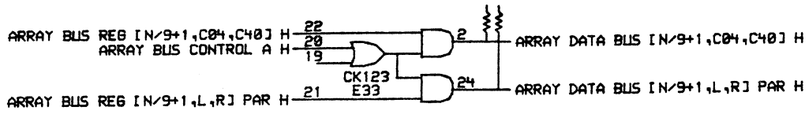
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	CHK'D.	DATE	BOARD LOCATION:		
			SHEET 1 OF 1		
VLI CAN: <2080.1.0060>2GMDP1.DRW	02-AUG-82 12:23	NEXT HIGHER ASSEMBLY:		SIZE D	CODE CS	NUMBER L0060-0-MDPI	REV. *
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0060-0					

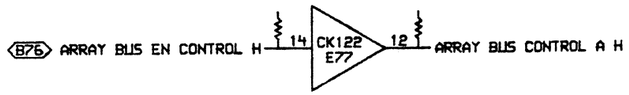


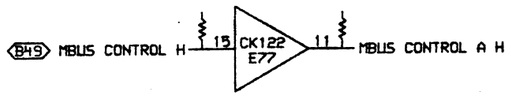


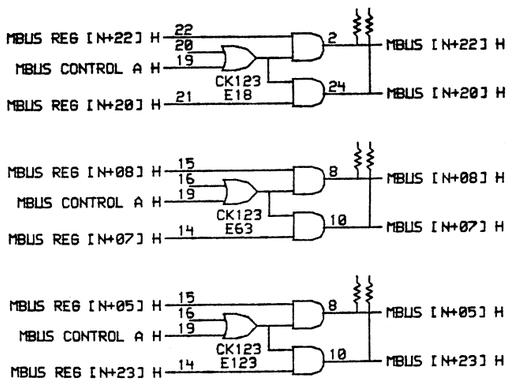


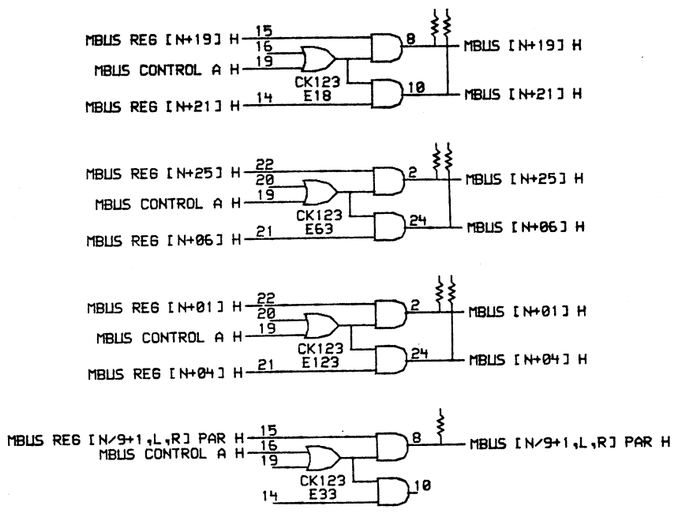


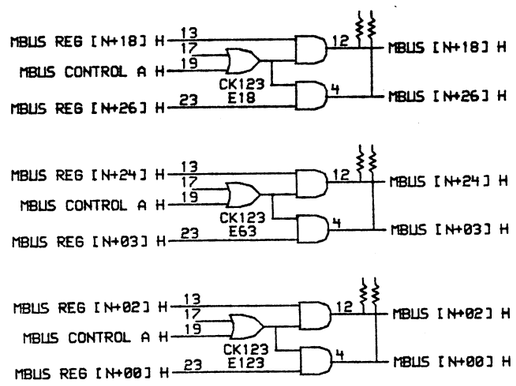












	DRN.	DATE 29-JAN-82	ENG.	DATE	TITLE: ARRAY BUS DATA MBUS DATA
	CHK'D.	DATE	BOARD LOCATION:		
			SHEET 1 OF 1		
VULCAN:<2080.L0060>19MDPJ.DRW	29-JAN-82 10:48	NEXT HIGHER ASSEMBLY:		SIZE D	CODE CS
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0060-0		NUMBER L0060-0-MDPJ	REV. *

DUAL 9 PAR GEN CK160 E91

ARRAY DATA BUS [N+00] H — 9 A
ARRAY DATA BUS [N+06] H — 10 A0
ARRAY DATA BUS [N+04] H — 11 A1
ARRAY DATA BUS [N+01] H — 12 A2
ARRAY DATA BUS [N+05] H — 13 A3
ARRAY DATA BUS [N+02] H — 14 A4
ARRAY DATA BUS [N+07] H — 15 A5
ARRAY DATA BUS [N+03] H — 16 A6
ARRAY DATA BUS [N+08] H — 17 A7

EVEN — 8 — ARRAY BUS BYTE [N/9+1,0,1] PAR H

DUAL 9 PAR GEN CK160 E107

MBUS [N+00] H — 9 A
MBUS [N+06] H — 10 A0
MBUS [N+02] H — 11 A1
MBUS [N+01] H — 12 A2
MBUS [N+05] H — 13 A3
MBUS [N+04] H — 14 A4
MBUS [N+08] H — 15 A5
MBUS [N+03] H — 16 A6
MBUS [N+07] H — 17 A7

EVEN — 8 — MBUS BYTE [N/9+1,0,1] PAR H

DUAL 9 PAR GEN CK160 E16

ARRAY BUS BYTE [N/9+1,0,1] PAR H — 3 A
ARRAY DATA BUS [N/9+1,C02,C20] H — 19 A0
ARRAY DATA BUS [N/9+1,C01,C10] H — 20 A1
ARRAY DATA BUS [N/9+1,C04,C40] H — 21 A2
ARRAY DATA BUS [N/9+1,L,R] PAR H — 22 A3
23 A4
24 A5
ARRAY BUS BYTE [N/9+1,2,3] PAR H — 1 A6
2 A7

EVEN — 4 — IB,ARRAY BUS [N/9+1,L,R] PAR ERROR H,NC] <A37>

DUAL 9 PAR GEN CK160 E1

ARRAY DATA BUS [N+18] H — 3 A
ARRAY DATA BUS [N+23] H — 19 A0
ARRAY DATA BUS [N+24] H — 20 A1
ARRAY DATA BUS [N+20] H — 21 A2
ARRAY DATA BUS [N+25] H — 22 A3
ARRAY DATA BUS [N+22] H — 23 A4
ARRAY DATA BUS [N+19] H — 24 A5
ARRAY DATA BUS [N+21] H — 1 A6
ARRAY DATA BUS [N+26] H — 2 A7

EVEN — 4 — ARRAY BUS BYTE [N/9+1,2,3] PAR H

DUAL 9 PAR GEN CK160 E17

MBUS [N+18] H — 3 A
MBUS [N+21] H — 19 A0
MBUS [N+23] H — 20 A1
MBUS [N+19] H — 21 A2
MBUS [N+26] H — 22 A3
MBUS [N+24] H — 23 A4
MBUS [N+25] H — 24 A5
MBUS [N+20] H — 1 A6
MBUS [N+22] H — 2 A7

EVEN — 4 — MBUS BYTE [N/9+1,2,3] PAR H

DUAL 9 PAR GEN CK160 E62

MBUS BYTE [N/9+1,0,1] PAR H — 9 A
10 A0
11 A1
12 A2
13 A3
14 A4
15 A5
MBUS [N/9+1,L,R] PAR H — 16 A6
MBUS BYTE [N/9+1,2,3] PAR H — 17 A7

EVEN — 8 — IB,MBUS [N/9+1,L,R] PAR ERROR H,NC] <B18>

UNIVSAL DEMUX/ DECODE CK170 E131

0F — 9
1F — 11 — ECC CONTROL [N+00] H
2F — 10 — ECC CONTROL [N+01] H
3F — 8 — ECC CONTROL [N+02] H
4F — 3 — ECC CONTROL [N+03] H
5F — 5 — ECC CONTROL [N+04] H
6F — 4 — ECC CONTROL [N+05] H
7F — 2

<A69> MEM SYN C01[B, A,] H — 12 A0
<A71> MEM SYN C02[B, A,] H — 13 A1
<A70> MEM SYN C04[B, A,] H — 15 A2 — ADR

<C44> IB,BYTE [N/9+1,L,R] A EN L,NC] — 16
<A67> IB,ECC CORRECT [N/9+1,L,R] L,NC] — 20 — ENA

24 B0
1 B1 — ADR

17 — ENB
19

TIE HI H — 21 — 22 — 23 — OUTPUT TURE HIGH

14 — M CNTRL

UNIVSAL DEMUX/ DECODE CK170 E41

0F — 9
1F — 11 — ECC CONTROL [N+06] H
2F — 10 — ECC CONTROL [N+07] H
3F — 8 — ECC CONTROL [N+08] H
4F — 3 — ECC CONTROL [N+18] H
5F — 5 — ECC CONTROL [N+19] H
6F — 4 — ECC CONTROL [N+20] H
7F — 2

12 A0
13 A1
15 A2 — ADR

16 — ENA
20

<A62> IB,BYTE [N/9+1,L,R] B EN L,NC]

24 B0
1 B1 — ADR

17 — ENB
19

TIE HI H — 21 — 22 — 23 — OUTPUT TURE HIGH

14 — M CNTRL

UNIVSAL DEMUX/ DECODE CK170 E40

0F — 9
1F — 11 — ECC CONTROL [N+21] H
2F — 10 — ECC CONTROL [N+22] H
3F — 8 — ECC CONTROL [N+23] H
4F — 3 — ECC CONTROL [N+24] H
5F — 5 — ECC CONTROL [N+25] H
6F — 4 — ECC CONTROL [N+26] H
7F — 2

12 A0
13 A1
15 A2 — ADR

16 — ENA
20

<A63> IB,BYTE [N/9+1,L,R] C EN L,NC]

24 B0
1 B1 — ADR

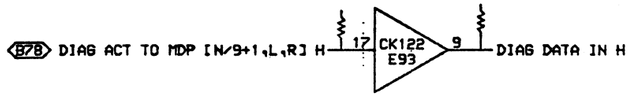
17 — ENB
19

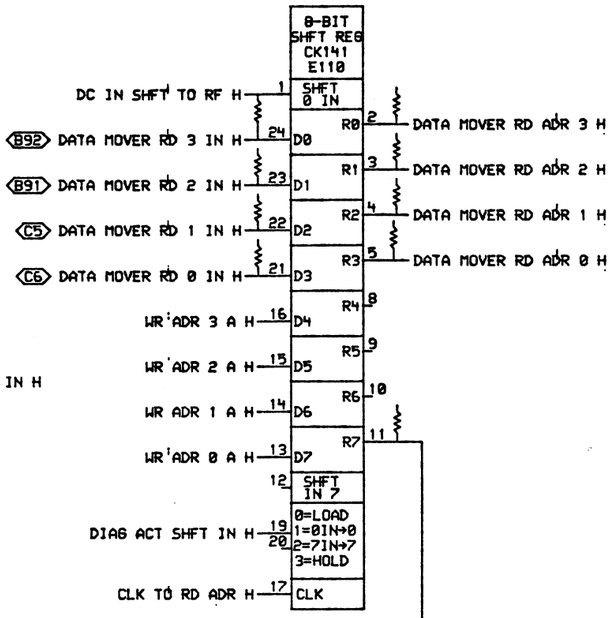
TIE HI H — 21 — 22 — 23 — OUTPUT TURE HIGH

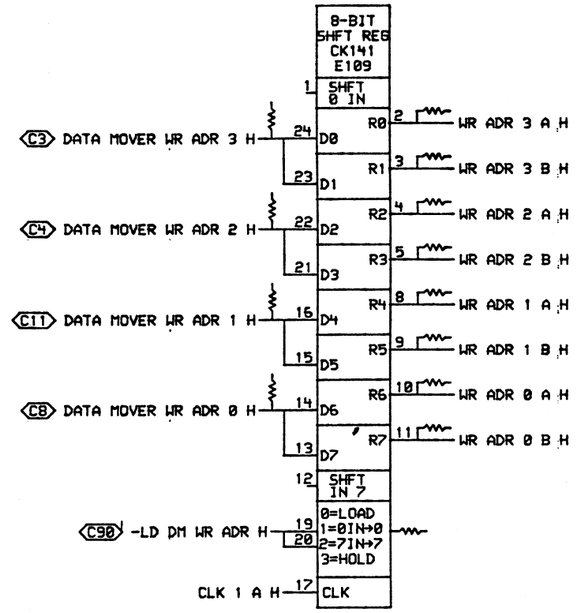
14 — M CNTRL

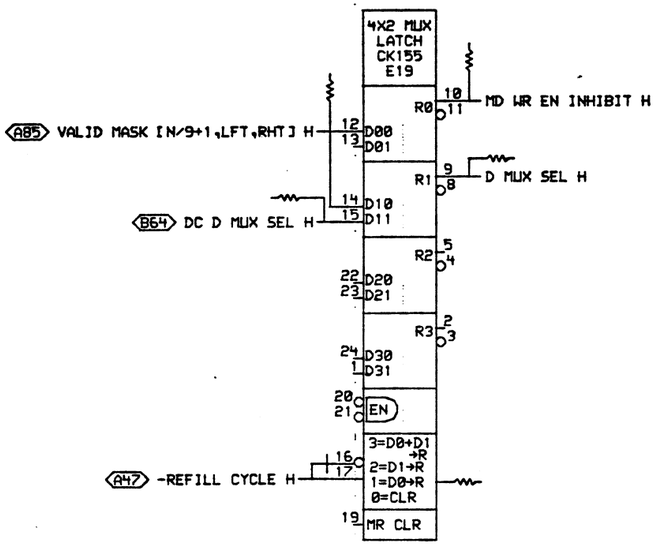
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	CHK'D.	DATE	BOARD LOCATION: SHEET 1 OF 1		
VULCAN:<2080.L0060>19MDPK.DRW 29-JAN-82 10:48			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0060-0-MDPK / REV. *
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0060-0		

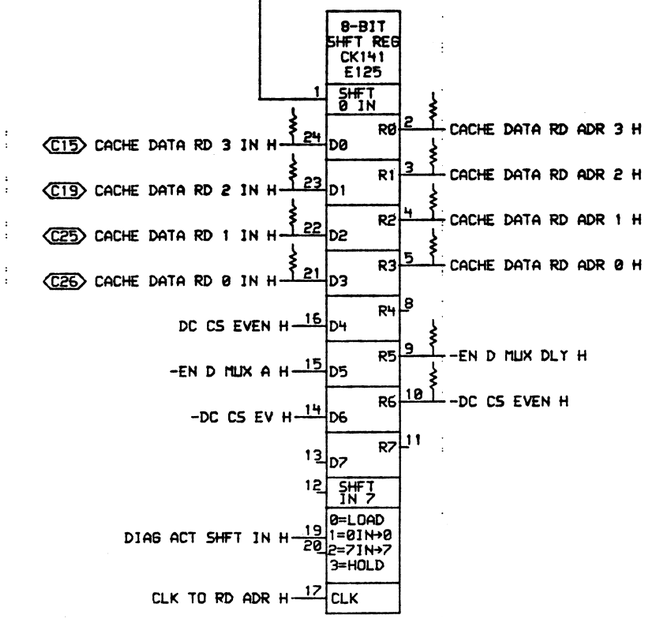
8 7 6 5 4 3 2 1

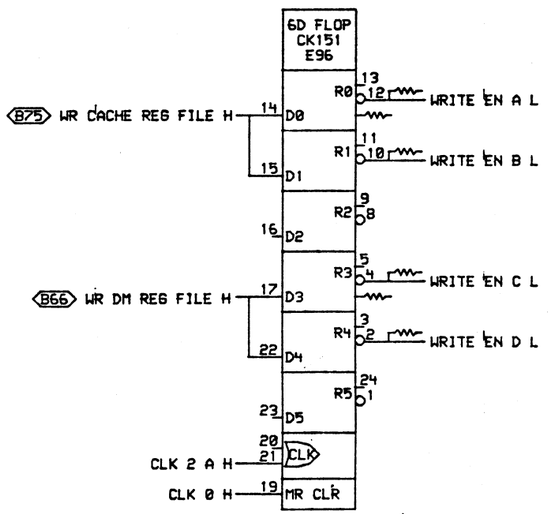












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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. S. Tarmy | DATE 29-AUG-82 | ENG. | DATE

CHK'D. | DATE | BOARD LOCATION: | SHEET 1 OF 1

VULCAN:<2080.L0060>27MDPL.DRW | 28-AUG-82 09:35 | NEXT HIGHER ASSEMBLY: *

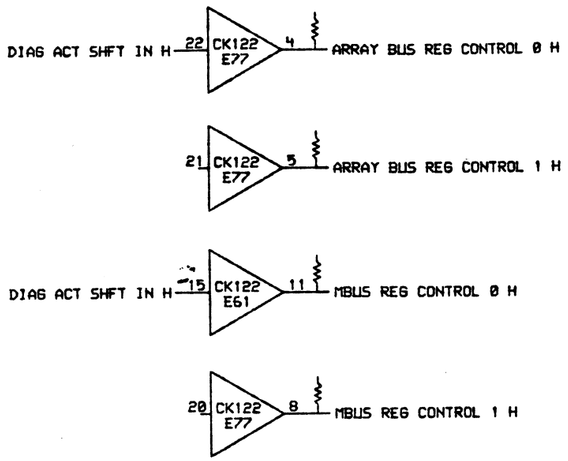
FIRST USED ON OPTION/MODEL: 2080 | B-DD-L0060-0

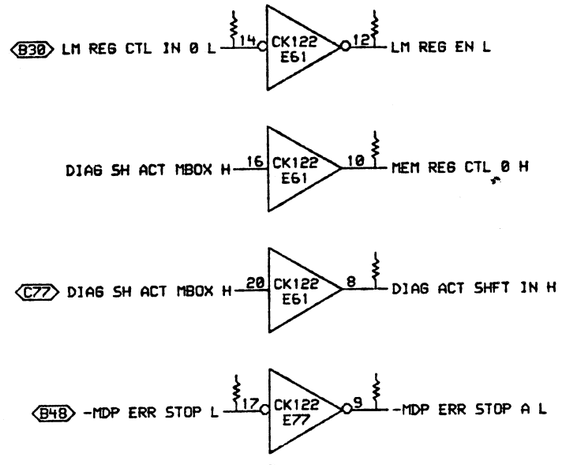
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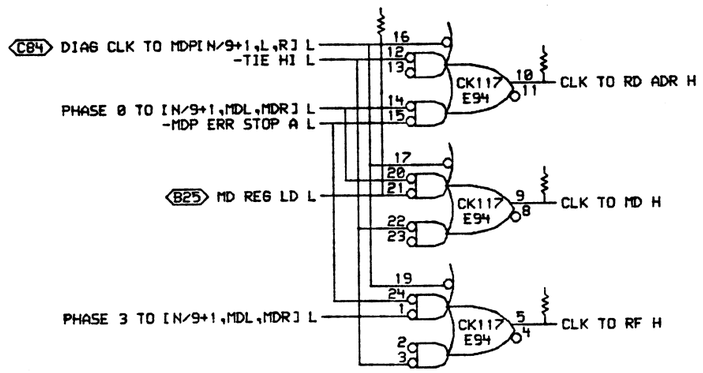
SIZE	CODE	NUMBER	REV.
D	CS	L0060-0-MDPL	*

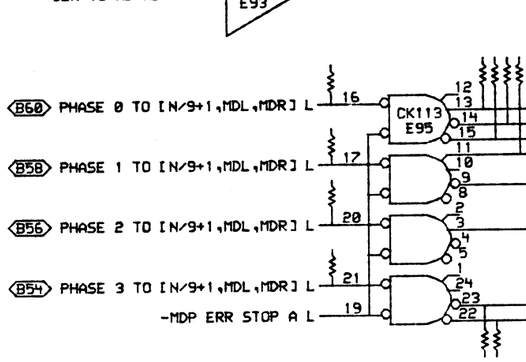
REV. *
NUMBER L0060-0-MDPL
CODE CS
SIZE D
B

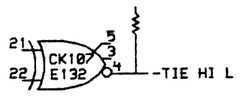
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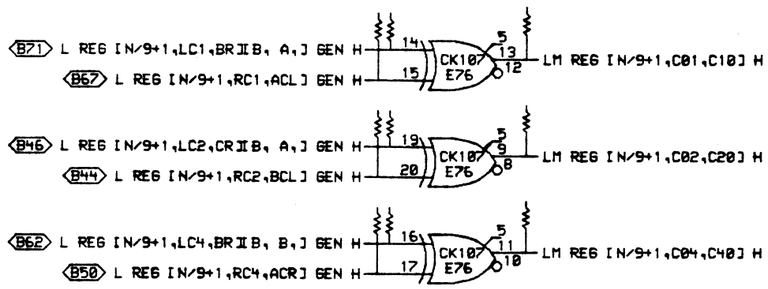


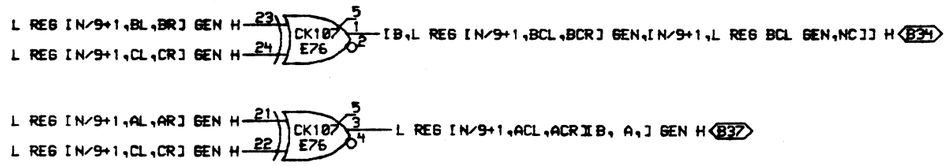


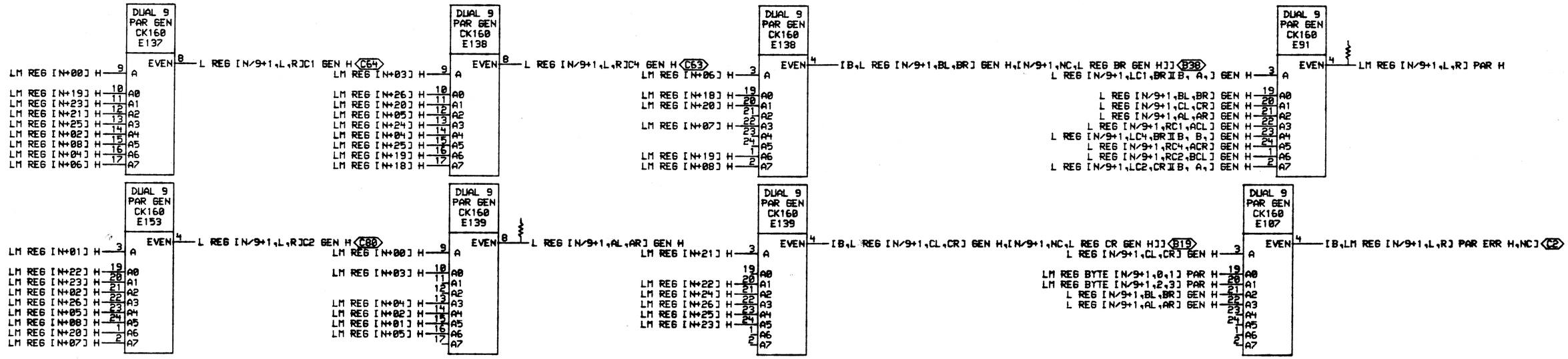
DIAG CLK TO [B,MDPIN/9+1,L,R],[N/9+1,MAP,MCC]] L —— A49

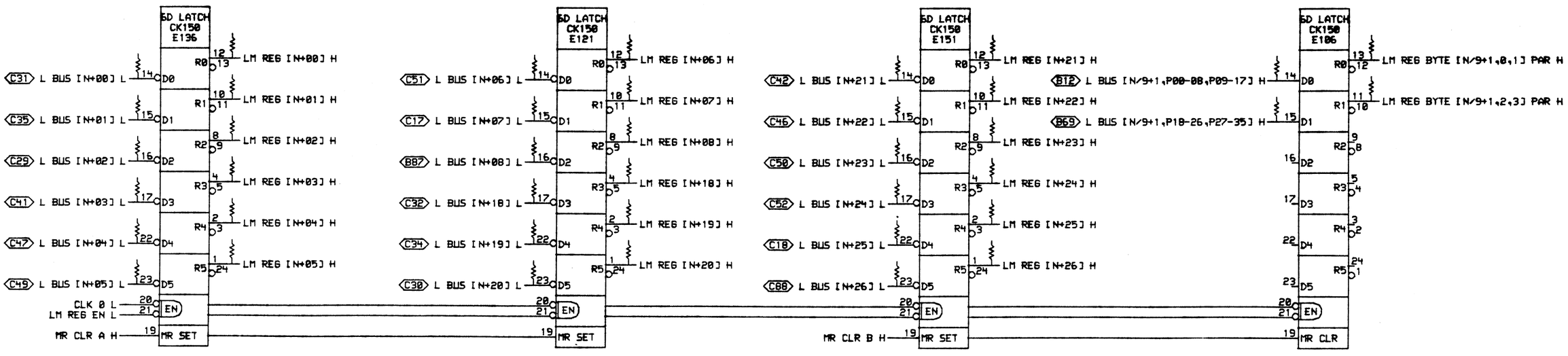
	DRN.	DATE 02-AUG-82	ENG.	DATE	TITLE: REG CONTR.		
	CHK'D.	DATE	BOARD LOCATION:		SHEET 1 OF 1		
VULCAN:(2080,L0060)26/MDPM.DRW 02-AUG-82 10:30			NEXT HIGHER ASSEMBLY:		SIZE D	CODE CS	NUMBER L0060-0-MDPM	REV. *
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0060-0				

8 7 6 5 4 3 2 1









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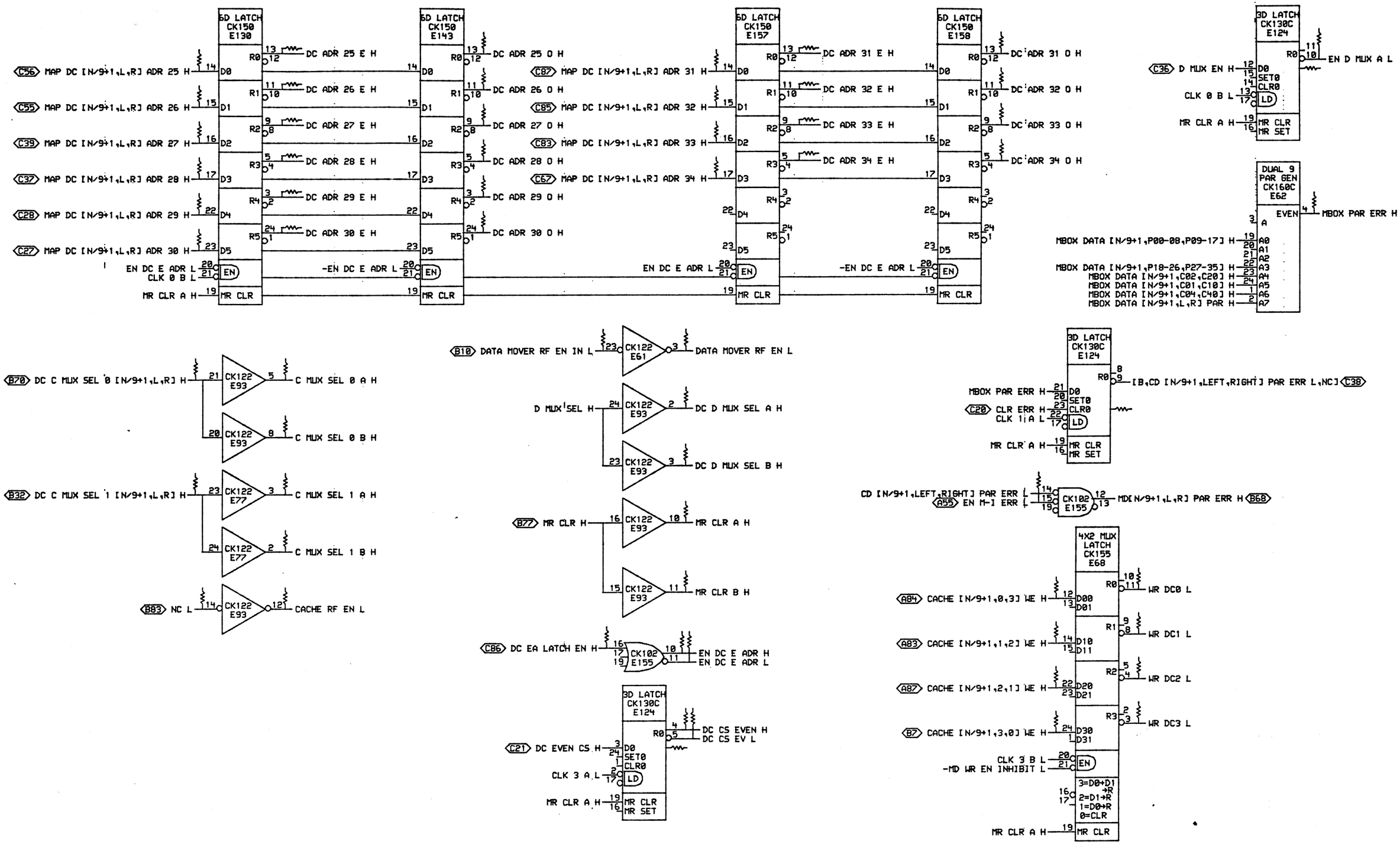
REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. S.Tormey — DATE 28-MAY-82 — ENG. — DATE
CHK D. — DATE — BOARD LOCATION: — SHEET 1 OF 1
VULCAN <2000,L0060>L0060-0-MDPN.DRW 27-MAY-82 21:46
NEXT HIGHER ASSEMBLY: B-DD-L0060-0
FIRST USED ON OPTION/MODEL: 2080

TITLE: LBUS CHECK BITS GEN

SIZE	CODE	NUMBER	REV.
D	CS	L0060-0-MDPN	*



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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. D. Doody | DATE 25-AUG-82 | ENG. | DATE

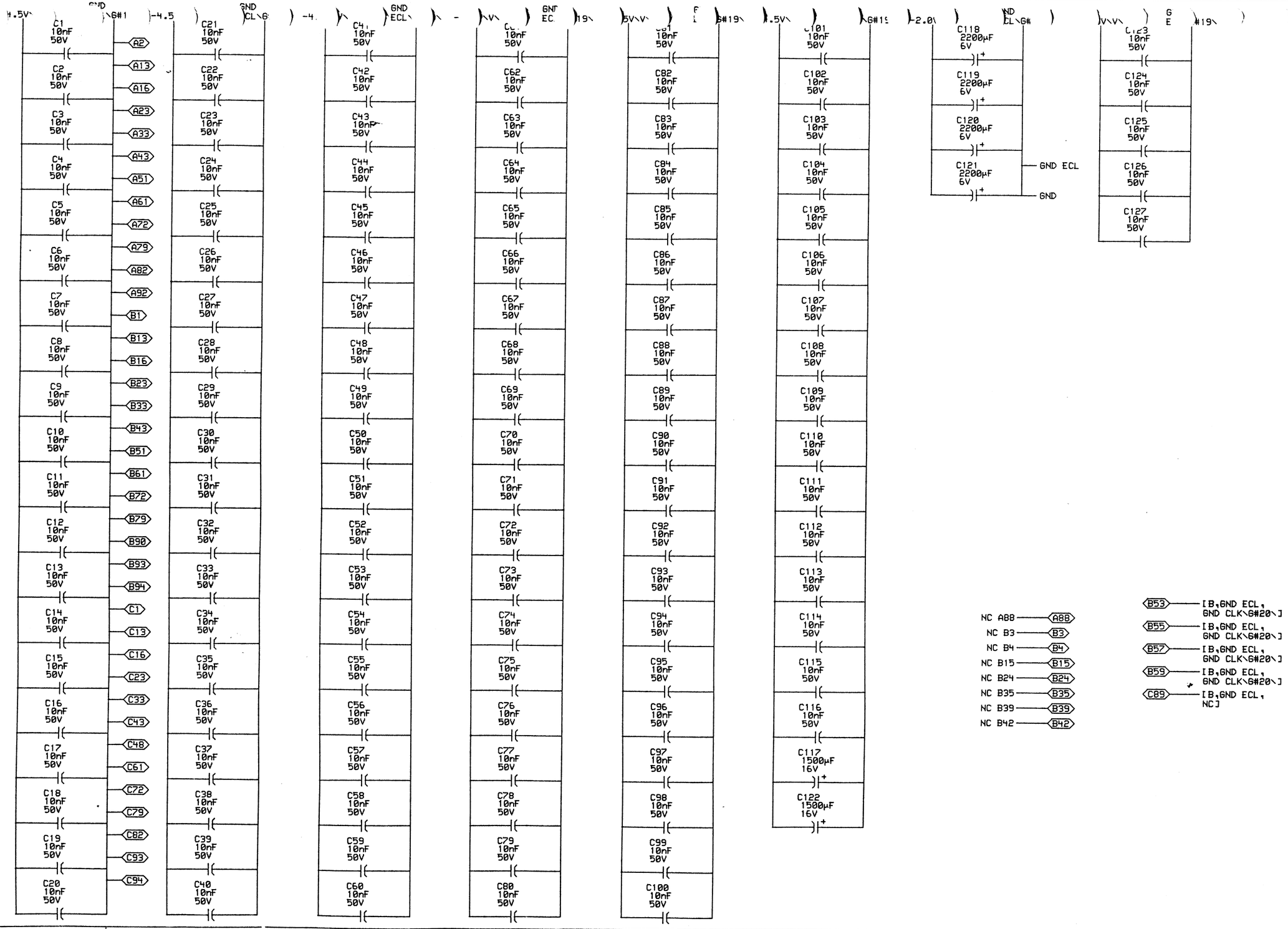
CHK D. | DATE | BOARD LOCATION: SHEET 1 OF 1

VULCAN:<2080.L0060>27MDPP.DRW 28-AUG-82 09:28 | NEXT HIGHER ASSEMBLY: B-DD-L0060-0

FIRST USED ON OPTION/MODEL: 2080

TITLE: INPUT BUFFERS

SIZE	CODE	NUMBER	REV.
D	CS	L0060-0-MDPP	*



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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN J.Looney | DATE 29-APR-82 | ENG. | DATE

CHK D. | DATE | BOARD LOCATION: | SHEET 1 OF 1

VLB,CAN:<2000.L0060>Z*MDPR.DRW | 28-APR-82 22:02 | NEXT HIGHER ASSEMBLY: B-DD-L0060-0

FIRST USED ON OPTION/MODEL: 2000

TITLE: MDP POWER & GROUND

SIZE	CODE	NUMBER	REV.
D	C5	L0060-0-MDPR	*

VULCAN:<2080.CAM>83CAM1
VULCAN:<2080.CAM>61CAM2
VULCAN:<2080.CAM>62CAM3
VULCAN:<2080.CAM>67CAM4
VULCAN:<2080.CAM>77CAM5
VULCAN:<2080.CAM>45CAM6
VULCAN:<2080.CAM>83CAM7
VULCAN:<2080.CAM>50CAM8
VULCAN:<2080.CAM>14CAM9
VULCAN:<2080.CAM>59CAMA
VULCAN:<2080.CAM>79CAMB
VULCAN:<2080.CAM>CAMC
VULCAN:<2080.CAM>10CAMD
VULCAN:<2080.CAM>79CAME
VULCAN:<2080.CAM>75CAMF
VULCAN:<2080.CAM>81CAMG
VULCAN:<2080.CAM>78CAMH
VULCAN:<2080.CAM>84CAMI
VULCAN:<2080.CAM>81CAMJ
VULCAN:<2080.CAM>82CAMK
VULCAN:<2080.CAM>12CAML
VULCAN:<2080.CAM>80CAMM
VULCAN:<2080.CAM>81CAMN
VULCAN:<2080.CAM>22CAMP
VULCAN:<2080.CAM>2CAMR
VULCAN:<2080.CAM>85CAMS

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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. S. Toomy
CHK'D.
DATE 10-FEB-83
DATE
ENG.
DATE
BOARD LOCATION:
SHEET OF
TITLE:

ARCH:<UHLER.PLOTS>LCAM.DPL.1 10-FEB-83 03:05
FIRST USED ON OPTION/MODEL:
NEXT HIGHER ASSEMBLY:

SIZE	CODE	NUMBER	REV.
D	SP		

E173 CK141
E172 CK136
Z138 Z137 Z136
E171 CK155
E170 CK141
Z135 Z134 Z133
E169 CK422
E168 CK422
Z132 Z131 Z130
E167 CK165
E166 CK101
Z129 Z128 Z127
E165 CK101
E164 CK102
Z126 Z125 Z124
E163 CK118
E162 CK102
Z123 Z122 Z121
E161 CK141

E160 CK102
E159 CK131
E158 CK170
E157 CK102
E156 CK165
E155 CK151
E154 CK117
E153 CK102
E152 CK136
E151 CK151
E150 CK117
E149 CK102
E148 CK107
E147 CK150
E146 CK102
E145 CK171
E144 CK141
E143 CK141
E142 CK155

E141 CK131
E140 CK151
Z114 Z113 Z112
E139 CK102
E138 CK101
Z111 Z110 Z109
E137 CK163
E136 CK155
Z108 Z107 Z106
E135 CK155
E134 CK170
Z105 Z104 Z103
E133 CK102
E132 CK171
Z102 Z101 Z100
E131 CK150
E130 CK141
Z099 Z098 Z097
E129 CK123

E128 CK164
E127 CK102
E126 CK164
E125 CK151
E124 CK131
E123 CK165
E122 CK171
E121 CK102
E120 CK151
E119 CK102
E118 CK155
E117 CK102
E116 CK117
E115 CK141
E114 CK155
E113 CK155
E112 CK141
E111 CK123
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E109 CK155
E108 CK151
Z090 Z089 Z088
E107 CK101
E106 CK102
Z087 Z086 Z085
E105 CK141
E104 CK117
Z084 Z083 Z082
E103 CK102
E102 CK141
Z081 Z080 Z079
E101 CK136
E100 CK155
Z078 Z077 Z076
E099 CK171
E098 CK171
Z075 Z074 Z073
E097 CK141

E096 CK141
E095 CK155
E094 CK141
E093 CK155
E092 CK160
E091 CK145
E090 CK145
E089 CK117
E088 CK113
E087 CK170
E086 CK150
E085 CK171
E084 CK145
E083 CK160
E082 CK145
E081 CK155
E080 CK155
E079 CK123
E078 CK141

E077 CK102
E076 CK155
Z066 Z065 Z064
E075 CK141
E074 CK170
Z063 Z062 Z061
E073 CK102
E072 CK131
Z060 Z059 Z058
E071 CK141
E070 CK102
Z057 Z056 Z055
E069 CK151
E068 CK155
Z054 Z053 Z052
E067 CK155
E066 CK155
Z051 Z050 Z049
E065 CK122

E064 CK171
E063 CK171
E062 CK141
E061 CK145
E060 CK145
E059 CK155
E058 CK136
E057 CK155
E056 CK107
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E046 CK123

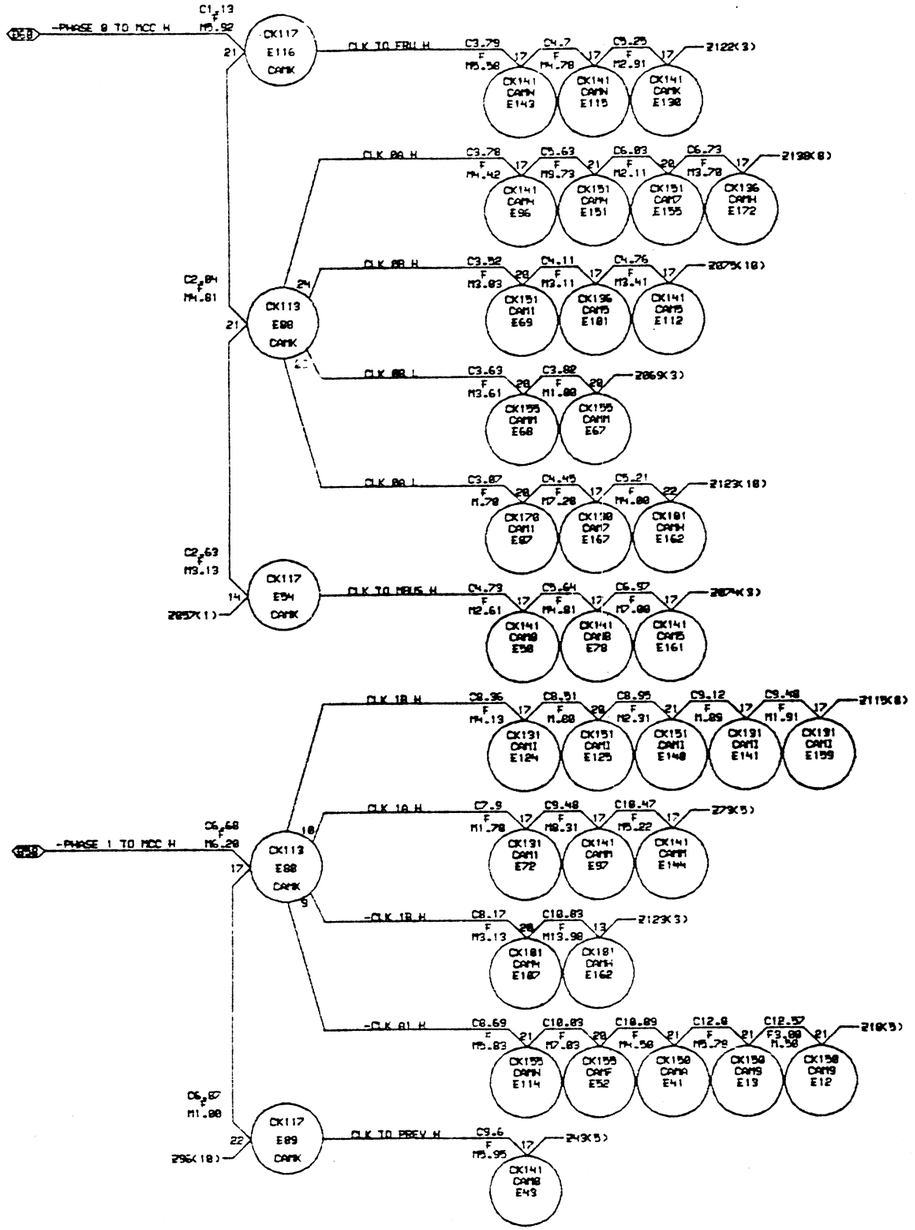
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E044 CK171
Z042 Z041 Z040
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E042 CK155
Z039 Z038 Z037
E041 CK150
E040 CK160
Z036 Z035 Z034
E039 CK422
E038 CK141
Z033 Z032 Z031
E037 CK422
E036 CK160
Z030 Z029 Z028
E035 CK141
E034 CK422
Z027 Z026 Z025
E033 CK123

E032 CK141
E031 CK141
E030 CK422
E029 CK422
E028 CK155
E027 CK141
E026 CK422
E025 CK422
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E023 CK422
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E021 CK422
E020 CK160
E019 CK422
E018 CK422
E017 CK123
E016 CK422
E015 CK160
E014 CK122

E013 CK150
E012 CK150
Z018 Z017 Z016
E011 CK422
E010 CK422
Z015 Z014 Z013
E009 CK422
E008 CK141
Z012 Z011 Z010
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Z003 Z002 Z001
E001 CK170

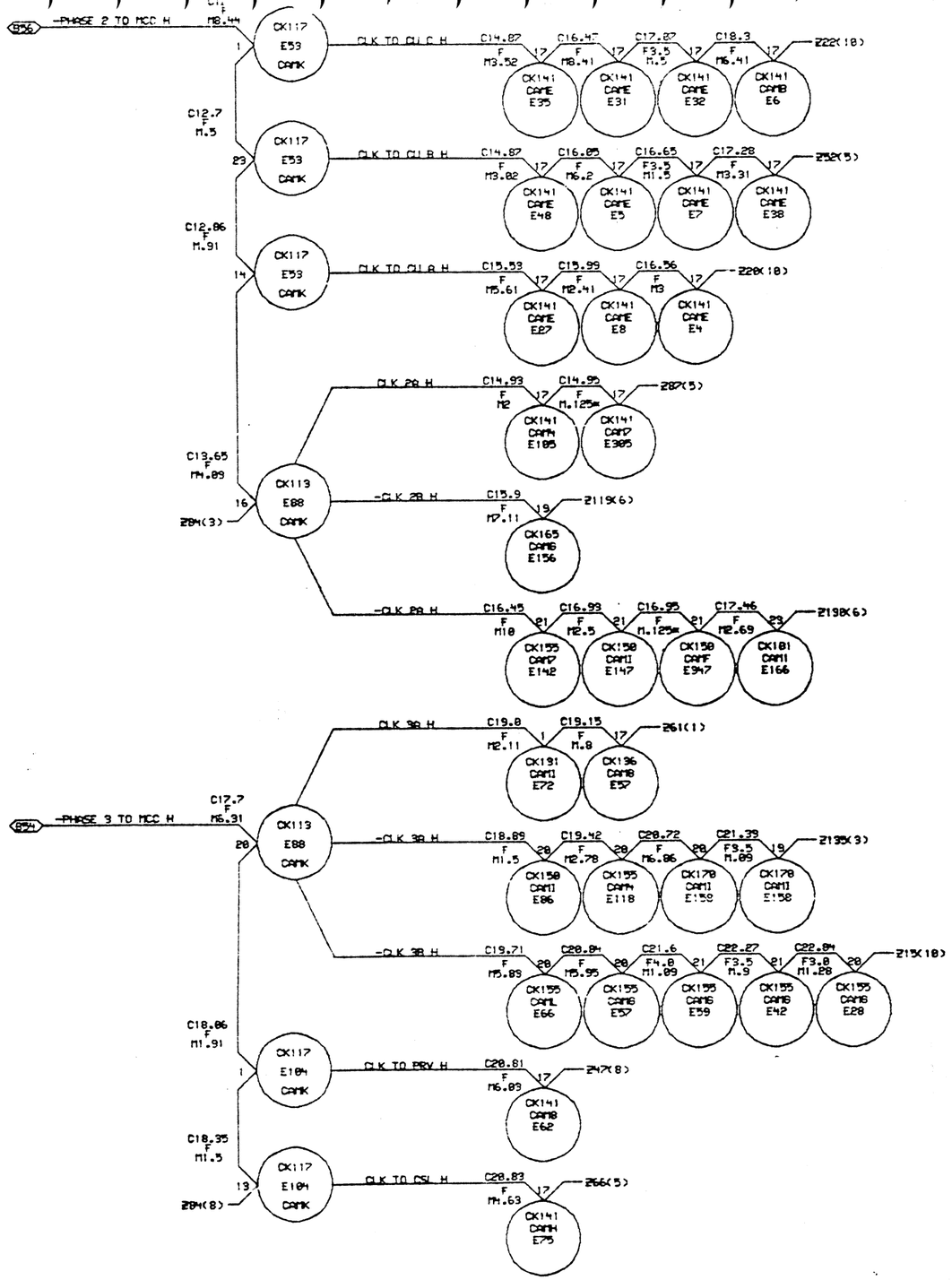
C122 C121 C120 C119 C118 C117

CAM



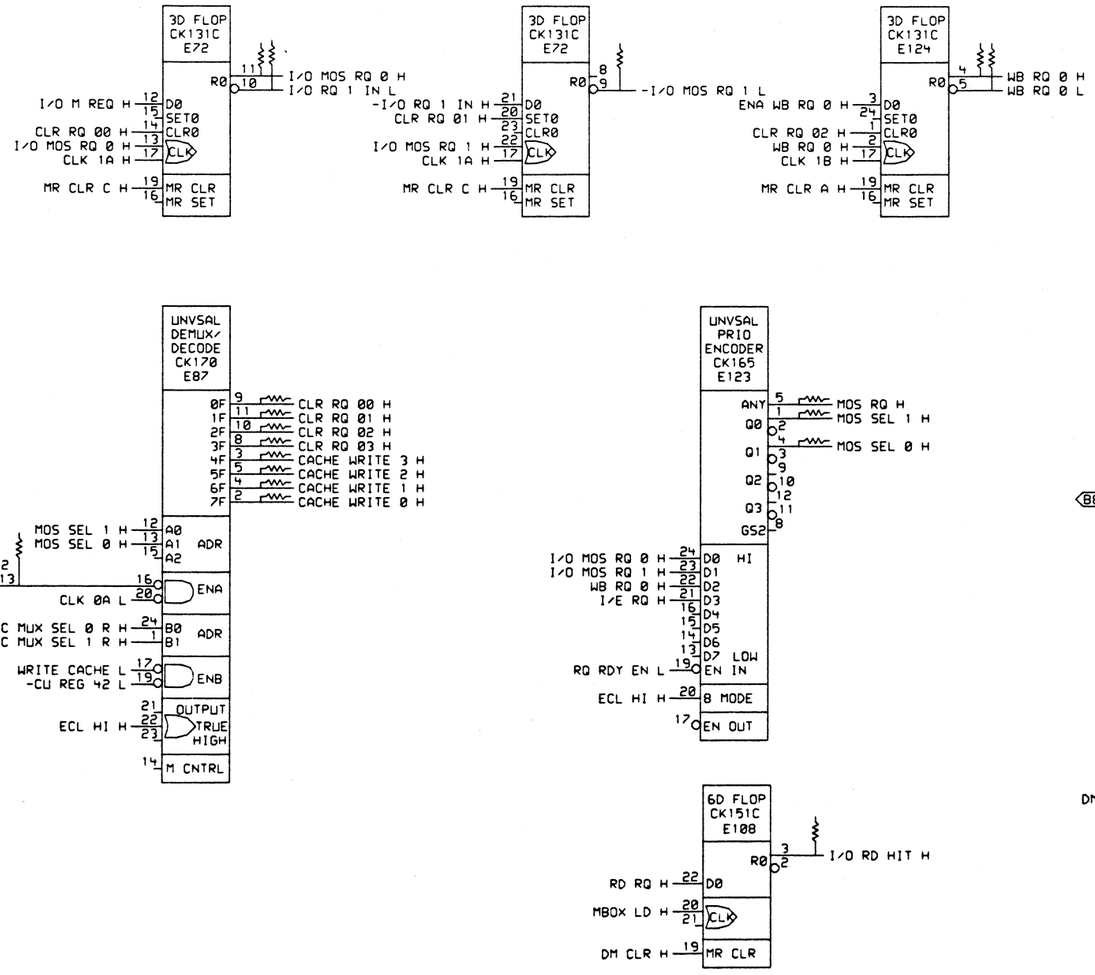
CAM1

	DRN.	DATE 25-SEP-82	ENG.	DATE	TITLE:		
	CHK'D.	DATE	BOARD LOCATION:				
			SHEET OF				
\TRAN\ROCK_BLOCKS\MAOM-TC1.DRV	09-AUG-82	13:07	NEXT HIGHER ASSEMBLY:		SIZE	CODE	NUMBER	REV.
FIRST USED ON OPTION/MODEL:					D	05		



CAMZ

	DRN.	DATE 29-SEP-82	ENG.	DATE	TITLE:
	CHK'D.	DATE	BOARD LOCATION:		
			SHEET OF		
XTRA1:<ROCK_BLOCKS>M4CAM-TC2.DRW 09-AUG-82 13:18			NEXT HIGHER ASSEMBLY:	SIZE D	CODE 05	NUMBER	REV.
FIRST USED ON OPTION/MODEL:					



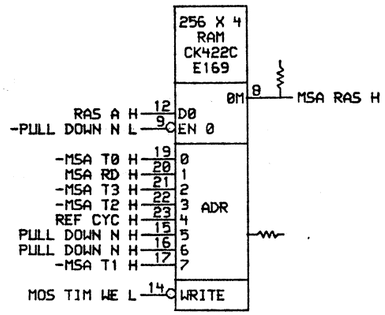
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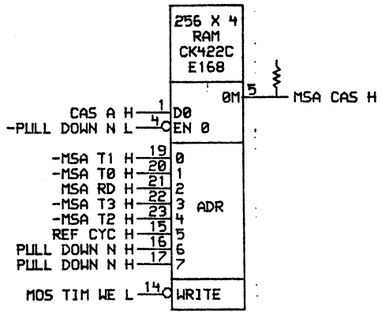
REVISIONS		
CHK	CHANGE NO.	REV

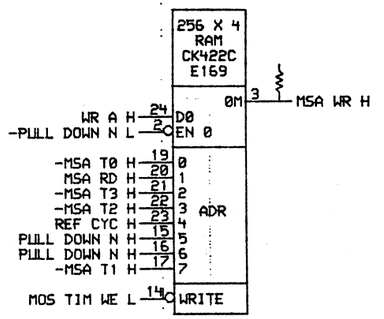
DRN: J.Toomey
CHK'D:
DATE: 04-JAN-83
ENG.
DATE
DATE
BOARD LOCATION:
SHEET 1 OF 1
VULCAN1:<2080.CAM>83CAM1.DRW 03-JAN-83 13:15
NEXT HIGHER ASSEMBLY: B-DD-L0059-0
FIRST USED ON OPTION/MODEL: 2080

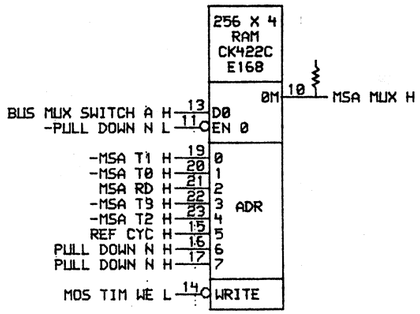
TITLE: MOS RQ FF
MOS PRI EN

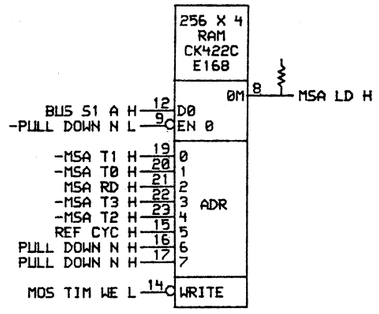
SIZE	CODE	NUMBER	REV.
D	CS	L0059-0-CAM1	*

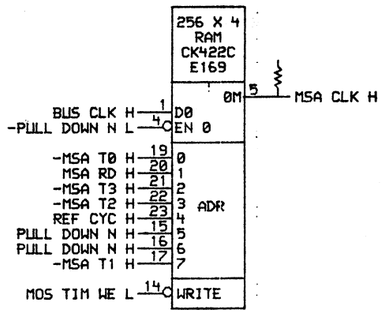


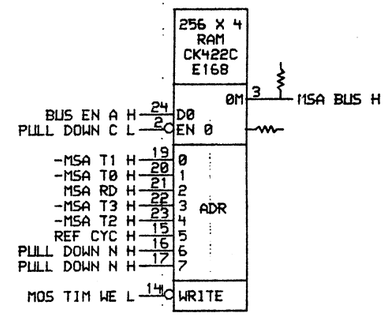


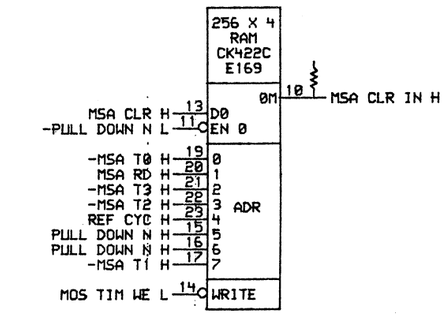


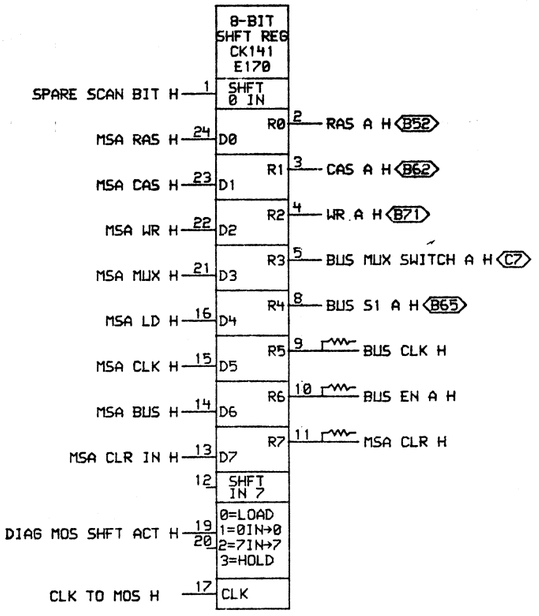


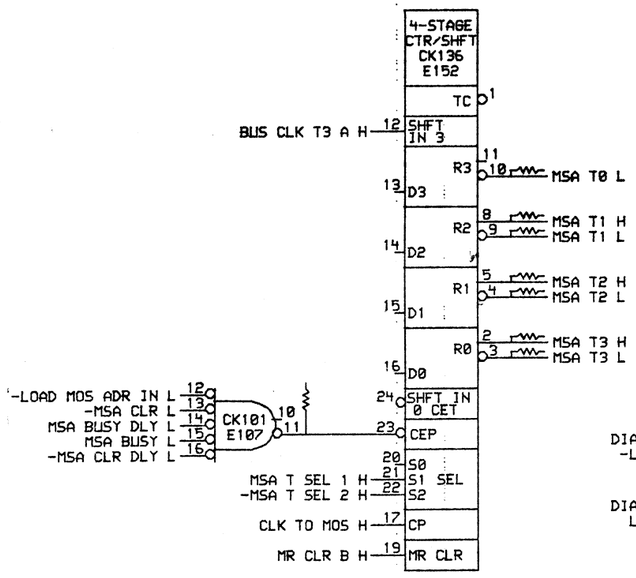


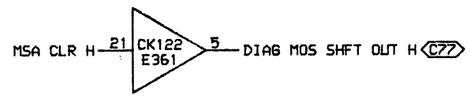


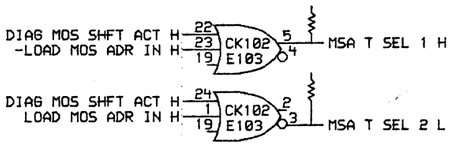












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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. J. Toomey | DATE 07-SEP-82 | ENG. | DATE
CHK D. | DATE | BOARD LOCATION:
SHEET 1 OF 1

VLLCAN:<2080.CAM>61CAM2.DRW 06-SEP-82 09:22 | NEXT HIGHER ASSEMBLY:
FIRST USED ON OPTION/MODEL: 2080 | B-DD-L0059-0

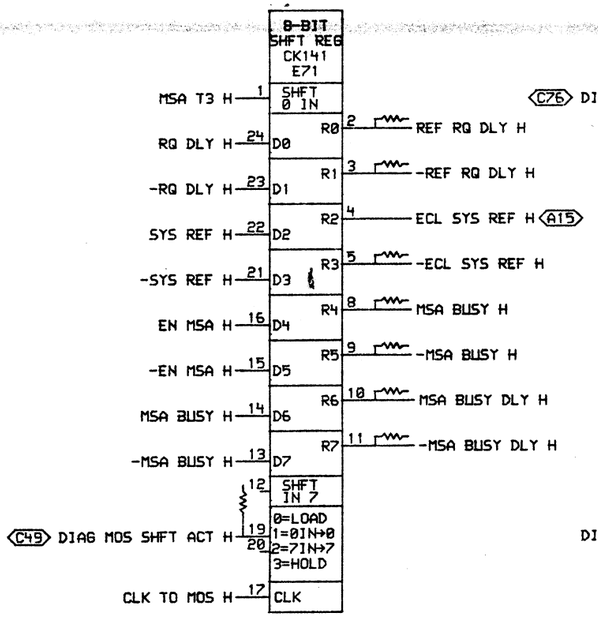
TITLE: MOS SEQUENCER CTL A

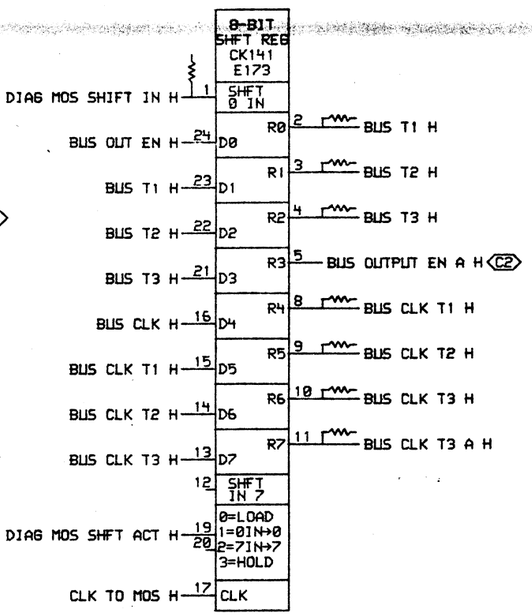
SIZE	CODE	NUMBER	REV.
D	CS	L0059-0-CAM2	*

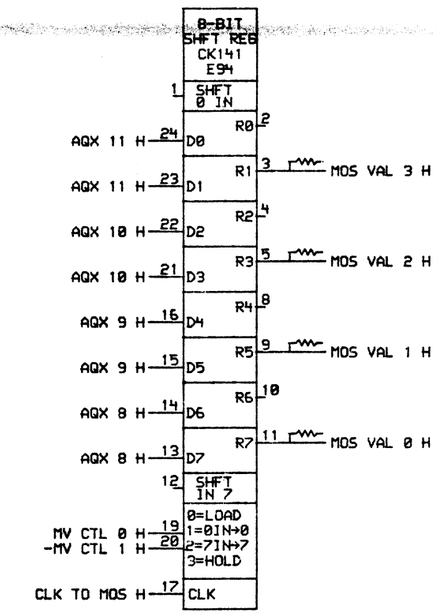
REV. * | NUMBER L0059-0-CAM2 | CODE CS | SIZE D

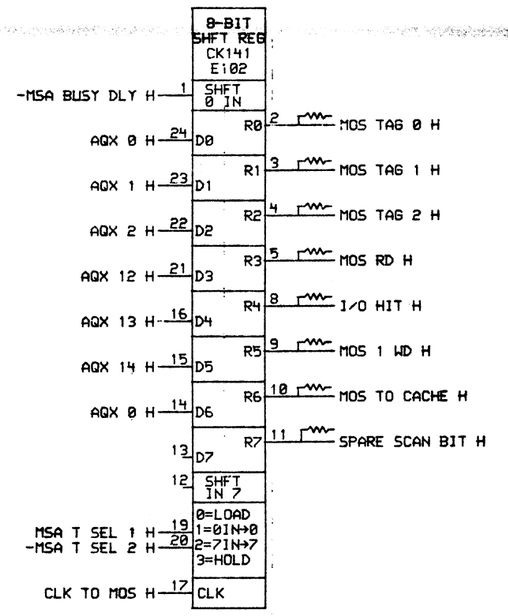
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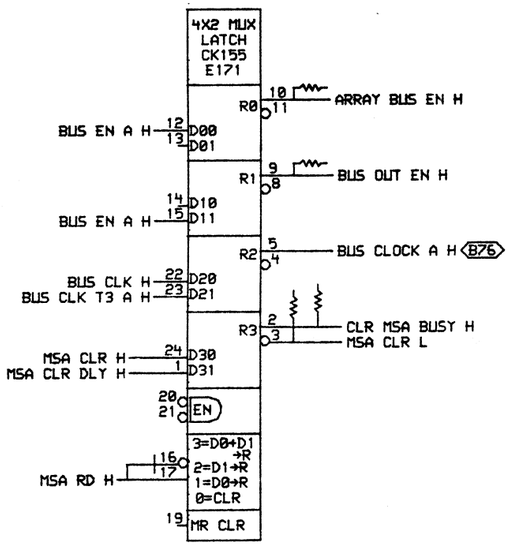
D C B A

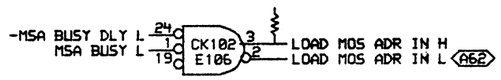


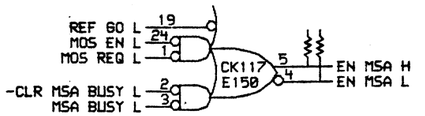


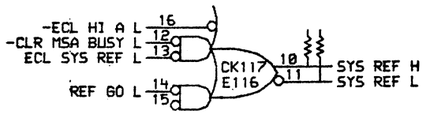


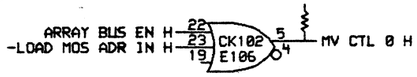


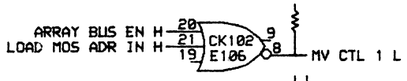


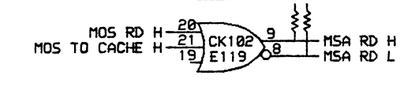


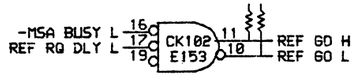


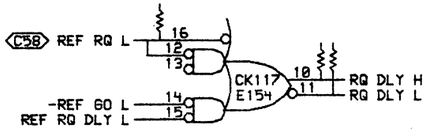


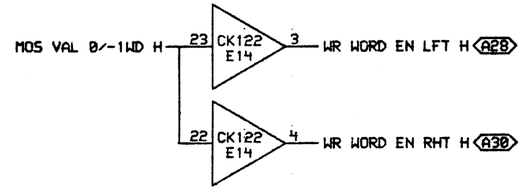




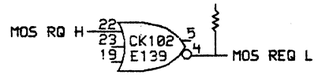












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REVISIONS		
CHK	CHANGE NO.	REV

DRN. S. Landry	DATE 23-SEP-82	ENG.	DATE
CHK D.	DATE	BOARD LOCATION:	
		SHEET 1 OF 1	
VULCAN:<2080.CAM>62CAM3.DRW 22-SEP-82 14:16		NEXT HIGHER ASSEMBLY:	
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0059-0	

TITLE: MOS START CTL

SIZE	CODE	NUMBER	REV.
D	CS	L0059-0-CAM3	*

DM VAL 0 H
I/O RD HIT H
CK102 E340
I/O WR/RD HIT H

141 CLK DLY H
WR DONE H
MBUS WR H
I/O ADR 03 H
CK117 E104
DM DONE H

141 CLK DLY L
CK102 E133
141 CLK DLY H

DM WR ADR 2 L
DM WR ADR 3 L
CK102 E103
WR DONE H

DM MOS CYC H
DM MBOX CYC H
CK102 E106
DM BUSY H
DM BUSY L

8-BIT SHFT REG CK141 E105

-D SEL 0 H
-D SEL 1 H
MBUS WR H
-141 CLK DLY H
DM DONE H
-DM RF EN IN H
-4WD DCYC IN H
-DM EV CYC H
CLK 2A H

DATA SEL 0 IN H
DATA SEL 1 IN H
MUX CK EN H
-MD REG LOAD H
DM CLR IN H
-DM RF EN H
-4WD DCYC H
-141 CLK DLY H

8-BIT SHFT REG CK141 E96

DM VAL 3 IN H
DM VAL 2 IN H
DM VAL 1 IN H
DM VAL 0 IN H
-DM LD H
CLK 0A H

DM VAL 0 H
MOS TAG 0 H
CLK 3A L
-DM BUSY L
BUS OUTPUT EN A L
4X2 MUX LATCH CK155C E118
DM TAG 0 H

MOS TAG 1 H
CTAG 1 H
DM TAG 1 H

MOS TAG 2 H
CTAG 2 H
DM TAG 2 H

DM RF EN L
-I/O 1WD DLY 2 L
CK102 E121
EN DM L

DM I/O HIT L
1 WD L
I/O 1 WD H

MR CLR A H
DM CLR IN H
DM CLR H

-ENA DM LD L
DM BUSY L
CK102 E77
DM LD H
DM LD L

MD REG LOAD L
EN DM L
CK102 E117
WR DM RF H

MD REG LOAD L
CK102 E119
WR CACHE RF H
MD REG LD GTD L

DM MOS CYC H
-MBUS WR H
CK102 E121
D SEL 0 L

DM MOS CYC H
-I/O HIT/WB H
CK102 E106
D SEL 1 L

MBOX WB H
I/O RD HIT H
CK102 E121
I/O HIT/WB L

DM BUSY L
-DM EV CYC L
CK102 E127
ENA DM EV CYC H

6D FLOP CK151C E108

CU REG 70 H
MBOX LD H
DM CLR H
DM MBOX CYC H

CU REG 64 H
MBUS WR H
MBUS WR L

CU REG 65 H
MBOX TO CACHE H

CU REG 66 H

MBOX WB H
MBOX WB L
BUS OUTPUT EN A H

6D FLOP CK151C E120

MOS LD H
DM MOS CYC H
DM MOS CYC L
MOS TO CACHE H

DM M2C H
DM M2C L
MOS 1 WD H
1 WD H
1 WD L

4X2 MUX LATCH CK155C E109

DM LD H
DM SKIP H
DM MOS CYC H

4X2 MUX LATCH CK155C E95

DM VAL 0 H
FOUR WD RD H
4WD DCYC IN L
DM RF EN A H
DM RF EN B H
DM RF EN IN L
WD 0 VAL H
MOS VAL 0 H
DM VAL 0 IN H
WD 1 VAL H
MOS VAL 1 H
DM VAL 1 IN H
WD 2 VAL H
MOS VAL 2 H
DM VAL 2 IN H
WD 3 VAL H
MOS VAL 3 H
DM VAL 3 IN H
DM MOS CYC H

6D FLOP CK151C E151

I/O HIT H
DM I/O HIT H
DM I/O HIT L
DM BUSY H
CLK 0A H
MR CLR B H
ENA DM LD H

3D FLOP CK131C E141

ENA DM EV CYC H
CLK 1B H
MR CLR A H
DM EV CYC H
DM EV CYC L

-MSA BUSY L
-DM MBOX CYC L
CLK 1B L
EN DM LD L
CU REG 70 L
CK101 E107
MBOX LD H

-DM M2C L
-1 WD L
CK102 E127
FOUR WD RD H

I/O WR/RD HIT H
MBUS WR H
CK102 E127
DM RF EN A H

-DM M2C L
-DM VAL 0 L
CK102 E127
DM RF EN B H

-DM I/O HIT L
-1 WD L
CK102 E55

-DM M2C L
CK102 E153

BUS OUTPUT EN A L
EN DM LD L
CK102 E119
MOS LD H

CU REG 70 H
CK102 E77
CU REG 70 L

BUS OUTPUT EN A H
CK102 E117
BUS OUTPUT EN A L

-DIAG ACT SHFT IN L
-DM BUSY L
CK102 E77
EN DM LD L

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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRW. Toomey | DATE 15-OCT-82 | ENG. | DATE

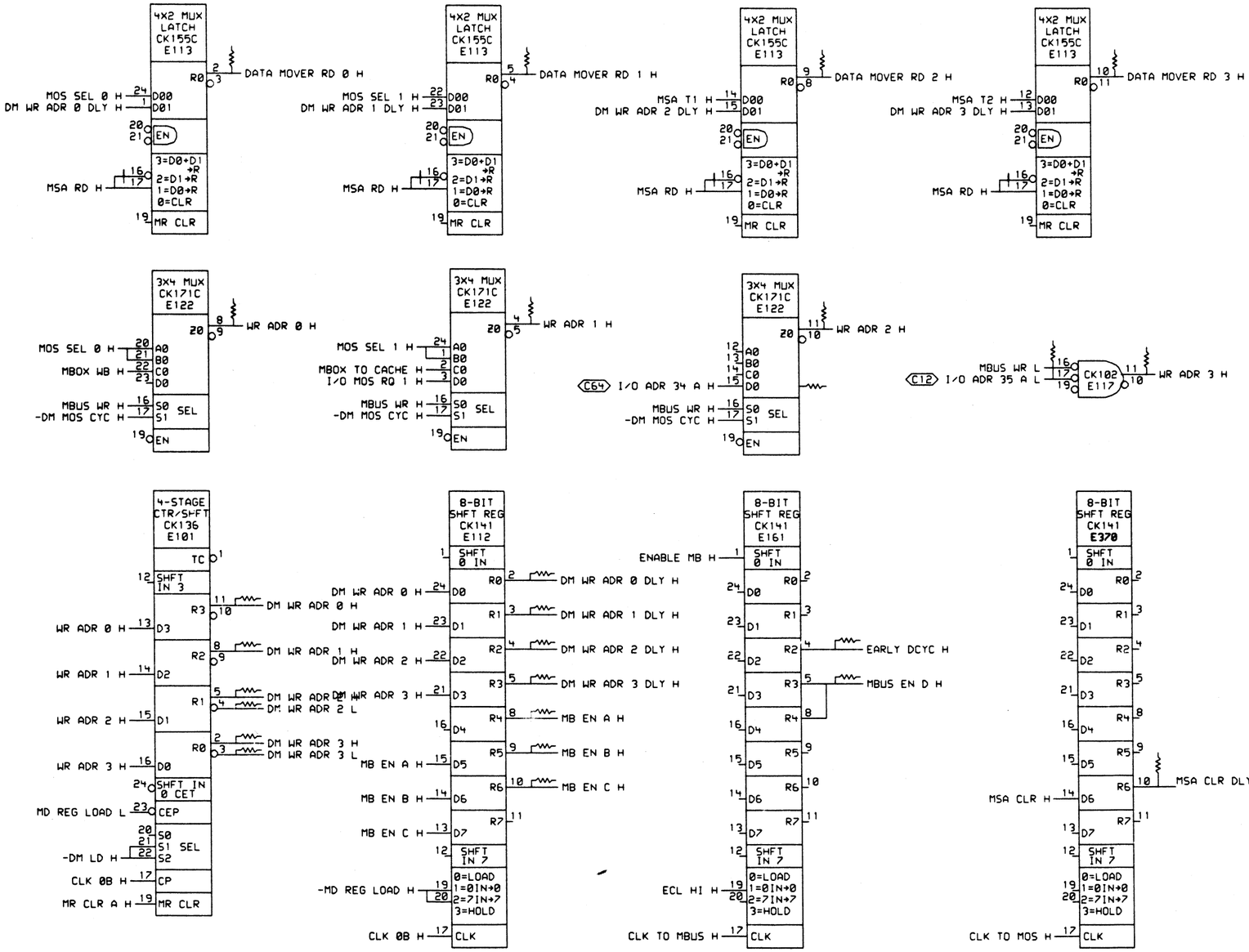
CHK "D." | DATE | BOARD LOCATION: | SHEET 1 OF 1

VULCAN:<2080.CAM>67CAM4.DRW 14-OCT-82 15:10 | NEXT HIGHER ASSEMBLY: B-DD-L0059-0

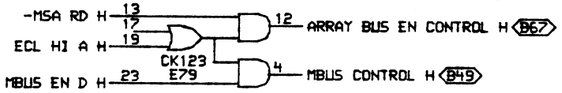
FIRST USED ON OPTION/MODEL: 2080

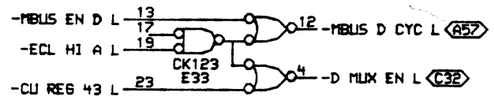
TITLE: DATA MOVER CTL

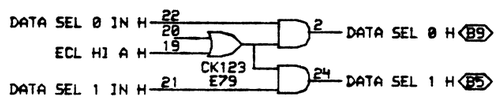
SIZE	CODE	NUMBER	REV.
D	CS	L0059-0-CAM4	*

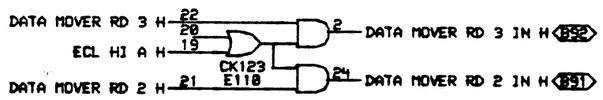


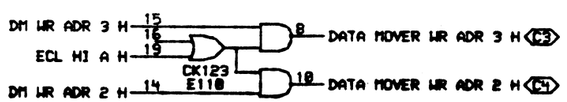
	DRN.	DATE 22-NOV-82	ENG.	DATE	TITLE:	DM RD/WR ADR CTL
	CHK'D.	DATE	BOARD LOCATION:			
			SHEET 1 OF 1			
ARCHI:<UHLER.PLOTS>??CAM5.DRW 21-NOV-82 11:27			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0059-0-CAM5 / REV. *	
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0059-0			

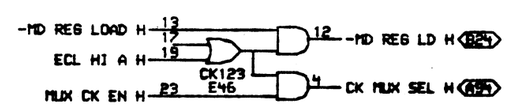


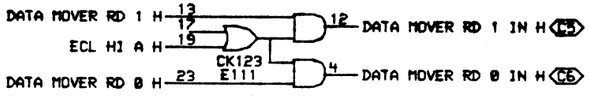


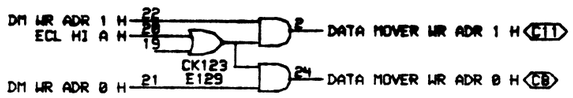


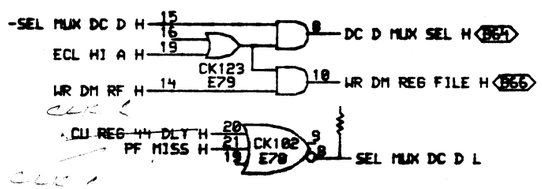












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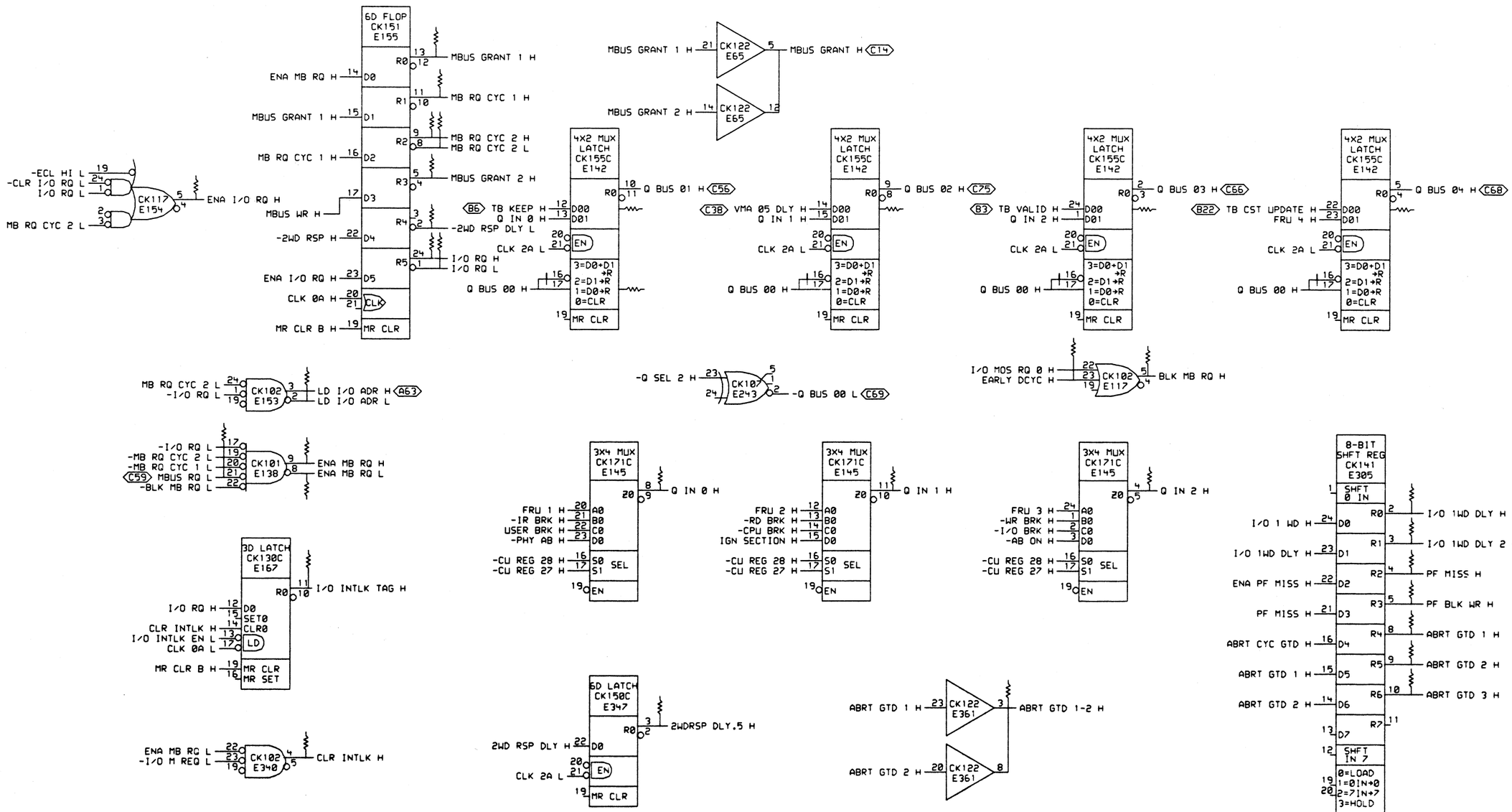
REVISIONS		
CHK	CHANGE NO.	REV

DRN. S.Toomey	DATE 28-MAY-82	ENG.	DATE
CHK'D.	DATE	BOARD LOCATION: SHEET 1 OF 1	

VULCAN:<2000.CAM>45CAM6.DRW 27-MAY-82 19:01
FIRST USED ON OPTION/MODEL: 2080
NEXT HIGHER ASSEMBLY: B-DD-L0059-0

TITLE: MDP DMU CONTROL LOGIC

SIZE	CODE	NUMBER	REV.
D	CS	L0059-0-CAM6	*



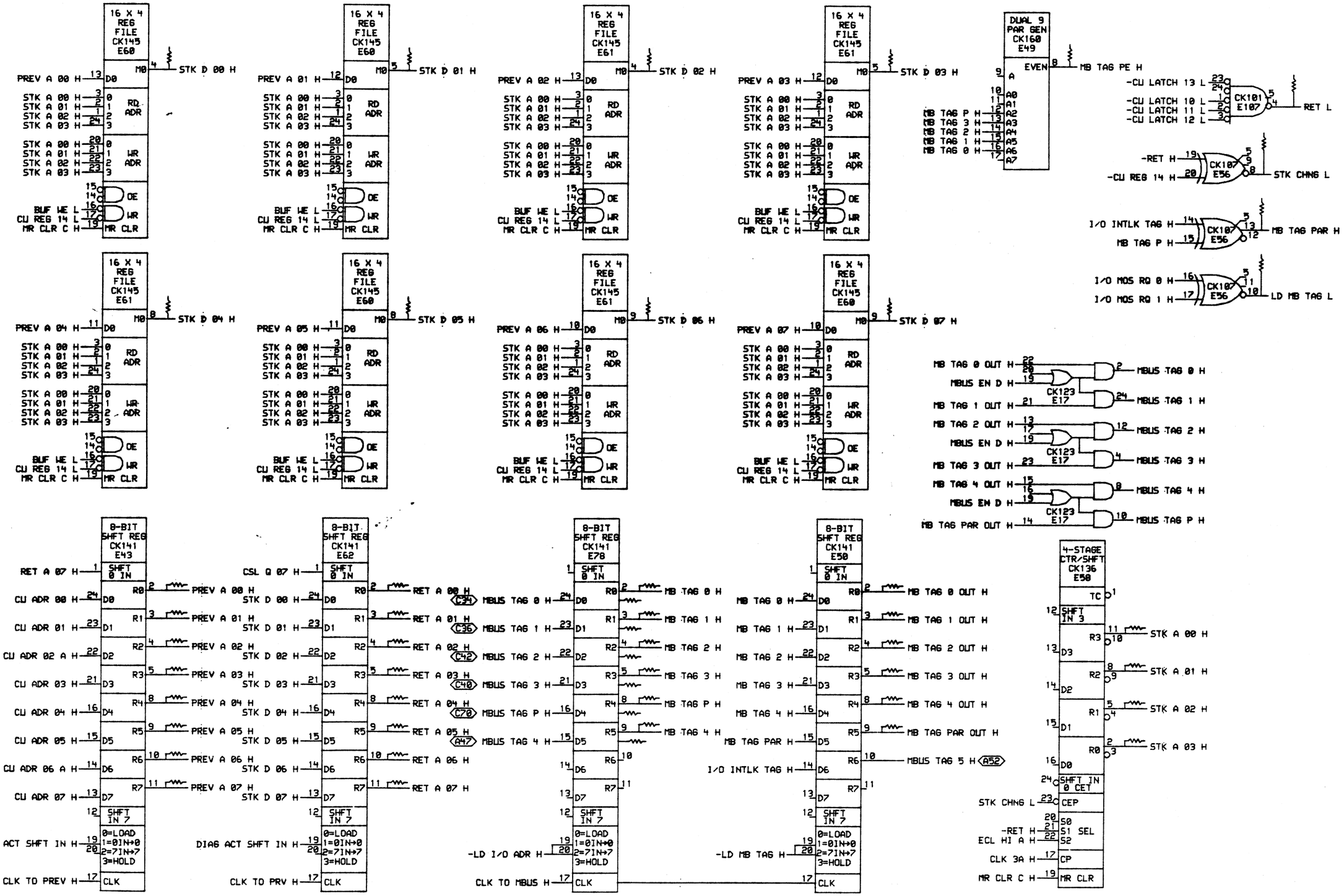
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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN: K. Toomey | DATE: 04-JAN-83 | ENG. | DATE | TITLE:
CHK: D. | DATE | BOARD LOCATION: | SHEET 1 OF 1
VLSCAN:<2080.CAM>83CAM7.DRW 03-JAN-83 11:13 | NEXT HIGHER ASSEMBLY:
FIRST USED ON OPTION/MODEL: 2080 | B-DD-L0059-0

SIZE	CODE	NUMBER	REV.
D	CS	L0059-0-CAM7	*



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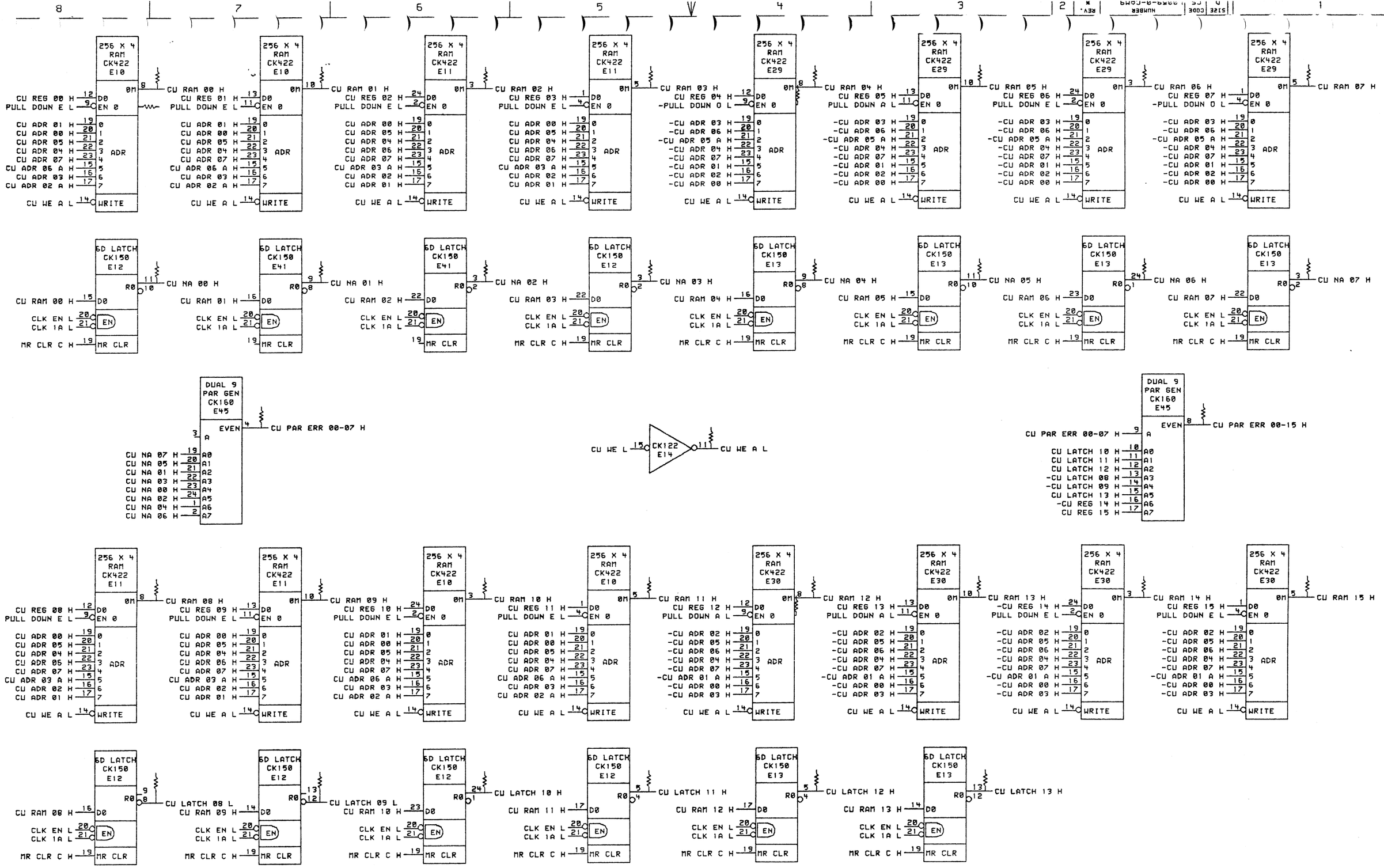
REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN J. Looney | DATE 12-JUL-82 | ENG. | DATE
CHK D. | DATE | BOARD LOCATION:
SHEET 1 OF 1
VLICAM:<2080.CAM>50CAM8.DRW 11-JUL-82 11:41 | NEXT HIGHER ASSEMBLY: B-DD-L0059-0
FIRST USED ON OPTION/MODEL: 2080

TITLE: CU STACK

SIZE	CODE	NUMBER	REV.
D	CS	L0059-0-CAM8	*



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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. S. Twomey | DATE 11-MAR-82 | ENG. | DATE

CHK'D. | DATE | BOARD LOCATION:

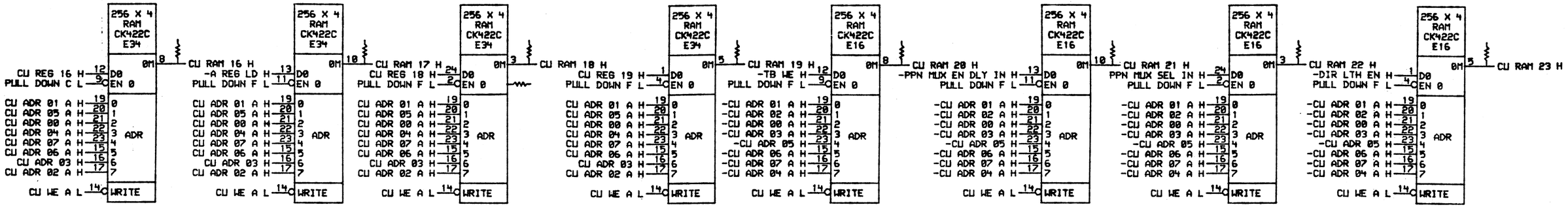
SHEET 1 OF 5

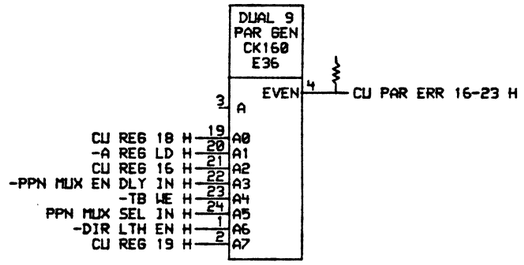
VULCAN:<0000.CAM>14CAM9.DRW | 10-MAR-82 21:54 | NEXT HIGHER ASSEMBLY:

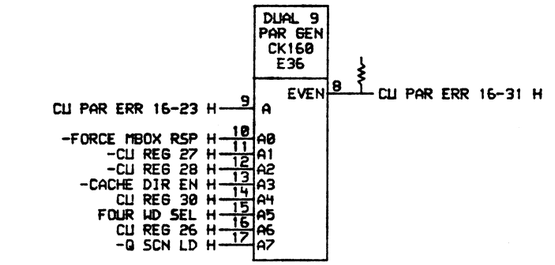
FIRST USED ON OPTION/MODEL: 2080 | B-DD-L0059-0

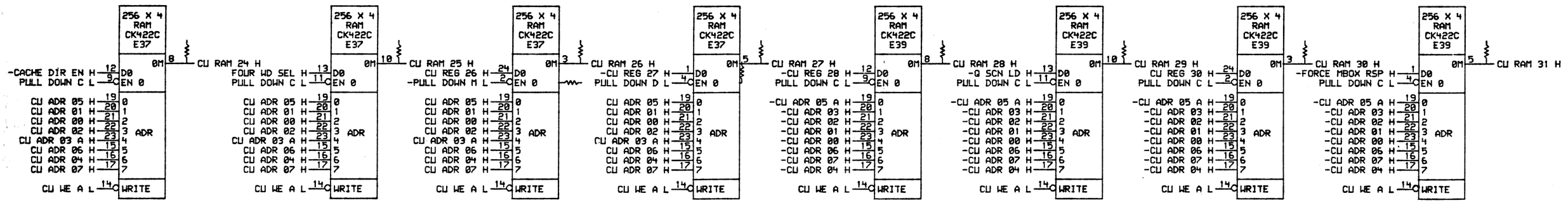
TITLE: CU RAM 00-15, CU LATCH 00-15 + P

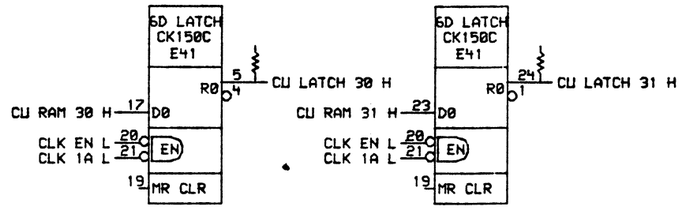
SIZE	CODE	NUMBER	REV.
D	CS	L0059-0-CAM9	*





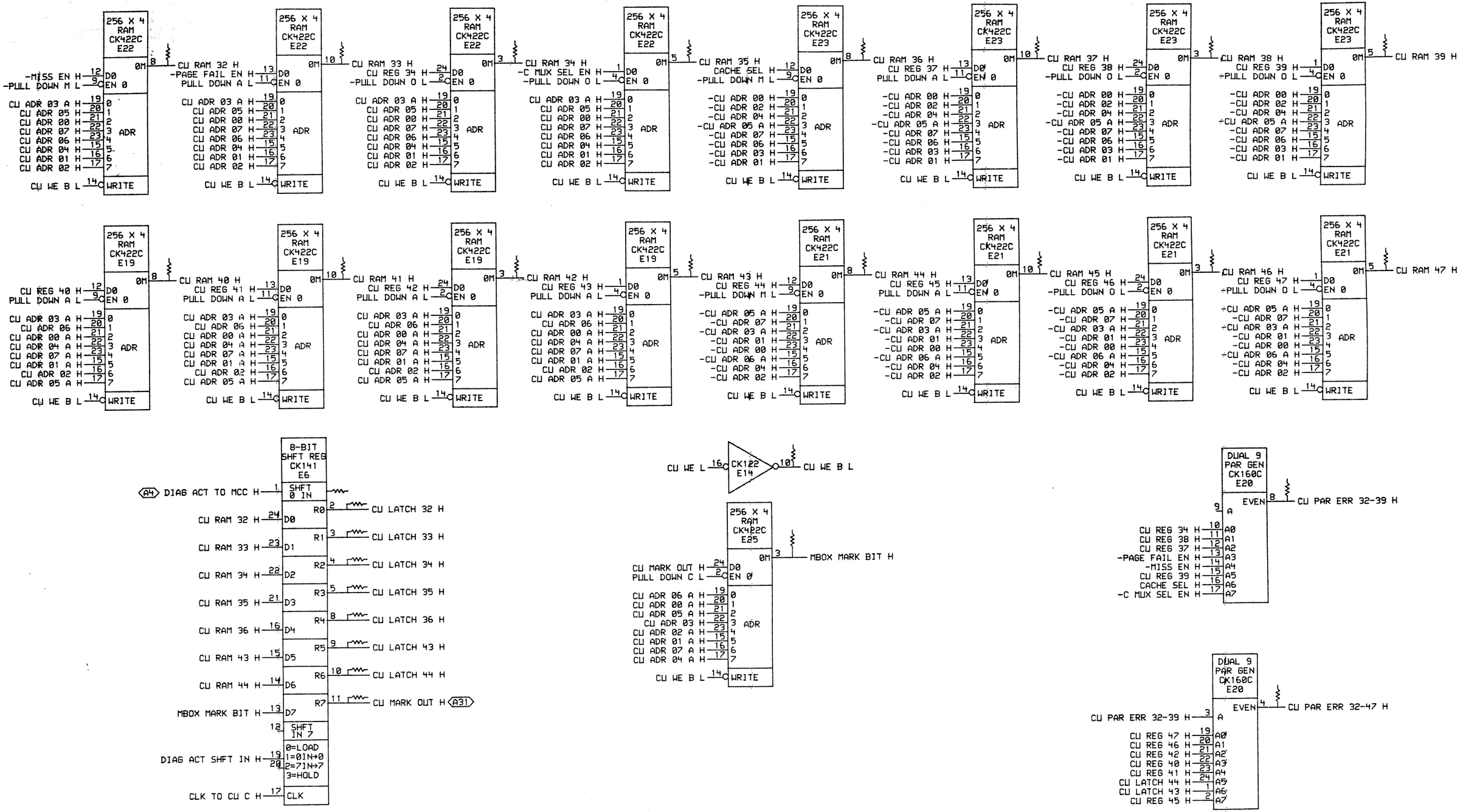






	DRN.	DATE 24-AUG-82	ENG.	DATE	TITLE: CU RAM 16-31, CU LATCH 16-31 + P
	CHK'D.	DATE	BOARD LOCATION: SHEET 2 OF 5		
VULCAN:<2080.CAM>59CAMA.DRW 17-AUG-82 09:44			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0059-0-CAMA / REV. *
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0059-0		

8 7 6 5 4 3 2 1



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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN S. Toomey | DATE 04-DEC-82 | ENG. | DATE

CHK D. | DATE | BOARD LOCATION: | SHEET 3 OF 5

VULCAN:<2080.CAM>79CAMB.DRW 03-DEC-82 16:17 | NEXT HIGHER ASSEMBLY:

FIRST USED ON OPTION/MODEL: 2080 | B-DD-L0059-0

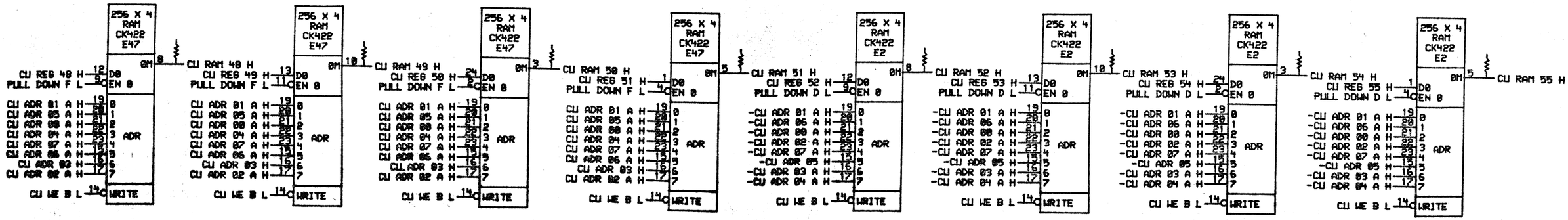
TITLE: CU RAM 32-47, CU LATCH 32-47 + P

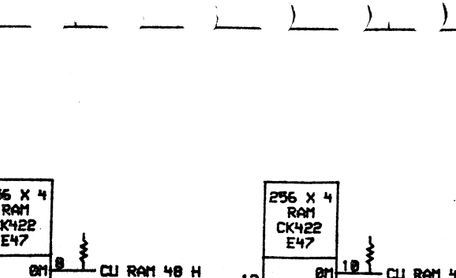
SIZE	CODE	NUMBER	REV.
D	CS	L0059-0-CAMB	*

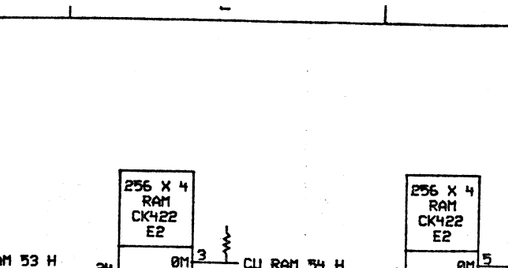
REV. * | NUMBER L0059-0-CAMB | CODE CS | SIZE D

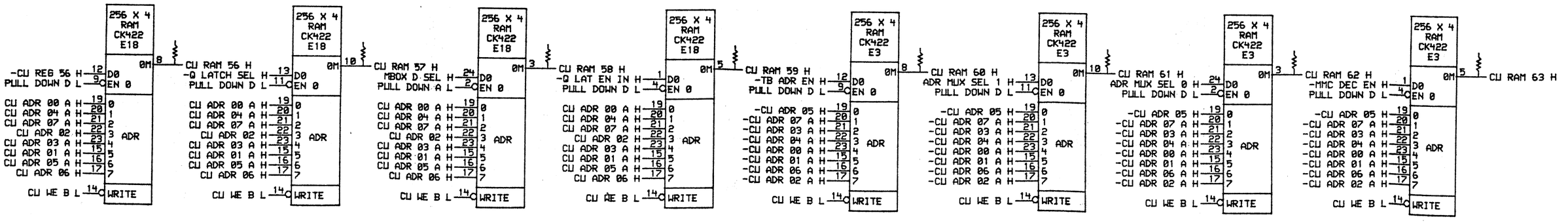
8 7 6 5 4 3 2 1

A B C D

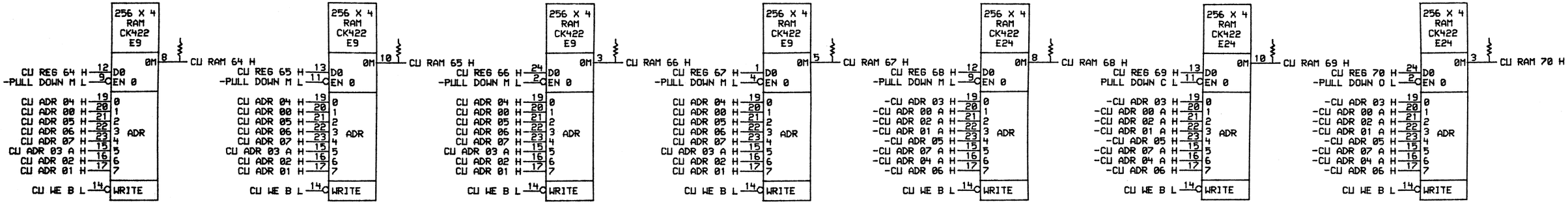


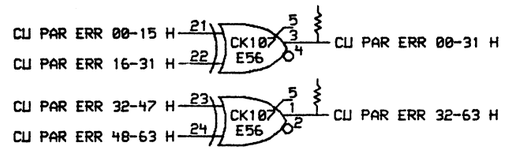


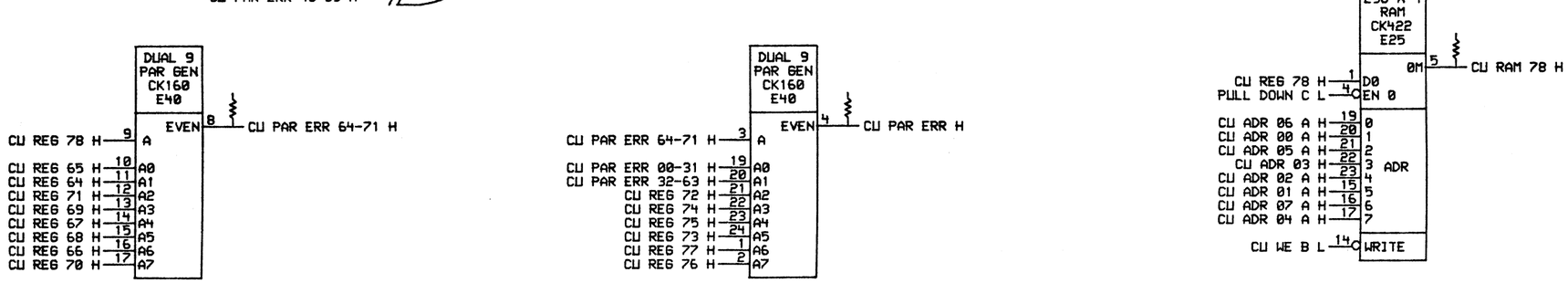


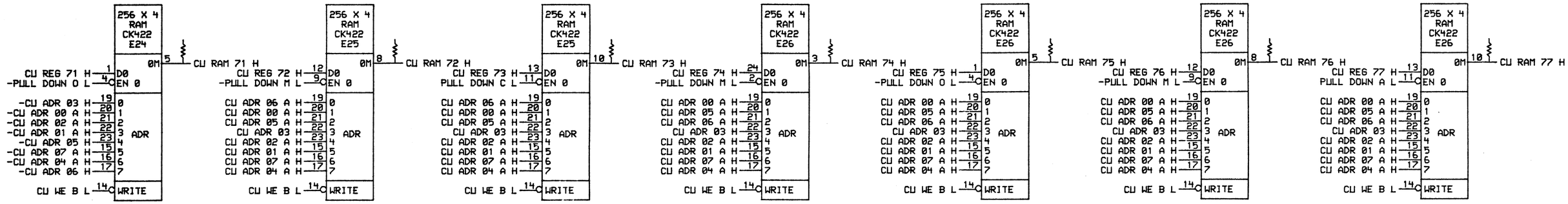


	DRN.	DATE 25-JAN-82	ENG.	DATE	TITLE: CU RAM 48-63, + PARITY
	CHK'D.	DATE	BOARD LOCATION: SHEET 4 OF 5		
VULCAN:<2080.CAM>CAMC.DRW 20-JAN-82 08:19		NEXT HIGHER ASSEMBLY: B-DD-L0059-0		SIZE D	CODE CS
FIRST USED ON OPTION/MODEL: 2080				NUMBER L0059-0-CAMC	REV. *









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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. J. Feeney | DATE 05-MAR-82 | ENG. | DATE

CHK'D. | DATE | BOARD LOCATION:

SHEET 4 OF 5

VULCAN:<2000.CAM>1@CAMD.DRW 05-MAR-82 10:10

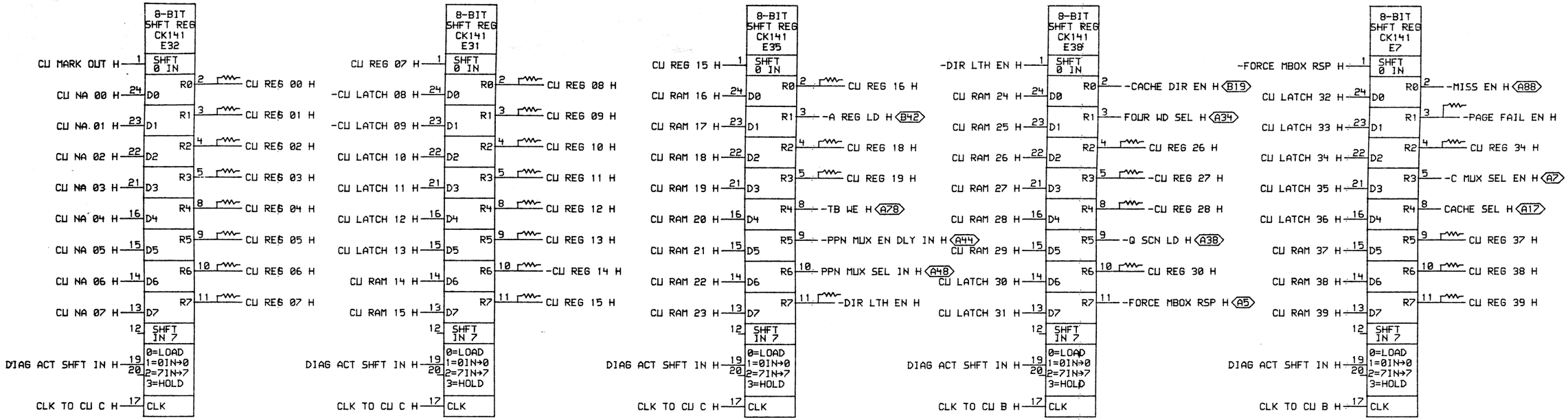
FIRST USED ON OPTION/MODEL: 2080

NEXT HIGHER ASSEMBLY: B-DD-L0059-0

TITLE: CU RAM 64-73
CU LATCH 64-73 + P

SIZE	CODE	NUMBER	REV.
D	CS	L0059-0-CAMD	*

REV. *
NUMBER L0059-0-CAMD
CODE CS
SIZE D



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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRW: S. Toomy
CHK'D.

DATE 04-DEC-82 | ENG. | DATE

BOARD LOCATION:

SHEET 5 OF 5

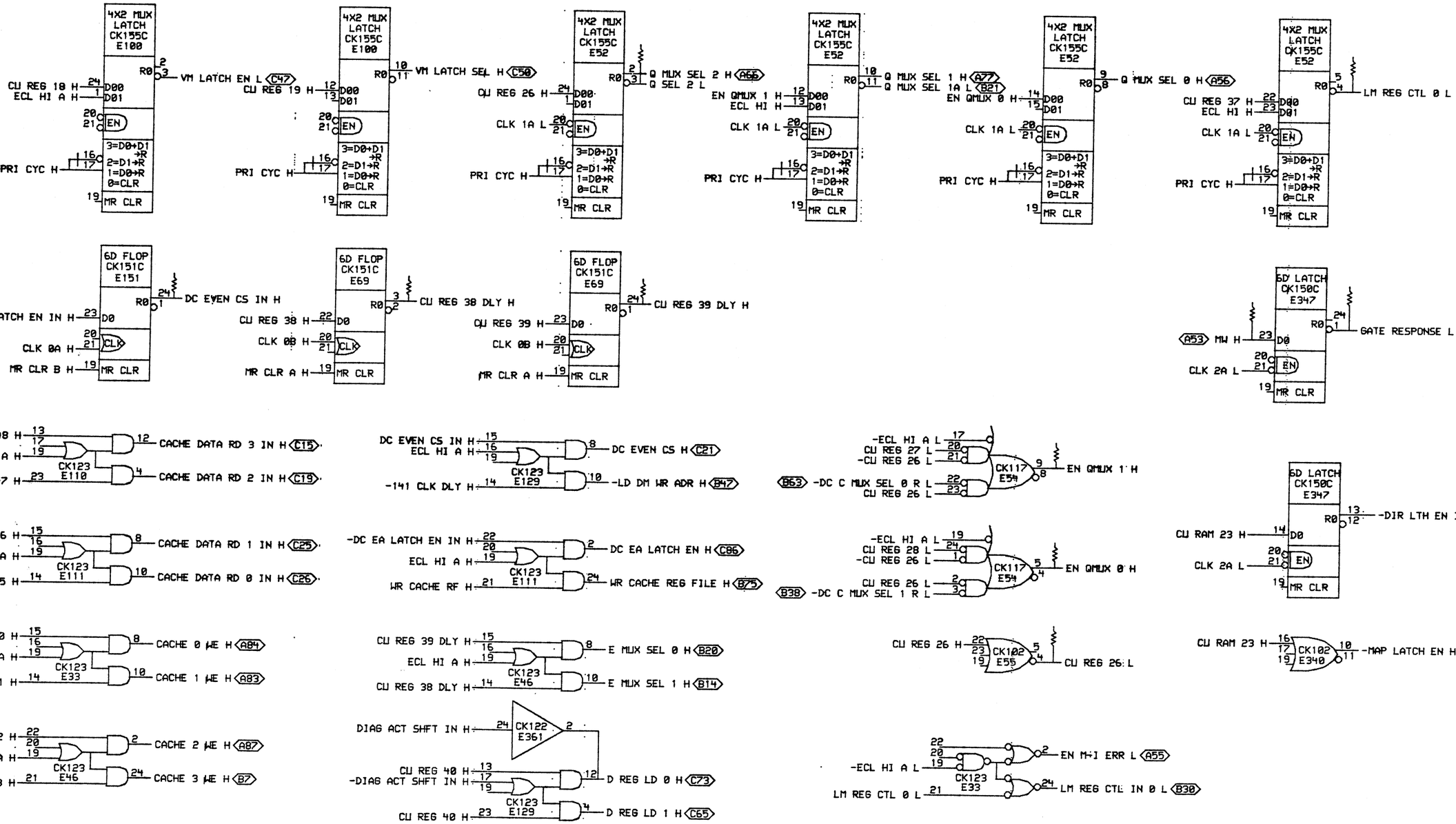
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FIRST USED ON OPTION/MODEL: 2080

NEXT HIGHER ASSEMBLY: B-DD-L0059-0

TITLE: CU REG 00-79

SIZE	CODE	NUMBER	REV.
D	CS	L0059-0-CAME	*



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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN S.Toomey | DATE 11-NOV-82 | ENG. | DATE

CHK D. | DATE | BOARD LOCATION: | SHEET 1 OF 1

VULCAN:<2080.CAM>75CAMF.DRW 10-NOV-82 20:28 | NEXT HIGHER ASSEMBLY: B-DD-L0059-0

FIRST USED ON OPTION/MODEL: 2080

TITLE: MDP CU CONTROL LOGIC

SIZE	CODE	NUMBER	REV.
D	CS	L0059-0-CAMF	*

3D FLOP CK131 E373
CTAG 1 H → D0 ; CTAG INCR H → SET0, CLR0, CLK ; R0 → LAST CTAG 1 H
CTAG 2 H → D1 ; CTAG INCR H → SET1, CLR1, CLK ; R1 → LAST CTAG 2 H
-CPU RD H → D2 ; CLK 3B H → SET2, CLR2, CLK ; R2 → -CPU RD DLY H
MR CLR, MR SET

UNVSAL PRIO ENCODER CK165 E156
ANY → PRI SEL ANY H
Q0 → PRI SEL 2 H
Q1 → PRI SEL 1 H
Q2 → PRI SEL 1 L
Q3 → PRI SEL 0 H
GS2 → PRI SEL 0 L
CONSOLE RQ H, ERROR TRAP H, CACHE MISS RQ H, CACHE REFILL H, I/O TRP H, SWP TRP H, EBOX RQ H, IBOX RQ H, CLK 2B L → D0 HI, D1, D2, D3, D4, D5, D6, D7 LOW, EN IN
ECL HI H → 8 MODE
EN OUT

UNVSAL DEMUX/ DECODE CK170 E74
0F → SET I/O MOS RW L
1F → HALT MBOX L
2F → REFILL CYC L (B10)
3F → I/O CYC L
4F → 2WD RSP L
5F → CPU 1 WD WR L
6F → PPGT Q LTCH L (B37)
7F
CU REG 76 H, CU REG 75 H, CU REG 73 H → A0, A1, A2 ADR
ENA
B0, B1 ADR
ENB
OUTPUT TRUE LOW
ECL HI A H → M CNTRL

CPU 1 WD WR L → CK102 E55 → CPU 1 WD WR H

2X8 MUX CK163C E137
2A → INIT ADR 06 H
FRU 0 H, MISS RD H, (C51) I/O ADR 01 H, FORCE PHY H, IQ 2 H → DA0, DA1, DA2, DA3, DA4, DA5, DA6, DA7
PRI SEL 2 H, PRI SEL 1 H, PRI SEL 0 H → S0, S1 SEL, S2

2X8 MUX CK163C E137
2A → INIT ADR 07 H
FRU 1 H, CACHE PF H, I/O ADR 02 H, PAG SYNC A H, AB ON H, IQ 3 H → DA0, DA1, DA2, DA3, DA4, DA5, DA6, DA7
S0, S1 SEL, S2

3X4 MUX CK171C E44
CU NA 00 H, RET A 00 H, PREV A 00 H, CSL Q 00 H → A0, B0, C0, D0
-CU LATCH 08 H, -CU LATCH 09 H → S0, S1 SEL
EN
NXT ADR 00 H

3X4 MUX CK171C E63
CU NA 01 H, RET A 01 H, PREV A 01 H, CSL Q 01 H → A0, B0, C0, D0
S0, S1 SEL
EN
NXT ADR 01 H

3X4 MUX CK171C E44
CU NA 02 H, RET A 02 H, PREV A 02 H, CSL Q 02 H → A0, B0, C0, D0
S0, S1 SEL
EN
NXT ADR 02 H

3X4 MUX CK171C E63
CU NA 03 H, RET A 03 H, PREV A 03 H, CSL Q 03 H → A0, B0, C0, D0
S0, S1 SEL
EN
NXT ADR 03 H

3X4 MUX CK171C E64
CU NA 04 H, RET A 04 H, PREV A 04 H, CSL Q 04 H → A0, B0, C0, D0
S0, S1 SEL
EN
NXT ADR 04 H

3X4 MUX CK171C E44
CU NA 05 H, RET A 05 H, PREV A 05 H, CSL Q 05 H → A0, B0, C0, D0
S0, S1 SEL
EN
NXT ADR 05 H

3X4 MUX CK171C E64
CU NA 06 H, RET A 06 H, PREV A 06 H, CSL Q 06 H → A0, B0, C0, D0
S0, S1 SEL
EN
NA ADR 06 H

3X4 MUX CK171C E63
CU NA 07 H, RET A 07 H, PREV A 07 H, CSL Q 07 H → A0, B0, C0, D0
S0, S1 SEL
EN
NA ADR 07 H

4X2 MUX LATCH CK155C E42
NXT ADR 00 H, PRI SEL 0 H → D00, D01
CLK 3B L → EN
PRI CYC IN H → 3=D0+D1→R, 2=D1→R, 1=D0→R, 0=CLR
MR CLR
R0 → CU ADR 00 H, CU ADR 00 L

4X2 MUX LATCH CK155C E59
NXT ADR 01 H, PRI SEL 1 H → D00, D01
CLK 3B L → EN
PRI CYC IN H → 3=D0+D1→R, 2=D1→R, 1=D0→R, 0=CLR
MR CLR
R0 → CU ADR 01 H, CU ADR 01 L

4X2 MUX LATCH CK155C E28
NXT ADR 02 H, PRI SEL 2 H → D00, D01
CLK 3B L → EN
PRI CYC IN H → 3=D0+D1→R, 2=D1→R, 1=D0→R, 0=CLR
MR CLR
R0 → CU ADR 02 H, CU ADR 02 L

4X2 MUX LATCH CK155C E42
NXT ADR 03 H, CPU SPC FCN H → D00, D01
CLK 3B L → EN
PRI CYC IN H → 3=D0+D1→R, 2=D1→R, 1=D0→R, 0=CLR
MR CLR
R0 → CU ADR 03 H, CU ADR 03 L

4X2 MUX LATCH CK155C E59
NXT ADR 04 H, IQ 0 H → D00, D01
CLK 3B L → EN
PRI CYC IN H → 3=D0+D1→R, 2=D1→R, 1=D0→R, 0=CLR
MR CLR
R0 → CU ADR 04 H, CU ADR 04 L

4X2 MUX LATCH CK155C E28
NXT ADR 05 H, IQ 1 H → D00, D01
CLK 3B L → EN
PRI CYC IN H → 3=D0+D1→R, 2=D1→R, 1=D0→R, 0=CLR
MR CLR
R0 → CU ADR 05 H, CU ADR 05 L

4X2 MUX LATCH CK155C E57
NXT ADR 06 H, INIT ADR 06 H → D00, D01
CLK 3B L → EN
PRI CYC IN H → 3=D0+D1→R, 2=D1→R, 1=D0→R, 0=CLR
MR CLR
R0 → CU ADR 06 H, CU ADR 06 L

4X2 MUX LATCH CK155C E57
NXT ADR 07 H, INIT ADR 07 H → D00, D01
CLK 3B L → EN
PRI CYC IN H → 3=D0+D1→R, 2=D1→R, 1=D0→R, 0=CLR
MR CLR
R0 → CU ADR 07 H, CU ADR 07 L

4X2 MUX LATCH CK155C E42
NXT ADR 00 H, PRI SEL 0 H → D00, D01
CLK 3B L → EN
PRI CYC IN H → 3=D0+D1→R, 2=D1→R, 1=D0→R, 0=CLR
MR CLR
R0 → CU ADR 00 A H, CU ADR 00 A L

4X2 MUX LATCH CK155C E59
NXT ADR 01 H, PRI SEL 1 H → D00, D01
CLK 3B L → EN
PRI CYC IN H → 3=D0+D1→R, 2=D1→R, 1=D0→R, 0=CLR
MR CLR
R0 → CU ADR 01 A H, CU ADR 01 A L

4X2 MUX LATCH CK155C E28
NXT ADR 02 H, PRI SEL 2 H → D00, D01
CLK 3B L → EN
PRI CYC IN H → 3=D0+D1→R, 2=D1→R, 1=D0→R, 0=CLR
MR CLR
R0 → CU ADR 02 H, CU ADR 02 A L

4X2 MUX LATCH CK155C E42
NXT ADR 03 H, CPU SPC FCN H → D00, D01
CLK 3B L → EN
PRI CYC IN H → 3=D0+D1→R, 2=D1→R, 1=D0→R, 0=CLR
MR CLR
R0 → CU ADR 03 A H, CU ADR 03 A L

4X2 MUX LATCH CK155C E59
NXT ADR 04 H, IQ 0 H → D00, D01
CLK 3B L → EN
PRI CYC IN H → 3=D0+D1→R, 2=D1→R, 1=D0→R, 0=CLR
MR CLR
R0 → CU ADR 04 A H, CU ADR 04 A L

4X2 MUX LATCH CK155C E28
NXT ADR 05 H, IQ 1 H → D00, D01
CLK 3B L → EN
PRI CYC IN H → 3=D0+D1→R, 2=D1→R, 1=D0→R, 0=CLR
MR CLR
R0 → CU ADR 05 A H, CU ADR 05 A L

4X2 MUX LATCH CK155C E57
NXT ADR 06 H, INIT ADR 06 H → D00, D01
CLK 3B L → EN
PRI CYC IN H → 3=D0+D1→R, 2=D1→R, 1=D0→R, 0=CLR
MR CLR
R0 → CU ADR 06 A H, CU ADR 06 A L

4X2 MUX LATCH CK155C E57
NXT ADR 07 H, INIT ADR 07 H → D00, D01
CLK 3B L → EN
PRI CYC IN H → 3=D0+D1→R, 2=D1→R, 1=D0→R, 0=CLR
MR CLR
R0 → CU ADR 07 A H, CU ADR 07 A L

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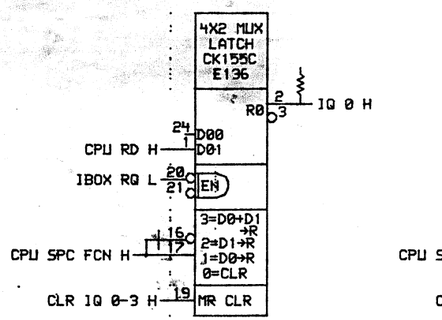
REVISIONS

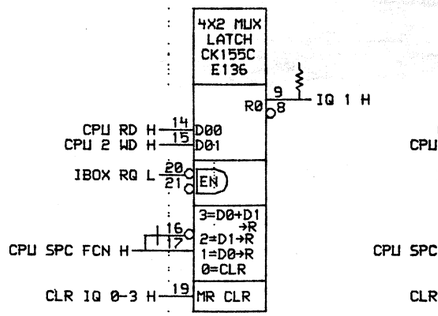
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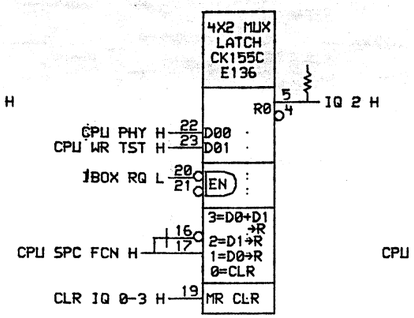
digital
DRN. D. Feeney — DATE 22-DEC-82 — ENG. — DATE
CHK D. — DATE — BOARD LOCATION: — SHEET 1 OF 1
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NEXT HIGHER ASSEMBLY: B-DD-L0059-0

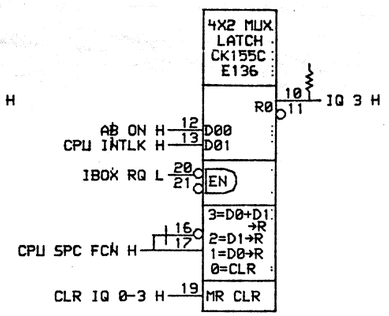
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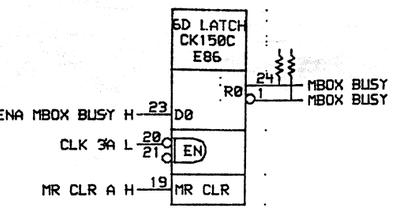
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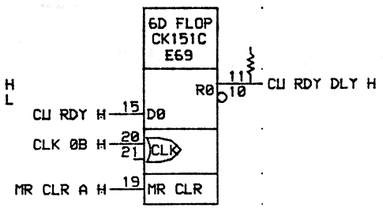


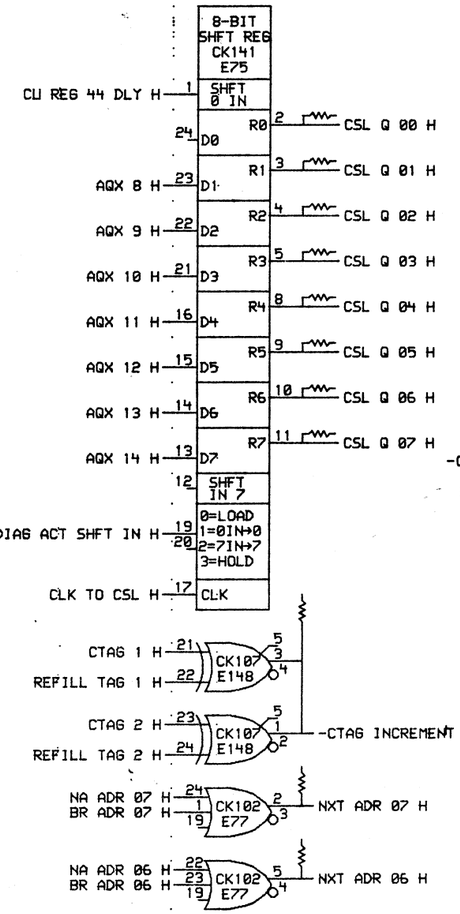


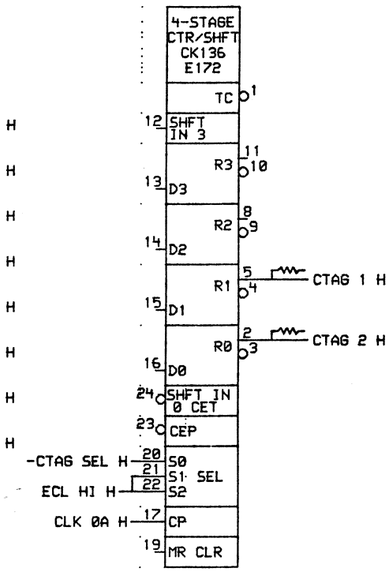


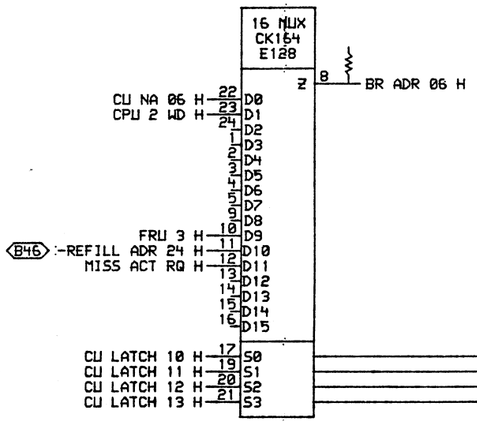


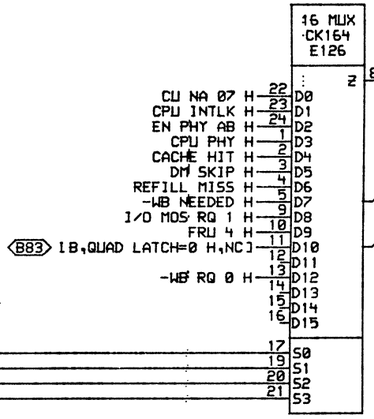


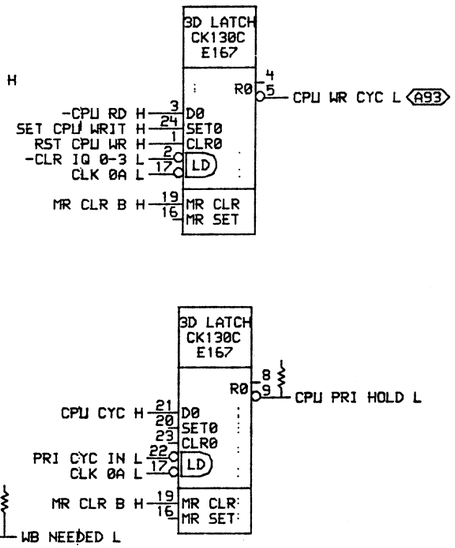


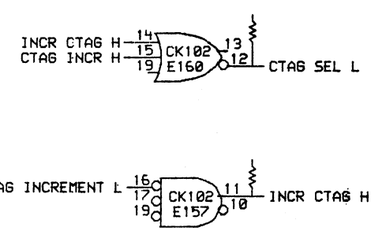


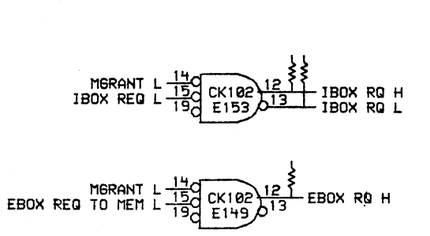


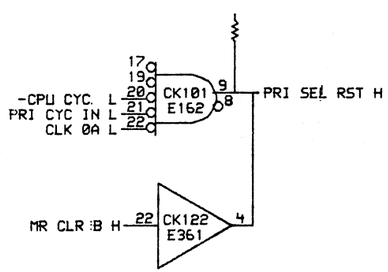


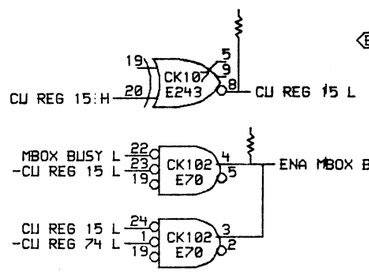


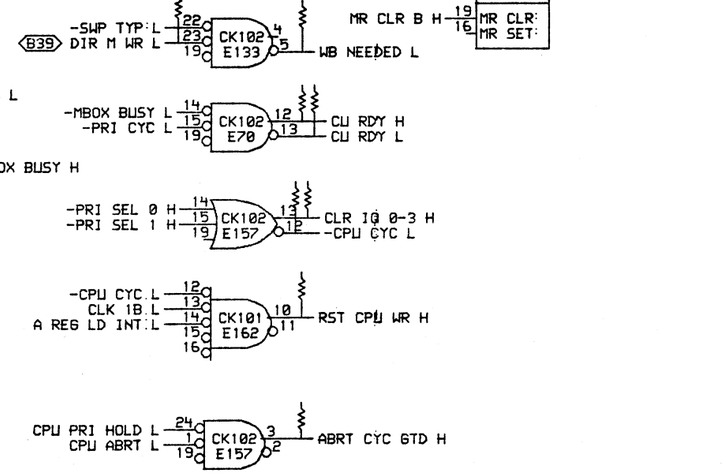












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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. J. Toomey 01-DEC-82 | ENG. | DATE

CHK'D. | DATE | BOARD LOCATION:

VULCAN:<2080.CAM>78CAMH.DRW 30-NOV-82 07:47 | SHEET 1 OF 1

FIRST USED ON OPTION/MODEL: 2080: | NEXT HIGHER ASSEMBLY: B-DD-L0059-0

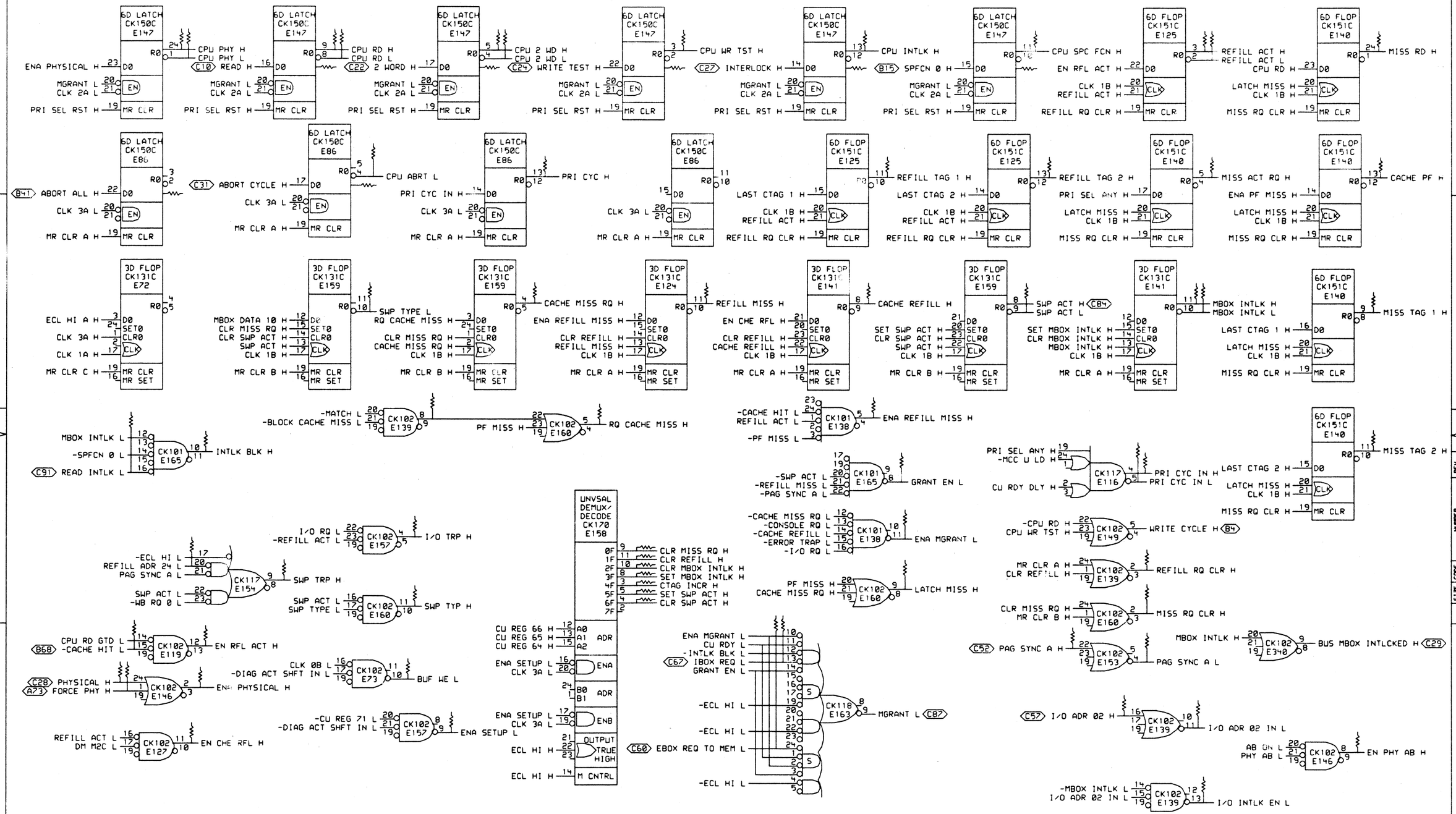
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D	CS	L0059-0-CAMH	*

8 7 6 5 4 3 2 1

D C B A

REV. * | NUMBER L0059-0-CAMH | CODE CS | SIZE D



REVISIONS		
CHK	CHANGE NO.	REV

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DRN. D. Toomey
CHK 'D.

DATE	ENG.	DATE
21-JAN-83		
DATE	BOARD LOCATION:	
	SHEET 1 OF 1	

VULCAN:<2080.CAM>B-CAMI.DRW 20-JAN-83 14:59
FIRST USED ON OPTION/MODEL: 2080

NEXT HIGHER ASSEMBLY: B-DD-L0059-0

TITLE: CPU CYC QUAL

SIZE	CODE	NUMBER	REV.
D	CS	L0059-0-CAMI	*

UNVSAL DEMUX/ DECODE CK170 E1

0F 9 WD0 VALID MASK H A14
1F 11 WD1 VALID MASK H A11
2F 10 WD2 VALID MASK H A3
3F 8 WD3 VALID MASK H B74
4F 3 WD0 VALID MASK H
5F 5 WD1 VALID MASK H
6F 4 WD2 VALID MASK H
7F 2 WD3 VALID MASK H

A26 VM LATCH 35 H 12, VM LATCH 34 H 13 → A0, A1, A2 ADR
-CPU 1 WD WR L 16, 20 → ENA
A1 -VM LATCH 35 H 24, A21 VM LATCH 34 H 1 → B0, B1 ADR
CPU 2 WD L 17, 19 → ENB
CPU 2 WD H 21, 22 → OUTPUT TRUE HIGH
ECL HI H 23
14 M CNTRL

I/O CYC L 22, C81 -I/O ADR 03 L 23, 19 → CK102 E73 4, 5

UNVSAL DEMUX/ DECODE CK170 E134

0F 9 FORCE ADR SEL H
1F 11 CLR I/O RQ H
2F 10 INCR QUAD H C35
3F 8 SET CPU WRIT H
4F 3 -LD AB COND H
5F 5 -LD AB EN H
6F 4 -LD QUAD H C20
7F 2 -CSL Q LD H

CU REG 69 H 12, CU REG 68 H 13 → A0, A1, A2 ADR
-CU REG 67 L 16, ENA CONST L 20 → ENA
CU REG 69 H 24, CU REG 68 H 1 → B0, B1 ADR
CU REG 67 L 17, ENA CONST L 19 → ENB
ECL HI A H 21, 22, 23 → OUTPUT TRUE HIGH
14 M CNTRL

-I/O CYC H 24 → CK122 E65 2 → -ANY VALID MASK H A24

CU REG 56 L 17 → CK122 E65 9 → AQ WR PULSE IN L B40

6D FLOP CK151C E125

CPU 2 WD H 16 → D0
CLK 1B H 20, REFILL ACT H 21 → CLK
REFILL RQ CLR H 19 → MR CLR
R0 9, 8 → 2 WRD RQ H, 2 WRD RQ L

4X2 MUX LATCH CK155C E135

-CPU PHY H 14, 15 → D00, D01
20, 21 → EN
FORCE ADR SEL H 16, 17 → 3=D0+D1 +R, 2=D1+R, 1=D0+R, 0=CLR
19 MR CLR
R0 9, 8 → VIRT ADR REF H B77

VM LATCH 35 L 16, -CPU 2 WD L 17, 19 → CK102 E133 11, 10

4X2 MUX LATCH CK155C E135

CU REG 41 H 24 → D00, D01
20, 21 → EN
FORCE ADR SEL H 16, 17 → 3=D0+D1 +R, 2=D1+R, 1=D0+R, 0=CLR
19 MR CLR
R0 2, 3 → DC EA LATCH EN IN H, DC EA LATCH EN IN L

4X2 MUX LATCH CK155C E135

CPU 2 WD H 22, 23 → D00, D01
20, 21 → EN
FORCE ADR SEL H 16, 17 → 3=D0+D1 +R, 2=D1+R, 1=D0+R, 0=CLR
19 MR CLR
R0 5, 4 → CPU 2WD A H A80

4X2 MUX LATCH CK155 E261

-35 MATCH H 12, 13 → D00, D01
-34 MATCH H 14, 15 → D10, D11
22, 23 → D20, D21
24, 1 → D30, D31
20, 21 → EN
2 WRD RQ H 16, 17 → 3=D0+D1 +R, 2=D1+R, 1=D0+R, 0=CLR
-REFILL CYC H 19 → MR CLR
R0 10, 11
R1 9, 8 → MRESP INHIBIT H A27
R2 5, 4
R3 2, 3

CU REG 67 H 24, 1, 19 → CK102 E149 2, 3 → CU REG 67 L

2WD RSP DLY H 16, 17, REFILL MBOX TAG 3 H 19 → CK102 E55 10, 11 → 2WD RSP DELAY H

VM LATCH 35 H 14, CU REG 48 H 15 → CK107 E243 5, 13, 12 → -35 MATCH H, -35 MATCH L

VM LATCH 34 H 16, CU REG 47 H 17 → CK107 E243 5, 11, 10 → -34 MATCH H

-CU REG 72 L 14, -DIAG ACT SHFT IN L 15, 19 → CK102 E117 12, 13 → ENA CONST L

23, 24, 35 MATCH H 1, -34 MATCH H 2, -2 WRD RQ H 3, -REFILL CYC H → CK101 E162 4, 5 → -REFILL MBOX TAG 3 L

-CPU RD DLY H 14 → CK122 E361 12 → -CPU RD GTD H

ABRT GTD 1-2 H 15 → CK122 E361 11

REFILL ACT H 17 → CK122 E361 9 → REFILL OR ABRT H

ABRT GTD 1-2 H 16 → CK122 E361 10

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REVISIONS

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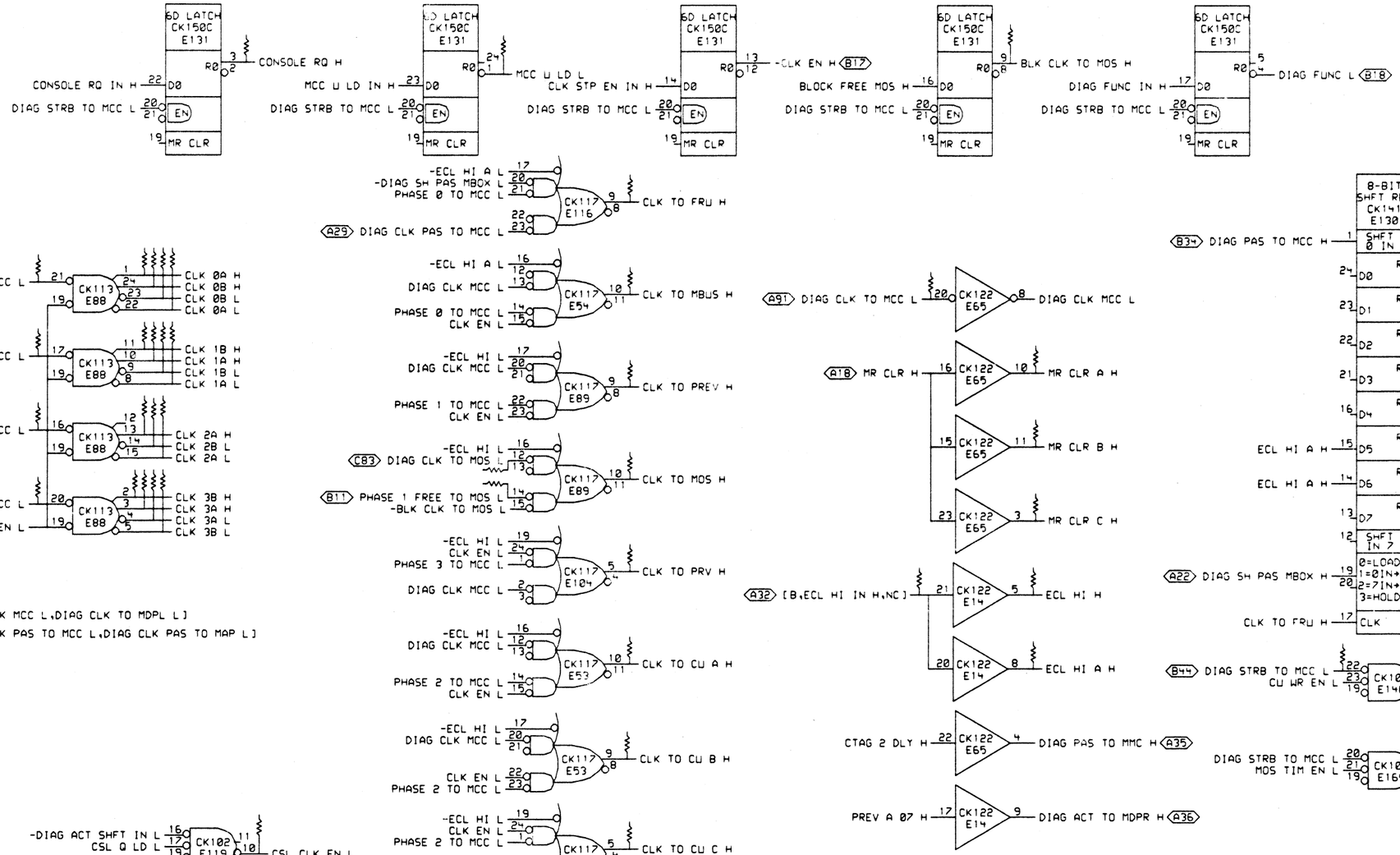
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DRN. S. Lasosky | DATE 22-DEC-82 | ENG. | DATE
CHK'D. | DATE | BOARD LOCATION: | SHEET 1 OF 1

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FIRST USED ON OPTION/MODEL: 2080

TITLE: MAP CONTROL LOGIC

SIZE	CODE	NUMBER	REV.
D	CS	L0059-0-CAMJ	*



REVISIONS		
CHK	CHANGE NO.	REV

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digital

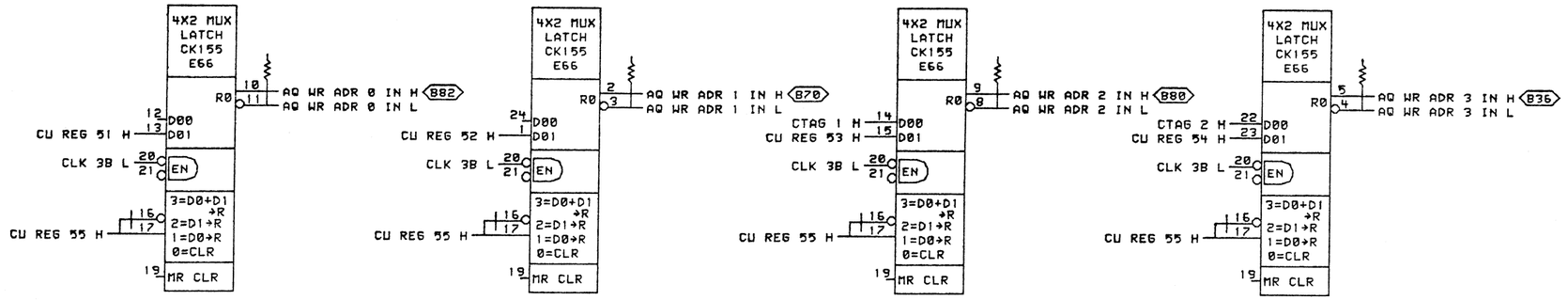
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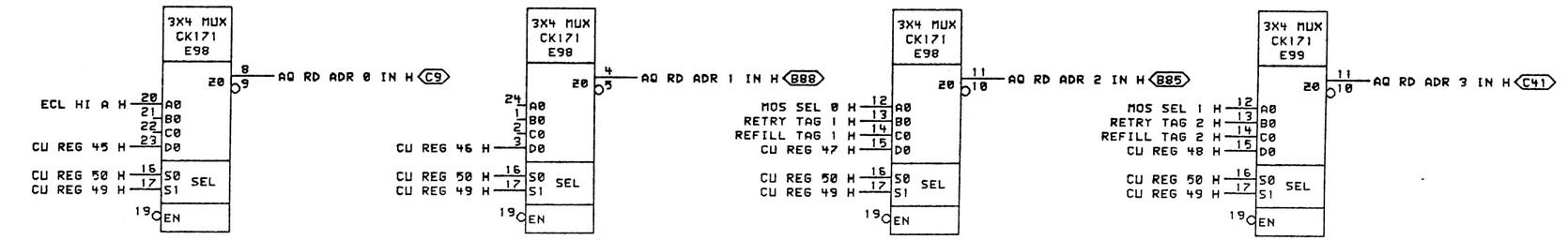
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NEXT HIGHER ASSEMBLY: B-DD-L0059-0

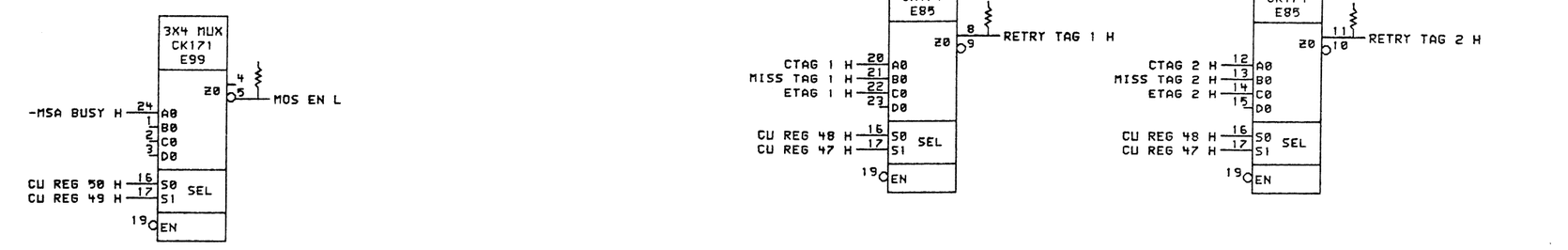
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SIZE	CODE	NUMBER	REV.
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NUMBER L0059-0-CAMK SIZE D CODE CS REV. *







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REVISIONS		
CHK	CHANGE NO.	REV

digital

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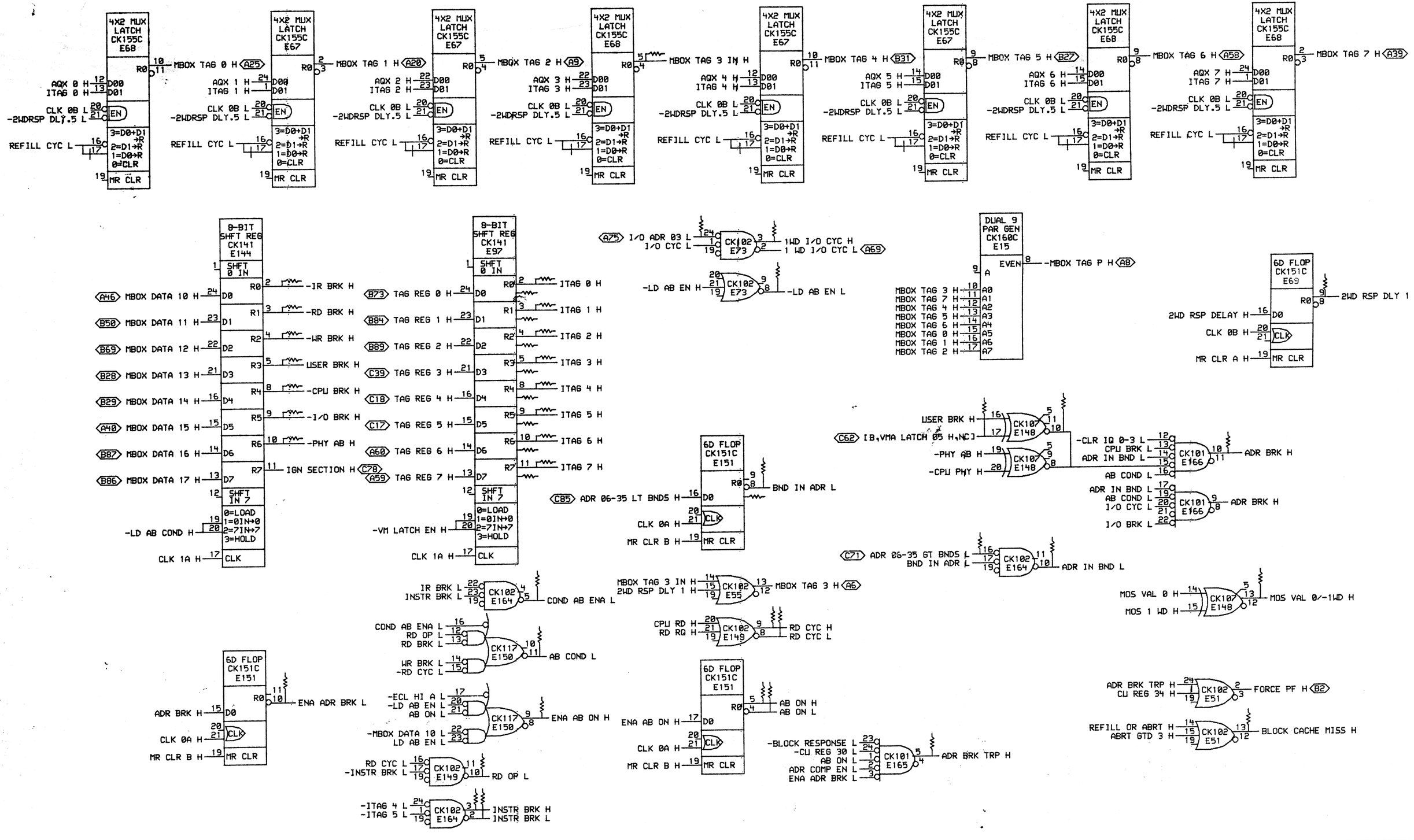
SHEET 1 OF 1

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FIRST USED ON OPTION/MODEL: 2080 | B-DD-L0059-0

TITLE:

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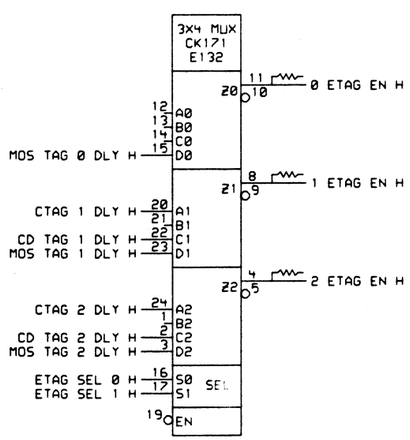
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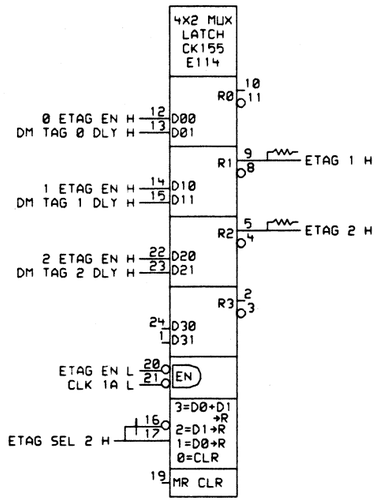
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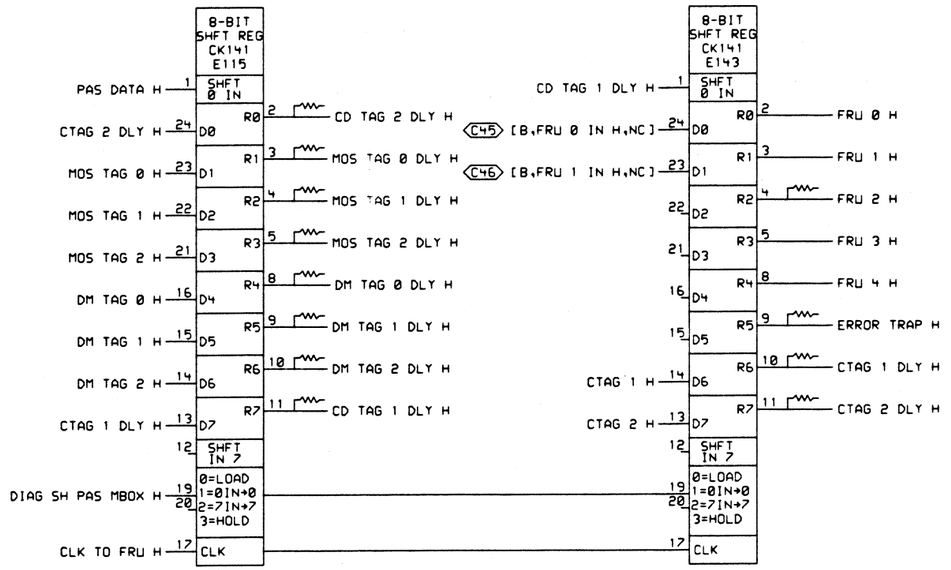
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CHK D.	DATE	BOARD LOCATION:	
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FIRST USED ON OPTION/MODEL: 2080		NEXT HIGHER ASSEMBLY: B-DD-L0059-0	

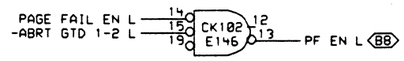
TITLE: IBOX TAGS

SIZE	CODE	NUMBER	REV.
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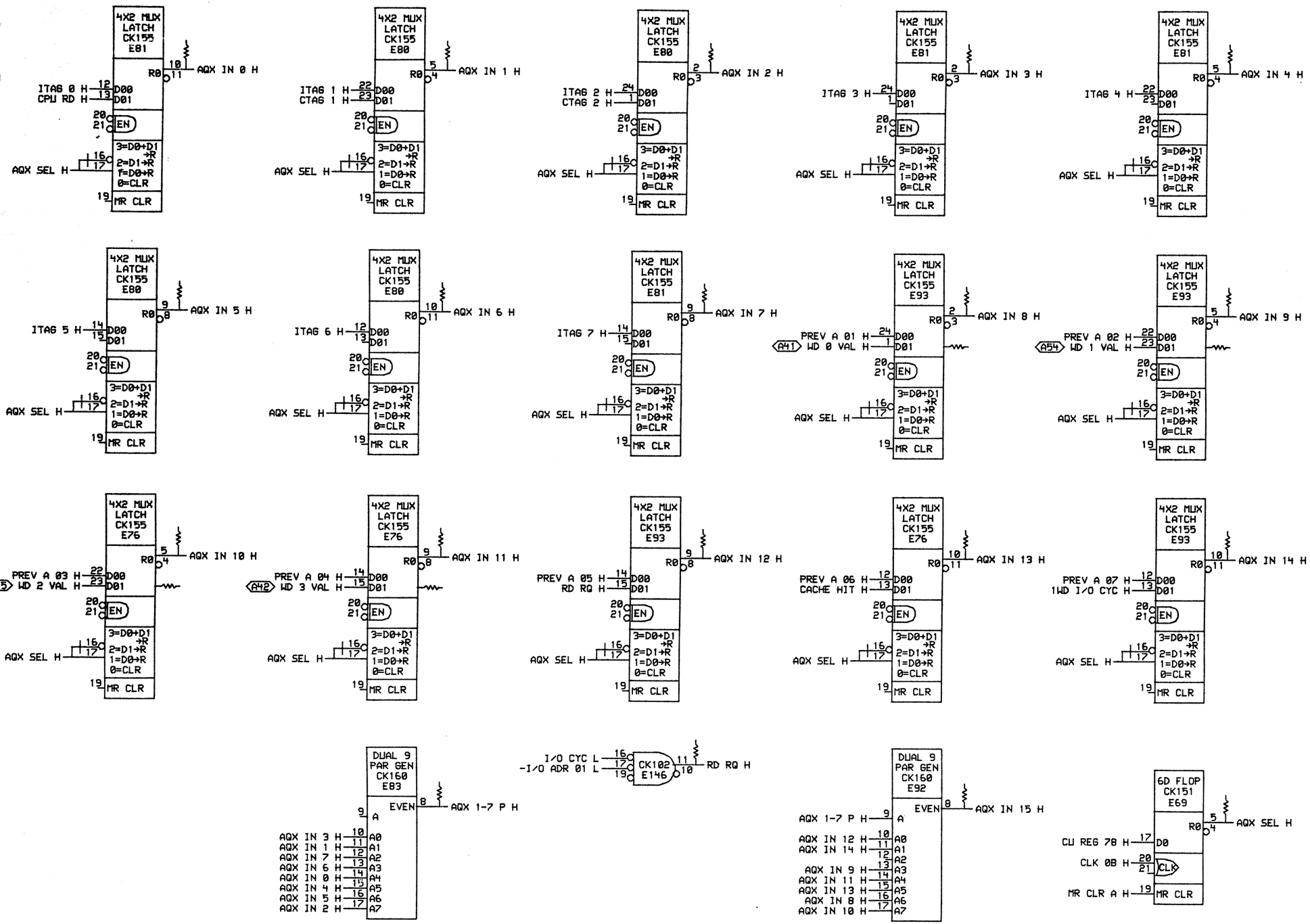
REVISIONS		
CHK	CHANGE NO.	REV

DRN: L.Toomey 22-DEC-82 | ENG. | DATE
CHK'D. | DATE | BOARD LOCATION:
SHEET 1 OF 1

VULCAN:<2080.CAM>B1CAMN.DRW 21-DEC-82 13:33 | NEXT HIGHER ASSEMBLY: B-DD-L0059-0 | FIRST USED ON OPTION/MODEL: 2080

TITLE: MCC FRU

SIZE	CODE	NUMBER	REV.
D	CS	L0059-0-CAMN	*



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REVISIONS		
CHK	CHANGE NO.	REV

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DRN. [illegible] DATE 27-MAR-82 | ENG. | DATE | TITLE:

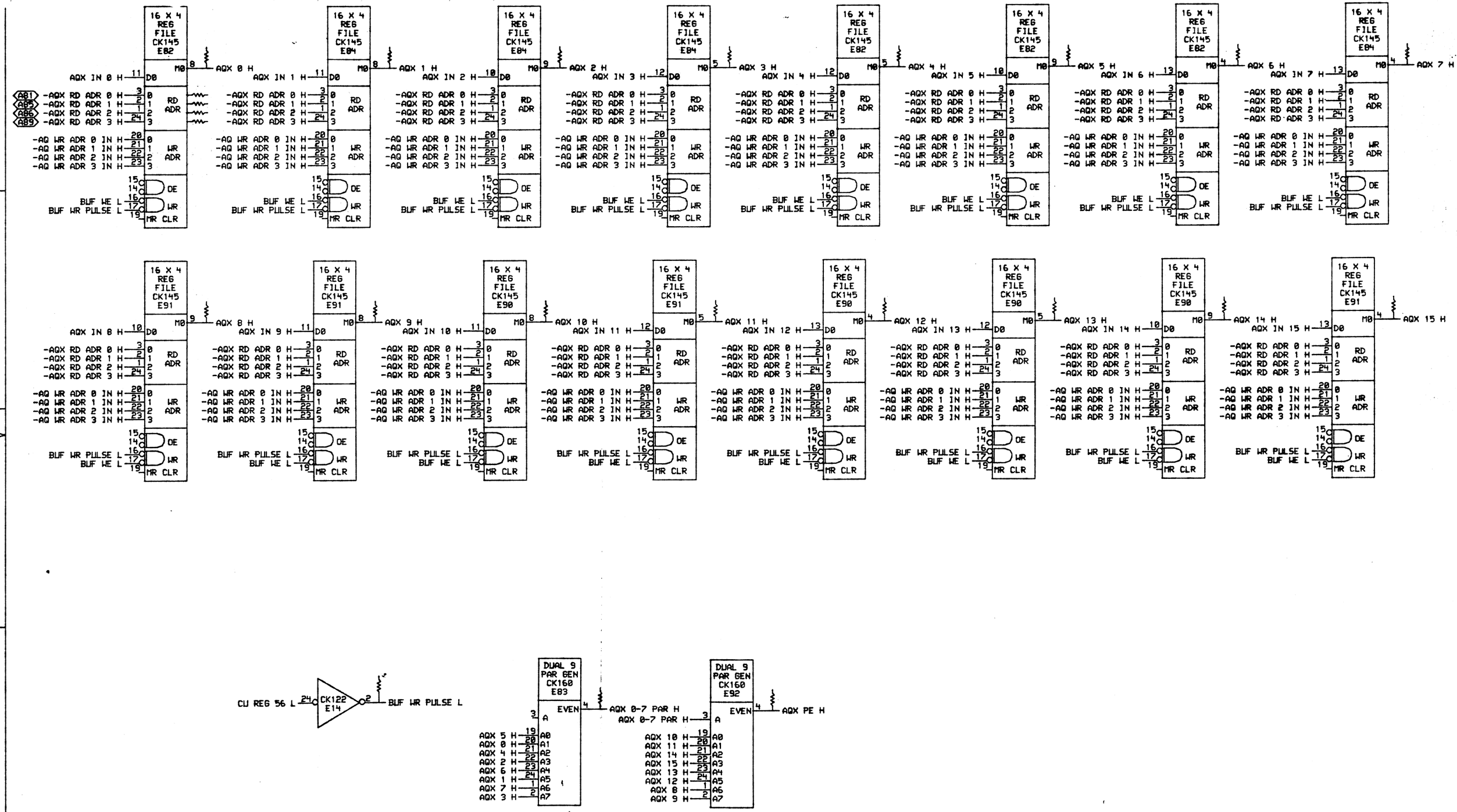
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FIRST USED ON OPTION/MODEL: 2080

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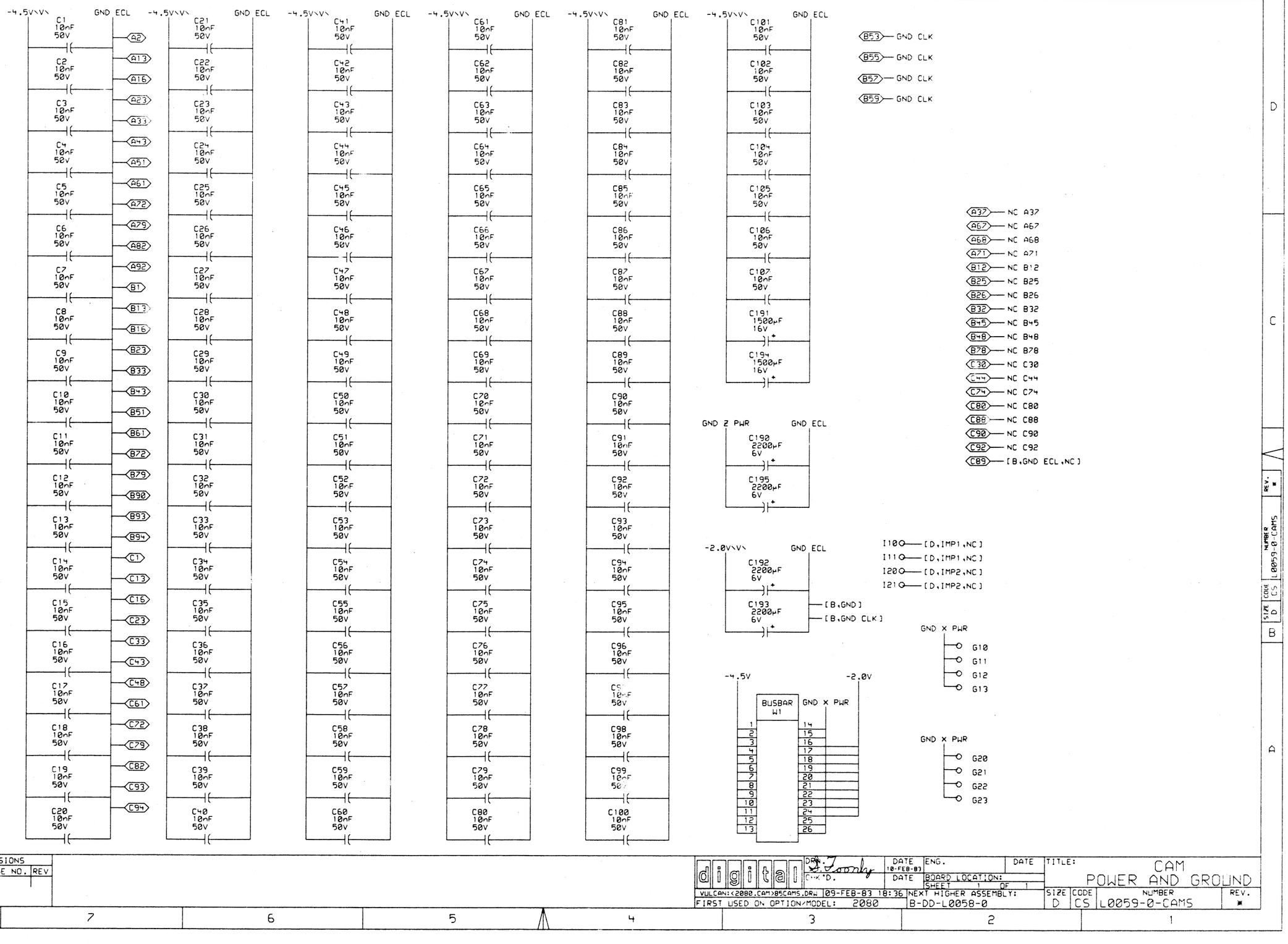
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REVISIONS		
CHK	CHANGE NO.	REV

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FIRST USED ON OPTION/MODEL: 2080		NEXT HIGHER ASSEMBLY: B-DD-L0059-0		SIZE D	CODE CS	NUMBER L0059-0-CAMR REV. *

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REVISIONS		
CHK	CHANGE NO.	REV

DRN: T.Toohy	DATE: 18-FEB-83	ENG.	DATE
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FIRST USED ON OPTION/MODEL: 2080		NEXT HIGHER ASSEMBLY: B-DD-L0058-0	

TITLE: CAM POWER AND GROUND

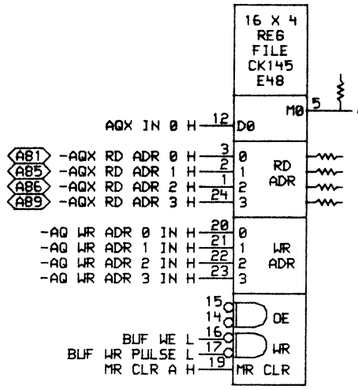
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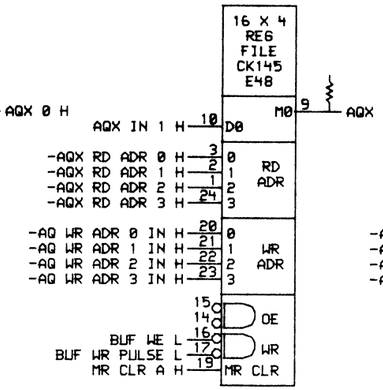
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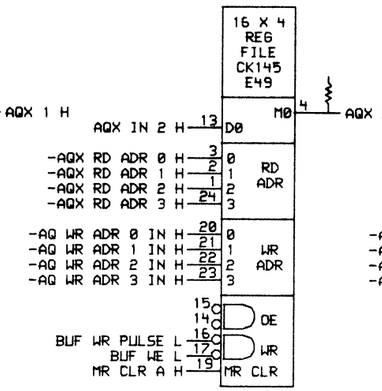
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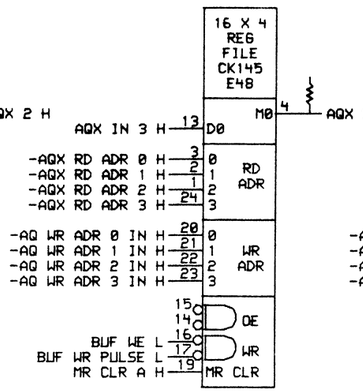
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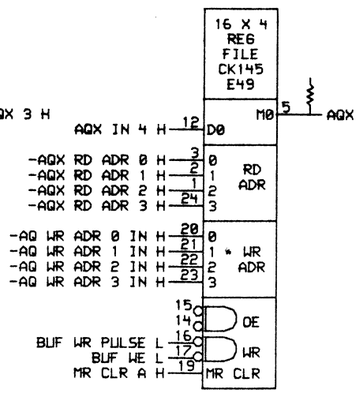
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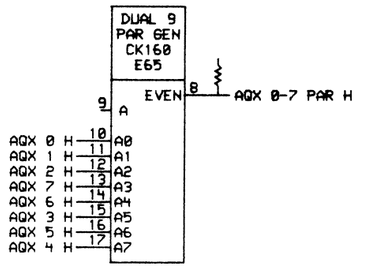


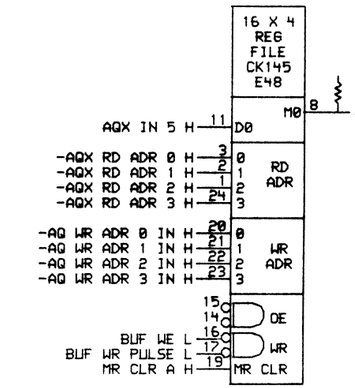


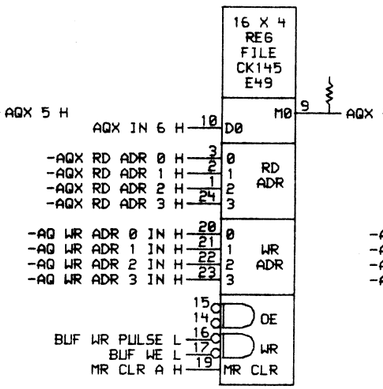


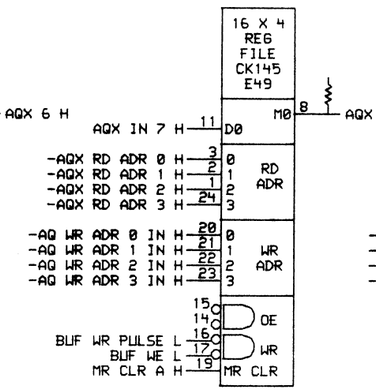


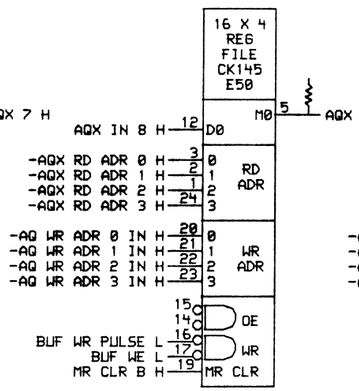


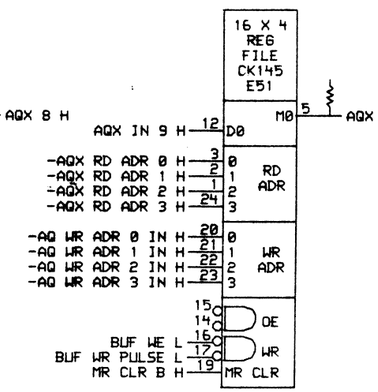


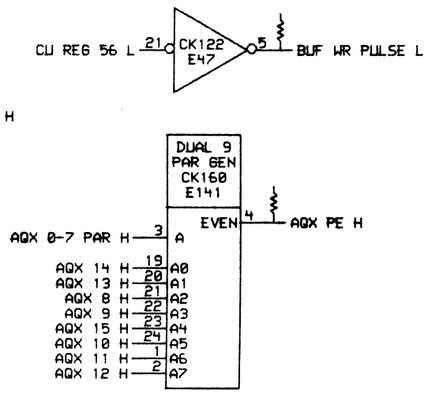


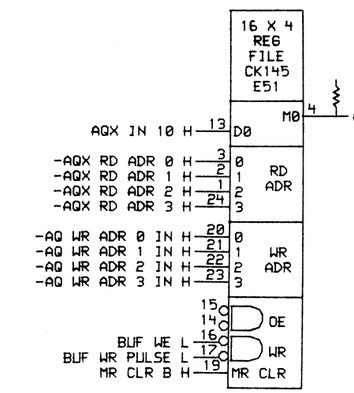


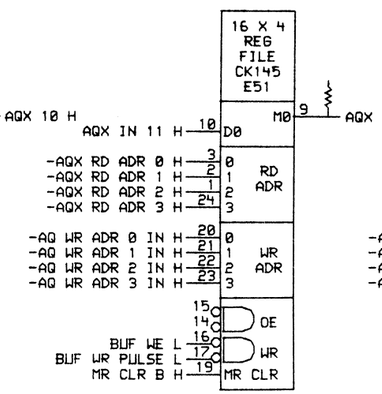


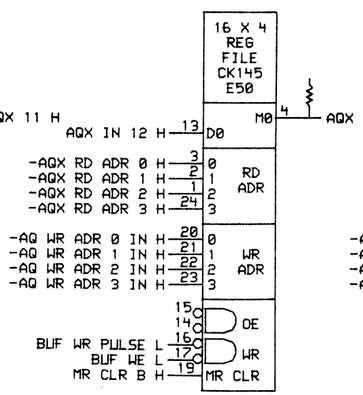


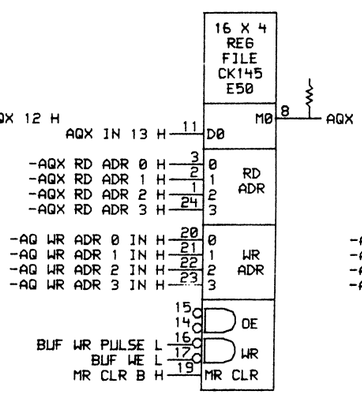


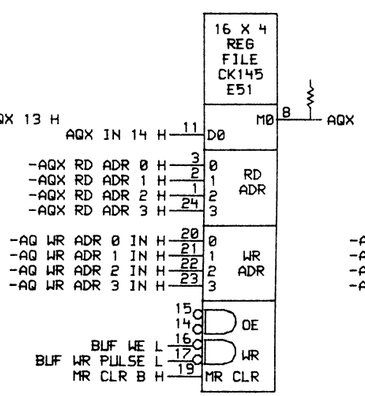


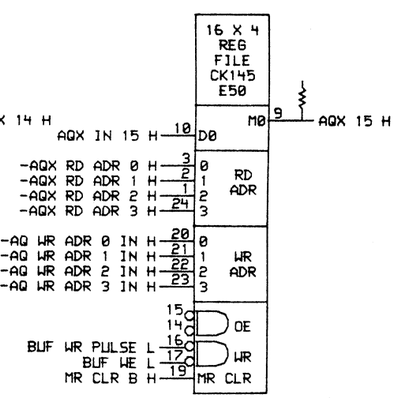












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REVISIONS		
CHK	CHANGE NO.	REV

DRN. J. Foody	DATE 23-FEB-82	ENG.	DATE
CHK D.	DATE	BOARD LOCATION:	
VULCAN:<2080,L0058>59MCC1.DRW 22-FEB-82 15:28		SHEET 1 OF 1	
FIRST USED ON OPTION/MODEL: 2080		NEXT HIGHER ASSEMBLY: B-DD-L0058-0	

TITLE: ADR QUT EXTENSION AQX 00-15

SIZE	CODE	NUMBER	REV.
D	CS	L0058-0-MCC1	*

256 X 4 RAM CK422 E132

RAS A H 12
PULL DOWN A L 9
-MSA T0 H 19
-MSA T1 H 20
-MSA T2 H 21
-MSA T3 H 22
MSA RD H 23
REF CYC H 15
-MSA T1 H 16
-MSA T0 H 17
MOS WE L 14

D0 EN 0 0M
D0 D1 D2 D3 D4 D5 D6 D7
0 1 2 3 4 5 6 7
ADR
WRITE

8 MSA RAS H

256 X 4 RAM CK422 E133

CAS A H 1
PULL DOWN A L 4
-MSA T1 H 19
-MSA T2 H 21
MSA RD H 23
-MSA T3 H 15
REF CYC H 16
-MSA T0 H 17
MOS WE L 14

5 MSA CAS H

256 X 4 RAM CK422 E132

WR A H 24
PULL DOWN A L 2
-MSA T0 H 19
-MSA T2 H 21
-MSA T3 H 22
MSA RD H 23
REF CYC H 15
-MSA T1 H 17
MOS WE L 14

3 MSA WR H

256 X 4 RAM CK422 E133

BUS MUX SWITCH A H 13
PULL DOWN A L 11
-MSA T1 H 19
-MSA T2 H 21
MSA RD H 23
-MSA T3 H 15
REF CYC H 16
-MSA T0 H 17
MOS WE L 14

10 MSA MUX H

256 X 4 RAM CK422 E133

BUS S1 A H 12
PULL DOWN A L 9
-MSA T1 H 19
-MSA T2 H 21
MSA RD H 23
-MSA T3 H 15
REF CYC H 16
-MSA T0 H 17
MOS WE L 14

8 MSA LD H

256 X 4 RAM CK422 E132

BUS CLK H 1
PULL DOWN A L 4
-MSA T0 H 19
-MSA T2 H 21
-MSA T3 H 22
MSA RD H 23
REF CYC H 15
-MSA T1 H 17
MOS WE L 14

5 MSA CLK H

256 X 4 RAM CK422 E133

-BUS EN A H 24
PULL DOWN A L 2
-MSA T1 H 19
-MSA T2 H 21
MSA RD H 23
-MSA T3 H 15
REF CYC H 16
-MSA T0 H 17
MOS WE L 14

3 MSA BUS H

256 X 4 RAM CK422 E132

MSA CLR H 13
PULL DOWN A L 11
-MSA T0 H 19
-MSA T2 H 21
-MSA T3 H 22
MSA RD H 23
REF CYC H 15
-MSA T1 H 17
MOS WE L 14

10 MSA PAR IN H

8-BIT SHFT REG CK141 E134

BUS CLK T4 H 1
MSA RAS H 24
MSA CAS H 23
MSA WR H 22
MSA MUX H 21
MSA LD H 16
MSA CLK H 15
MSA BUS H 14
MSA PAR IN H 13
DIAG ACT SHFT IN H 19 20
CLK TO MOS H 17

SHFT 0 IN
D0 D1 D2 D3 D4 D5 D6 D7
SHFT IN 7
0=LOAD
1=0IN→0
2=7IN→7
3=HOLD
CLK

R0 2 RAS A H (B52)
R1 3 CAS A H (B62)
R2 4 WR A H (B71)
R3 5 BUS MUX SWITCH A H (C7)
R4 8 BUS S1 A H (B65)
R5 9 BUS CLK H
R6 10 -BUS EN A H
R7 11 MSA CLR H

MSA CLR H
THIS NET HAS A
DISCREAT RESISTOR
ON THE MODULE
AS A TERM

NET E019P10 HAS A DISCRETE RESISTOR AS A TERM

-LOAD MOS ADR IN L 16
-MSA CLR L 17 19
CK102 E19 11 10

NET E078P10 HAS A DISCRETE RESISTOR AS A TERM

MSA BUSY DLY L 16 17 19
CK102 E78 11 10

4-STAGE CTR/SHFT CK136 E131

MSA CLR H 12
MSA T0 L 13
MSA T1 H 14
MSA T1 L
MSA T2 H 15
MSA T2 L
MSA T3 H 16
MSA T3 L
SHFT IN 0 CET 24
CEP 23
MSA T SEL 1 H 20
-MSA T SEL 2 H 21
CLK TO MOS H 22
CLEAR MSA H 17
19

TC 1
SHFT IN 3
D3 D2 D1 D0
R3 11 10
R2 8 9
R1 5 4
R0 2 3
S0 S1 SEL S2
CP
MR CLR

MR CLR B H 16 17 19
CK102 E128 10 11
CLEAR MSA H

DIAG ACT SHFT IN H 14
-LOAD MOS ADR IN H 15 19
CK102 E118 13 12
MSA T SEL 1 H

DIAG ACT SHFT IN H 16
LOAD MOS ADR IN H 17
EN ERRORS H 19
CK102 E119 10 11
MSA T SEL 2 L

-DIAG ACT SHFT IN L 23
MSA T0 L 24
MSA T1 L 1
MSA BUSY L 2
MSA T2 L 3
CK101 E130 5 4

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REVISIONS		
CHK	CHANGE NO.	REV

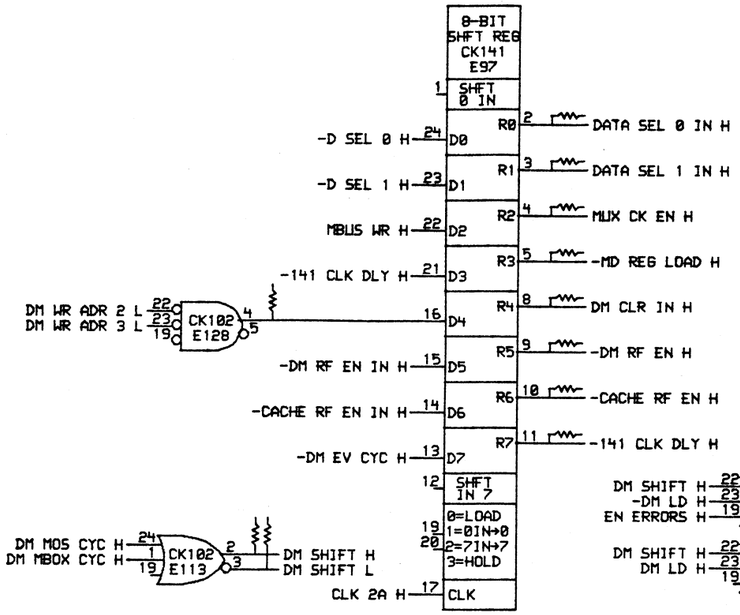
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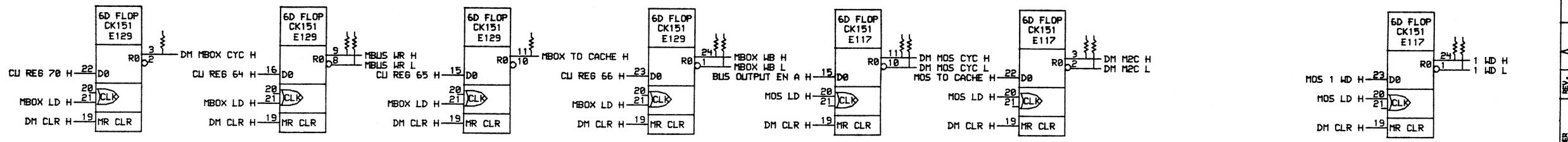
DRN. T. Toomey
CHK D.
DATE 27-JAN-82
ENG.
DATE
DATE
BOARD LOCATION:
SHEET 1 OF 1

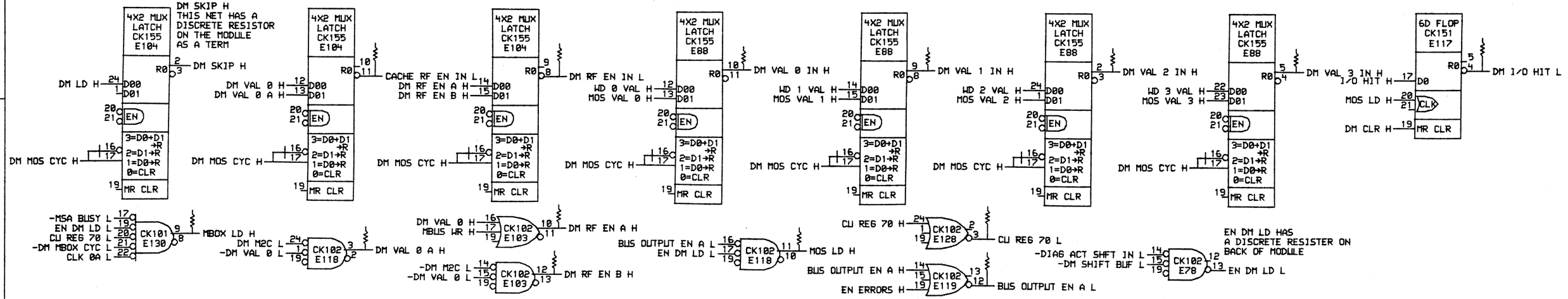
VULCAN:<2080.L0058>H7MCC2.DRW 26-JAN-82 10:01
FIRST USED ON OPTION/MODEL: 2080
NEXT HIGHER ASSEMBLY: B-DD-L0058-0

TITLE: MOS SEQUENCER CTL A

SIZE	CODE	NUMBER	REV.
D	CS	L0058-0-MCC2	*







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REVISIONS		
CHK	CHANGE NO.	REV

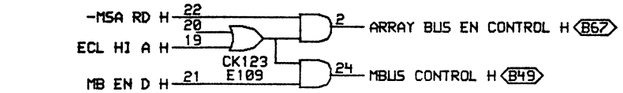
digital

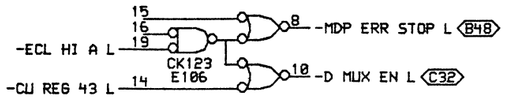
DRN. S. Toomey 05-FEB-82 15:30
CHK'D.
VULCAN:<2080.L0058>5MCC4.DRW
FIRST USED ON OPTION/MODEL: 2080

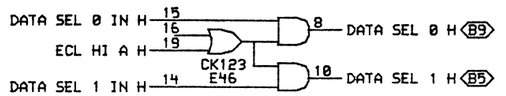
ENG. DATE
BOARD LOCATION:
SHEET 1 OF 1
NEXT HIGHER ASSEMBLY: B-DD-L0058-0

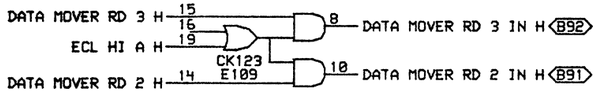
TITLE: DATA MOVER CTL

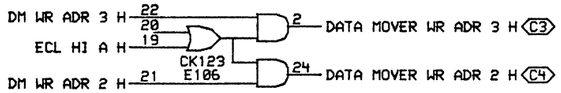
SIZE	CODE	NUMBER	REV.
D	CS	L0058-0-MCC4	*

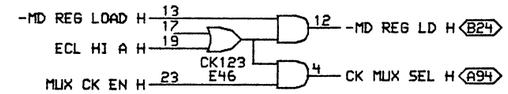


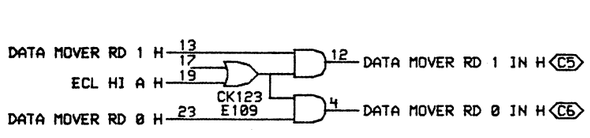


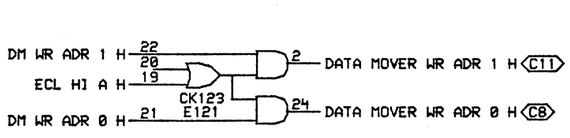


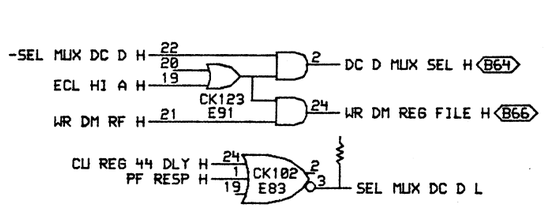




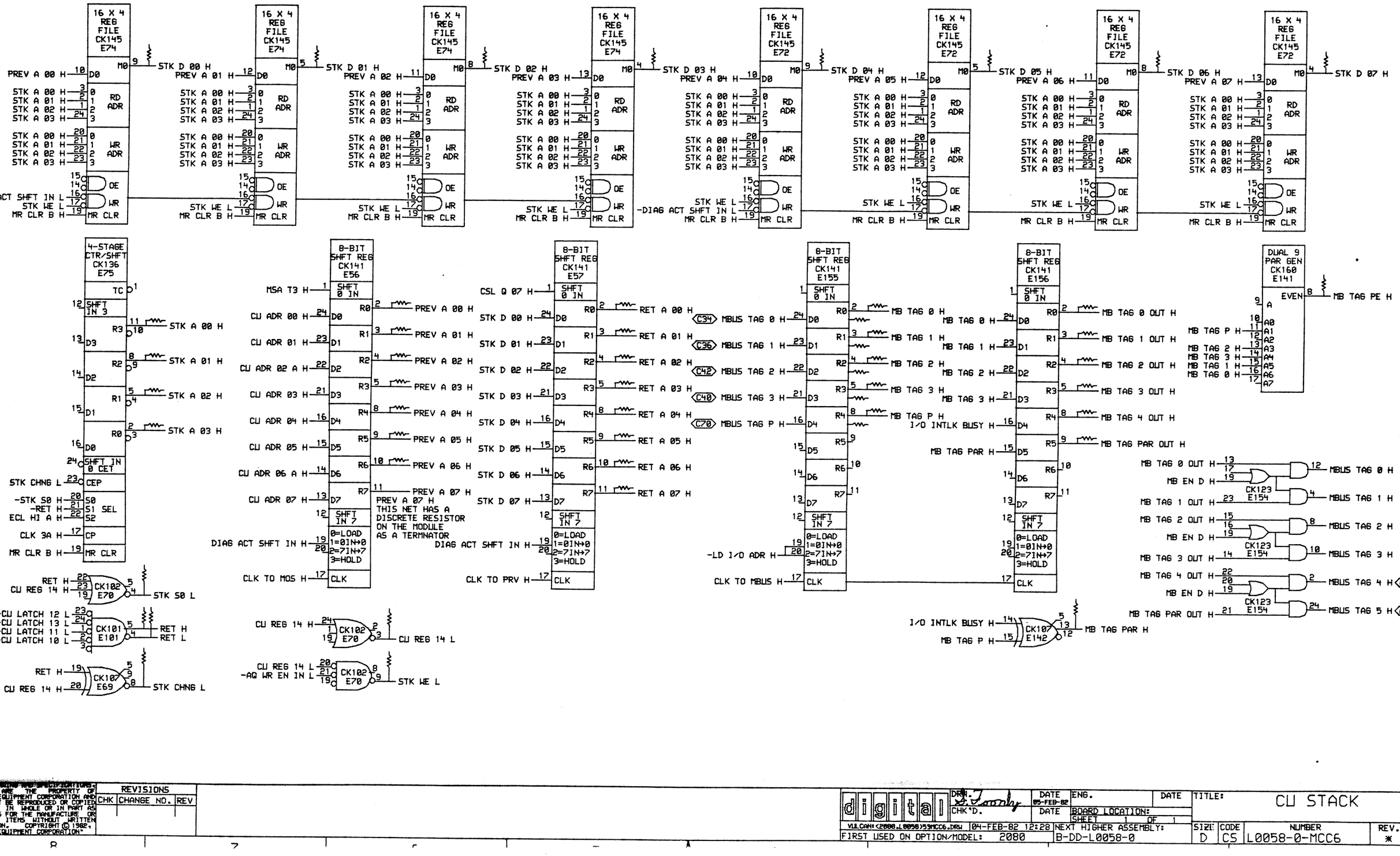








	DRN.	DATE 11-FEB-82	ENG.	DATE	TITLE:	MDP DMU CONTROL LOGIC
	CHK'D.	DATE	BOARD LOCATION: SHEET 1 OF 1			
VULCAN:<2000.L0058>57MCC5.DRW 10-FEB-82 13:53			NEXT HIGHER ASSEMBLY:		SIZE D / CODE C5	NUMBER L0058-0-MCC5 / REV. *
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0058-0			



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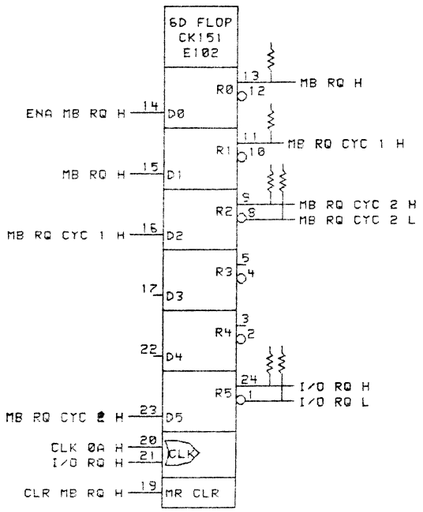
REVISIONS		
CHK	CHANGE NO.	REV

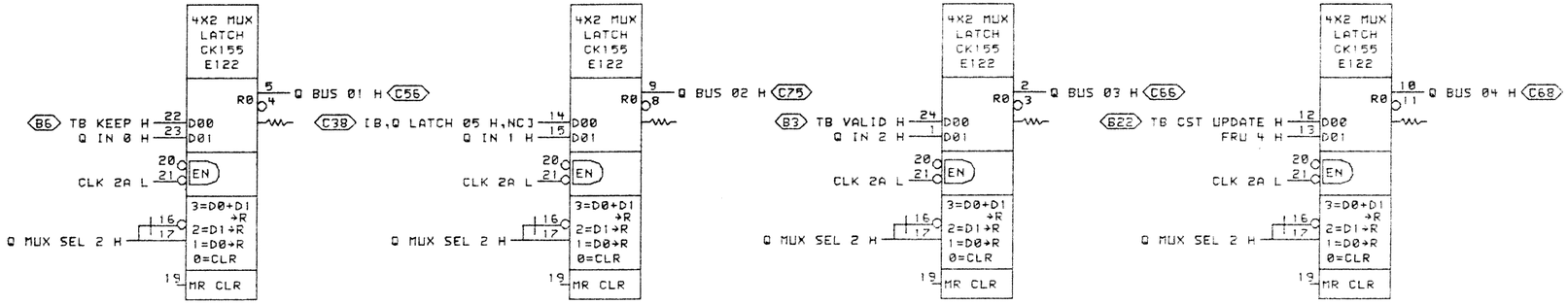
digital	DRN. D. Foomby	DATE 05-FEB-82	ENG.	DATE
	CHK'D.	DATE	BOARD LOCATION:	
			SHEET 1 OF 1	

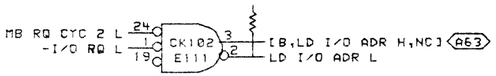
VILCAN<2080.L0058>53MCC6.DRW 04-FEB-82 12:28 | NEXT HIGHER ASSEMBLY: B-DD-L0058-0 | FIRST USED ON OPTION/MODEL: 2080

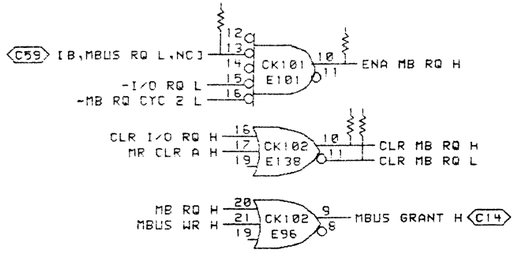
TITLE: CU STACK

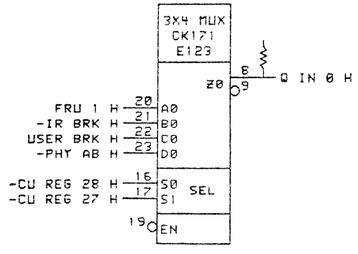
SIZE	CODE	NUMBER	REV.
D	CS	L0058-0-MCC6	*

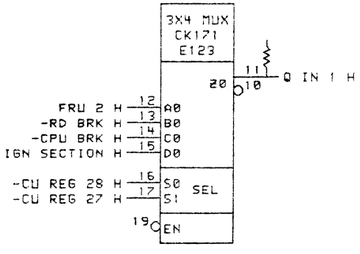


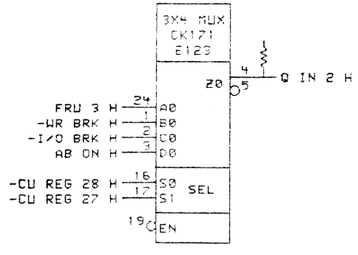












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REVISIONS		
CHK	CHANGE NO.	REV

digital

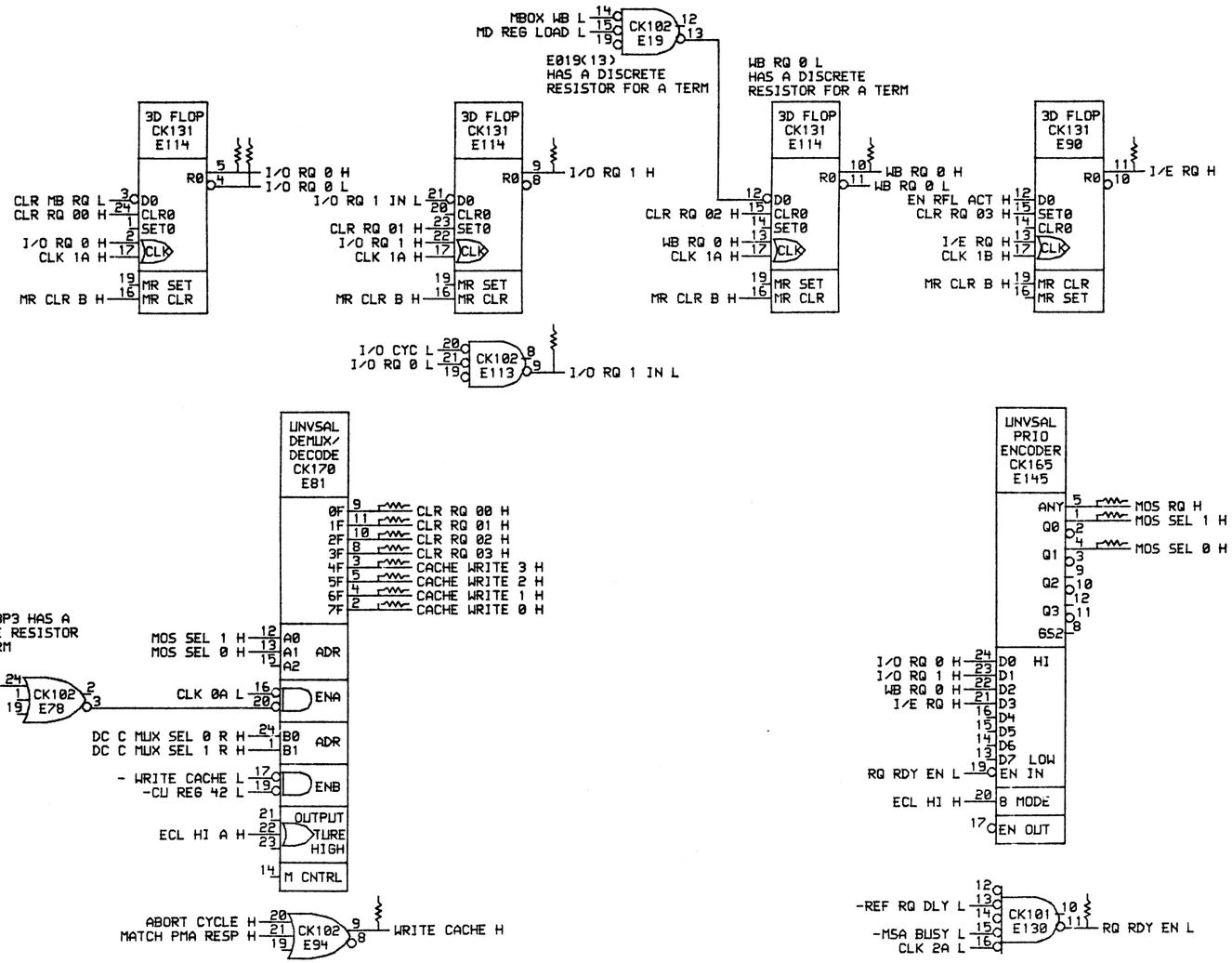
DRN. S. Cooley | DATE 04-JAN-82 | ENG. | DATE | TITLE:

CHK'D. | DATE | BOARD LOCATION: | SHEET 1 OF 1

VULCAN1:<2000.L0058>35MCC7.DRW 17-DEC-81 14:07 | NEXT HIGHER ASSEMBLY: B-DD-L0058-0 | FIRST USED ON OPTION/MODEL: 2080

SIZE	CODE	NUMBER	REV.
D	CS	L0058-0-MCC7	*

REV. * | SIZE D | CODE CS | NUMBER L0058-0-MCC7



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REVISIONS		
CHK	CHANGE NO.	REV

DRW. S. Toomey	DATE 10-FEB-82	ENG.	DATE
CHK D.	DATE	BOARD LOCATION: SHEET 1 OF 1	

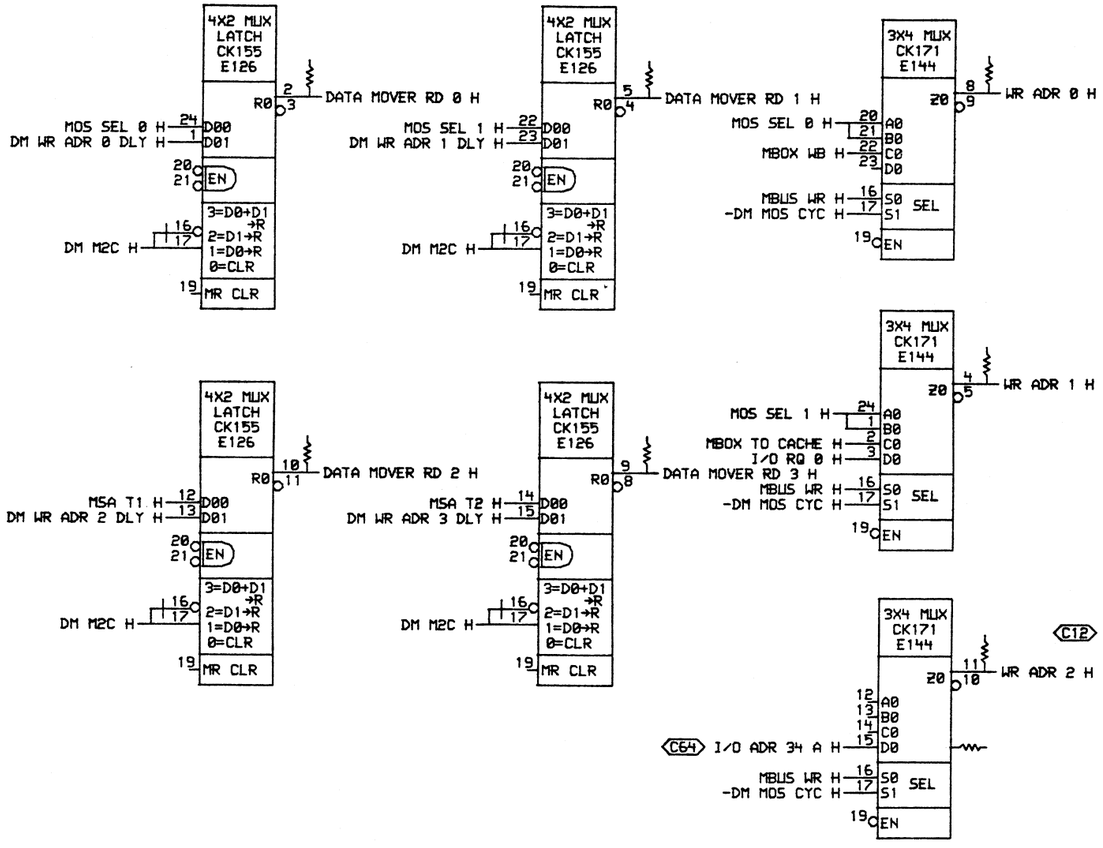
VILCAN:<2080.L0058>56MCC8.DRW 09-FEB-82 09:19

FIRST USED ON OPTION/MODEL: 2080

NEXT HIGHER ASSEMBLY: B-DD-L0058-0

TITLE: MOS RQ FF MOS PRI EN

SIZE	CODE	NUMBER	REV.
D	CS	L0058-0-MCC8	*



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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. S. Toomey 04-FEB-82 | ENG. | DATE | DATE | CHK D.

VULCAN:<2080.L0058>53MCC9.DRW 04-FEB-82 12:29

FIRST USED ON OPTION/MODEL: 2080

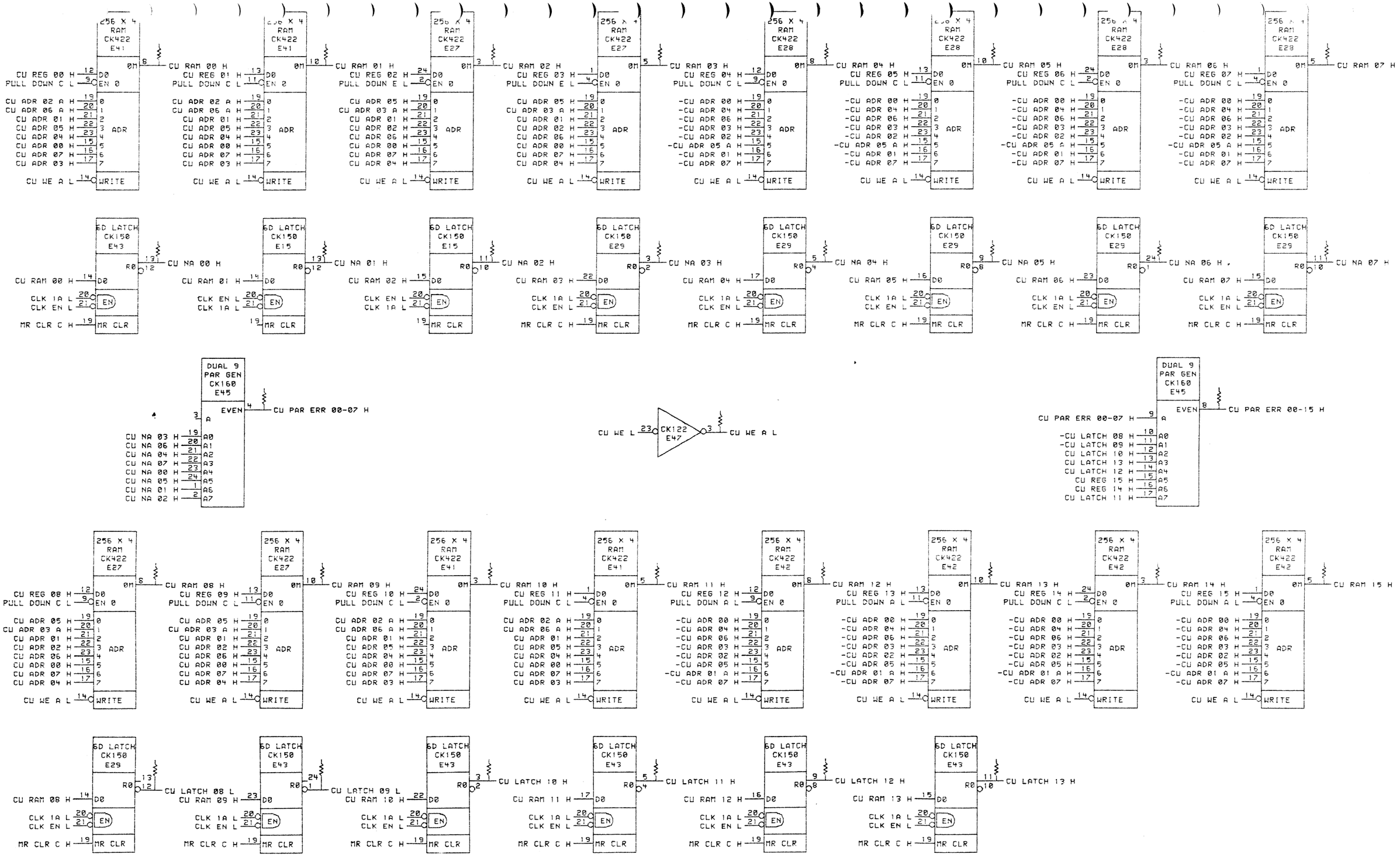
NEXT HIGHER ASSEMBLY: B-DD-L0058-0

BOARD LOCATION:

SHEET 1 OF 1

TITLE: DM RD/WR ADR CTL

SIZE	CODE	NUMBER	REV.
D	CS	L0058-0-MCC9	*



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REVISIONS		
CHK	CHANGE NO.	REV

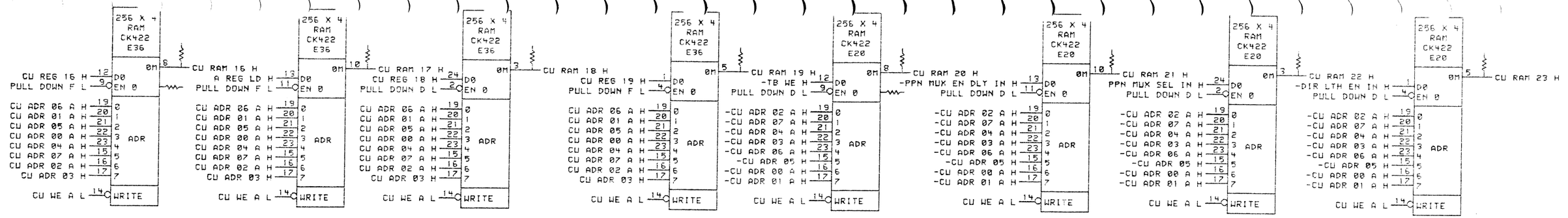
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DR: J. Toomey 04-JAN-82 | ENG. | DATE
CHK: D. | DATE | BOARD LOCATION:
SHEET 1 OF 5

VULCAN1<E0B8.L0058>12MCCA.DRW | 23-OCT-81 11:45 | NEXT HIGHER ASSEMBLY:
FIRST USED ON OPTION/MODEL: 2080 | B-DD-L0058-0

TITLE: CU RAM 00-15, CU LATCH 00-15 + P

SIZE	CODE	NUMBER	REV.
D	CS	L0058-0-MCCA	*



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REVISIONS		
CHK	CHANGE NO.	REV

digital

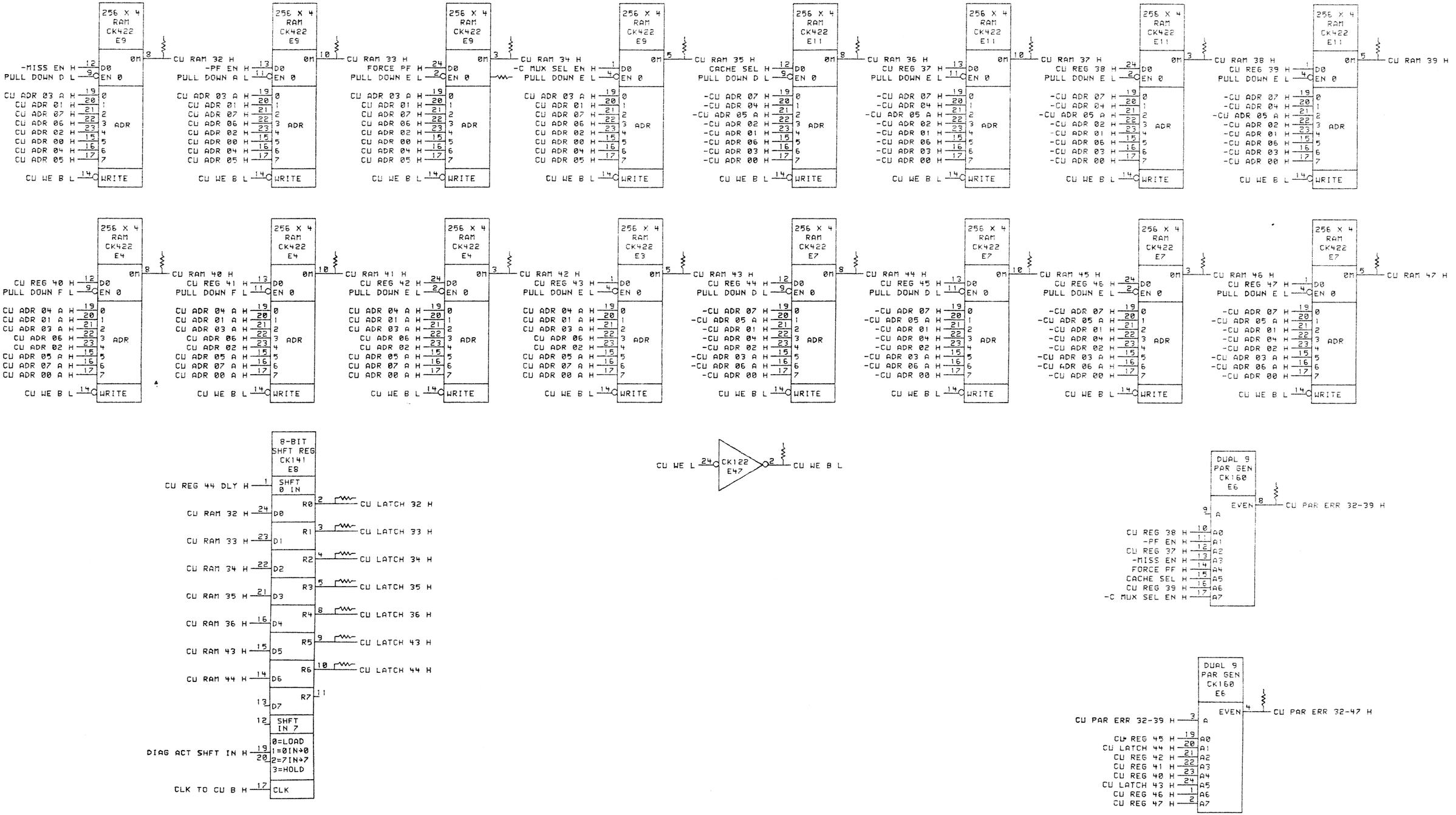
DRN E. Toomey 04-JAN-82 | ENG. | DATE | TITLE: CU RAM 16-31, CU LATCH 16-31 + P

CHK'D. | DATE | BOARD LOCATION: | SHEET 2 OF 5

VULCAN:<2000.L0058>13MCCB.DRW | 30-OCT-81 10:56 | NEXT HIGHER ASSEMBLY: B-DD-L0058-0

FIRST USED ON OPTION/MODEL: 2080

SIZE	CODE	NUMBER	REV.
D	CS	L0058-0-MCCB	*



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REVISIONS		
CHK	CHANGE NO.	REV.

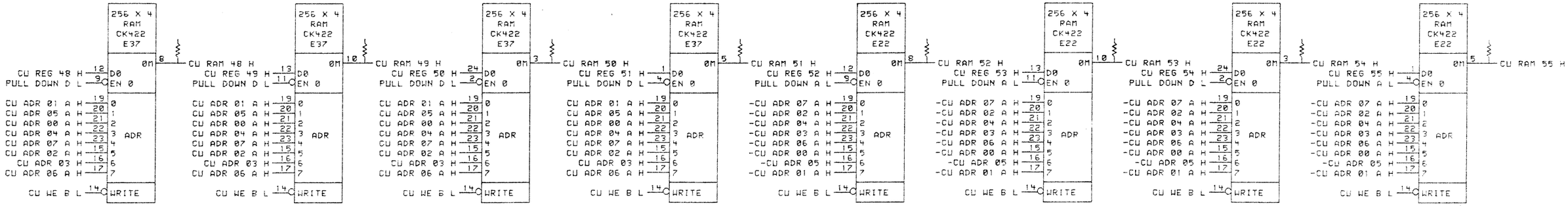
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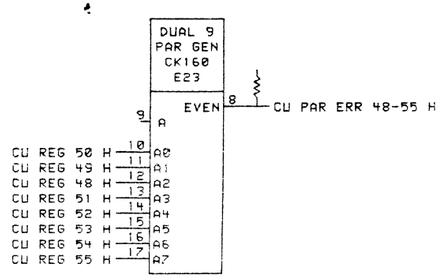
DRN. E. Toomey 04-JAN-82 | ENG. | DATE | CHK'D. | DATE | BOARD LOCATION: | SHEET 3 OF 5

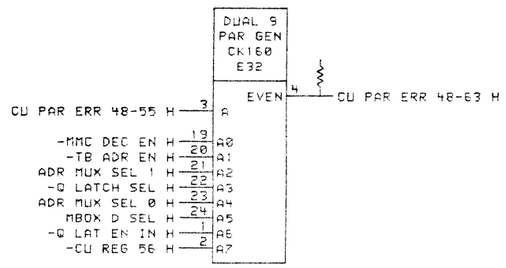
VULCAN:<E008.L0050>18MCCC.DRW 07-NOV-81 08:45 | NEXT HIGHER ASSEMBLY: | FIRST USED ON OPTION/MODEL: 2080 | B-DD-L0050-2

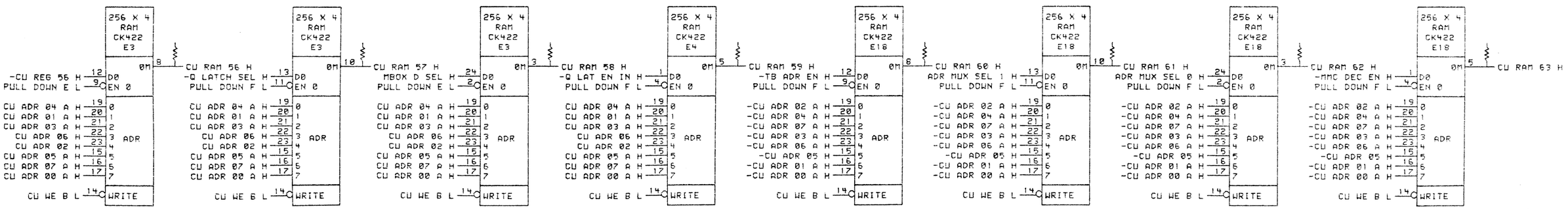
TITLE: CU RAM 32-47, CU LATCH 32-47 + P

SIZE	CODE	NUMBER	REV.
D	CS	L0050-2-MCCC	*









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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN F. Foosby | DATE 04-JAN-82 | ENG. | DATE

CHK'D. | DATE | BOARD LOCATION:

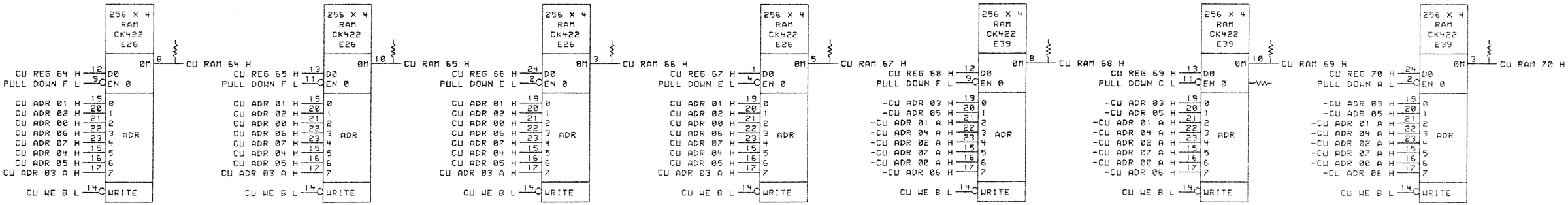
SHEET 4 OF 5

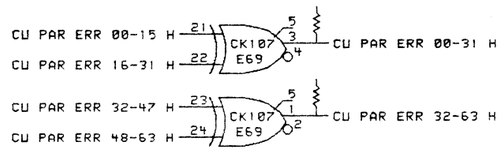
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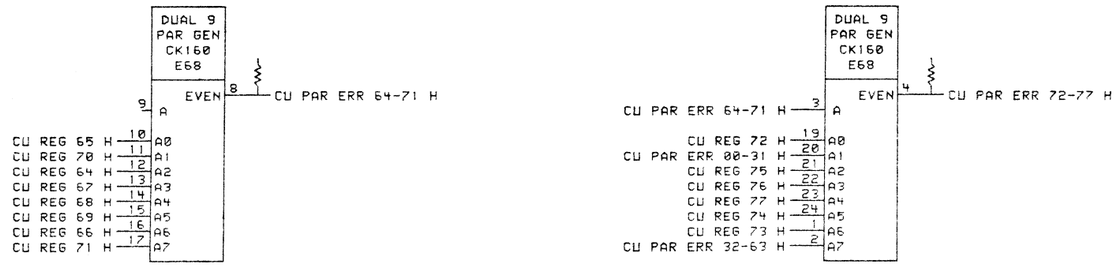
FIRST USED ON OPTION/MODEL: 2080

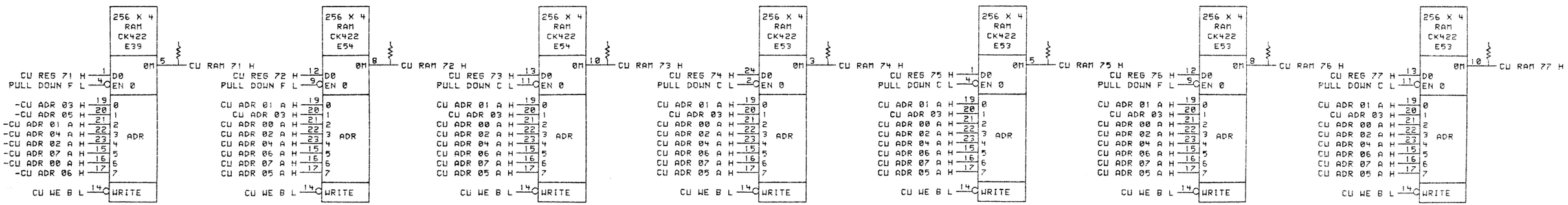
TITLE: CU RAM 48-63, + PARITY

SIZE	CODE	NUMBER	REV.
D	CS	L0058-0-MCCD	*









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CHK	CHANGE NO.	REV

digital

DR'D. S. Toomey
CHK'D.

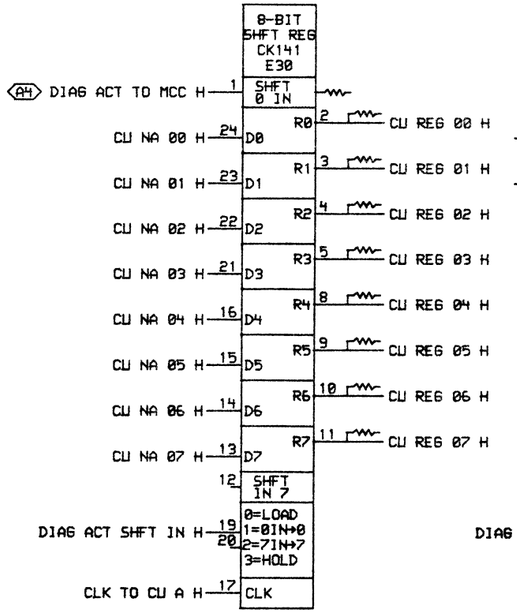
DATE	ENG.	DATE
04-JAN-82		
DATE	BOARD LOCATION:	
	SHEET 4 OF 5	

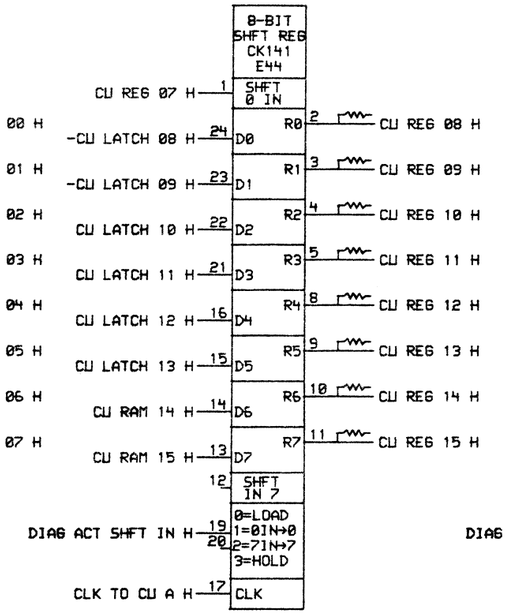
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FIRST USED ON OPTION/MODEL: 2080
NEXT HIGHER ASSEMBLY: B-DD-L0058-0

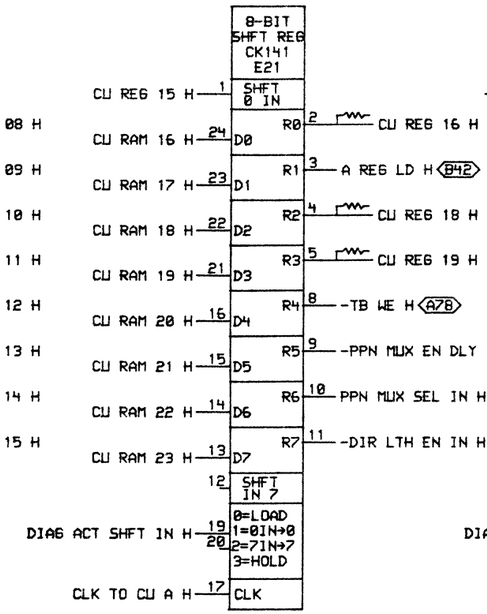
TITLE: CU RAM 64-73
CU LATCH 64-73 + P

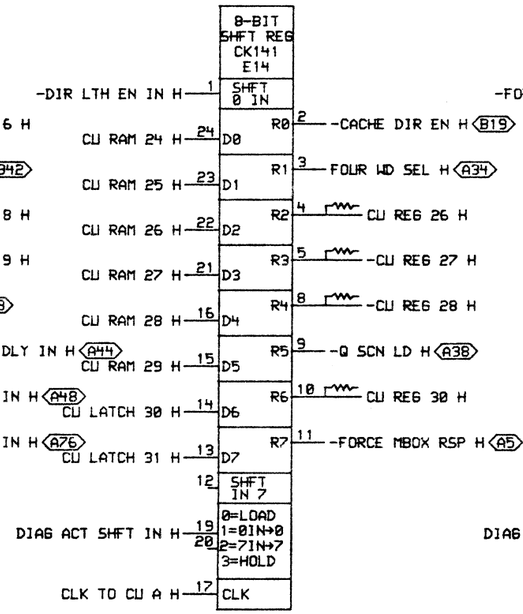
SIZE	CODE	NUMBER	REV.
D	CS	L0058-0-MCCE	*

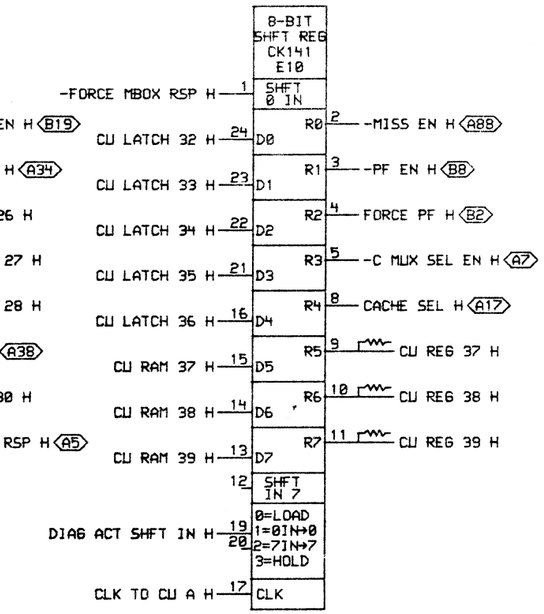
REV. *
NUMBER L0058-0-MCCE
CODE CS
SIZE D

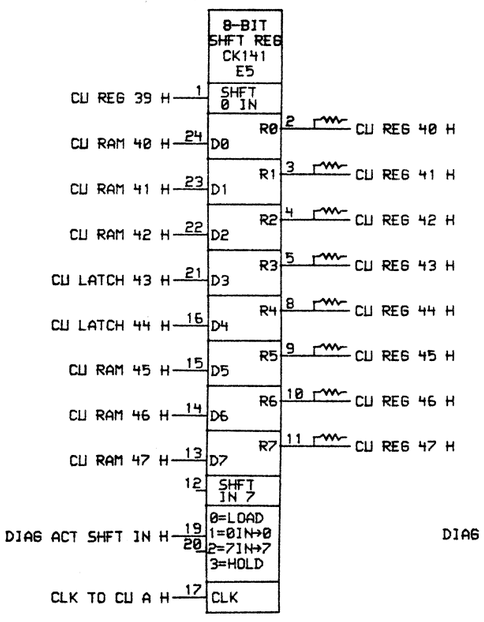


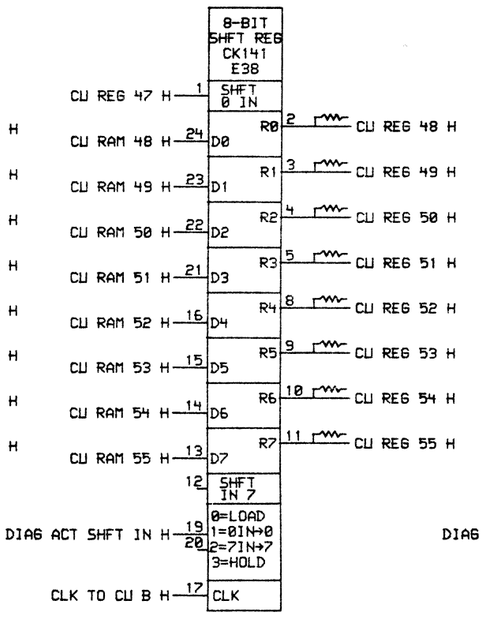


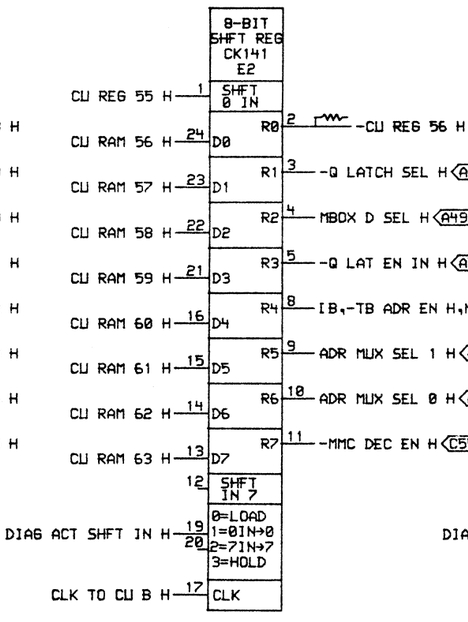


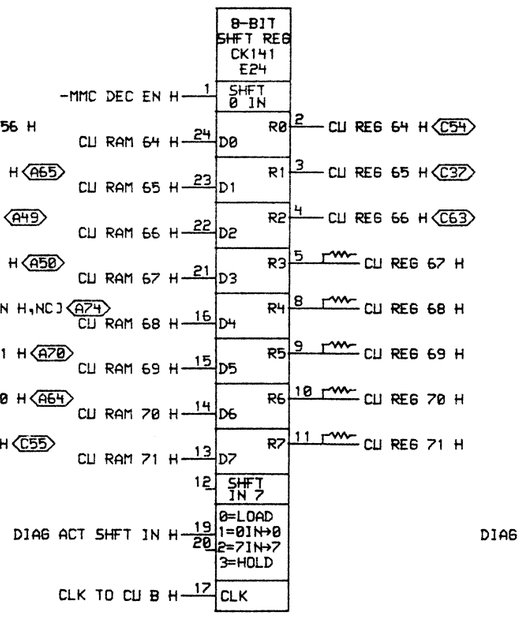


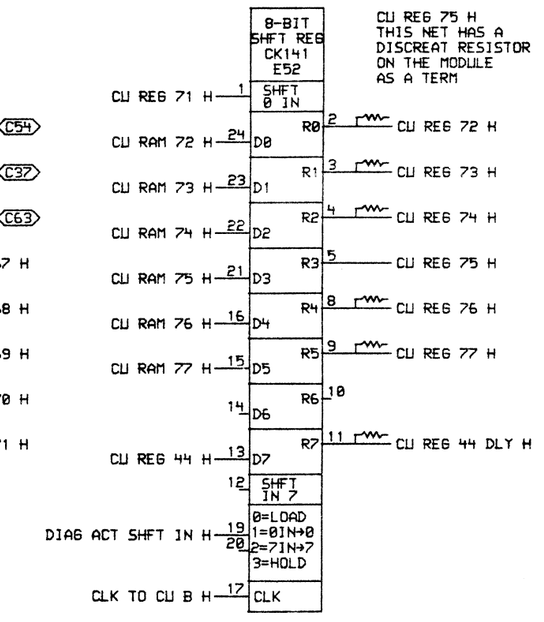




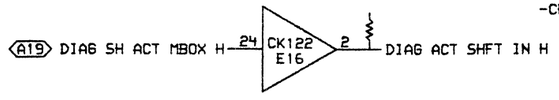


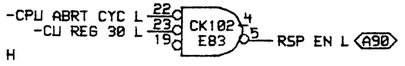




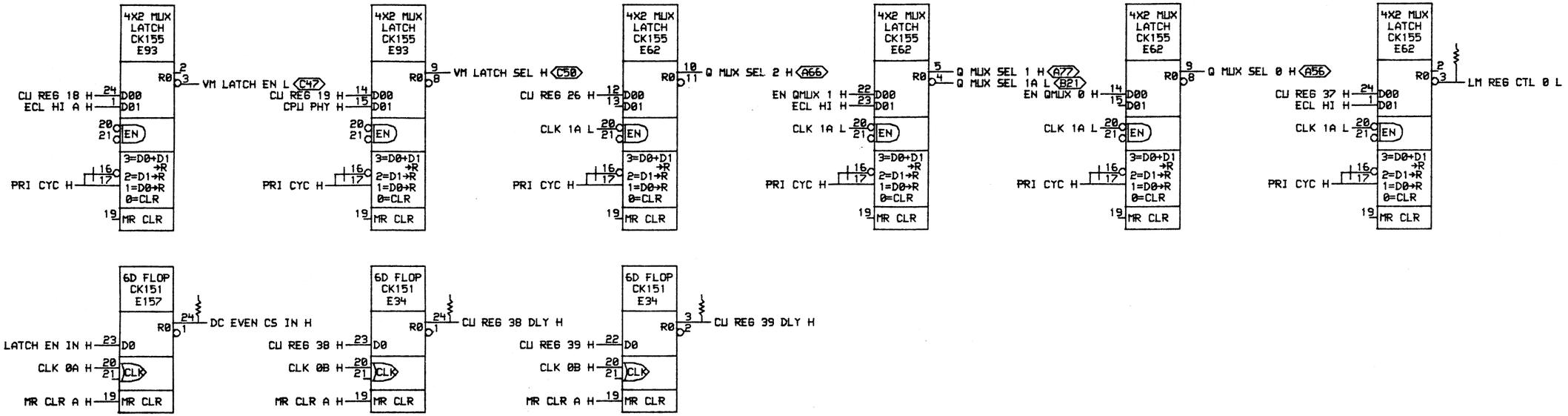


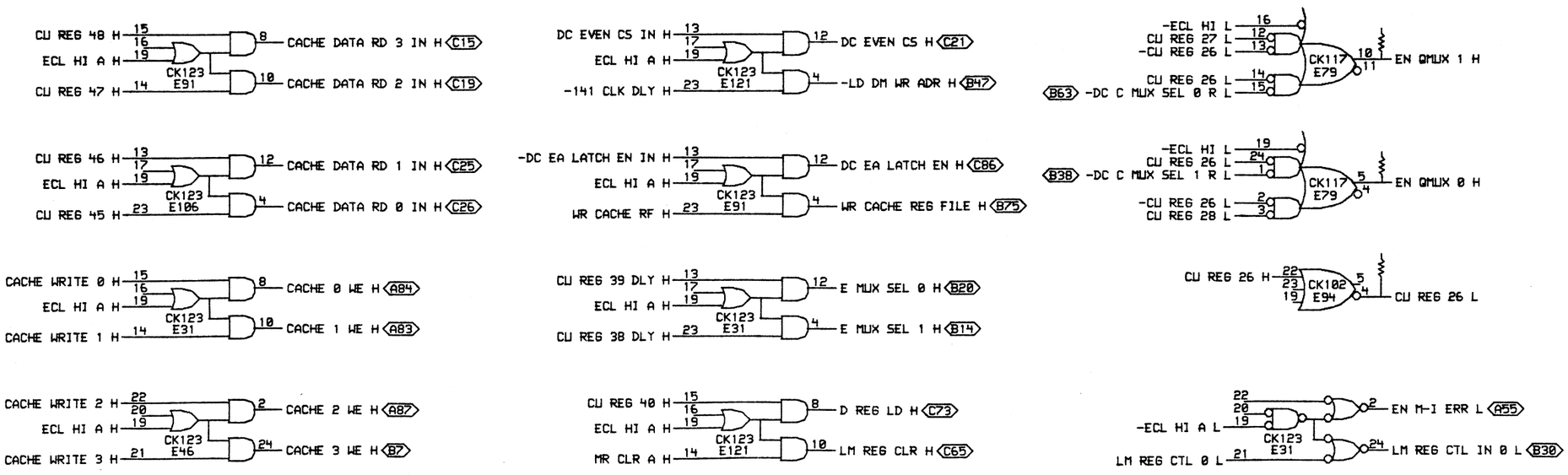
CU REG 75 H
THIS NET HAS A
DISCREAT RESISTOR
ON THE MODULE
AS A TERM





	DRN.	DATE 08-JAN-82	ENG.	DATE	TITLE: CU REG 00-73
	CHK'D.	DATE	BOARD LOCATION:		
			SHEET 5 OF 5		
V18:CAM:<2000.L0058>M0CCF.DRW 06-JAN-82 15:59			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0058-0-MCCF / REV. *
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0058-0		





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REVISIONS		
CHK	CHANGE NO.	REV

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VULCAN:<2000.L0058>54MCCG.DRW | 05-FEB-82 15:36 | NEXT HIGHER ASSEMBLY: B-DD-L0058-0

FIRST USED ON OPTION/MODEL: 2080

DRN. J. Landry | DATE 06-FEB-82 | ENG. | DATE

CHK D. | DATE | BOARD LOCATION: | SHEET 1 OF 1

TITLE: MDP CU CONTROL LOGIC

SIZE	CODE	NUMBER	REV.
D	CS	L0058-0-MCCG	*

UNVSAL PRIO ENCODER CK165 E147

ANY — PRI SEL ANY H
Q0 — PRI SEL 2 H
Q1 — PRI SEL 1 H / PRI SEL 1 L
Q2 — PRI SEL 0 H / PRI SEL 0 L
Q3
GS2

PRI SEL 0 L & PRI SEL 1 L EACH NET HAS A DISCRETE RESISTOR ON THE MODULE AS A TERM

CONSOLE RQ H — D0 HI
D1
CACHE MISS RQ H — D2
CACHE REFILL H — D3
I/O TRP H — D4
SWP TRP H — D5
EBOX RQ H — D6
IBOX RQ H — D7 LOW
CLK 2B L — EN IN
ECL HI H — 8 MODE
EN OUT

2X8 MUX CK163 E116

ZA — INIT ADR 06 H
FRU 0 H — DA0
MISS RD H — DA1
DA2
DA3
C51 I/O ADR 01 H — DA4
DA5
DA6
IQ 2 H — DA7
PRI SEL 2 H — S0
PRI SEL 1 H — S1 SEL
PRI SEL 0 H — S2

2X8 MUX CK163 E116

ZA — INIT ADR 07 H
FRU 1 H — DA0
CACHE PF H — DA1
DA2
I/O ADR 02 H — DA3
PAG SYNC A H — DA4
AB EN H — DA5
IQ 3 H — DA6
DA7
S0
S1 SEL
S2

3X4 MUX CK171 E59

CU NA 00 H — A0
RET A 00 H — B0
PREV A 00 H — C0
CSL Q 00 H — D0
-CU LATCH 08 H — S0 SEL
-CU LATCH 09 H — S1
EN
Z0 — NXT ADR 00 H

3X4 MUX CK171 E60

CU NA 01 H — A0
RET A 01 H — B0
PREV A 01 H — C0
CSL Q 01 H — D0
S0 SEL
S1
EN
Z0 — NXT ADR 01 H

3X4 MUX CK171 E60

CU NA 02 H — A0
RET A 02 H — B0
PREV A 02 H — C0
CSL Q 02 H — D0
S0 SEL
S1
EN
Z0 — NXT ADR 02 H

3X4 MUX CK171 E59

CU NA 03 H — A0
RET A 03 H — B0
PREV A 03 H — C0
CSL Q 03 H — D0
S0 SEL
S1
EN
Z0 — NXT ADR 03 H

3X4 MUX CK171 E59

CU NA 04 H — A0
RET A 04 H — B0
PREV A 04 H — C0
CSL Q 04 H — D0
S0 SEL
S1
EN
Z0 — NXT ADR 04 H

3X4 MUX CK171 E73

CU NA 05 H — A0
RET A 05 H — B0
PREV A 05 H — C0
CSL Q 05 H — D0
S0 SEL
S1
EN
Z0 — NXT ADR 05 H

3X4 MUX CK171 E73

CU NA 06 H — A0
RET A 06 H — B0
PREV A 06 H — C0
CSL Q 06 H — D0
S0 SEL
S1
EN
Z0 — NA ADR 06 H

3X4 MUX CK171 E73

CU NA 07 H — A0
RET A 07 H — B0
PREV A 07 H — C0
CSL Q 07 H — D0
S0 SEL
S1
EN
Z0 — NA ADR 07 H

4X2 MUX LATCH CK155 E55

NXT ADR 00 H — D00
PRI SEL 0 H — D01
CLK 3B L — EN
PRI CYC IN H — 3=D0+D1 →R; 2=D1→R; 1=D0→R; 0=CLR
MR CLR
R0 — CU ADR 00 H / CU ADR 00 L

4X2 MUX LATCH CK155 E58

NXT ADR 01 H — D00
PRI SEL 1 H — D01
CLK 3B L — EN
PRI CYC IN H — 3=D0+D1 →R; 2=D1→R; 1=D0→R; 0=CLR
MR CLR
R0 — CU ADR 01 H / CU ADR 01 L

4X2 MUX LATCH CK155 E25

NXT ADR 02 H — D00
PRI SEL 2 H — D01
CLK 3B L — EN
PRI CYC IN H — 3=D0+D1 →R; 2=D1→R; 1=D0→R; 0=CLR
MR CLR
R0 — CU ADR 02 H / CU ADR 02 L

4X2 MUX LATCH CK155 E40

NXT ADR 03 H — D00
CPU SPC FCN H — D01
CLK 3B L — EN
PRI CYC IN H — 3=D0+D1 →R; 2=D1→R; 1=D0→R; 0=CLR
MR CLR
R0 — CU ADR 03 H / CU ADR 03 L

4X2 MUX LATCH CK155 E40

NXT ADR 04 H — D00
IQ 0 H — D01
CLK 3B L — EN
PRI CYC IN H — 3=D0+D1 →R; 2=D1→R; 1=D0→R; 0=CLR
MR CLR
R0 — CU ADR 04 H / CU ADR 04 L

4X2 MUX LATCH CK155 E58

NXT ADR 05 H — D00
IQ 1 H — D01
CLK 3B L — EN
PRI CYC IN H — 3=D0+D1 →R; 2=D1→R; 1=D0→R; 0=CLR
MR CLR
R0 — CU ADR 05 H / CU ADR 05 L

4X2 MUX LATCH CK155 E25

NXT ADR 06 H — D00
INIT ADR 06 H — D01
CLK 3B L — EN
PRI CYC IN H — 3=D0+D1 →R; 2=D1→R; 1=D0→R; 0=CLR
MR CLR
R0 — CU ADR 06 H / CU ADR 06 L

4X2 MUX LATCH CK155 E55

NXT ADR 07 H — D00
INIT ADR 07 H — D01
CLK 3B L — EN
PRI CYC IN H — 3=D0+D1 →R; 2=D1→R; 1=D0→R; 0=CLR
MR CLR
R0 — CU ADR 07 H / CU ADR 07 L

4X2 MUX LATCH CK155 E55

NXT ADR 00 H — D00
PRI SEL 0 H — D01
CLK 3B L — EN
PRI CYC IN H — 3=D0+D1 →R; 2=D1→R; 1=D0→R; 0=CLR
MR CLR
R0 — CU ADR 00 A H / CU ADR 00 A L

4X2 MUX LATCH CK155 E58

NXT ADR 01 H — D00
PRI SEL 1 H — D01
CLK 3B L — EN
PRI CYC IN H — 3=D0+D1 →R; 2=D1→R; 1=D0→R; 0=CLR
MR CLR
R0 — CU ADR 01 A H / CU ADR 01 A L

4X2 MUX LATCH CK155 E40

NXT ADR 02 H — D00
PRI SEL 2 H — D01
CLK 3B L — EN
PRI CYC IN H — 3=D0+D1 →R; 2=D1→R; 1=D0→R; 0=CLR
MR CLR
R0 — CU ADR 02 A H / CU ADR 02 A L

4X2 MUX LATCH CK155 E25

NXT ADR 03 H — D00
IQ 0 H — D01
CLK 3B L — EN
PRI CYC IN H — 3=D0+D1 →R; 2=D1→R; 1=D0→R; 0=CLR
MR CLR
R0 — CU ADR 03 A H / CU ADR 03 A L

4X2 MUX LATCH CK155 E40

NXT ADR 04 H — D00
IQ 1 H — D01
CLK 3B L — EN
PRI CYC IN H — 3=D0+D1 →R; 2=D1→R; 1=D0→R; 0=CLR
MR CLR
R0 — CU ADR 04 A H / CU ADR 04 A L

4X2 MUX LATCH CK155 E58

NXT ADR 05 H — D00
INIT ADR 06 H — D01
CLK 3B L — EN
PRI CYC IN H — 3=D0+D1 →R; 2=D1→R; 1=D0→R; 0=CLR
MR CLR
R0 — CU ADR 05 A H / CU ADR 05 A L

4X2 MUX LATCH CK155 E25

NXT ADR 06 H — D00
INIT ADR 07 H — D01
CLK 3B L — EN
PRI CYC IN H — 3=D0+D1 →R; 2=D1→R; 1=D0→R; 0=CLR
MR CLR
R0 — CU ADR 06 A H / CU ADR 06 A L

4X2 MUX LATCH CK155 E55

NXT ADR 07 H — D00
INIT ADR 07 H — D01
CLK 3B L — EN
PRI CYC IN H — 3=D0+D1 →R; 2=D1→R; 1=D0→R; 0=CLR
MR CLR
R0 — CU ADR 07 A H / CU ADR 07 A L

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CHK	CHANGE NO.	REV

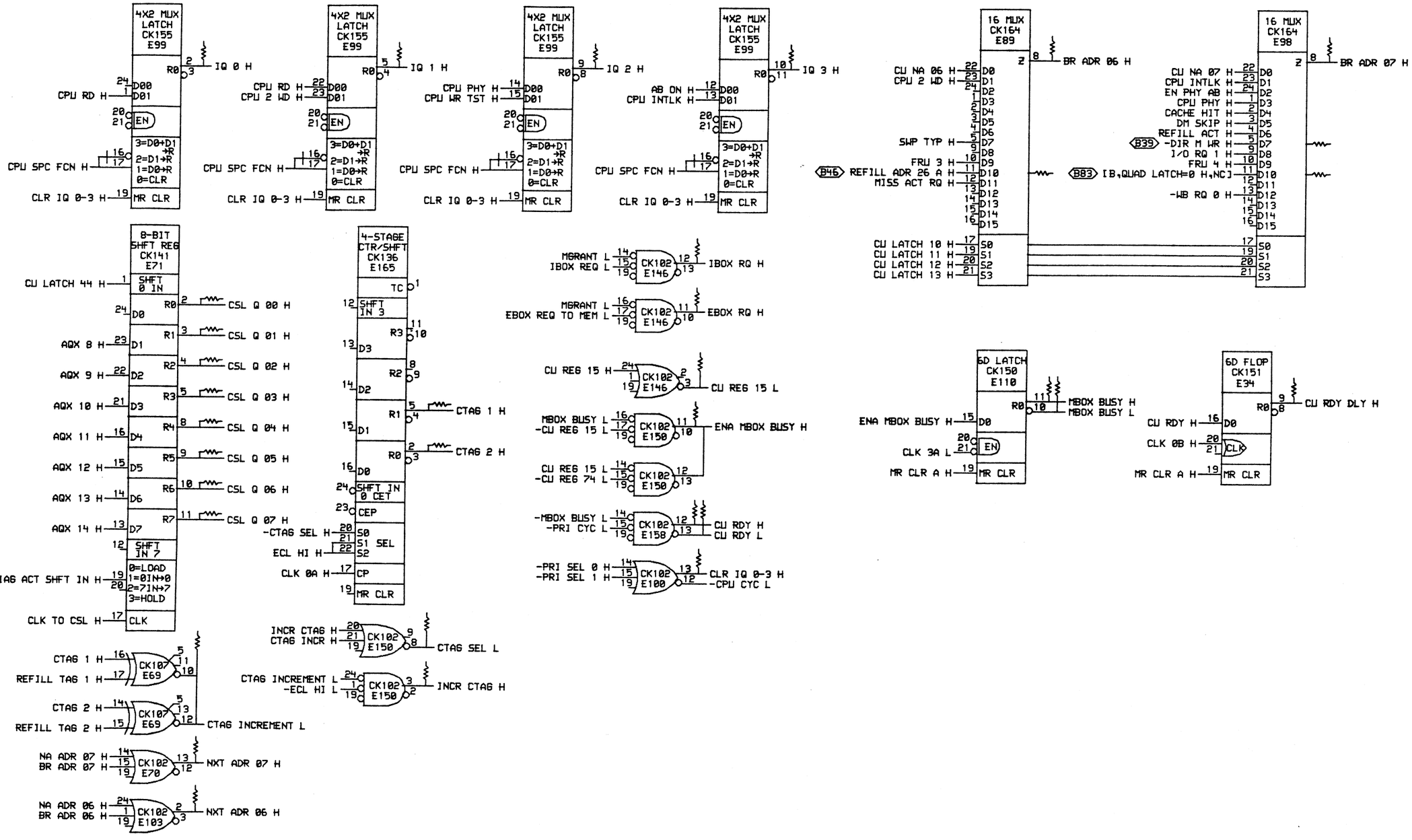
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DRA'D. Toomey — DATE 04-JAN-82 — ENG. — DATE — TITLE:
CHK'D. — DATE — BOARD LOCATION: — SHEET 1 OF 1

VULCAN:<2080.L0058>38MCCH.DRW | 04-JAN-82 14:26 | NEXT HIGHER ASSEMBLY:
FIRST USED ON OPTION/MODEL: 2080 | B-DD-L0058-0

SIZE	CODE	NUMBER	REV.
D	CS	L0058-0-MCCH	*

REV. *
NUMBER L0058-0-MCCH
SIZE D CODE CS



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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. S. Toomey | DATE 05-FEB-82 | ENG. | DATE | TITLE:
CHK'D. | DATE | BOARD LOCATION: | SHEET 1 OF 1

VULCAN!<2080.L0058>55MCCI.DRW | 05-FEB-82 17:56 | NEXT HIGHER ASSEMBLY: B-DD-L0058-0

FIRST USED ON OPTION/MODEL: 2080

SIZE	CODE	NUMBER	REV.
D	CS	L0058-0-MCCI	*

6D LATCH CK150 E108
ENA PHYSICAL H
MGRANT L
CLK 2A L
MR CLR A H
CPU PHY H
CPU PHY L
C10 READ H
CPU RD H
CPU RD L
C22 2 WORD H
CPU 2 WD H
CPU 2 WD L
C24 WRITE TEST H
CPU WR TST H
C27 INTERLOCK H
CPU INTLK H
B15 SPFCN 0 H

6D FLOP CK151 E86
CPU SPC FCN H
EN RFL ACT H
CLK 1A H
CACHE MISS H
MISS RQ CLR H
REFILL ACT H
REFILL ACT L
CPU RD H
MISS RD H

6D LATCH CK150 E110
B41 ABORT ALL H
CLK 3A L
MR CLR A H
CPU ABRT ALL H
C31 ABORT CYCLE H
CPU ABRT CYC H
PRI CYC IN H
PRI CYC H
PRI CYC L
EN PHYSICAL DLY H
CPU PHY WR H
CTAG 1 H
CTAG 2 H
REFILL TAG 1 H
REFILL TAG 2 H
PRI SEL ANY H
MISS ACT RQ H
B78 PF RESP H
CACHE PF H

3D FLOP CK131 E90
ECL HI A H
CLK 3A H
CLK 1B H
MR CLR B H
AQ WR EN IN H
AQ WR EN IN L
B12 MBOX DATA 10 H
CLR SWP ACT H
SWP ACT H
SWP TYPE L
RQ CACHE MISS H
CLR MISS RQ H
CACHE MISS RQ H
ENA REFILL MISS H
CLR REFILL H
REFILL MISS H
EN CHE RFL H
CACHE REFILL H
SET SWP ACT H
SWP ACT L
SET MBOX INTLK H
CLR MBOX INTLK H
MBOX INTLK H
MBOX INTLK L

CK102 E96
-PMA MATCH H
PF RESP H
RQ CACHE MISS H

CK102 E100
REFILL ACT L
-CACHE HIT L
ENA REFILL MISS H

CK117 E139
INTERLOCK H
EN INTLK BLK H
READ H
INTLK BLK H

CK102 E111
PRI CYC L
ENA PHYSICAL L
EN PHYSICAL DLY H

CK102 E138
MBOX INTLK L
-SPFCN 0 L
EN INTLK BLK H

CK102 E103
I/O RQ L
-REFILL ACT L
I/O TRP H

CK117 E159
-ECL HI L
C80 IB,REFILL ADR 26 A L,NCJ
PAG SYNC A L
SWP ACT L
-WB RQ 0 L
SWP TRP H

CK102 E150
SWP ACT L
SWP TYPE L
SWP TYP H

CK102 E83
CPU RD L
B68 -CACHE HIT L
EN RFL ACT H

CK102 E94
C28 PHYSICAL H
A73 FORCE PHY H
ENA PHYSICAL H
ENA PHYSICAL L

CK102 E70
AQ WR EN IN H
BUF WE L

CK102 E146
REFILL ACT L
DM M2C L
EN CHE RFL H

CK102 E100
REFILL ACT H
CACHE PF H
CACHE MISS H

UNVSAL DEMUX/ DECODE CK170 E148
0F CLR MISS RQ H
1F CLR REFILL H
2F CLR MBOX INTLK H
3F SET MBOX INTLK H
4F CTAG INCR H
5F SET SWP ACT H
6F CLR SWP ACT H
7F ADR COMP EN H
CU REG 66 H
CU REG 65 H
CU REG 64 H
A0
A1
A2
ADR
CLK 3A L
-CU REG 71 L
ENA
B0
B1
ADR
CLK 3A L
-CU REG 71 L
ENB
OUTPUT
ECL HI H
TURE HIGH
ECL HI H
M CNTRL

CK118 E153
ENA MGRANT L
CU RDY L
-INTLK BLK L
C67 IBOX REQ L
GRANT EN L
C60 EBOX REQ TO MEM L
-ECL HI A L
-ECL HI A L
-ECL HI A L
MGRANT L C87

CK101 E140
-CACHE MISS RQ L
-PAG SYNC A L
GRANT EN L

CK101 E101
-CACHE REFILL L
-I/O RQ L
-ERROR TRAP L
-CONSOLE RQ L
ENA MGRANT L

CK117 E139
PRI SEL ANY H
-MCC U LD H
CU RDY DLY H
PRI CYC IN H

CK102 E96
-CPU RD H
CPU WR TST H
WRITE CYCLE H B4

CK102 E103
MR CLR B H
CLR REFILL H
MISS RQ CLR H

CK102 E158
C52 PAG SYNC A H
PAG SYNC A L

CK102 E158
C57 I/O ADR 02 H
I/O ADR 02 IN L

CK102 E128
AB ON L
PHY AB L
EN PHY AB H

CK102 E158
MBOX INTLK L
I/O ADR 02 IN L
I/O INTLK BUSY H

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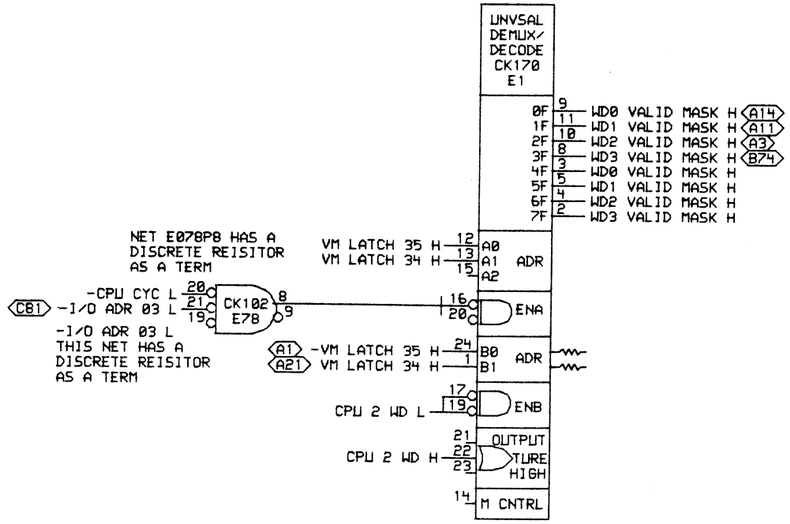
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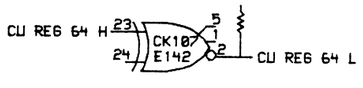
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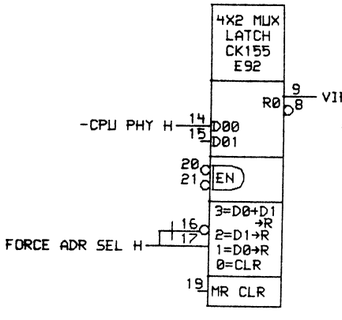
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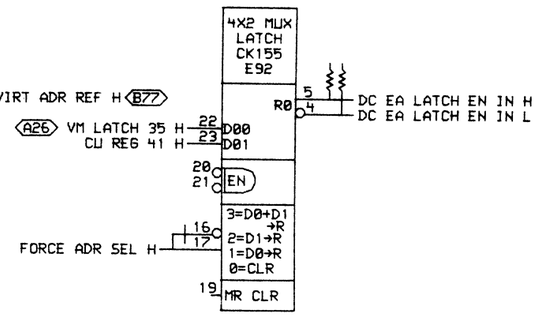
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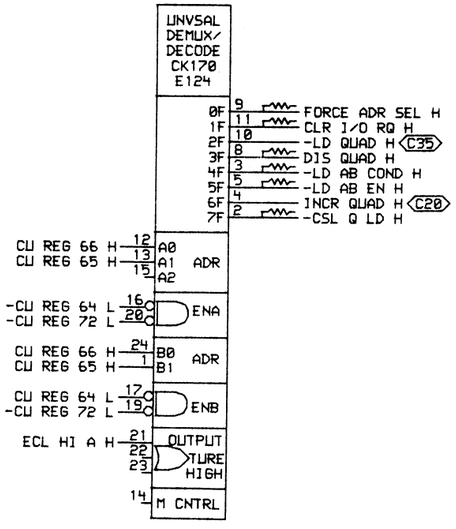
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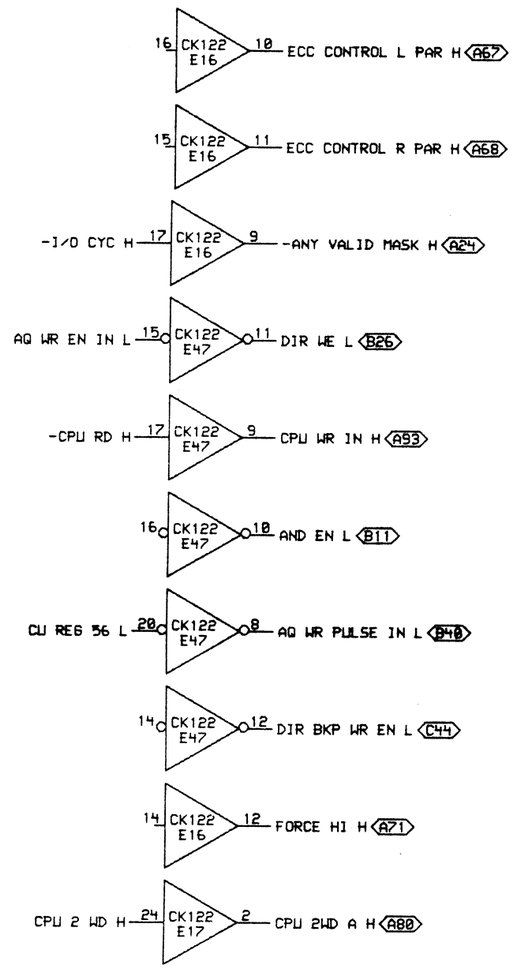












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REVISIONS		
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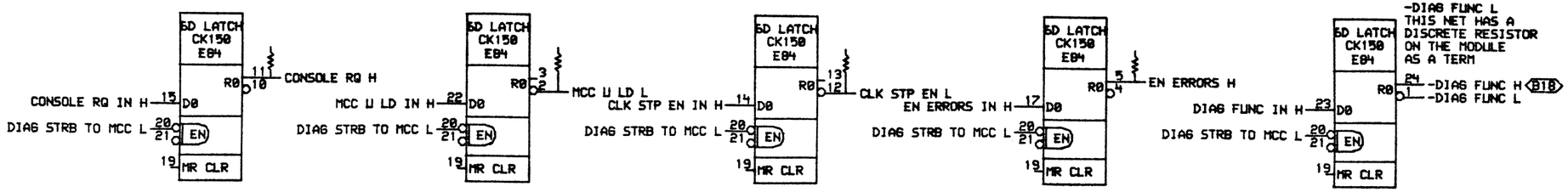
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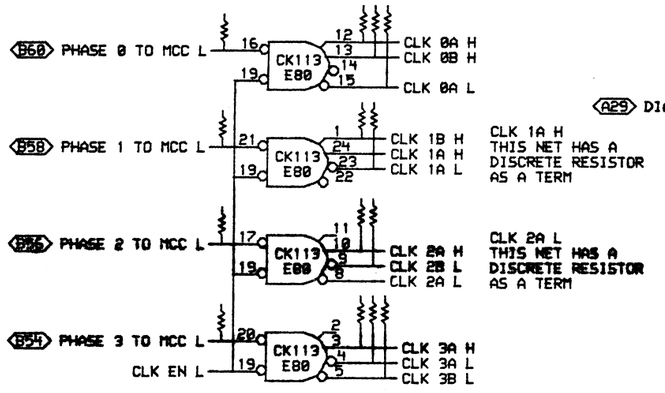
CHK D. | DATE | BOARD LOCATION: | SHEET 1 OF 1

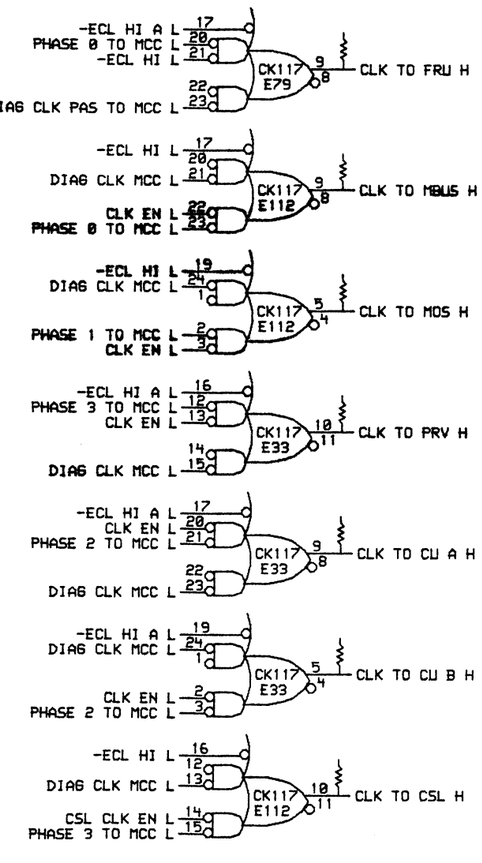
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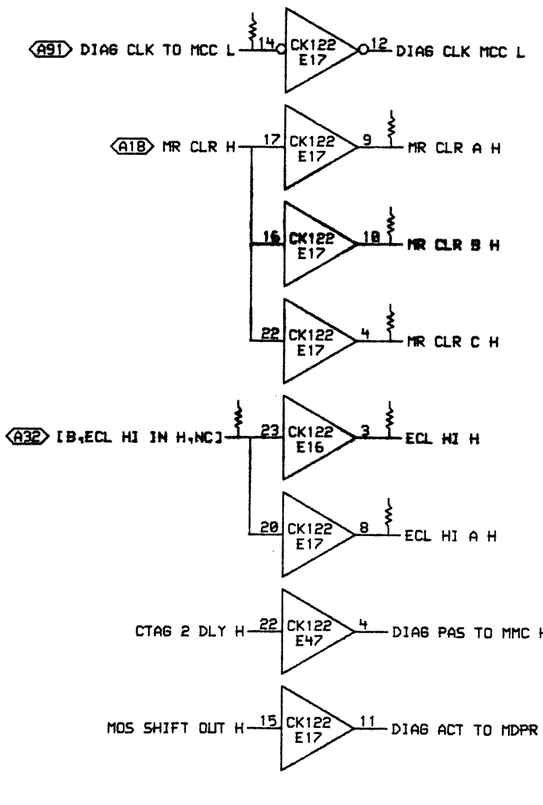
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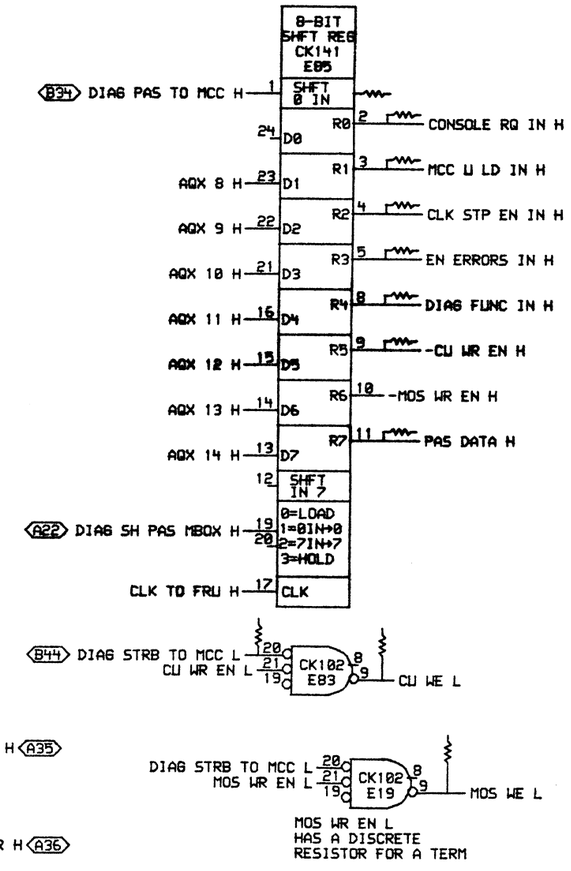
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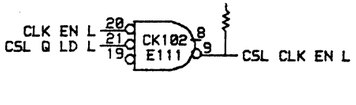












A10 [B,DIAG CLK MCC L,DIAG CLK TO MDPL L]

A12 [B,DIAG CLK PAS TO MCC L,DIAG CLK PAS TO MAP L]

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REVISIONS		
CHK	CHANGE NO.	REV

digital

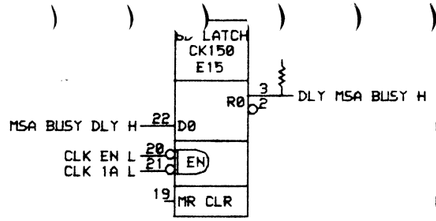
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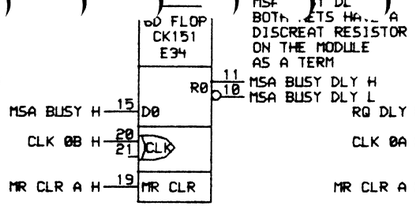
CHK D. | DATE | BOARD LOCATION: | SHEET 1 OF 1

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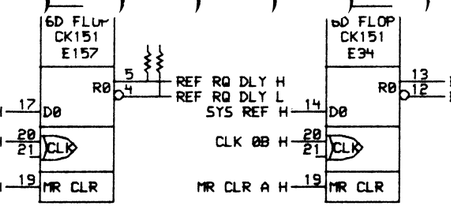
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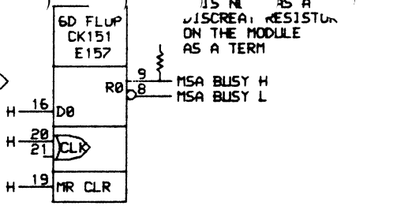




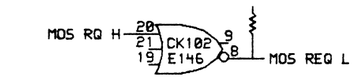
MSA BUSY DLY H & MSA BUSY DLY L BOTH NETS HAVE A DISCREAT RESISTOR ON THE MODULE AS A TERM

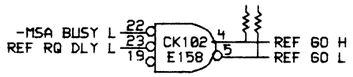


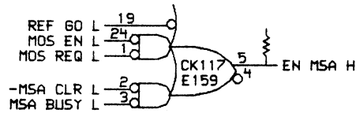
ECL SYS REF L HAS A DISCRETE RESISTOR

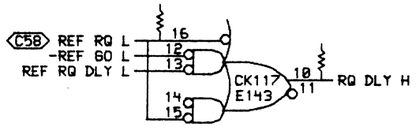


A BUF IS NE AS A DISCREAT RESISTOR ON THE MODULE AS A TERM

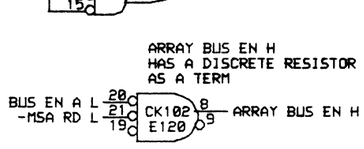


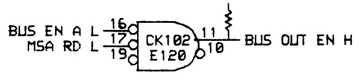


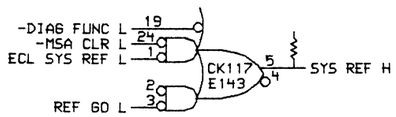


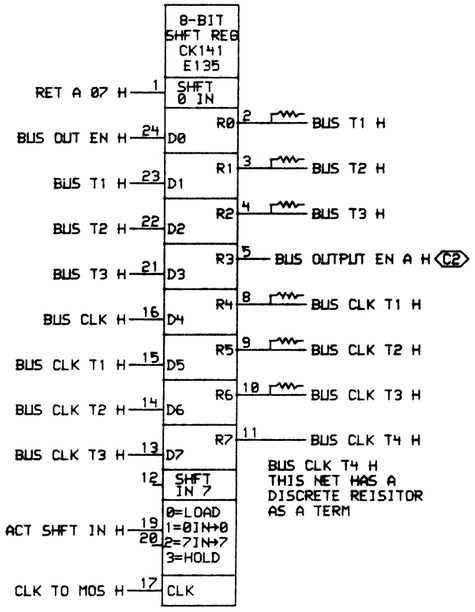


ARRAY BUS EN H HAS A DISCRETE RESISTOR AS A TERM

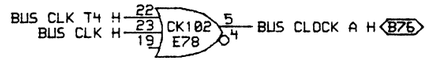


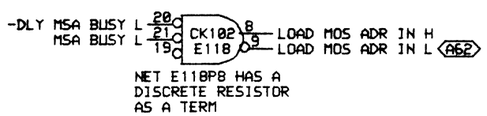




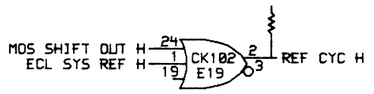


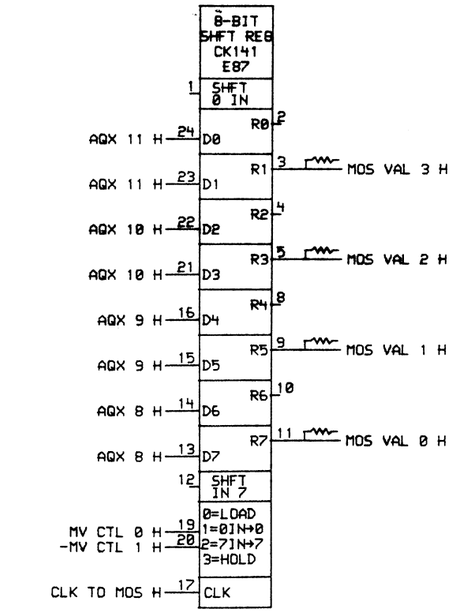
BUS CLK T4 H THIS NET HAS A DISCRETE REISITOR AS A TERM

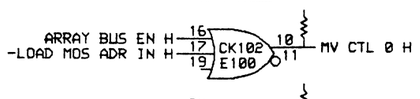


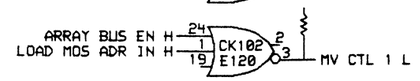


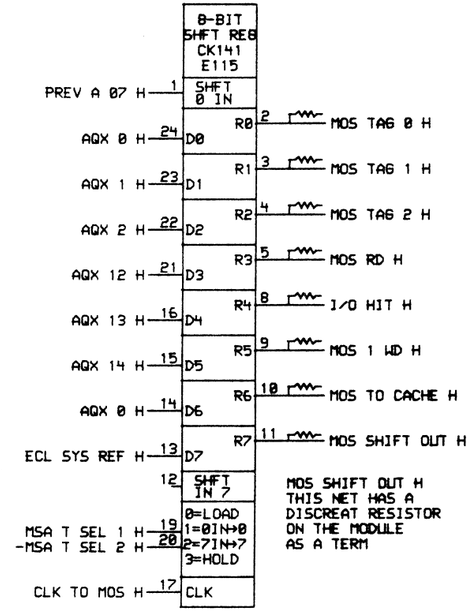
NET E118P8 HAS A DISCRETE RESISTOR AS A TERM



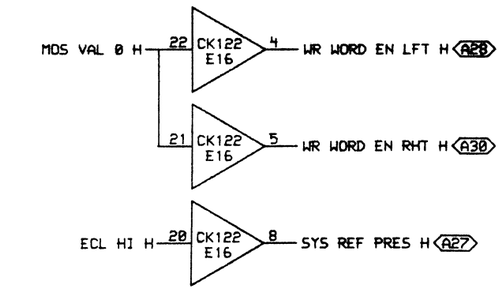


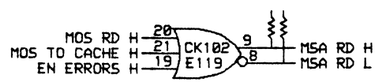






MOS SHIFT OUT H THIS NET HAS A DISCREAT RESISTOR ON THE MODULE AS A TERM





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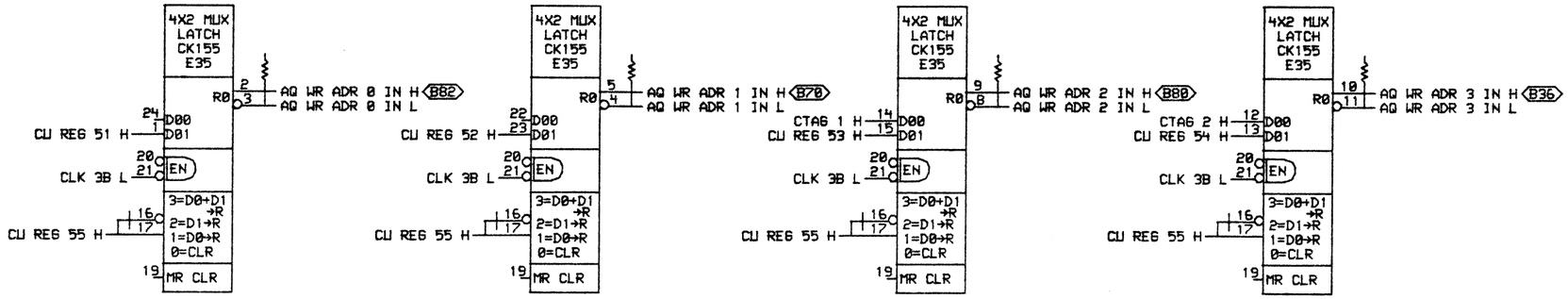
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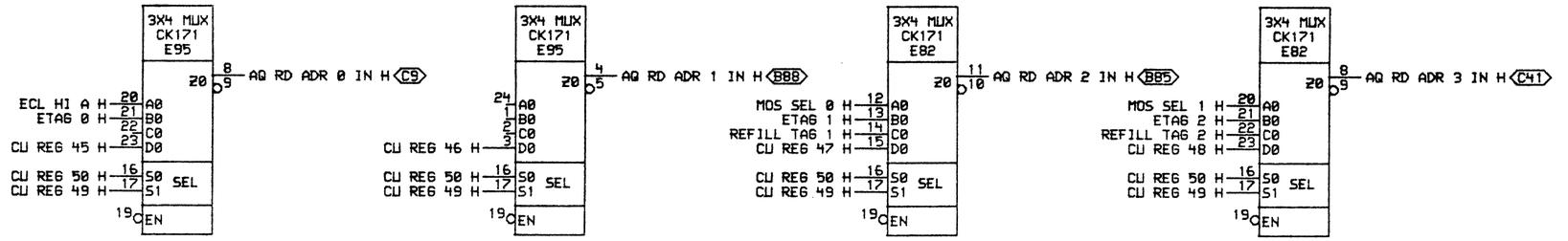
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DATE 25-FEB-82
ENG.
DATE
DATE
BOARD LOCATION:
SHEET 1 OF 1

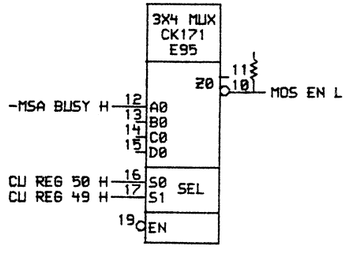
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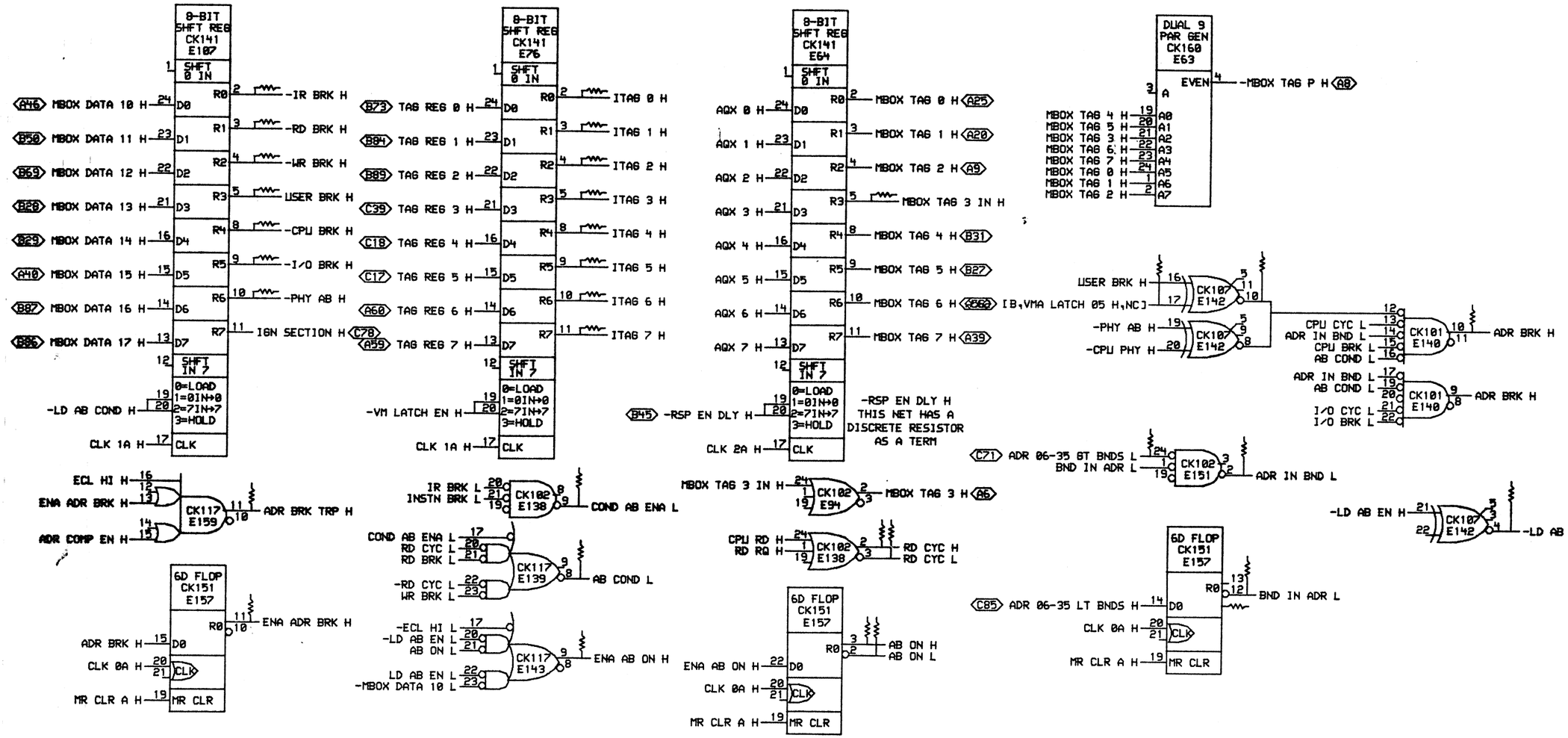
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FIRST USED ON OPTION/MODEL: 2080			B-DD-L0058-0		D	CS	L0058-0-MCCN	*



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REVISIONS		
CHK	CHANGE NO.	REV

digital

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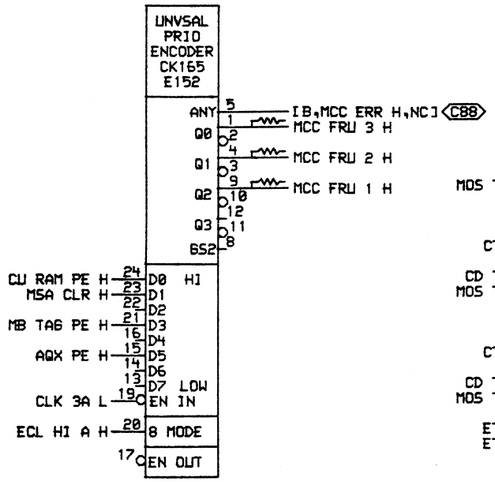
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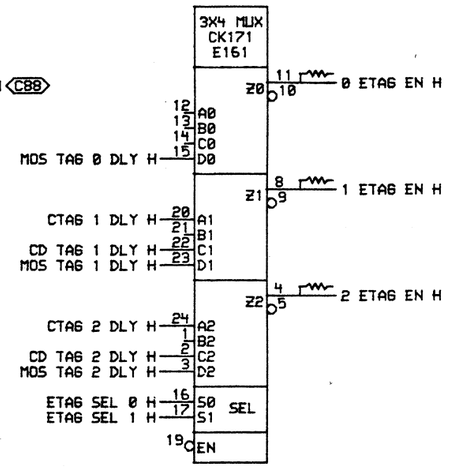
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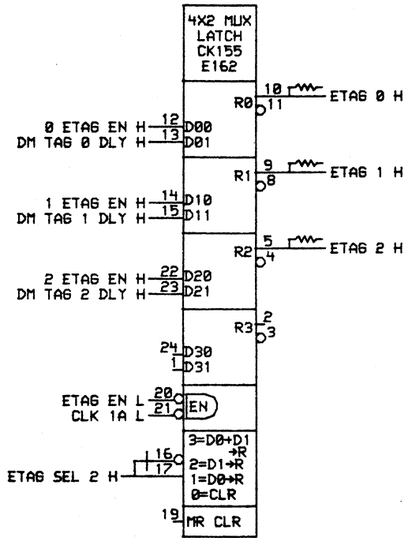
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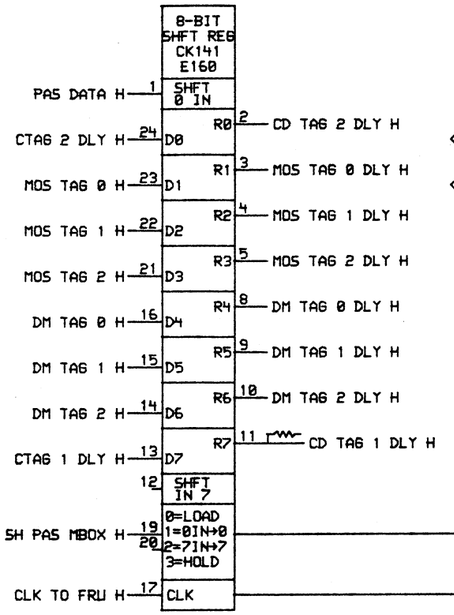
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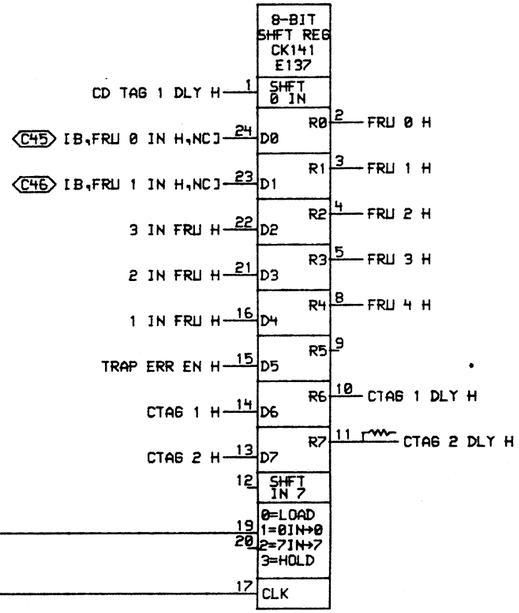
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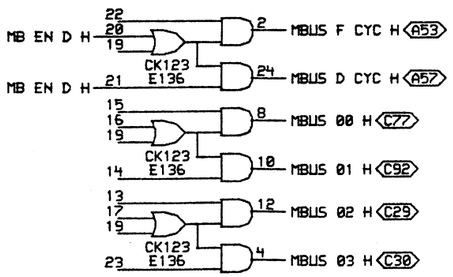


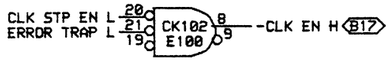


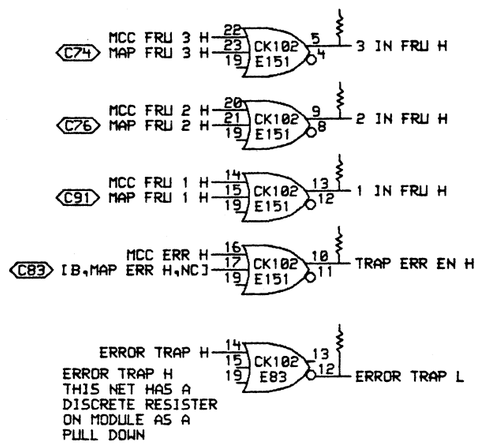












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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. S.Toomey | DATE 05-FEB-82 | ENG. | DATE

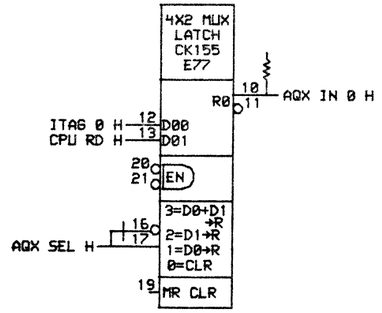
CHK D. | DATE | BOARD LOCATION: | SHEET 1 OF 1

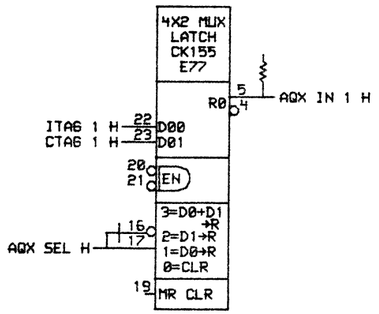
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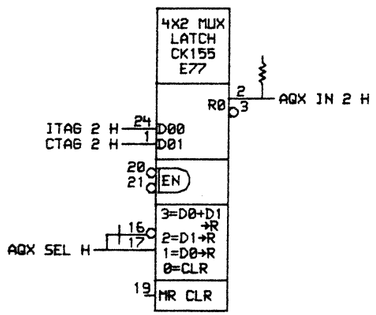
FIRST USED ON OPTION/MODEL: 2080 | B-DD-L0058-0

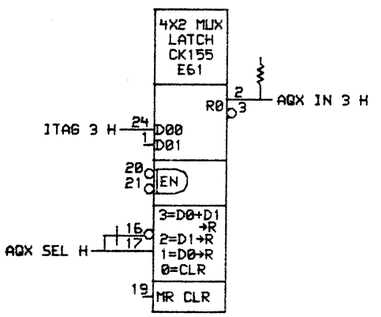
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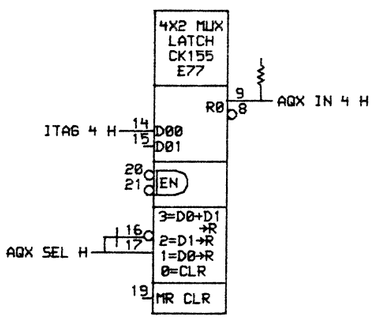
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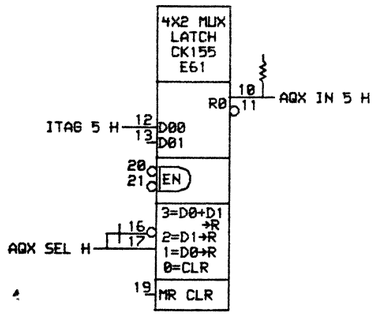


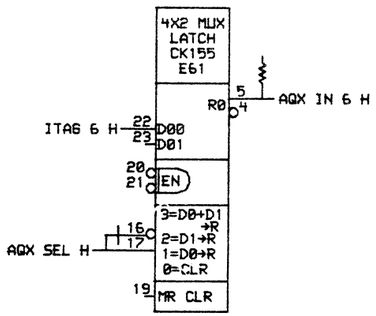


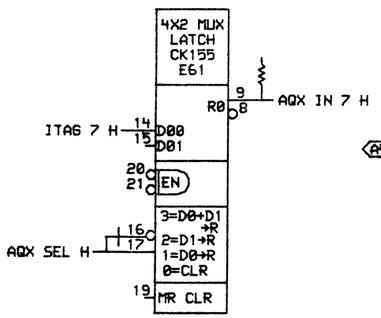


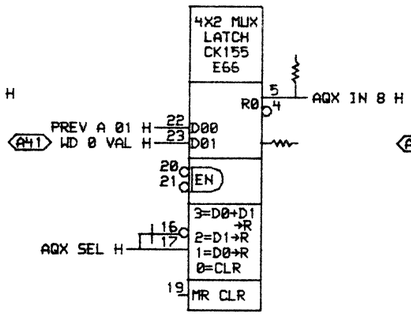


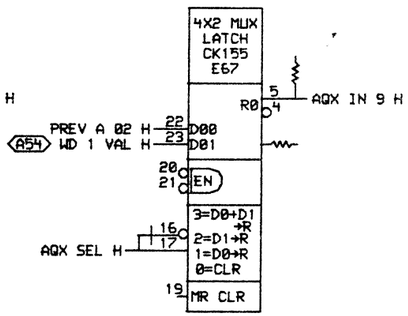


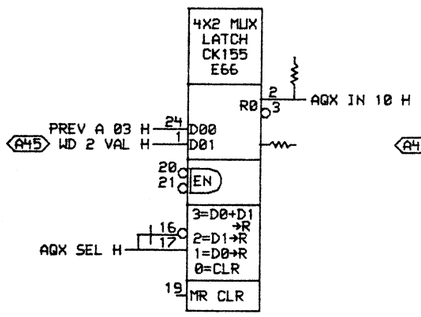


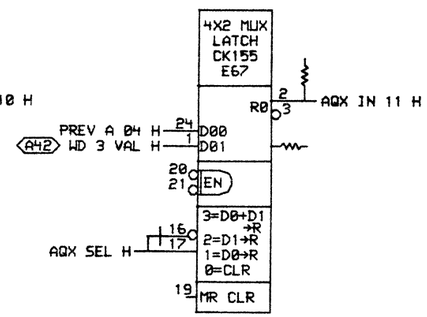


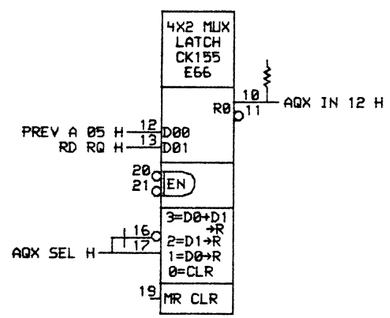


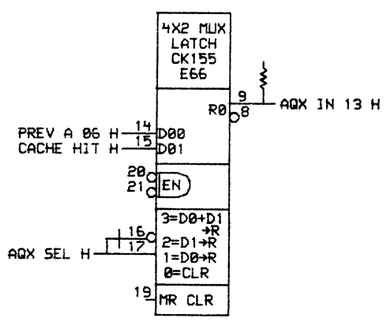


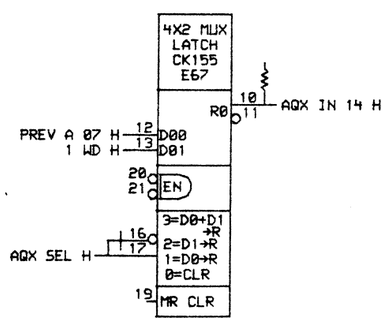


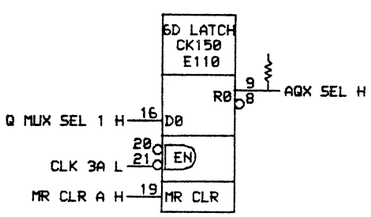


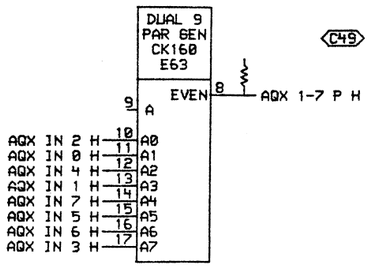


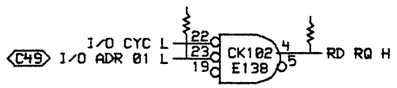


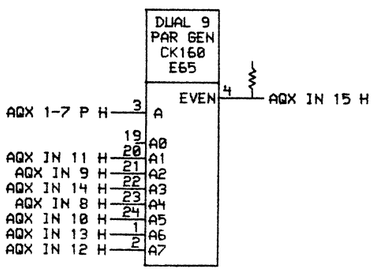












	DRN.	DATE 08-JAN-82	ENG.	DATE	TITLE:
	CHK'D.	DATE	BOARD LOCATION:		
			SHEET 1 OF 1		
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FIRST USED ON OPTION/MODEL: 2080		B-DD-L0058-0			

| | | |

VULCAN:<2080.L0061>25MMC1
VULCAN:<2080.L0061>25MMC2
VULCAN:<2080.L0061>25MMC3
VULCAN:<2080.L0061>25MMC4
VULCAN:<2080.L0061>25MMC5
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VULCAN:<2080.L0061>59MMC9
VULCAN:<2080.L0061>52MMCA
VULCAN:<2080.L0061>58MMCB
VULCAN:<2080.L0061>58MMCC
VULCAN:<2080.L0061>25MMCD
VULCAN:<2080.L0061>25MMCE
VULCAN:<2080.L0061>39MMCF
VULCAN:<2080.L0061>51MMCG
VULCAN:<2080.L0061>49MMCH
VULCAN:<2080.L0061>25MMCI
VULCAN:<2080.L0061>35MMCJ
VULCAN:<2080.L0061>48MMCK
VULCAN:<2080.L0061>50MMCL
VULCAN:<2080.L0061>56MMCM
VULCAN:<2080.L0061>60MMCN
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VULCAN:<2080.L0061>58MMCR
VULCAN:<2080.L0061>61MMCS

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REVISIONS

CHK	CHANGE NO.	REV

digital

DR'N. Toomky DATE 10-FEB-83 ENG. DATE TITLE:
CHK'D. DATE BOARD LOCATION:
SHEET OF
ARCH:<UHLER.PLOTS>LMMC.DPL.1 10-FEB-83 03:05 NEXT HIGHER ASSEMBLY:
FIRST USED ON OPTION/MODEL:

SIZE	CODE	NUMBER	REV.
D	SP		

E141	E128	E115	E102	E090	E077	E064	E051	E039	E026	E013
E140	E127	E114	E101	E089	E076	E063	E050	E038	E025	E012
E139	E126	E113	E100	E088	E075	E062	E049	E037	E024	E011
E138	E125	E112	E099	E087	E074	E061	E048	E036	E023	E010
E137	E124	E111	E098	E086	E073	E060	E047	E035	E022	E009
E136	E123	E110	E097	E085	E072	E059	E046	E034	E021	E008
E135	E122	E109	E096	E084	E071	E058	E045	E033	E020	E007
E134	E121	E108	E095	E083	E070	E057	E044	E032	E019	E006
E133	E120	E107	E094	E082	E069	E056	E043	E031	E018	E005
E132	E119	E106	E093	E081	E068	E055	E042	E030	E017	E004
E131	E118	E105	E092	E080	E067	E054	E041	E029	E016	E003
+ C095 + + C094 + + C093 + + C092 + + C091 + + C090 +										
E130	E117	E104	E091	E079	E066	E053	E040	E028	E015	E002
E129	E116	E103		E078	E065	E052		E027	E014	E001

"141"

E141 CK474	E128 CK474	E115 CK474	E102 CK474	E090 CK474	E077 CK150	E064 CK141	E051 CK422	E039 CK422	E026 CK155	E013 CK131
E140 CK474	E127 CK474	E114 CK474	E101 CK474	E089 CK474	E076 CK150	E063 CK422	E050 CK422	E038 CK422	E025 CK163	E012 CK155
C075 Z140 Z139	C069 Z127 Z126	C062 Z114 Z113	C055 Z101 Z100	C048 Z089 Z088	C041 Z076 Z075	C034 Z063 Z062	C027 Z050 Z049	C020 Z038 Z037	C013 Z025 Z024	C006 Z012 Z011
E139 CK171	E126 CK171	E113 CK171	E100 CK166	E088 CK136	E075 CK141	E062 CK422	E049 CK422	E037 CK422	E024 CK155	E011 CK155
E138 CK171	E125 CK171	E112 CK166	E099 CK155	E087 CK155	E074 CK136	E061 CK141	E048 CK422	E036 CK422	E023 CK163	E010 CK141
C075 Z138 Z137	C068 Z125 Z124	C061 Z112 Z111	C054 Z099 Z098	C047 Z087 Z086	C040 Z074 Z073	C033 Z061 Z060	C026 Z048 Z047	C019 Z036 Z035	C012 Z023 Z022	C005 Z010 Z009
E137 CK171	E124 CK171	E111 CK160	E098 CK166	E086 CK117	E073 CK122	E060 CK155	E047 CK155	E035 CK155	E022 CK170	E009 CK102
E136 CK160	E123 CK102	E110 CK117	E097 CK160	E085 CK117	E072 CK102	E059 CK171	E046 CK113	E034 CK166	E021 CK170	E008 CK117
C074 Z136 Z135	C067 Z123 Z122	C060 Z110 Z109	C053 Z097 Z096	C045 Z085 Z084	C039 Z072 Z071	C032 Z059 Z058	C025 Z046 Z045	C018 Z034 Z033	C011 Z021 Z020	C004 Z008 Z007
E135 CK141	E122 CK141	E109 CK145	E096 CK145	E084 CK145	E071 CK150	E058 CK171	E045 CK136	E033 CK160	E020 CK102	E007 CK117
E134 CK145	E121 CK145	E108 CK155	E095 CK155	E083 CK155	E070 CK150	E057 CK171	E044 CK145	E032 CK136	E019 CK136	E006 CK150
C073 Z134 Z133	C066 Z121 Z120	C059 Z108 Z107	C052 Z095 Z094	C044 Z083 Z082	C038 Z070 Z069	C031 Z057 Z056	C024 Z044 Z043	C017 Z032 Z031	C010 Z019 Z018	C003 Z006 Z005
E133 CK171	E120 CK155	E107 CK141	E094 CK170	E082 CK141	E069 CK150	E056 CK160	E043 CK155	E031 CK145	E018 CK145	E005 CK155
E132 CK141	E119 CK155	E106 CK155	E093 CK155	E081 CK155	E068 CK122	E055 CK141	E042 CK141	E030 CK155	E017 CK155	E004 CK160
C072 Z132 Z131	C065 Z119 Z118	C058 Z106 Z105	C051 Z093 Z092	C043 Z081 Z080	C037 Z068 Z067	C030 Z055 Z054	C023 Z042 Z041	C016 Z030 Z029	C009 Z017 Z016	C002 Z004 Z003
E131 CK160	E118 CK160	E105 CK160	E092 CK107	E080 CK141	E067 CK160	E054 CK150	E041 CK141	E029 CK150	E016 CK160	E003 CK155
+ C095 1500µF	+ + C094 2200µF	+ + C093 2200µF	+ + C092 2200µF	+ + C091 2200µF	+ + C090 1500µF	+				
E130 CK155	E117 CK155	E104 CK141	E091 CK165	E079 CK107	E066 CK163	E053 CK123	E040 CK160	E028 CK123	E015 CK141	E002 CK155
C071 Z130 Z129	C064 Z117 Z116	C057 Z104 Z103	C050 Z091	C042 Z079 Z078	C036 Z066 Z065	C029 Z053 Z052	C022 Z040	C015 Z028 Z027	C008 Z015 Z014	C001 Z002 Z001
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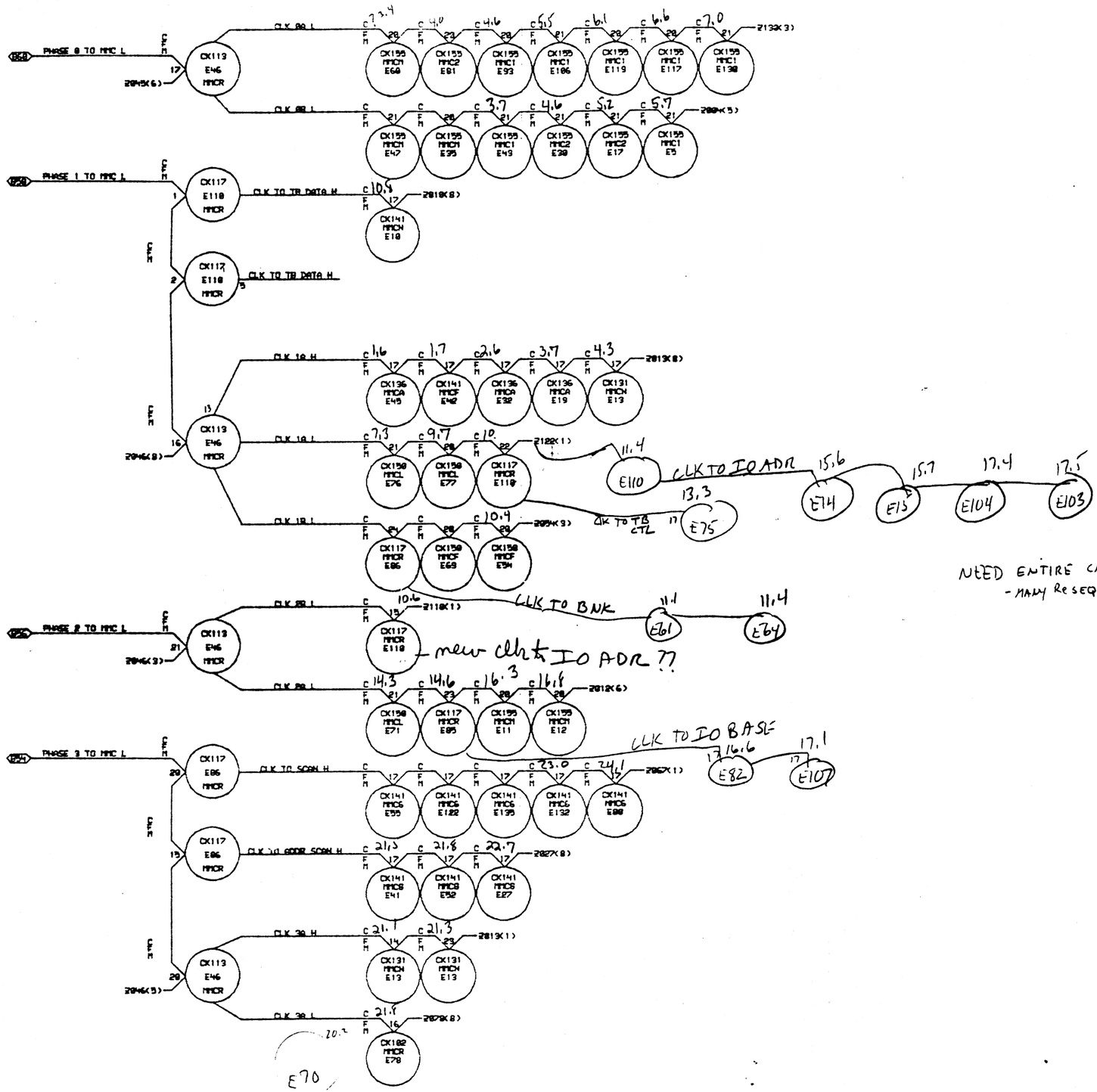
D

C

B

A

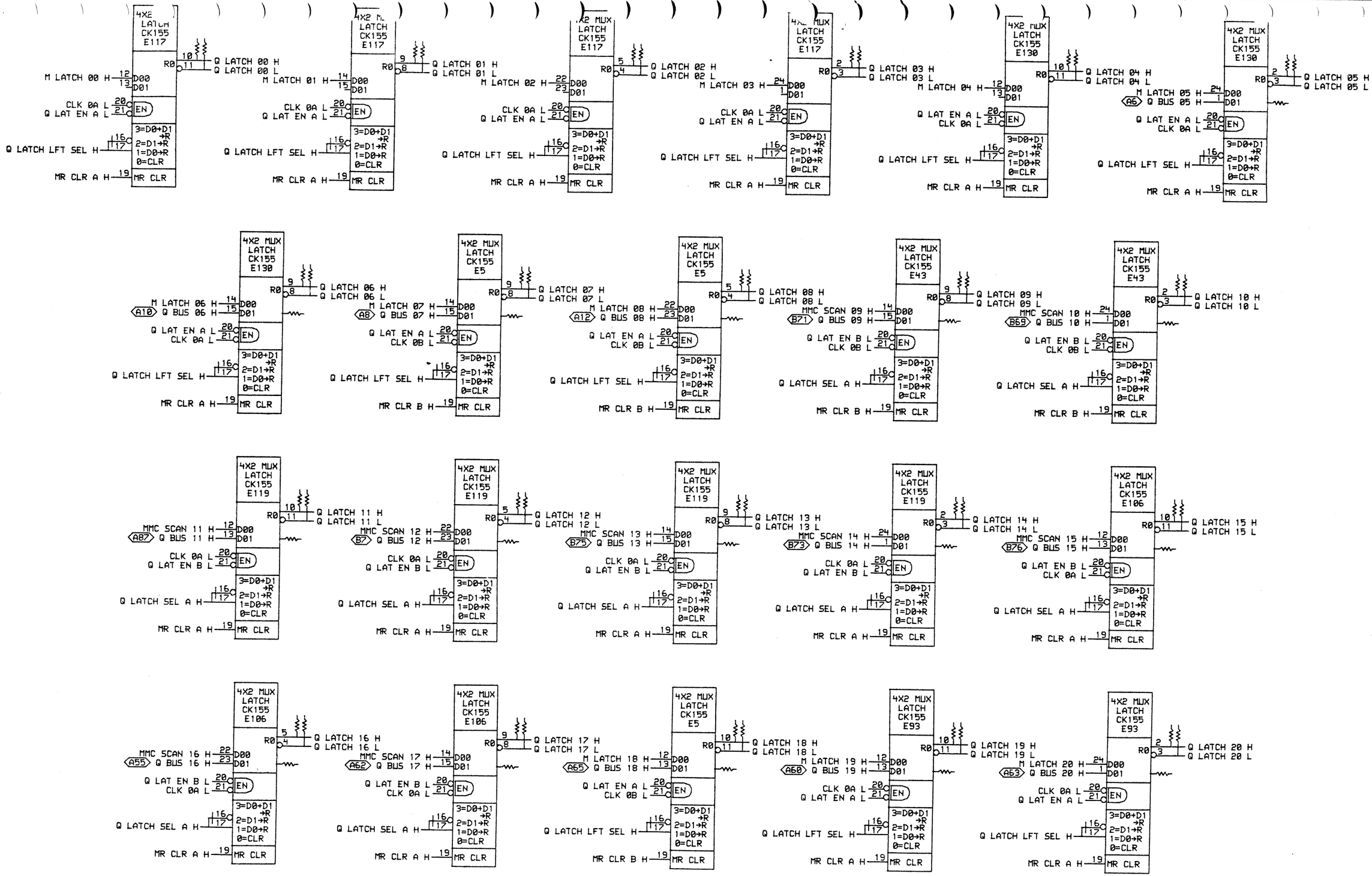
7mm



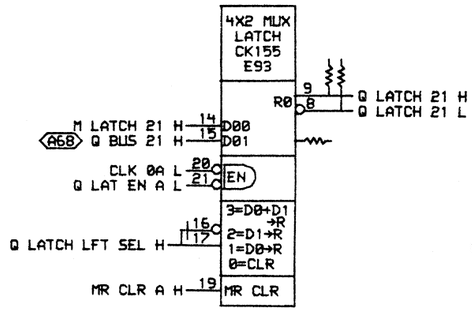
NEED ENTIRE CALCULATION
- MANY RESEQUENCED NETS

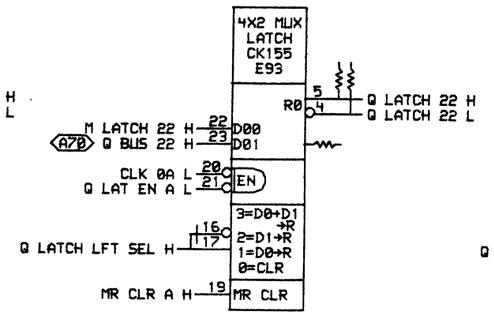
MMC

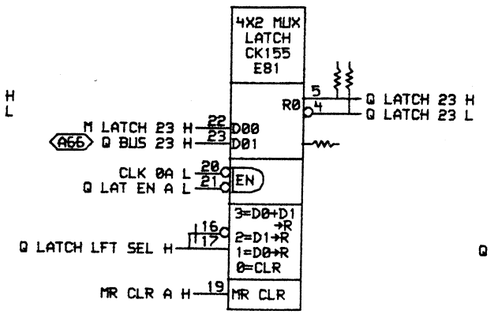
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	CHK'D.	DATE	BOARD LOCATION:		
XTRA:<ROCK.BLOCKS>2MMC-TC1.DAT	17-JUL-82 23:00		SHEET OF NEXT HIGHER ASSEMBLY:	SIZE D	CODE CS
FIRST USED ON OPTION/MODEL:					NUMBER REV.

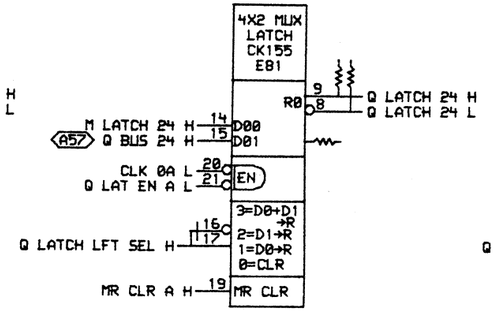


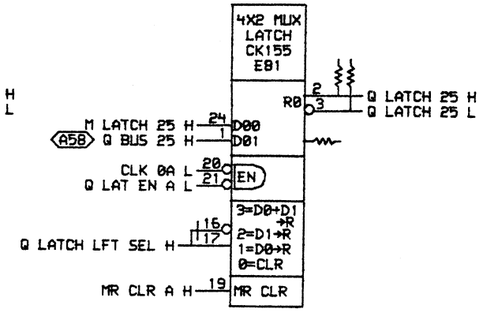
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	CHK'D.	DATE	BOARD LOCATION:		
			SHEET 1 OF 3		
VLILCAN:<2080,L0061>25MMC1.DRW	02-FEB-82 07:25	NEXT HIGHER ASSEMBLY:		SIZE D	CODE CS NUMBER L0061-0-MMC1 REV. *
FIRST USED ON OPTION/MODEL:	2080	B-DD-L0061-0			

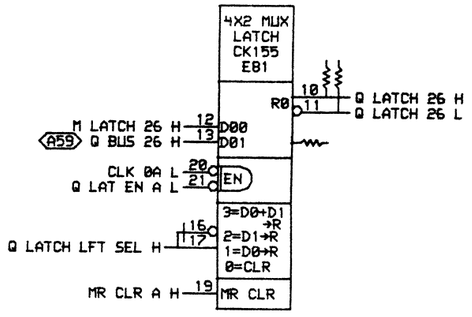


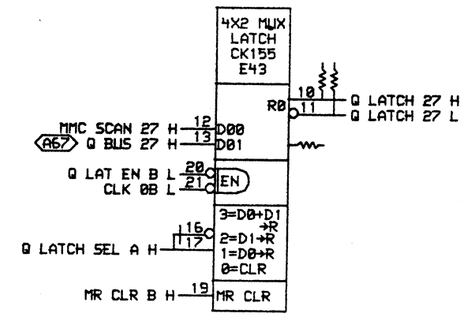


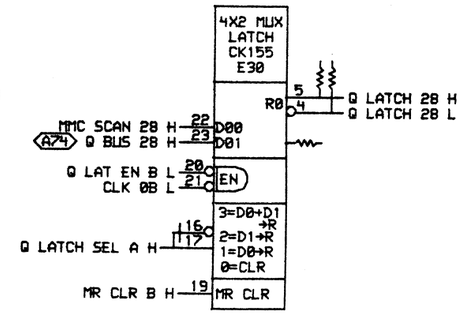


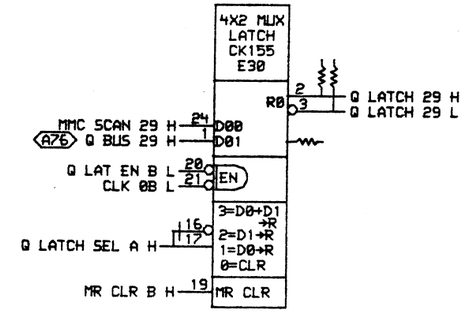


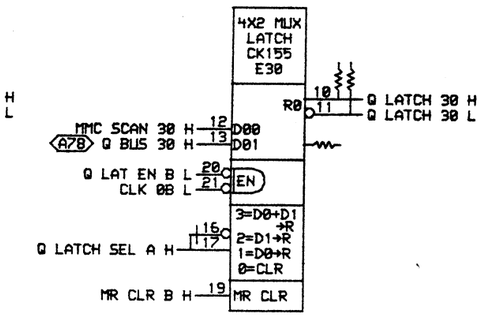


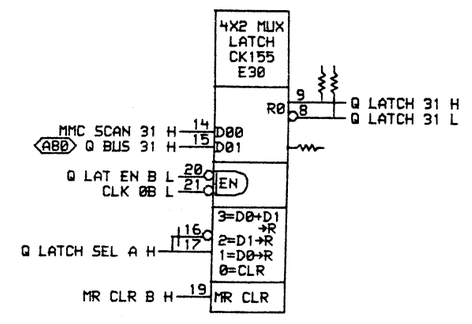


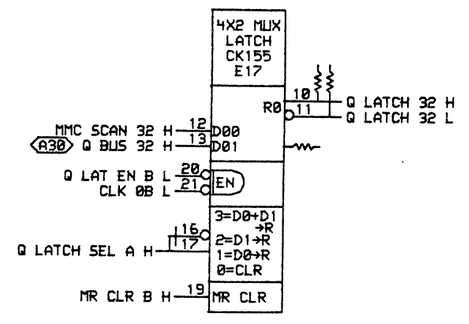


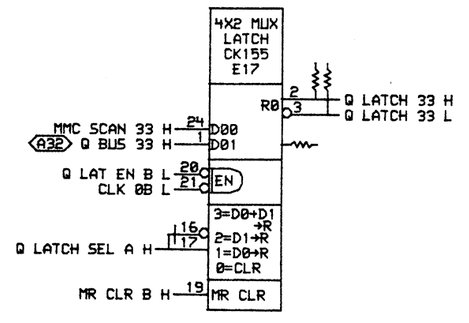


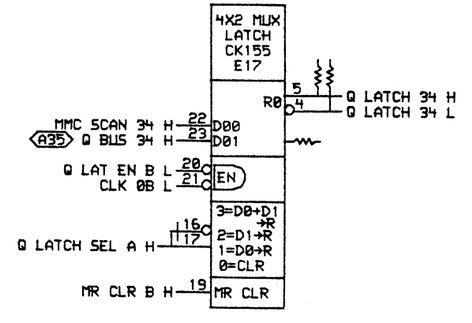


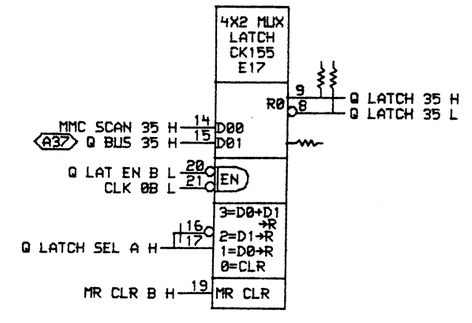




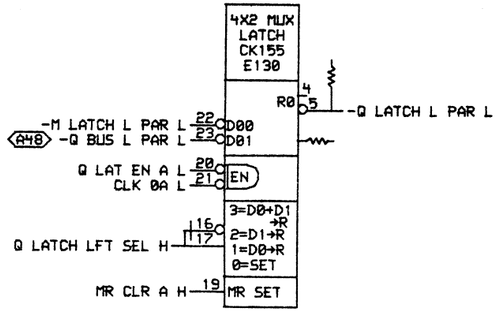


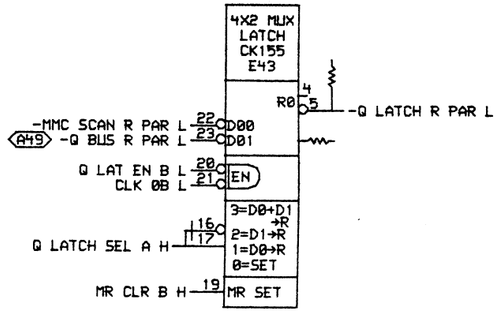


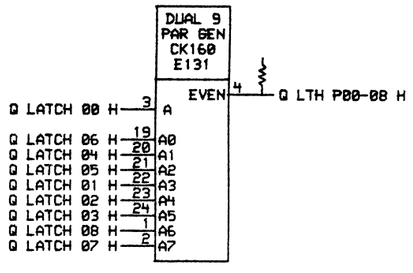


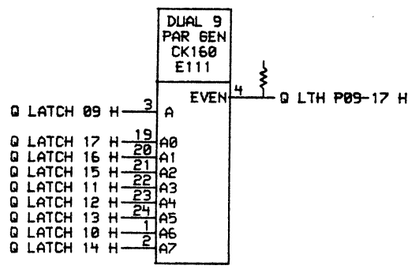


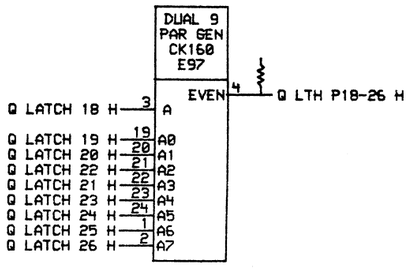
	DRN.	DATE 02-FEB-82	ENG.	DATE	TITLE: Q LATCH BITS 21 - 34
	CHK'D.	DATE	BOARD LOCATION: SHEET 2 OF 3		
V3.CAM:<2000.L0061>25MMC2.DRW 01-FEB-82 15:43			NEXT HIGHER ASSEMBLY:	SIZE D	CODE CS NUMBER L0061-0-MMC2 REV. *
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0061-0		

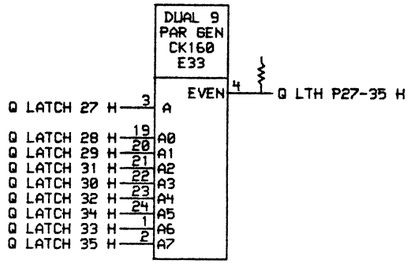


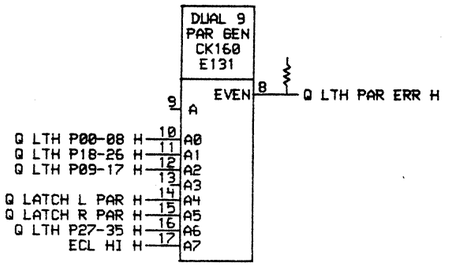


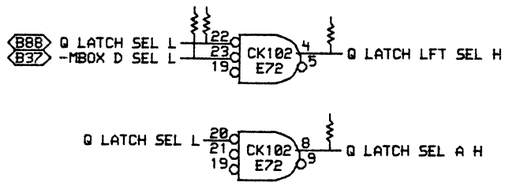


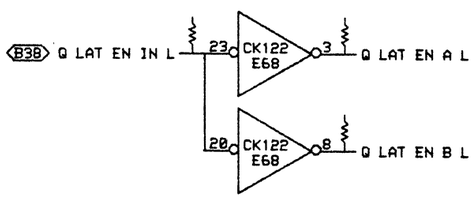






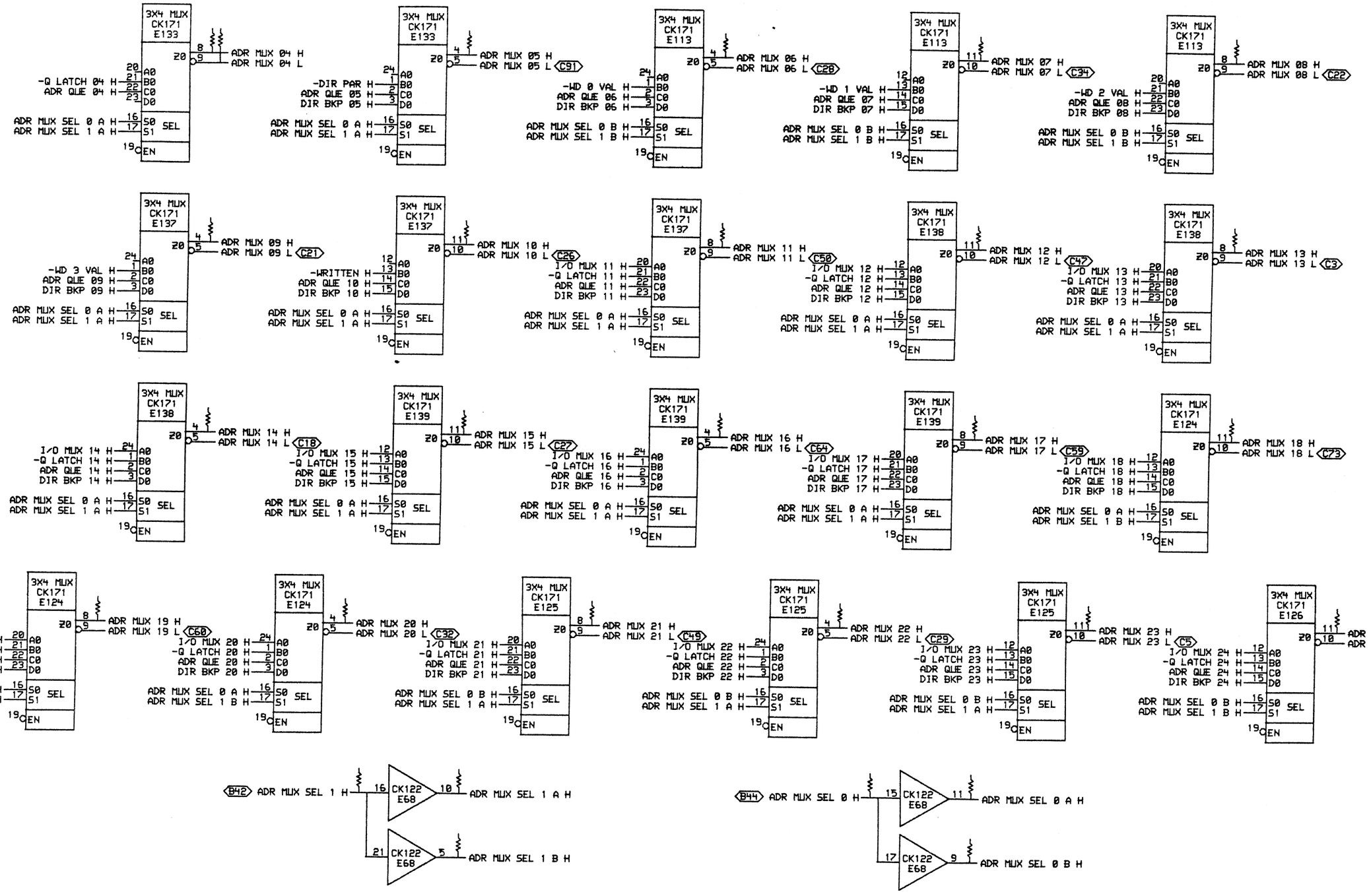




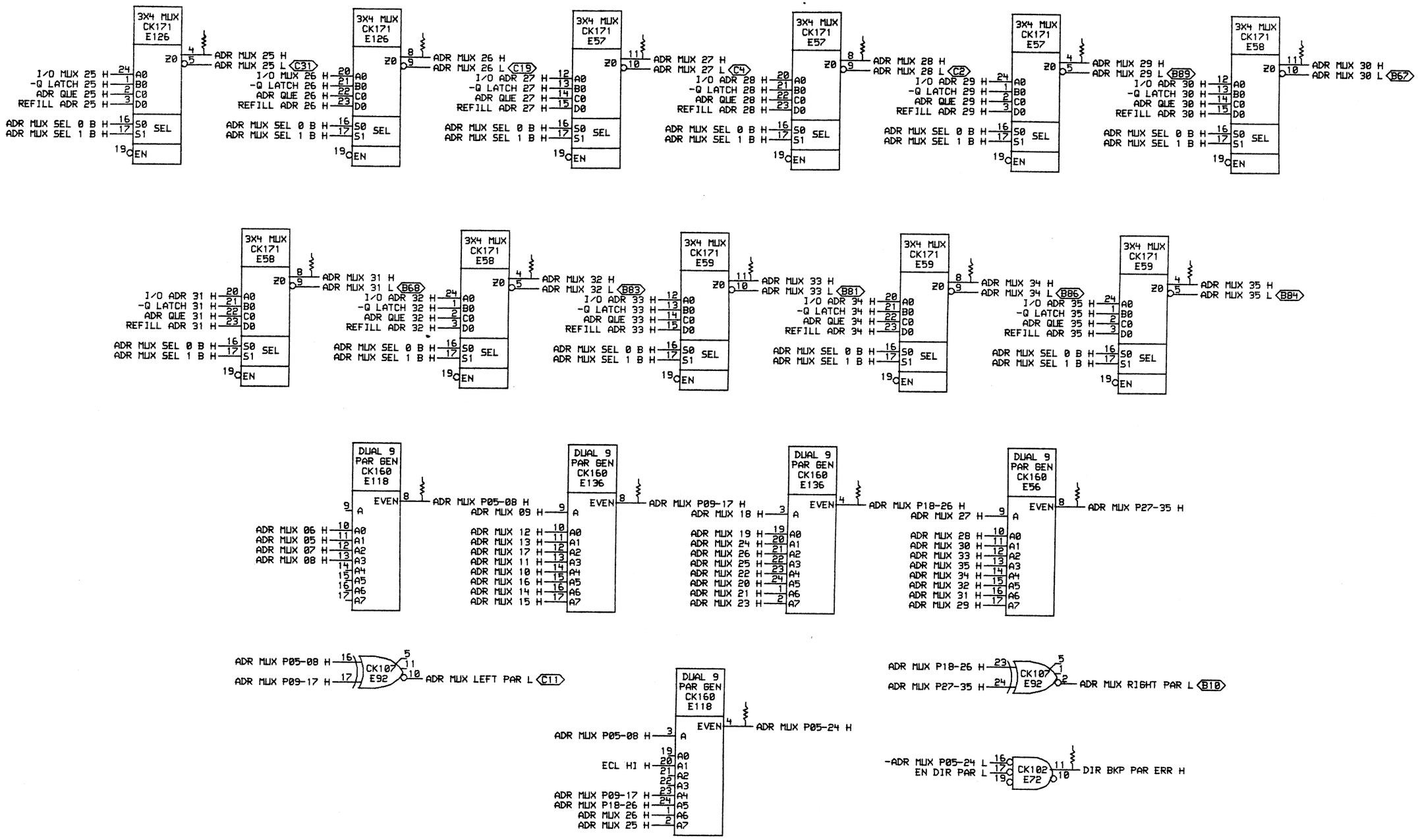


	DRN.	DATE 02-FEB-82	ENG.	DATE	TITLE: Q LATCH CTL & PARITY
	CHK'D.	DATE	BOARD LOCATION: SHEET 3 OF 3		
VULCAN:(2000,L0061)25MMC3.DRW	02-FEB-82 07:30	NEXT HIGHER ASSEMBLY:			
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0061-0			

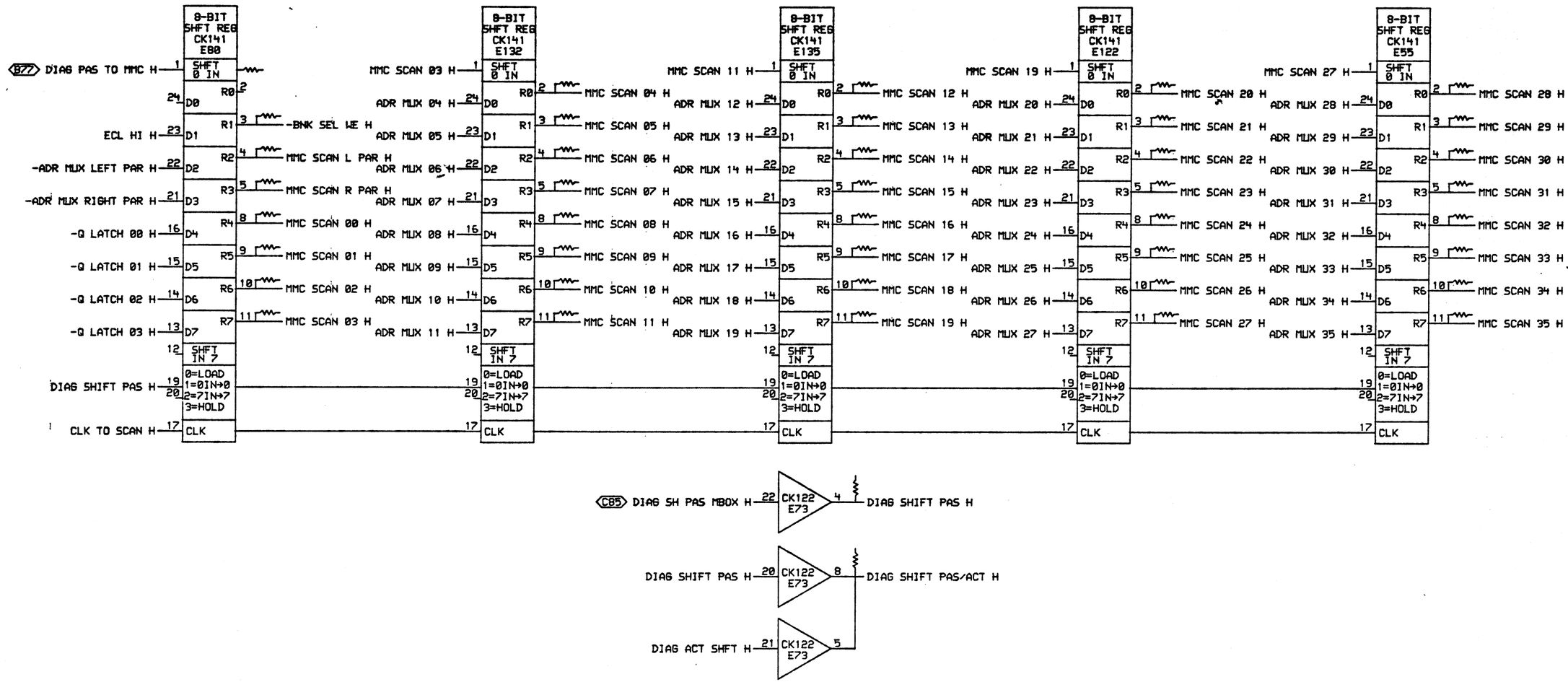
SIZE	CODE	NUMBER	REV.
D	CS	L0061-0-MMC3	*



	DRN.	DATE 08-FEB-82	ENG.	DATE	TITLE: ADR MUX BITS 04-24
	CHK'D.	DATE	BOARD LOCATION:	SHEET 1 OF 2	
VULCAN:<2080.L0061>ZSMMC4.DRW	01-FEB-82 14:43	NEXT HIGHER ASSEMBLY:		SIZE D	CODE CS
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0061-0		NUMBER L0061-0-MMC4	REV. *



	DRN.	DATE 02-FEB-82	ENG.	DATE	TITLE: ADR MUX BITS 25-35 & P
	CHK'D.	DATE	BOARD LOCATION:		SHEET 2 OF 2
VULCAN:(2080.L0061)25MMC5.DRW	01-FEB-82 14:41	NEXT HIGHER ASSEMBLY:		SIZE D	CODE CS
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0051-0		NUMBER L0061-0-MMC5	REV. *



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REVISIONS		
CHK	CHANGE NO.	REV

digital

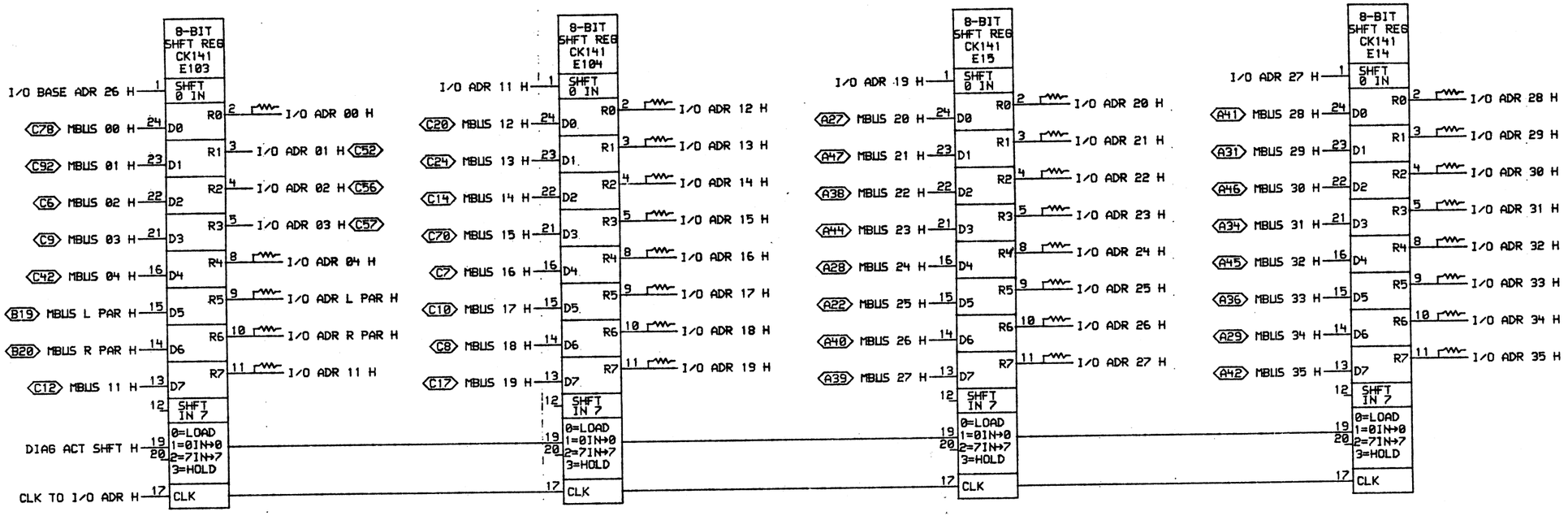
DRN. S. Toomby | DATE 30-JUL-82 | ENG. | DATE
CHK D. | DATE | BOARD LOCATION: | SHEET 1 OF

VULCAN:<2080.L0061>M5MMC6.DRW 29-JUL-82 17:06 | NEXT HIGHER ASSEMBLY: B-DD-L0061-0
FIRST USED ON OPTION/MODEL: 2080

TITLE: MMC SCAN REG BITS 00-35 & P

SIZE	CODE	NUMBER	REV.
D	CS	L0061-0-MMC6	*

REV. *
NUMBER L0061-0-MMC6
CODE CS
SIZE D



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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN: S. Toomey | DATE: 12-OCT-82 | ENG. | DATE

CHK: D. | DATE | BOARD LOCATION: | SHEET 1 OF 1

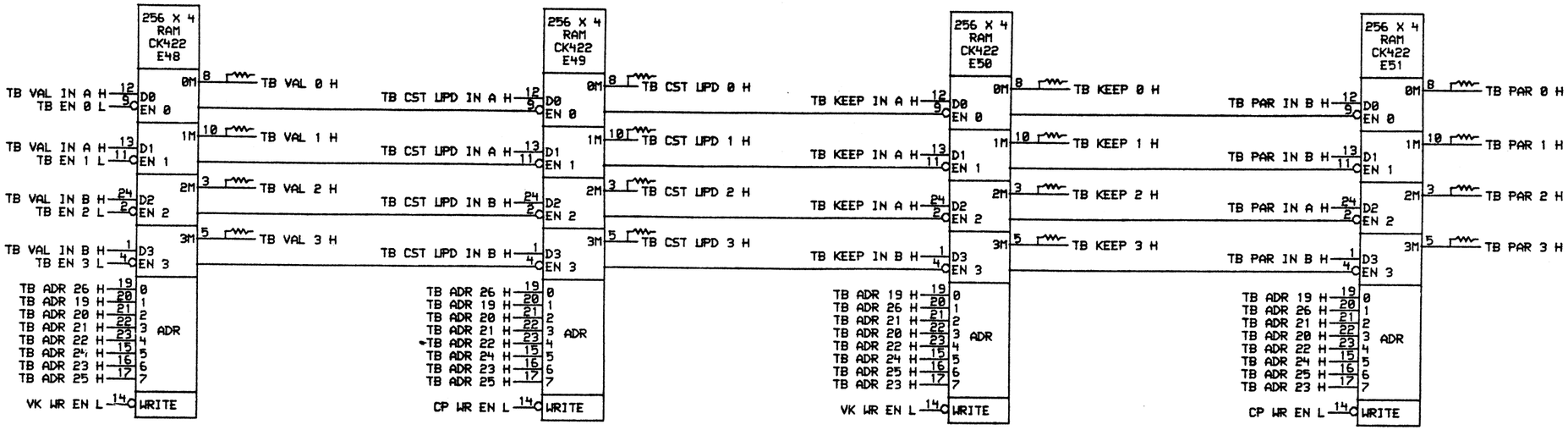
V1.1 CAM: <2080.L0061>53MMC7.DRW | 11-OCT-82 18:30 | NEXT HIGHER ASSEMBLY: B-DD-L0061-0

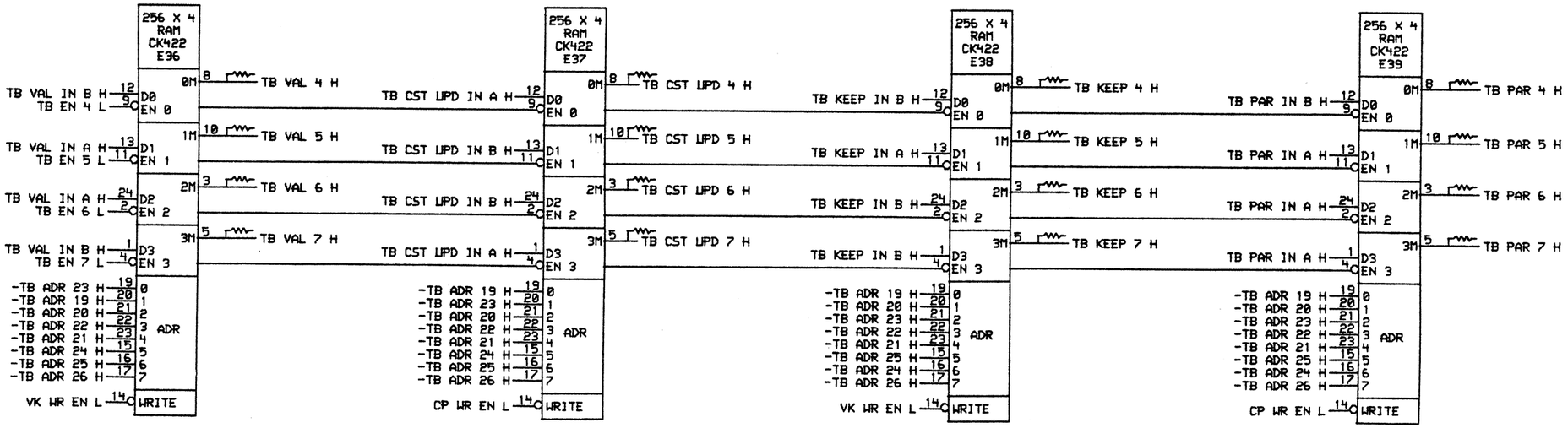
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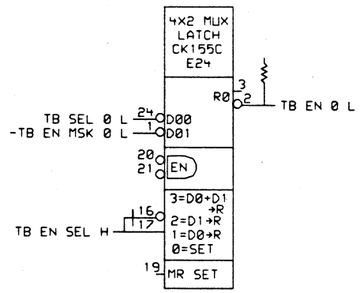
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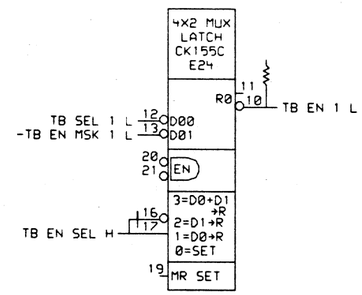
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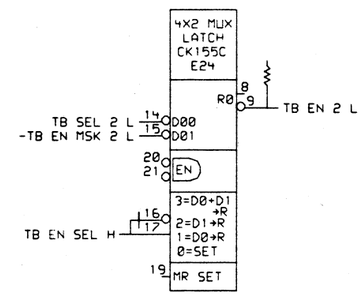


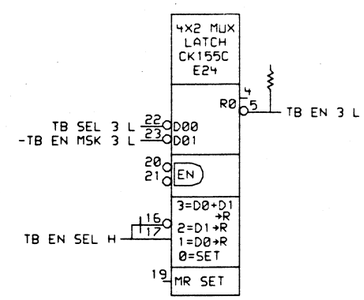


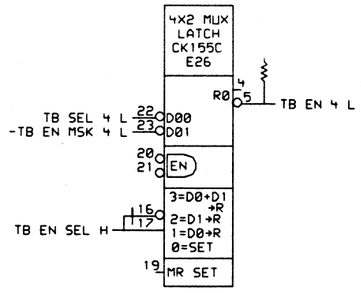
	DRN.	DATE 8E-FEB-82	ENG.	DATE	TITLE: TB CLR
	CHK'D.	DATE	BOARD LOCATION: SHEET 1 OF 2		
VULCAN\<2000.L0061>E5MMC8.DRW 01-FEB-82 15:15			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0061-0-MMC8 / REV. *
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0061-0		

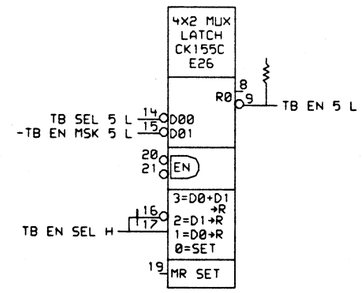


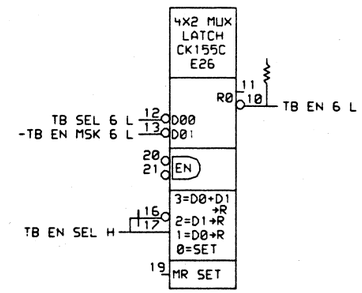


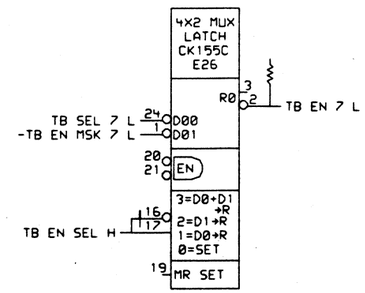


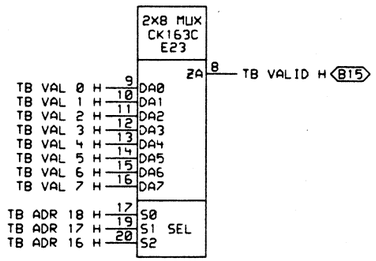


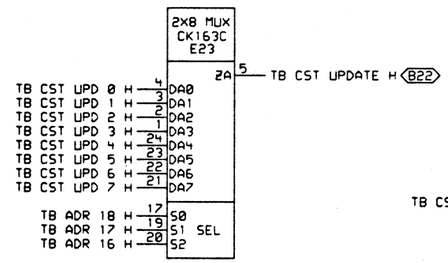


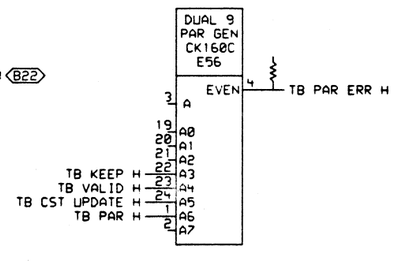


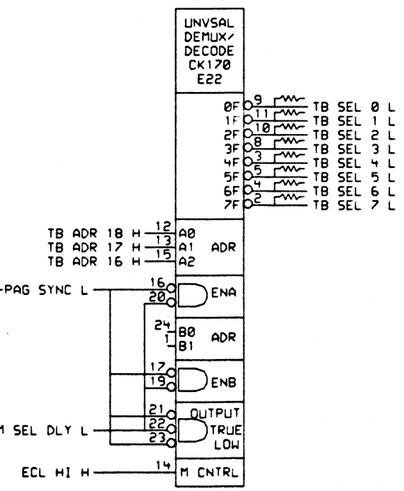


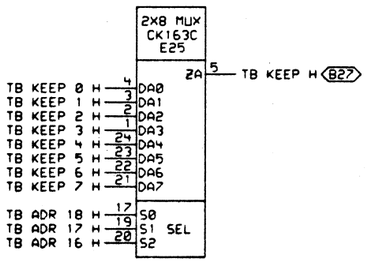


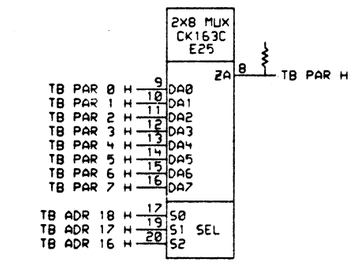


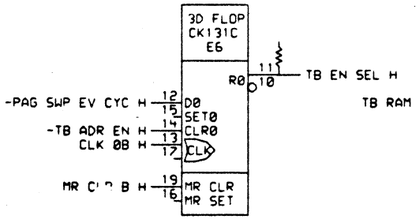












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REVISIONS		
CHK	CHANGE NO.	REV

digital

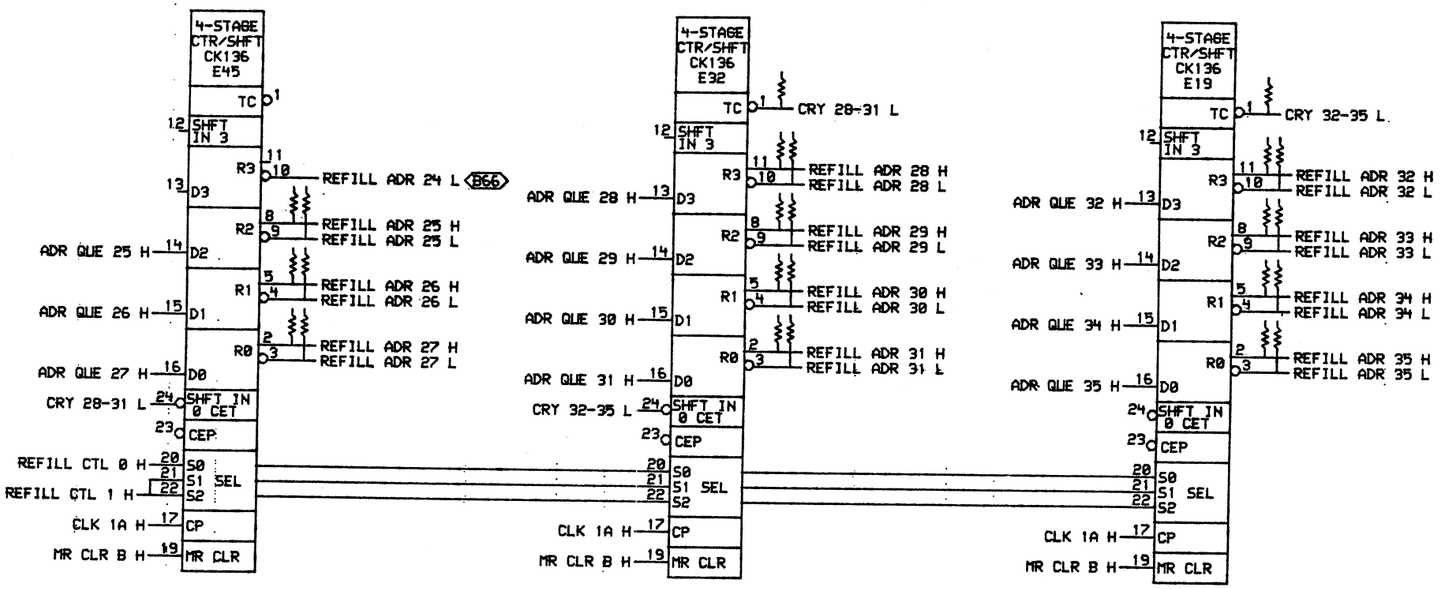
DRN J.Toomey 30-DEC-82 | ENG. | DATE
CHK'D. | DATE | BOARD LOCATION:
SHEET 2 OF 2

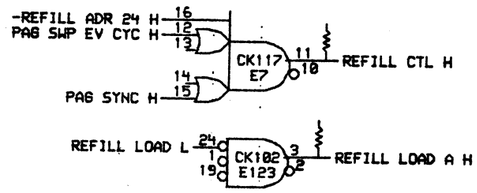
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FIRST USED ON OPTION/MODEL: 2080

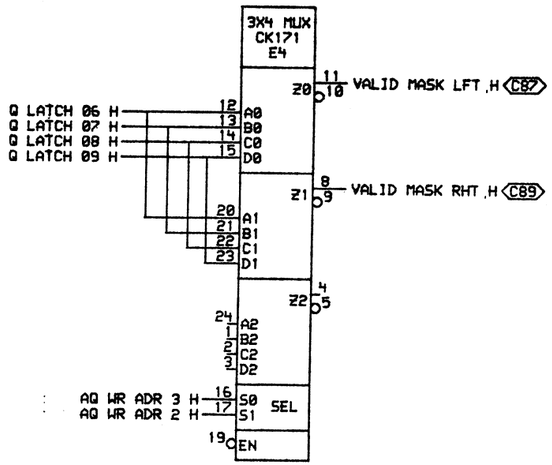
NEXT HIGHER ASSEMBLY: B-DD-L0061-0

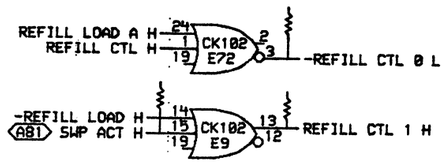
TITLE: TB CLR

SIZE	CODE	NUMBER	REV.
D	CS	L0061-0-MMC9	*









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REVISIONS		
CHK	CHANGE NO.	REV

digital

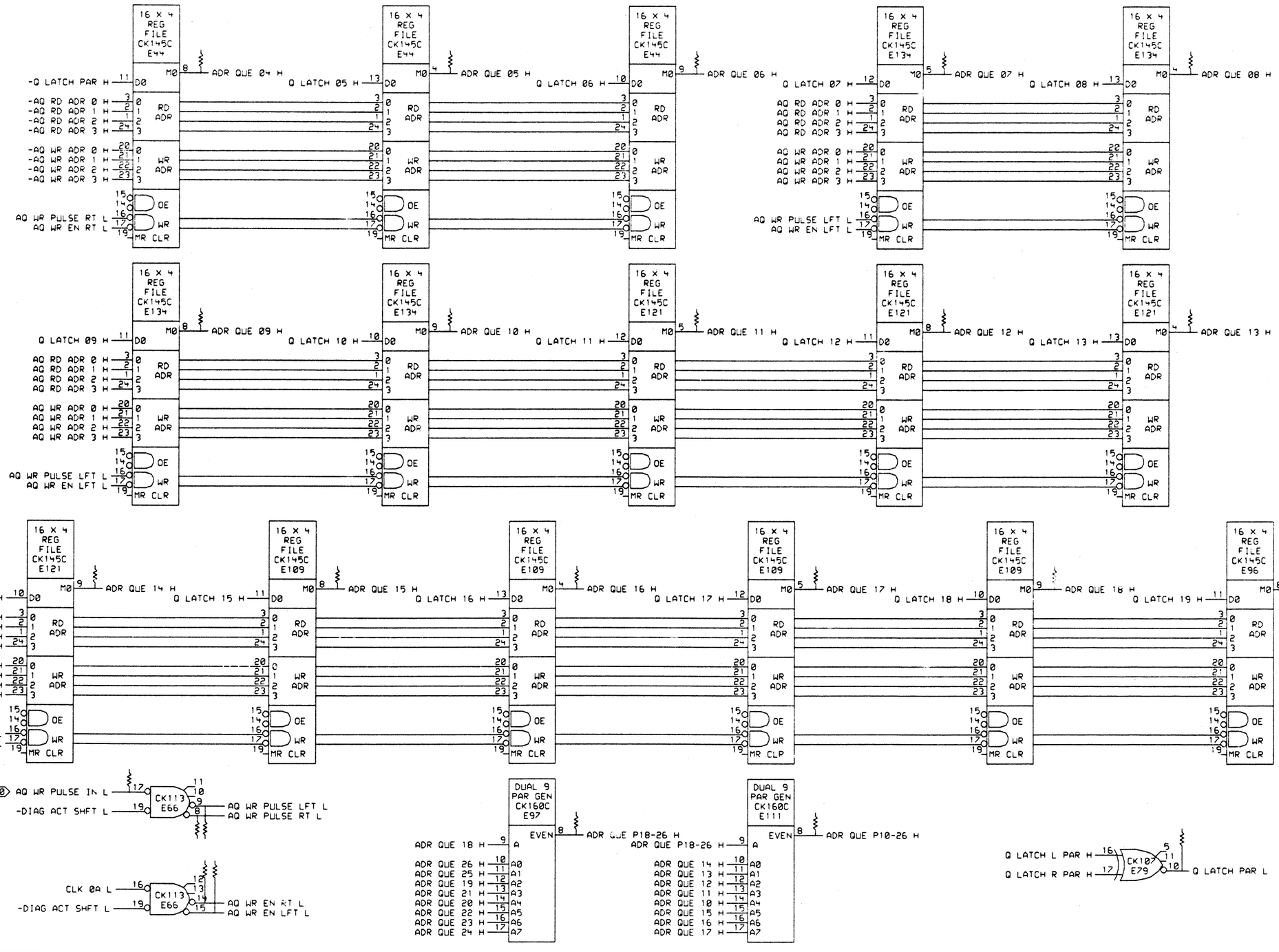
DRN: J. Looney | DATE: 07-OCT-82 | ENG. | DATE | TITLE: REFILL LATCH BITS 25-35

CHK: D. | DATE | BOARD LOCATION: | SHEET 1 OF 1

VULCAN:(2080.L0061)52MMCA.DRW | 06-OCT-82 10:29 | NEXT HIGHER ASSEMBLY: B-DD-L0061-0

FIRST USED ON OPTION/MODEL: 2080

SIZE	CODE	NUMBER	REV.
D	CS	L0061-0-MMCA	*



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REVISIONS		
CHK	CHANGE NO.	REV

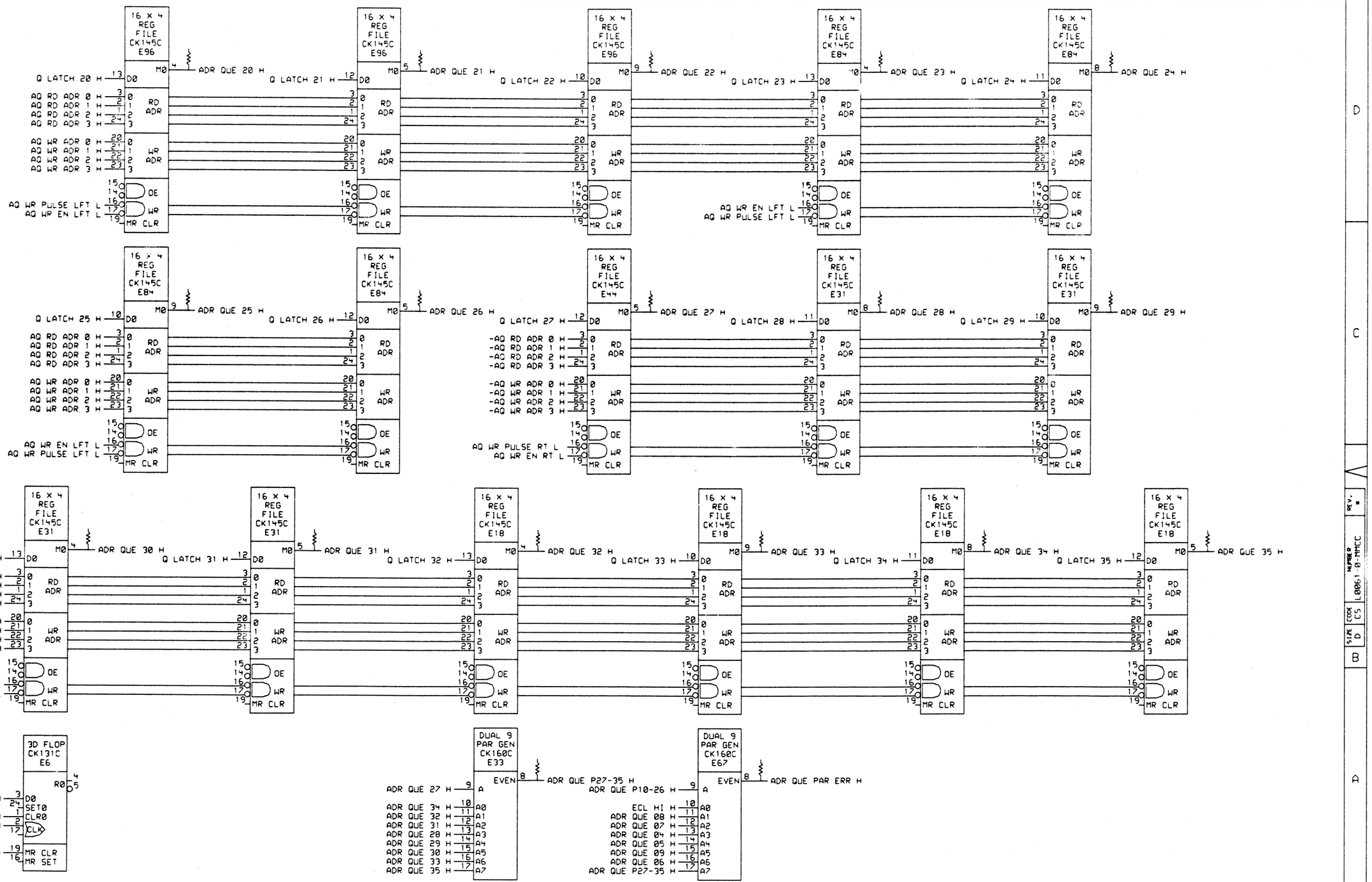
digital

DRN. S. Toomey
CHK'D.
DATE 28-DEC-82
ENG.
DATE
DATE
BOARD LOCATION:
SHEET 1 OF 2

VULCAN:<2080.L0061>58MMCB.DRW | 27-DEC-82 15:23 | NEXT HIGHER ASSEMBLY:
FIRST USED ON OPTION/MODEL: 2080 | B-DD-L0061-0

TITLE: ADR QUE BITS 04 - 19

SIZE	CODE	NUMBER	REV.
D	CS	L0061-0-MMCB	*



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CHK	CHANGE NO.	REV

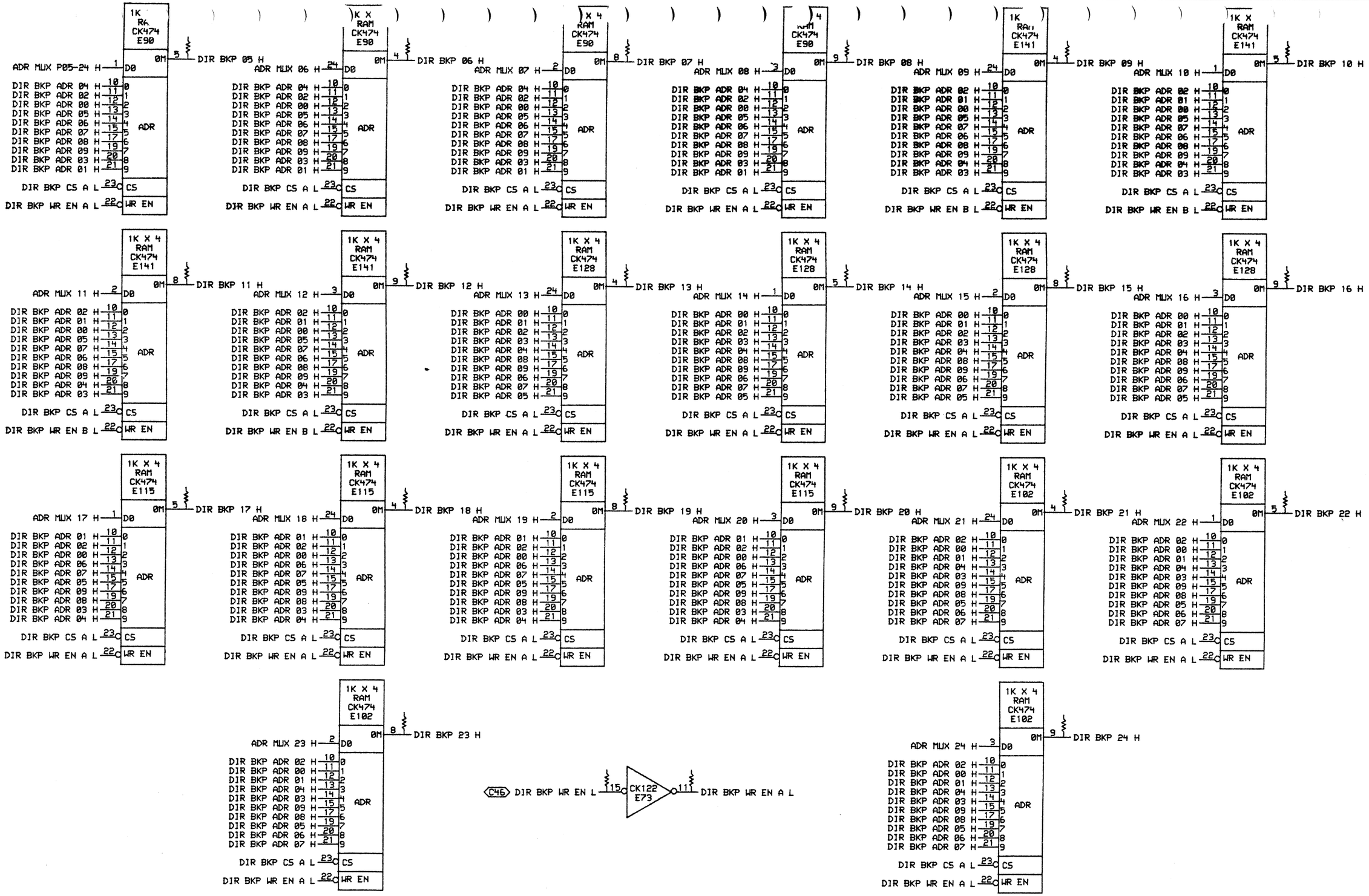
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DRN. L. Toomey
CHK'D.
DATE 28-DEC-82
ENG.
DATE
DATE
BOARD LOCATION:
SHEET 2 OF 2

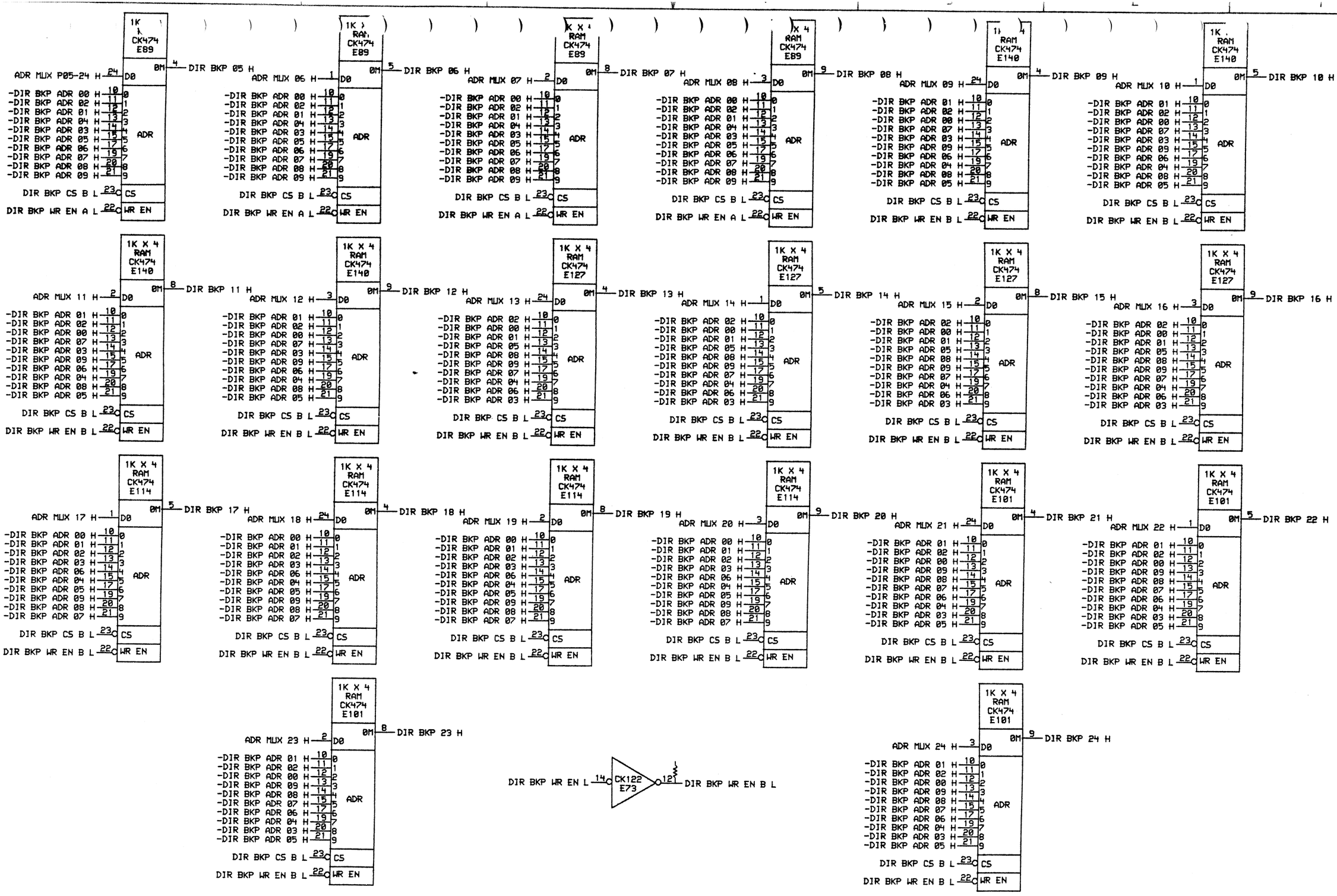
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FIRST USED ON OPTION/MODEL: 2080

TITLE: ADR QUE BITS 20 - 35

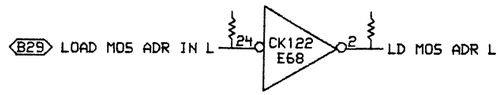
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D	CS	L0061-0-MMCC	*

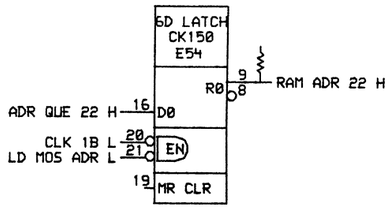


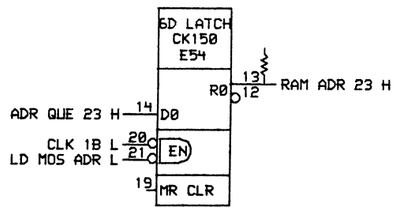
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	CHK'D.	DATE	BOARD LOCATION:		
			SHEET 1 OF 2		
VULCAN:<2080.L0061>25MMCD.DRW	01-FEB-82 14:37	NEXT HIGHER ASSEMBLY:		SIZE D	CODE CS
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0061-0		NUMBER L0061-0-MMCD	REV. *

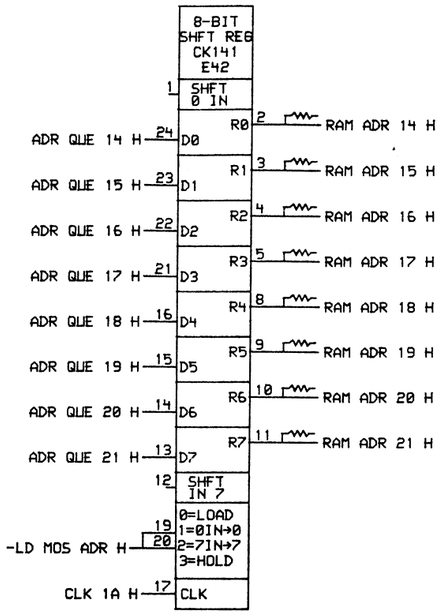


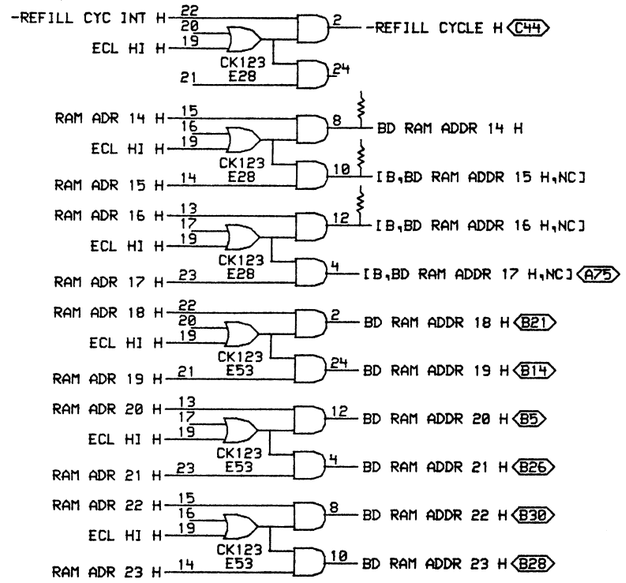
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	CHK'D.	DATE	BOARD LOCATION:		
			SHEET 2 OF 2		
VLI CAM:<2080.L0061>25MMCE.DRW 01-FEB-82 14:35			NEXT HIGHER ASSEMBLY:	SIZE D	CODE CS NUMBER L0061-0-MMCE REV. *
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0061-0		

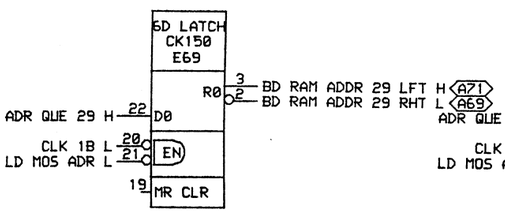


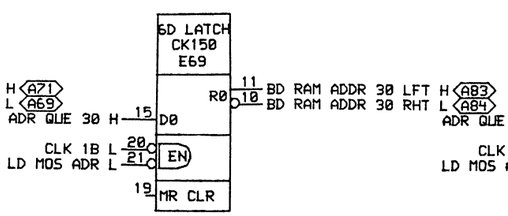


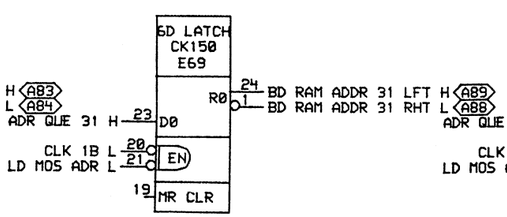


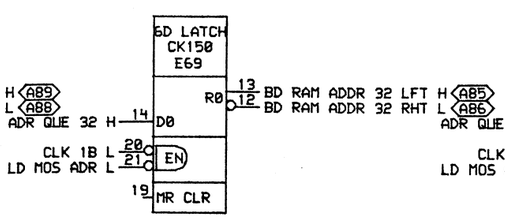


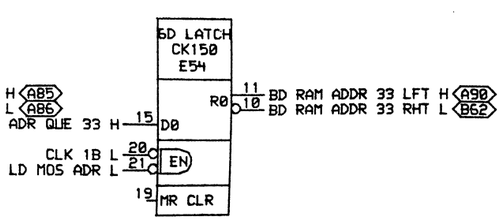












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REVISIONS		
CHK	CHANGE NO.	REV

digital

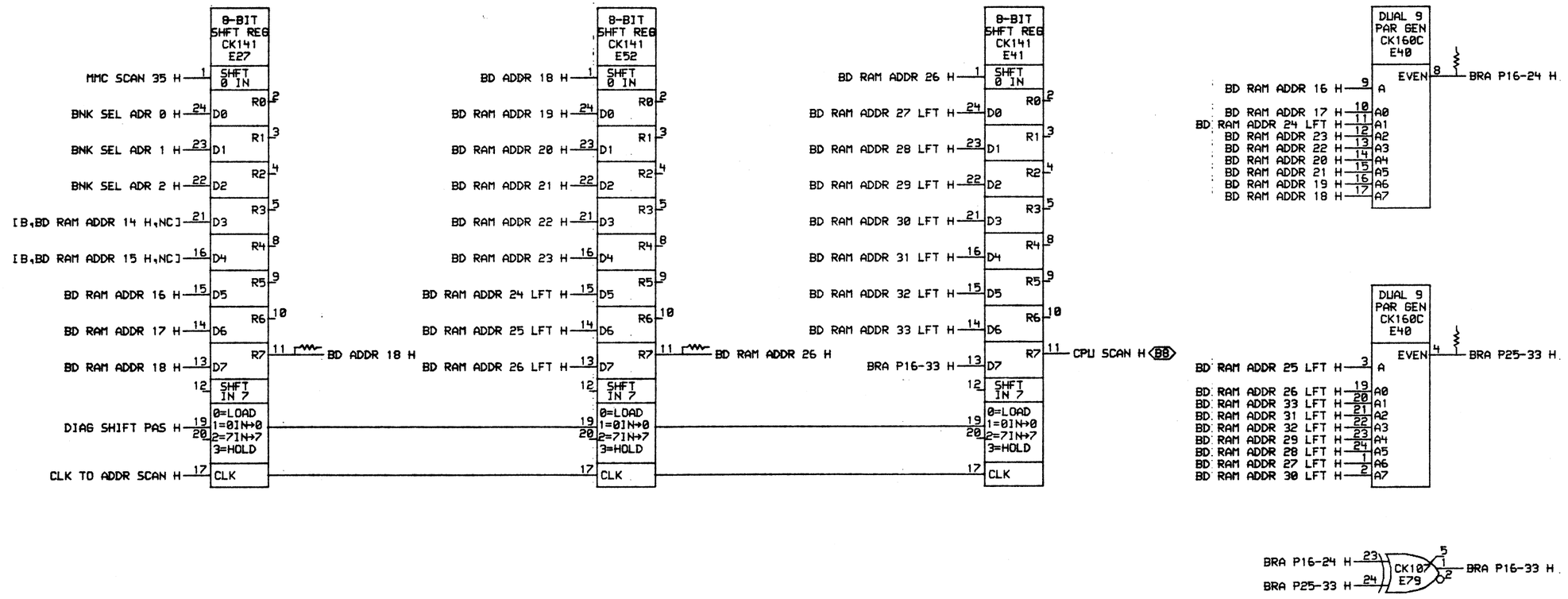
DRN. D. Toomey
CHK D.

DATE	ENG.	DATE
30-APR-82		
DATE	BOARD LOCATION:	
	SHEET 1 OF 1	

VULCAN:<2000.L0061>39MMCF.DRW 29-APR-82 19:06
FIRST USED ON OPTION/MODEL: 2000
NEXT HIGHER ASSEMBLY: B-DD-L0061-0

TITLE: MEM ADDR BITS 14-33

SIZE	CODE	NUMBER	REV.
D	CS	L0061-0-MMCF	*



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REVISIONS		
CHK	CHANGE NO.	REV

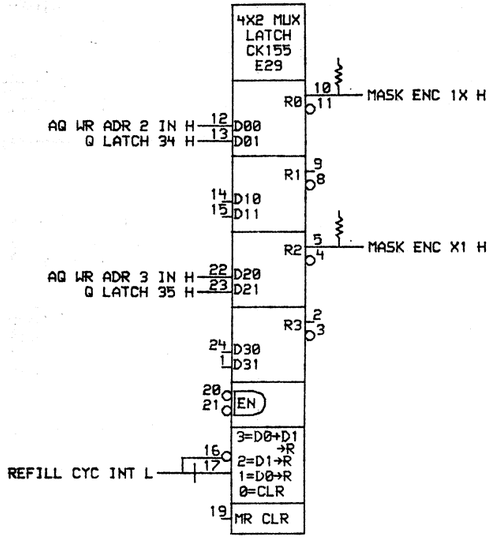
VLSCAN:<2080.L0061>51MMCG.DRW | 03-OCT-82 15:27 | NEXT HIGHER ASSEMBLY: B-DD-L0061-0

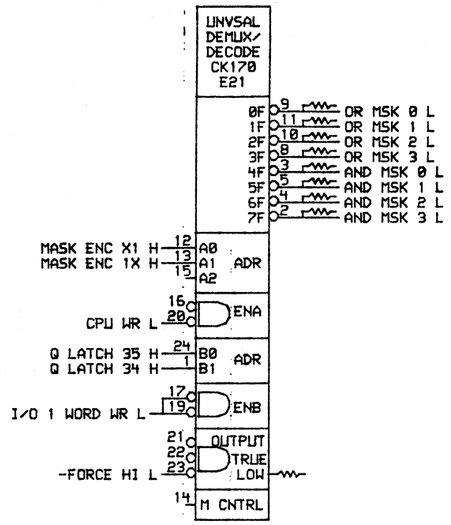
FIRST USED ON OPTION/MODEL: 2080

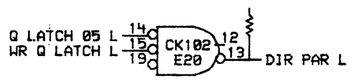
DRN. S. Looney | DATE 04-OCT-82 | ENG. | DATE | CHK D. | DATE | BOARD LOCATION: | SHEET 1 OF 1

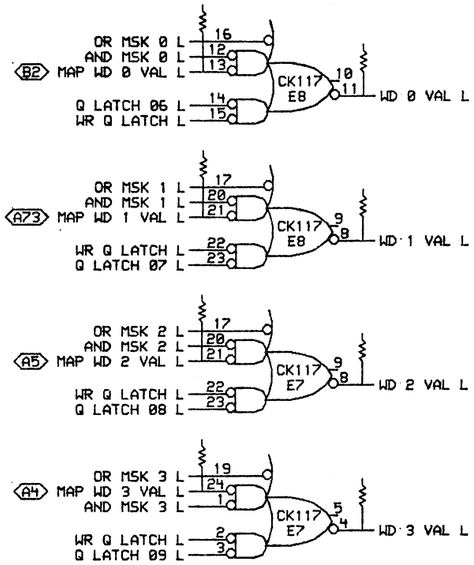
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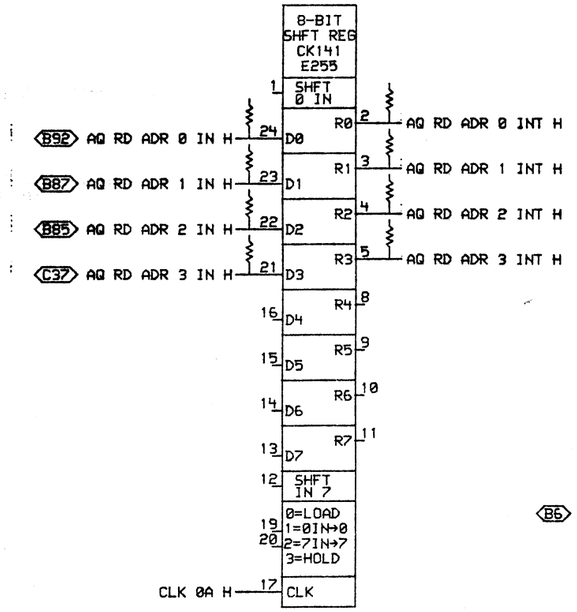
SIZE	CODE	NUMBER	REV.
D	CS	L0061-0-MMCG	*

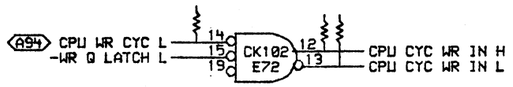


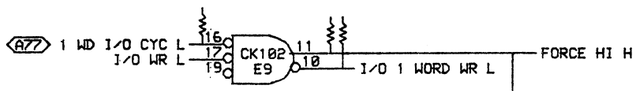


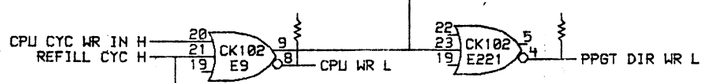


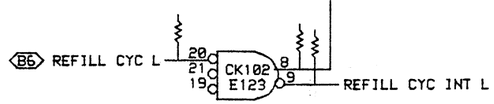


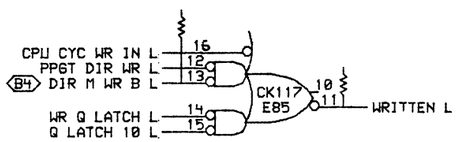












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REVISIONS		
CHK	CHANGE NO.	REV

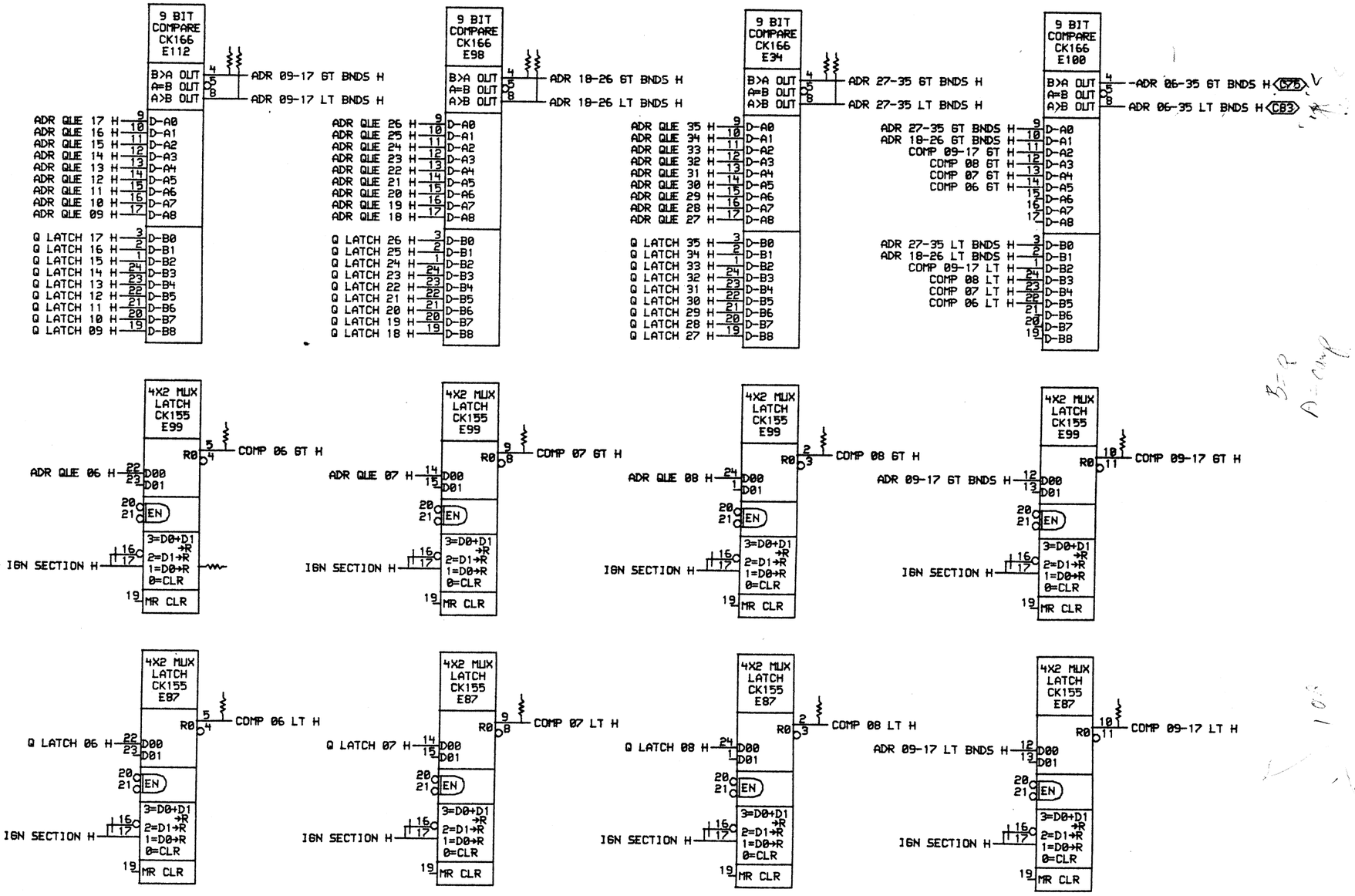
VLB,CAN:<2080.L0061>49MMCH.DRW 07-SEP-82 11:39
FIRST USED ON OPTION/MODEL: 2080

DRN. S. Landry | DATE 08-SEP-82 | ENG. | DATE
CHK D. | DATE | BOARD LOCATION: | SHEET 1 OF 1
NEXT HIGHER ASSEMBLY: B-DD-L0061-0

TITLE: VALID & WRITTEN BITS

SIZE	CODE	NUMBER	REV.
D	CS	L0061-0-MMCH	*

A= Bound
B= ROF ADR



B=R
A=CMP

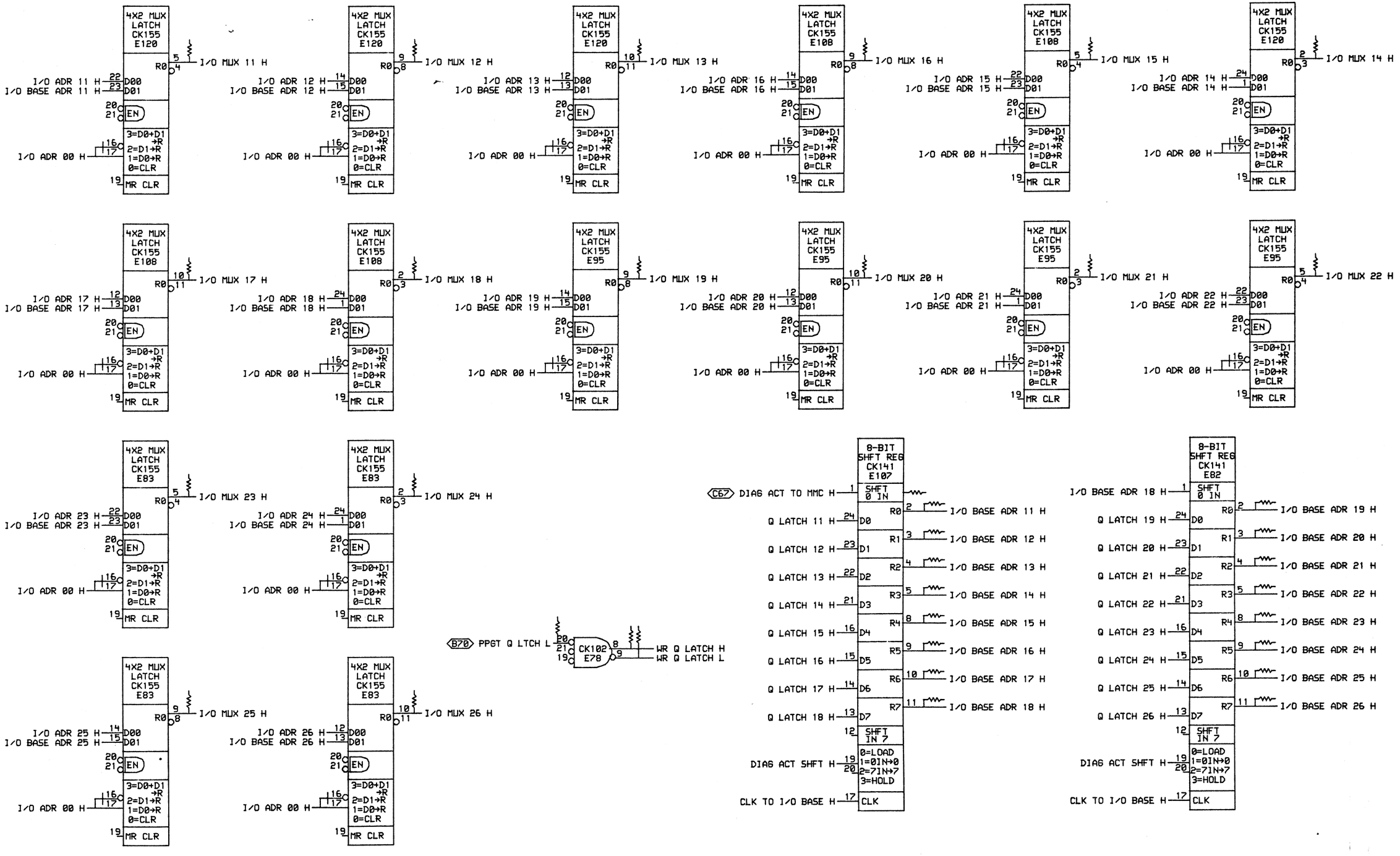
ADR ≤ END ≤

BND ≤ ADR ≤ BND

A>B ⇒ BND > ADR
⇒ ADR ≤ BND

B>A ⇒ ADR > BND
⇒ BND ≤ ADR

	DRN.	DATE 02-FEB-82	ENG.	DATE	TITLE: ADR BRK COMPARATORS
	CHK'D.	DATE	BOARD LOCATION:		
			SHEET 1 OF 1		
VULCAN\<2080.L0061>2*MMCI.DRW	01-FEB-82 13:39	NEXT HIGHER ASSEMBLY:		SIZE D	CODE CS
FIRST USED ON OPTION/MODEL:	2080	B-DD-L0061-0		NUMBER L0061-0-MMCI	REV. *



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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. B. Tooley — DATE 27-APR-82 — ENG. — DATE

CHK'D. — DATE — BOARD LOCATION:

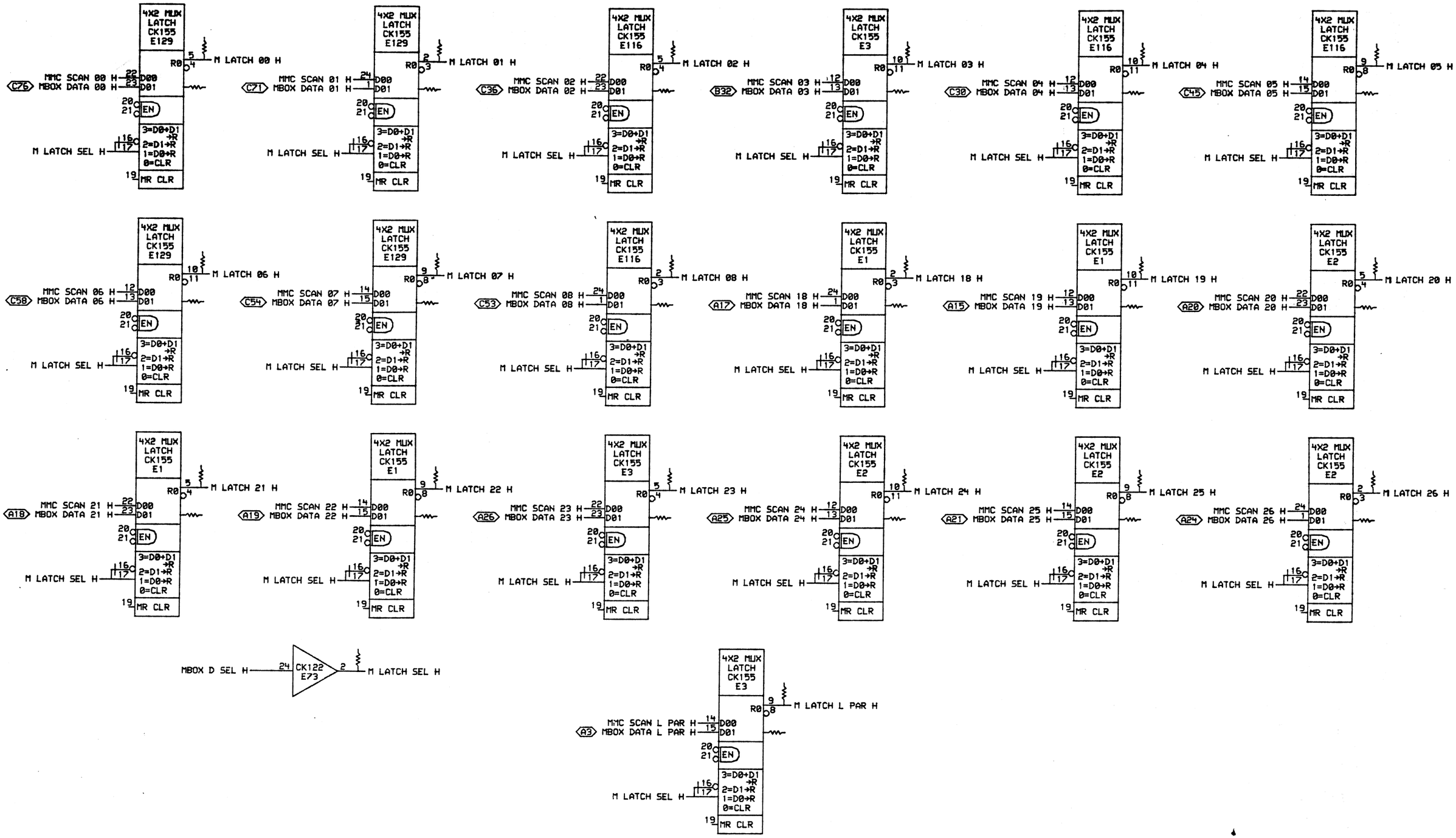
SHEET 1 OF 1

VULCAN!<F088_L0061>35MMCJ.DRW 26-APR-82 11:48 — NEXT HIGHER ASSEMBLY: B-DD-L0061-0

FIRST USED ON OPTION/MODEL: 2080

TITLE: I/O BASE ADR 11 - 26

SIZE	CODE	NUMBER	REV.
D	CS	L0061-0-MMCJ	*



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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN: D. Loomby
CHK: D.
DATE: 09-AUG-82
ENG.
DATE
BOARD LOCATION:
SHEET 1 OF 1

VULCAN:<2000.L0061>M-MMCK.DRW | 08-AUG-82 14:01 | NEXT HIGHER ASSEMBLY: B-DD-L0061-0

FIRST USED ON OPTION/MODEL: 2080

TITLE: M LATCH 00-08 & 18-26

SIZE	CODE	NUMBER	REV.
D	CS	L0061-0-MMCK	*

NUMBER L0061-0-MMCK | CODE CS | SIZE D | REV. *

6D LATCH CK150C E77
<C69> [B,DIR BKP IN 0 H,NC] 17 D0
R0 5 4 -DIR BKP CS A H, DIR BKP CS B L
CLK 1A L 20, BKP WE L 21 EN
19 MR CLR

6D LATCH CK150C E77
<C74> [B,DIR BKP IN 1 H,NC] 16 D0
R0 9 8 DIR BKP ADR 00 H, DIR BKP ADR 00 L
CLK 1A L 20, BKP WE L 21 EN
19 MR CLR

6D LATCH CK150C E77
Q LATCH 25 H 14 D0
R0 13 12 DIR BKP ADR 01 H, DIR BKP ADR 01 L
CLK 1A L 20, BKP WE L 21 EN
19 MR CLR

6D LATCH CK150C E77
Q LATCH 26 H 15 D0
R0 11 10 DIR BKP ADR 02 H, DIR BKP ADR 02 L
CLK 1A L 20, BKP WE L 21 EN
19 MR CLR

6D LATCH CK150C E77
Q LATCH 27 H 23 D0
R0 24 1 DIR BKP ADR 03 H, DIR BKP ADR 03 L
CLK 1A L 20, BKP WE L 21 EN
19 MR CLR

6D LATCH CK150C E77
Q LATCH 28 H 22 D0
R0 3 2 DIR BKP ADR 04 H, DIR BKP ADR 04 L
CLK 1A L 20, BKP WE L 21 EN
19 MR CLR

6D LATCH CK150C E76
Q LATCH 29 H 14 D0
R0 13 12 DIR BKP ADR 05 H, DIR BKP ADR 05 L
BKP WE L 20, CLK 1A L 21 EN
19 MR CLR

6D LATCH CK150C E76
Q LATCH 30 H 15 D0
R0 11 10 DIR BKP ADR 06 H, DIR BKP ADR 06 L
BKP WE L 20, CLK 1A L 21 EN
19 MR CLR

16 CK122 E73 10 BKP WE L

6D LATCH CK150C E76
Q LATCH 31 H 16 D0
R0 9 8 DIR BKP ADR 07 H, DIR BKP ADR 07 L
BKP WE L 20, CLK 1A L 21 EN
19 MR CLR

6D LATCH CK150C E76
Q LATCH 32 H 17 D0
R0 5 4 DIR BKP ADR 08 H, DIR BKP ADR 08 L
BKP WE L 20, CLK 1A L 21 EN
19 MR CLR

6D LATCH CK150C E76
Q LATCH 33 H 22 D0
R0 3 2 DIR BKP ADR 09 H, DIR BKP ADR 09 L
BKP WE L 20, CLK 1A L 21 EN
19 MR CLR

6D LATCH CK150C E71
AQ RD ADR 0 INT H 15 D0
R0 11 10 AQ RD ADR 0 H, AQ RD ADR 0 L
20, CLK 2A L 21 EN
19 MR CLR

6D LATCH CK150C E71
AQ RD ADR 1 INT H 16 D0
R0 9 8 AQ RD ADR 1 H, AQ RD ADR 1 L
20, CLK 2A L 21 EN
19 MR CLR

6D LATCH CK150C E71
AQ RD ADR 2 INT H 17 D0
R0 5 4 AQ RD ADR 2 H, AQ RD ADR 2 L
20, CLK 2A L 21 EN
19 MR CLR

6D LATCH CK150C E71
AQ RD ADR 3 INT H 14 D0
R0 13 12 AQ RD ADR 3 H, AQ RD ADR 3 L
20, CLK 2A L 21 EN
19 MR CLR

6D LATCH CK150C E70
<B82> AQ WR ADR 0 IN H 15 D0
R0 11 10 AQ WR ADR 0 H, AQ WR ADR 0 L
20, 21 EN
19 MR CLR

6D LATCH CK150C E70
<B80> AQ WR ADR 1 IN H 16 D0
R0 9 8 AQ WR ADR 1 H, AQ WR ADR 1 L
20, 21 EN
19 MR CLR

6D LATCH CK150C E70
<B39> AQ WR ADR 2 IN H 17 D0
R0 5 4 AQ WR ADR 2 H, AQ WR ADR 2 L
20, 21 EN
19 MR CLR

6D LATCH CK150C E70
<B35> AQ WR ADR 3 IN H 22 D0
R0 3 2 AQ WR ADR 3 H, AQ WR ADR 3 L
20, 21 EN
19 MR CLR

6D LATCH CK150C E71
AQ RD ADR 0 INT H 22 D0
R0 3 2 AQX RD ADR 0 L <A1>
20, CLK 2A L 21 EN
19 MR CLR

6D LATCH CK150C E71
AQ RD ADR 1 INT H 23 D0
R0 24 1 AQX RD ADR 1 L <B36>
20, CLK 2A L 21 EN
19 MR CLR

6D LATCH CK150C E70
AQ RD ADR 2 H 14 D0
R0 13 12 AQX RD ADR 2 L <C88>
20, 21 EN
19 MR CLR

6D LATCH CK150C E70
AQ RD ADR 3 H 23 D0
R0 24 1 AQX RD ADR 3 L <C90>
20, 21 EN
19 MR CLR

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REVISIONS

CHK	CHANGE NO.	REV

digital

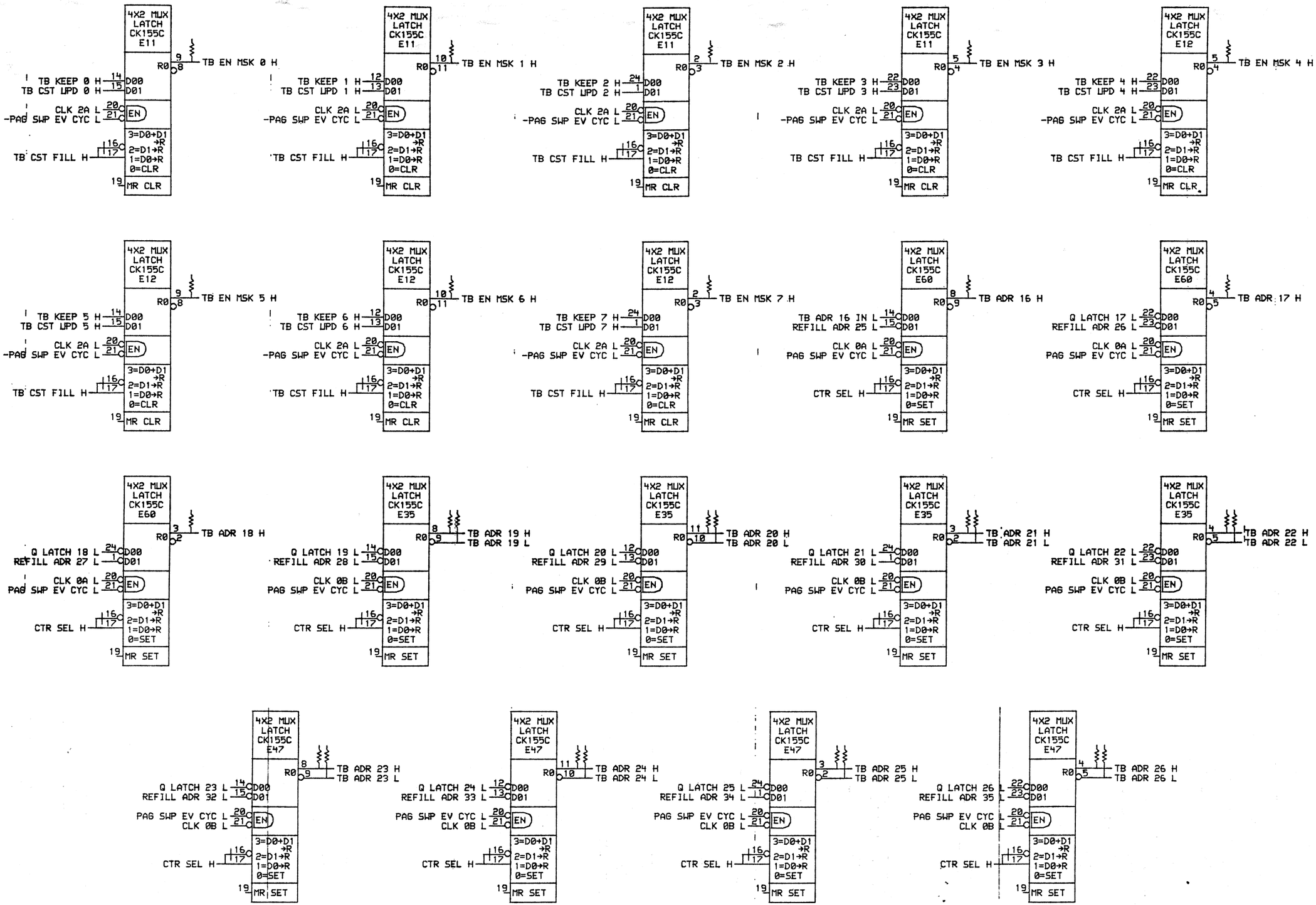
DRN. S. Toomey | DATE 28-SEP-82 | ENG. | DATE | TITLE:
CHK *D. | DATE | BOARD LOCATION: | SHEET 1 OF 1

VLE,CAN:<2080.L0061>50MMCL.DRW | 28-SEP-82 07:27 | NEXT HIGHER ASSEMBLY: B-DD-L0061-0

FIRST USED ON OPTION/MODEL: 2080

SIZE	CODE	NUMBER	REV.
D	CS	L0061-0-MMCL	*

NUMBER L0061-0-MMCL | SIZE D | CODE CS | REV. *



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REVISIONS		
CHK	CHANGE NO.	REV

digital

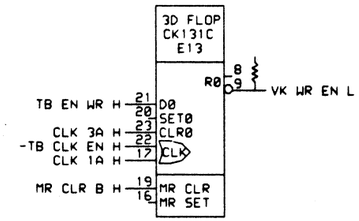
DRN. T.Toomey 02-NOV-82 | ENG. | DATE
CHK D. | DATE 03-NOV-82 | BOARD LOCATION: | DATE
VULCAN:<2080.L0061>56MMCM.DRW 02-NOV-82 20:46 | SHEET 1 OF 1
FIRST USED ON OPTION/MODEL: 2080 | NEXT HIGHER ASSEMBLY: B-DD-L0061-0

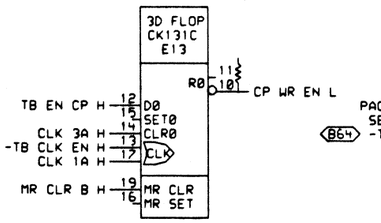
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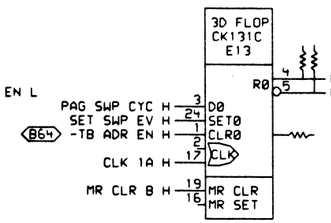
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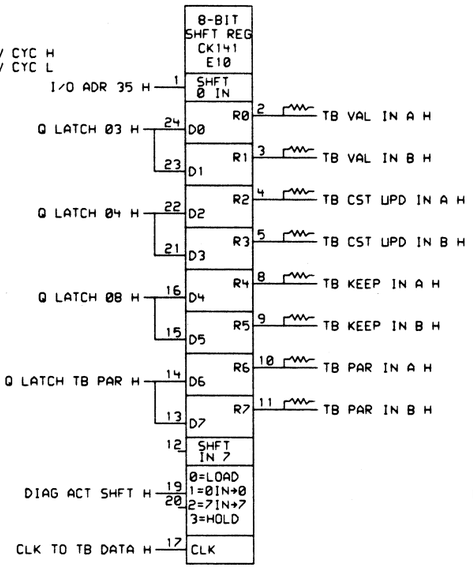
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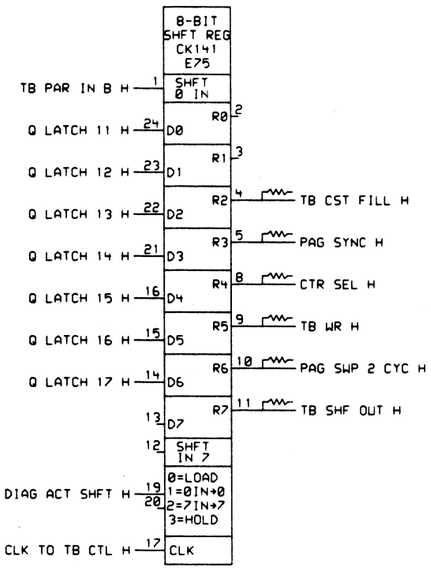
D C B A

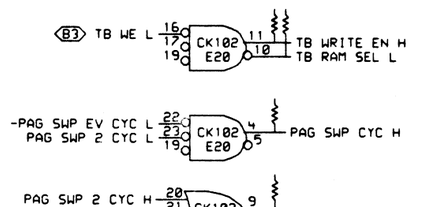


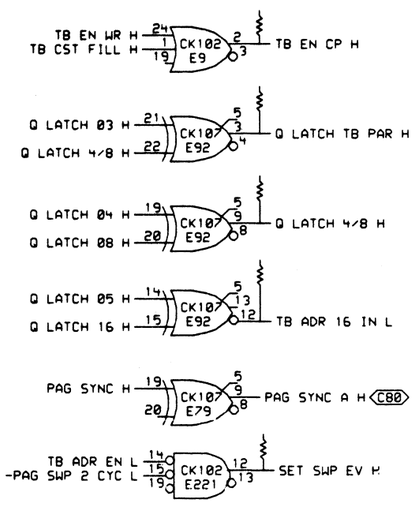


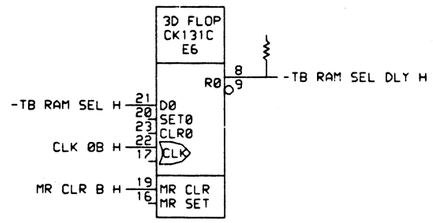




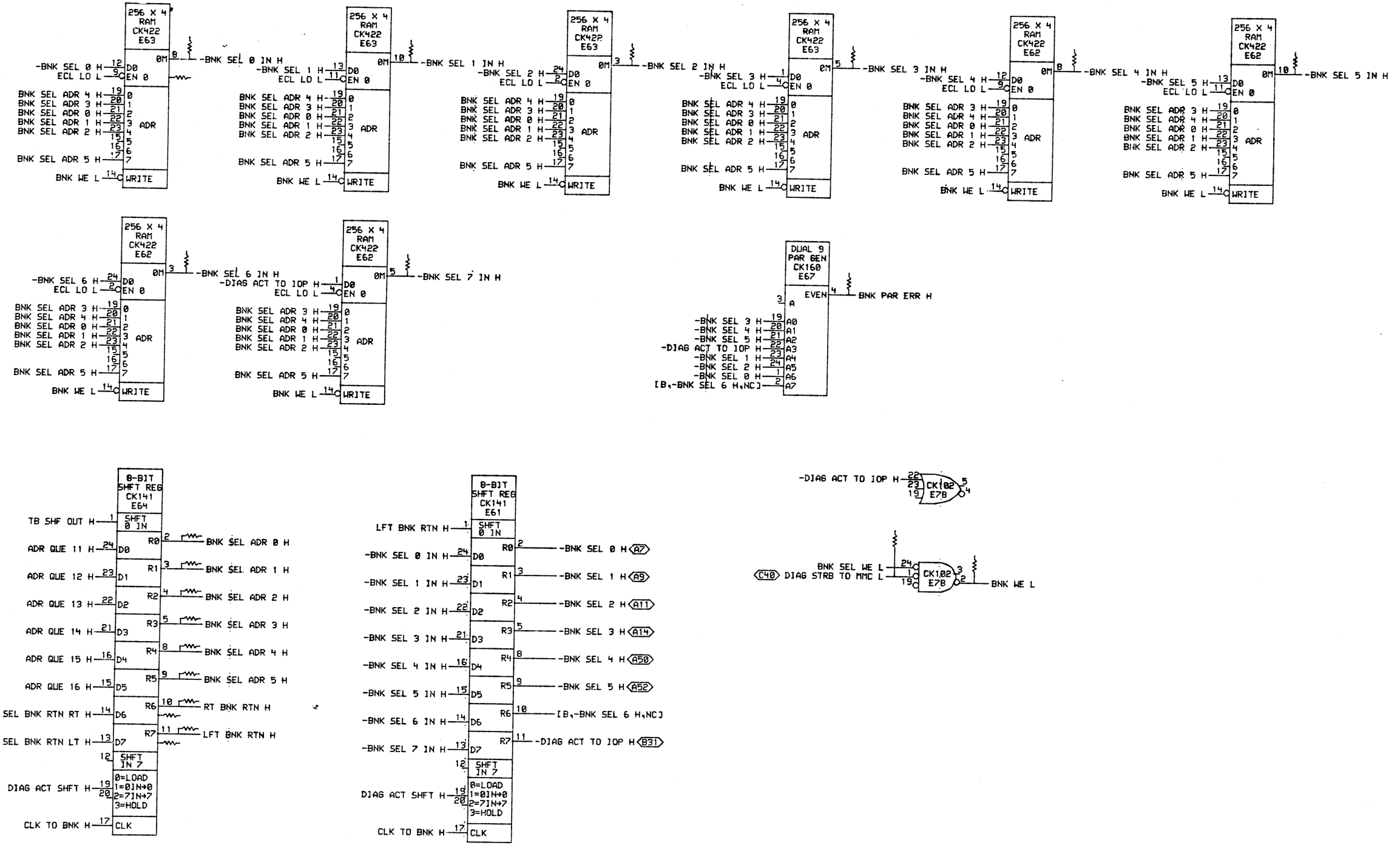








	DRN.	DATE 22-NOV-82	ENG.	DATE	TITLE: TB ADR CONTROL
	CHK'D.	DATE	BOARD LOCATION: SHEET 1 OF 1		
ARCH:<UHLER.PLOTS>57MMCN.DRW 15-NOV-82 06:48			NEXT HIGHER ASSEMBLY:	SIZE D	CODE CS
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0061-0	NUMBER L0061-0-MMCN	REV. *



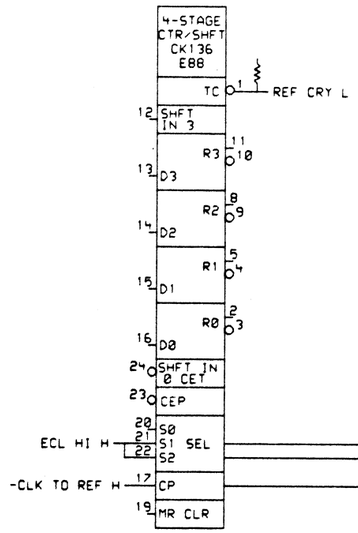
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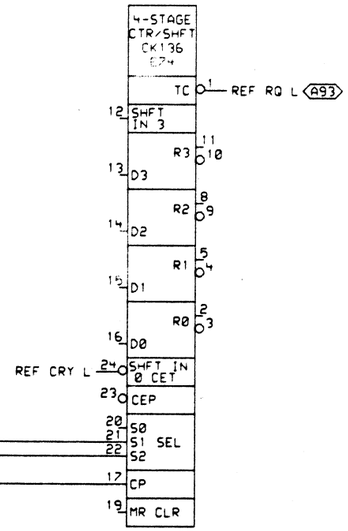
REVISIONS		
CHK	CHANGE NO.	REV

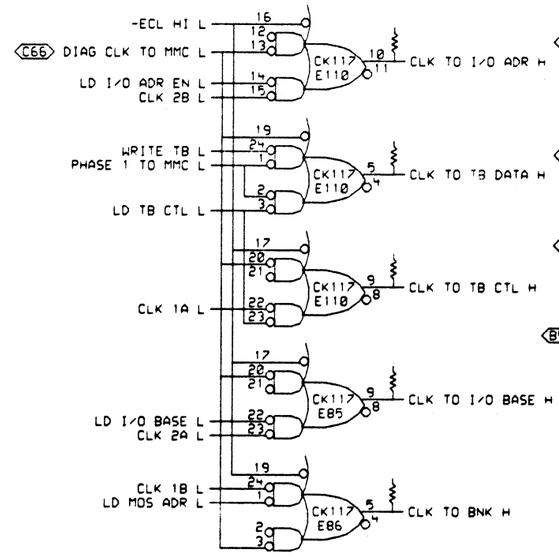
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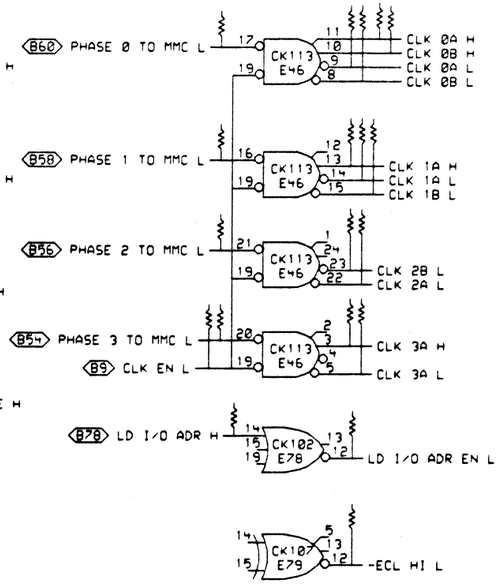
DRN. D. Toomey 14-JUL-82 | ENG. | DATE | TITLE:
CHK D. | DATE | BOARD LOCATION:
SHEET OF
VULCAN (2080,L0061)44MMCP.DRW | 14-JUL-82 07:29 | NEXT HIGHER ASSEMBLY: B-DD-L0061-0
FIRST USED ON OPTION/MODEL: 2080

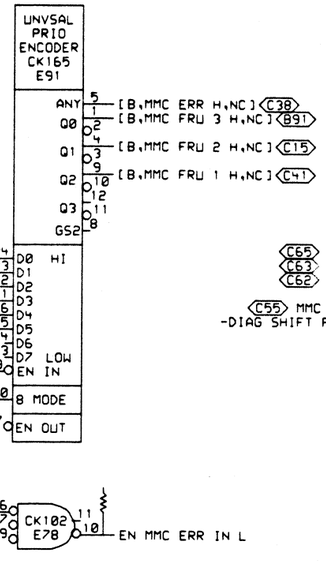
SIZE	CODE	NUMBER	REV.
D	CS	L0061-0-MMCP	*

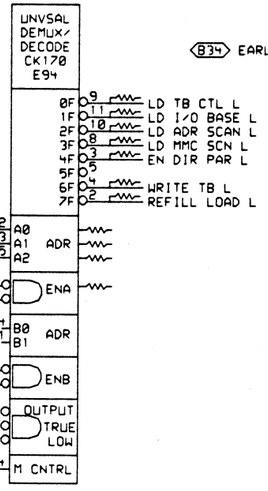


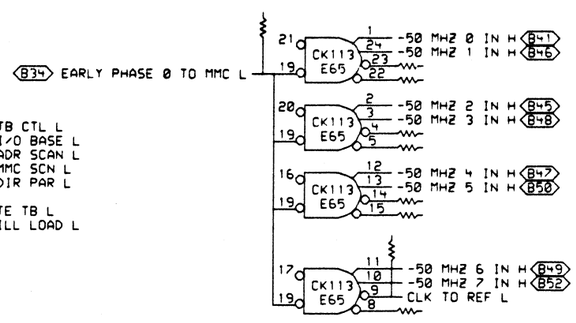


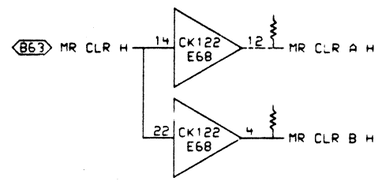






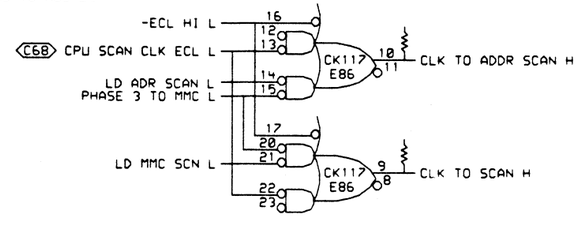






C77 [B.DIAG CLK TO MMC L.DIAG CLK TO MDPR L]

C51 [B.CPU SCAN CLK ECL L.DIAG CLK PAS TO MCC L]



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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. D. Toomey | DATE 28-DEC-82 | ENG. | DATE

CHK'D. | DATE | BOARD LOCATION: | SHEET 1 OF 1

VULCAN:<2080.L0061>58MMCR.DRW | 27-DEC-82 15:28 | NEXT HIGHER ASSEMBLY: B-DD-L00[illegible]-0

FIRST USED ON OPTION/MODEL: 2080

TITLE: CLK BUFFER

SIZE	CODE	NUMBER	REV.
D	CS	L0061-0-MMCR	*

-4.5V GND ECL

C1 10nF 50V — A2
C2 10nF 50V — A13
C3 10nF 50V — A16
C4 10nF 50V — A23
C5 10nF 50V — A33
C6 10nF 50V — A43
C7 10nF 50V — A51
C8 10nF 50V — A61
C9 10nF 50V — A72
C10 10nF 50V — A79
C11 10nF 50V — A82
C12 10nF 50V — A92
C13 10nF 50V — B1
C14 10nF 50V — B13
C15 10nF 50V — B16
C16 10nF 50V — B23
C17 10nF 50V — B33
C18 10nF 50V — B43
C19 10nF 50V — B51
C20 10nF 50V — B61

-4.5V GND ECL

C21 10nF 50V — C82
C22 10nF 50V — C93
C23 10nF 50V — C94
C24 10nF 50V
C25 10nF 50V
C26 10nF 50V
C27 10nF 50V
C28 10nF 50V
C29 10nF 50V
C30 10nF 50V
C31 10nF 50V
C32 10nF 50V
C33 10nF 50V
C34 10nF 50V
C35 10nF 50V
C36 10nF 50V
C37 10nF 50V
C38 10nF 50V
C39 10nF 50V
C40 10nF 50V

-4.5V GND ECL

C41 10nF 50V
C42 10nF 50V
C43 10nF 50V
C44 10nF 50V
C45 10nF 50V
C46 10nF 50V
C47 10nF 50V
C48 10nF 50V
C49 10nF 50V
C50 10nF 50V
C51 10nF 50V
C52 10nF 50V
C53 10nF 50V
C54 10nF 50V
C55 10nF 50V
C56 10nF 50V
C57 10nF 50V
C58 10nF 50V
C59 10nF 50V
C60 10nF 50V

-4.5V GND ECL

C61 10nF 50V — B72
C62 10nF 50V — B79
C63 10nF 50V — B90
C64 10nF 50V — B93
C65 10nF 50V — B94
C66 10nF 50V — C1
C67 10nF 50V — C13
C68 10nF 50V — C16
C69 10nF 50V — C23
C70 10nF 50V — C33
C71 10nF 50V — C43
C72 10nF 50V — C48
C73 10nF 50V — C61
C74 10nF 50V — C72
C75 10nF 50V — C79
C76 10nF 50V
C77 10nF 50V
C190 1500µF 16V
C195 1500µF 16V

-2.0V GND ECL

C191 2200µF 5V
C192 2200µF 6V
C193 2200µF 6V
C194 2200µF 6V — [B.GND] — [B.GND CLK]

B53 — GND CLK
B55 — GND CLK
B57 — GND CLK
B59 — GND CLK

-4.5V -2.0V

BUSBAR W1

1	14
2	15
3	16
4	17
5	18
6	19
7	20
8	21
9	22
10	23
11	24
12	25
13	26

231 TERMS
297 TERMS

GND X PWR
G10
G11
G12
G13

GND X PWR
G20
G21
G22
G23

GND X PWR
P10
P11
P12
P13
P14
P15

GND X PWR
P20
P21
P22
P23
P24
P25

I10 — [D.IMP1.NC]
I11 — [D.IMP1.NC]
I20 — [D.IMP2.NC]
I21 — [D.IMP2.NC]

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REVISIONS		
CHK	CHANGE NO.	REV

digital

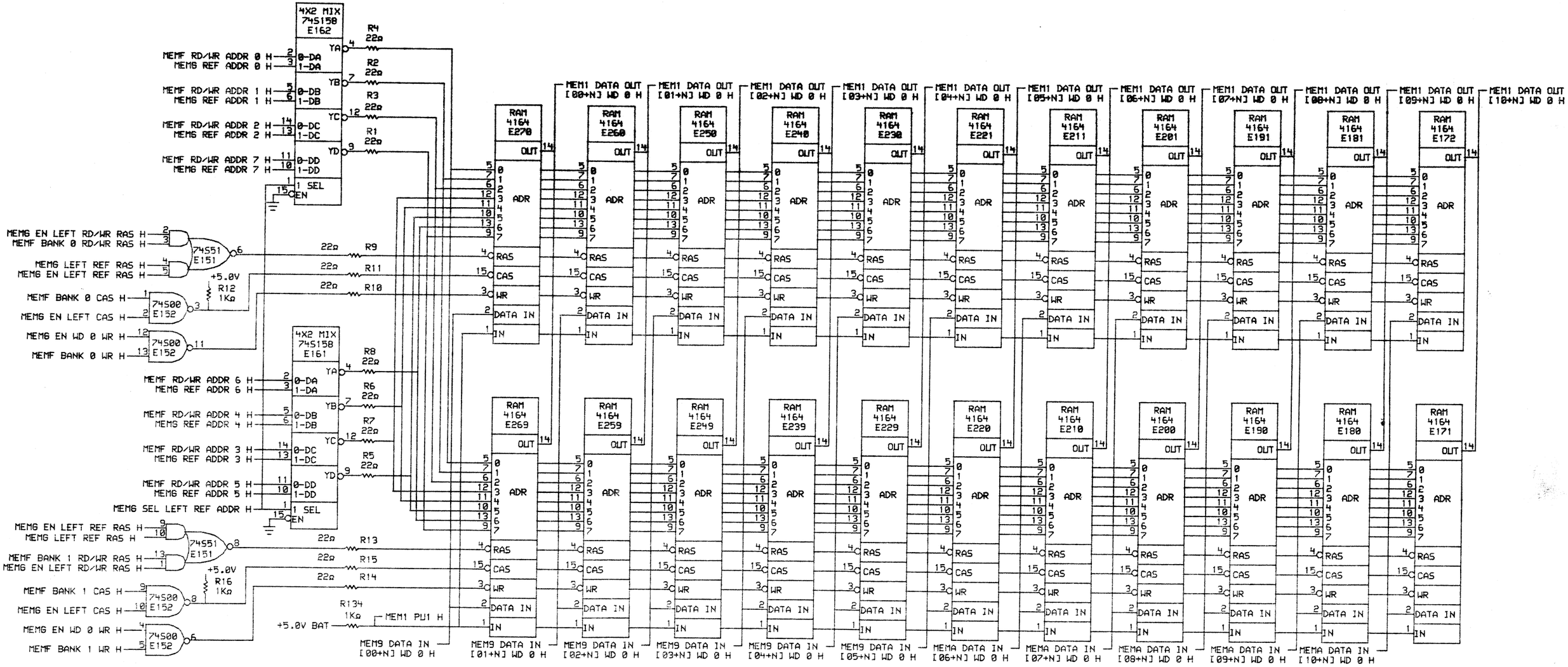
DRN. D. Toomey — DATE 10-FEB-83 — ENG. — DATE
CHK 'D. — DATE — BOARD LOCATION:
SHEET 1 OF 1

VULCAN:<2080.L0061>61MMCS.DRW | 09-FEB-83 18:04 | NEXT HIGHER ASSEMBLY: B-DD-L0061-0
FIRST USED ON OPTION/MODEL: 2080

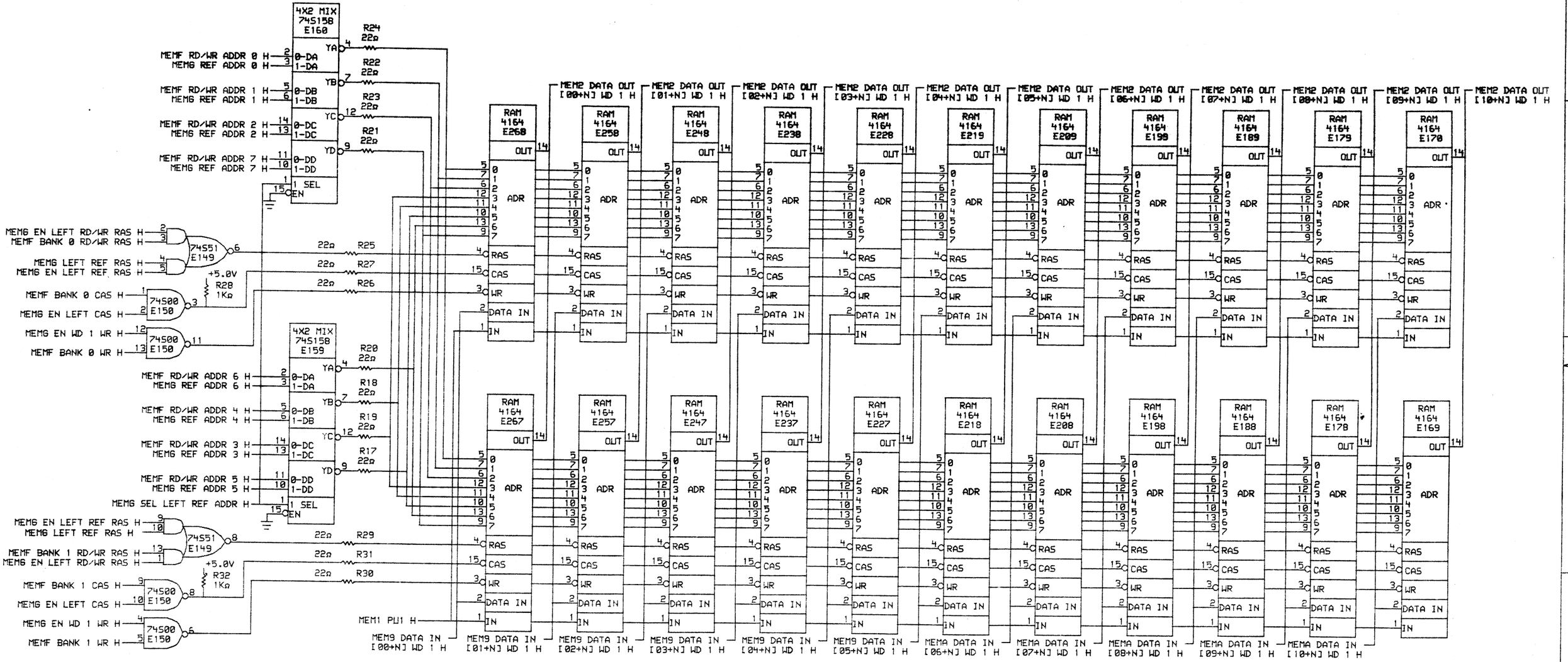
TITLE: MMC POWER AND GROUND

SIZE	CODE	NUMBER	REV.
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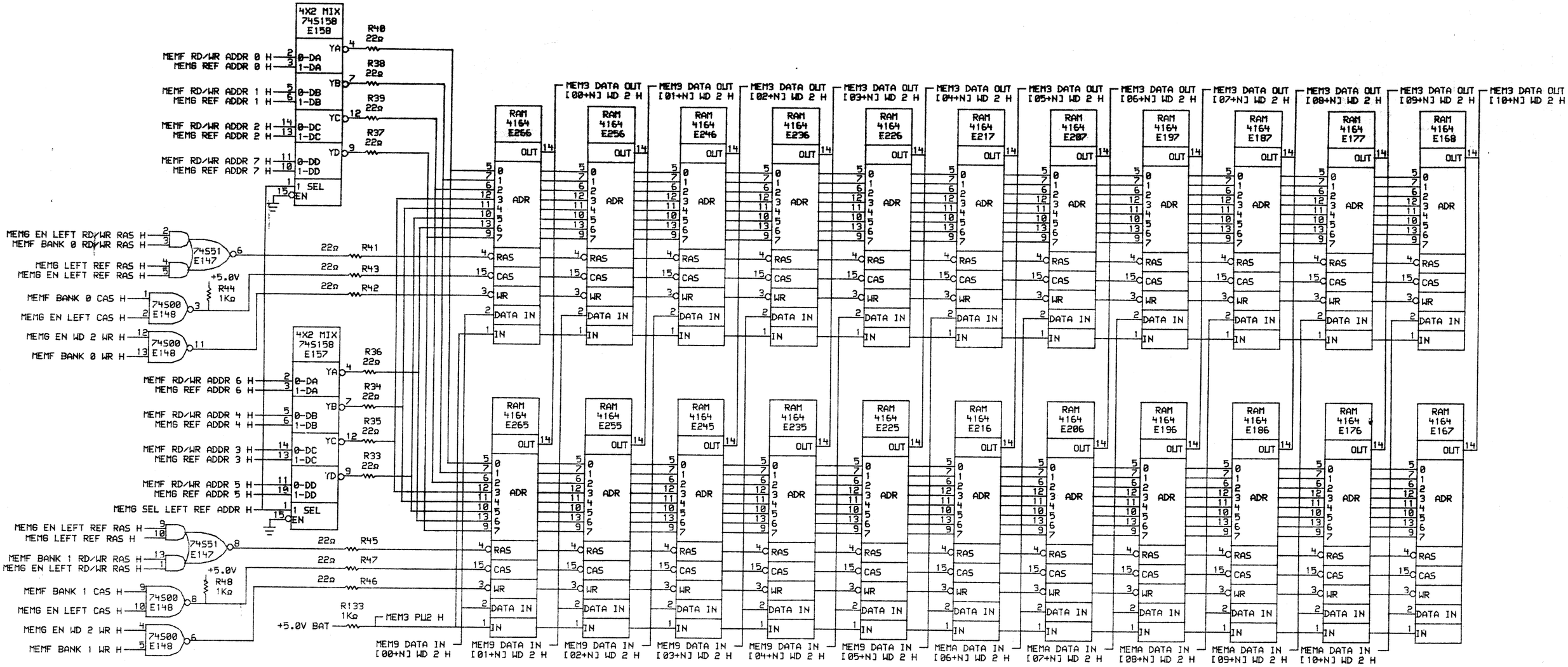
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VULCAN:<2080.L0030>TARR1
VULCAN:<2080.L0030>TARR2
VULCAN:<2080.L0030>TARR3
VULCAN:<2080.L0030>TARR4
VULCAN:<2080.L0030>TARR5
VULCAN:<2080.L0030>TARR6
VULCAN:<2080.L0030>TARR7
VULCAN:<2080.L0030>TARR8
VULCAN:<2080.L0030>TARR9
VULCAN:<2080.L0030>TARRA
VULCAN:<2080.L0030>TARRB
VULCAN:<2080.L0030>TARRC
VULCAN:<2080.L0030>TARRD
VULCAN:<2080.L0030>TARRE
VULCAN:<2080.L0030>TARRF
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VULCAN:<2080.L0030>TARRI
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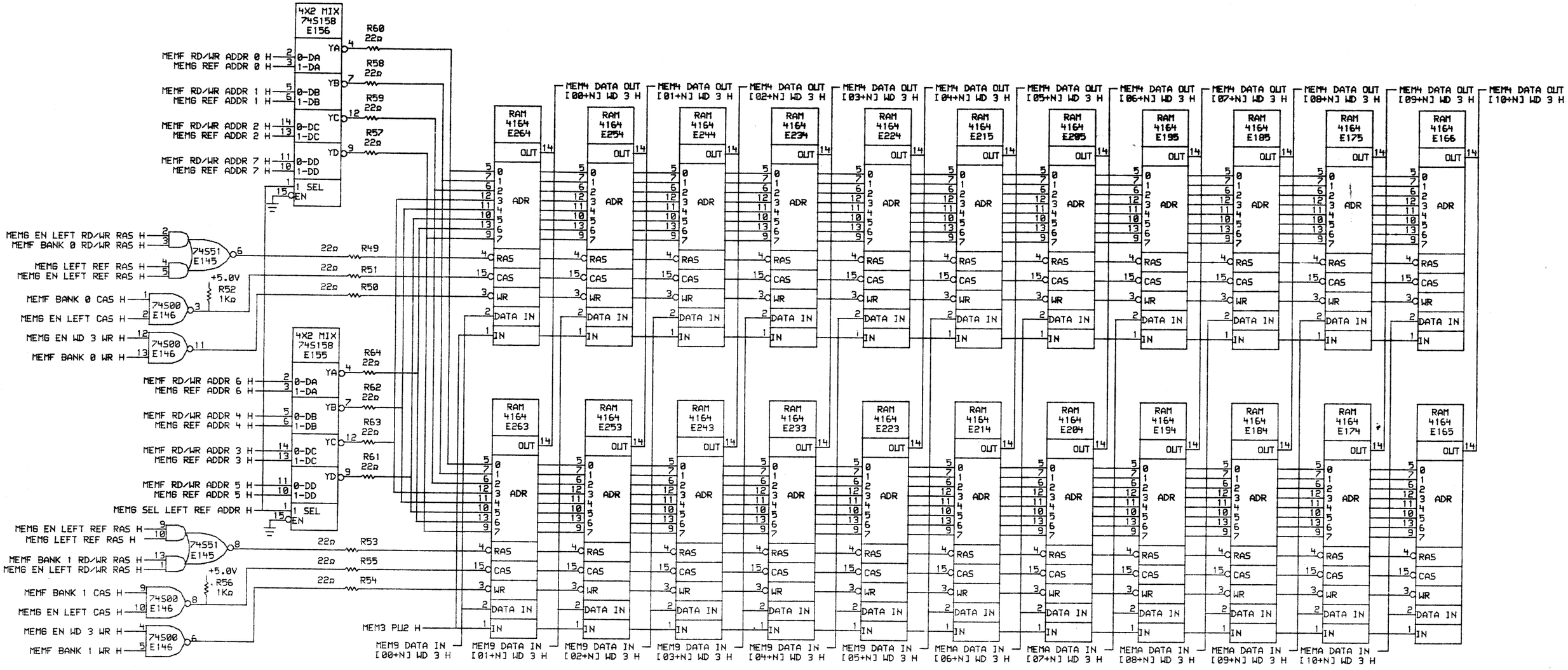
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	CHK'D.	DATE	BOARD LOCATION: SHEET OF		
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FIRST USED ON OPTION/MODEL: 2080			B-DD-L0030-0		



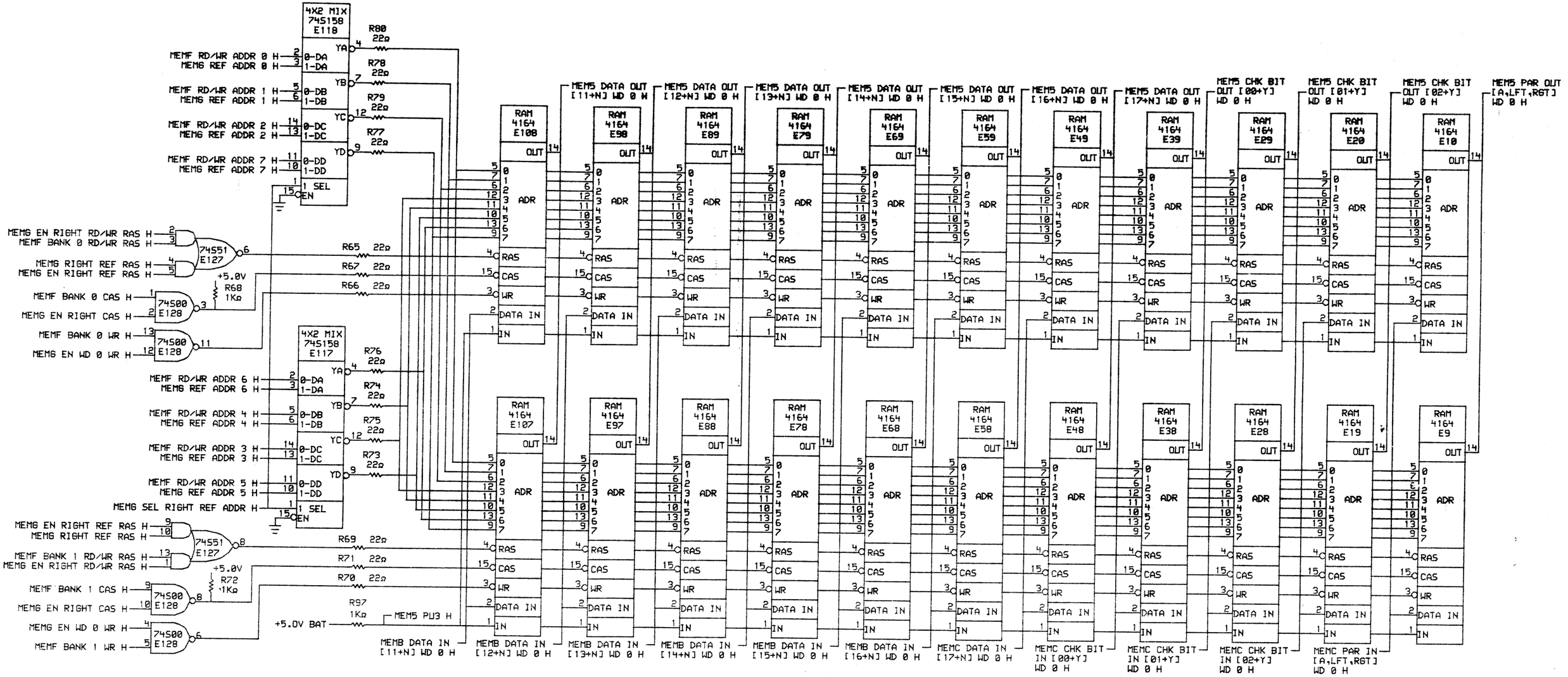
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	CHK'D.	DATE	BOARD LOCATION: SHEET OF		
VULCAN:<2080.L0030>TARR2.DRW 29-DEC-80 09:56			NEXT HIGHER ASSEMBLY:	SIZE D	CODE CS NUMBER L0030-0-MEM2 REV.
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0030-0		



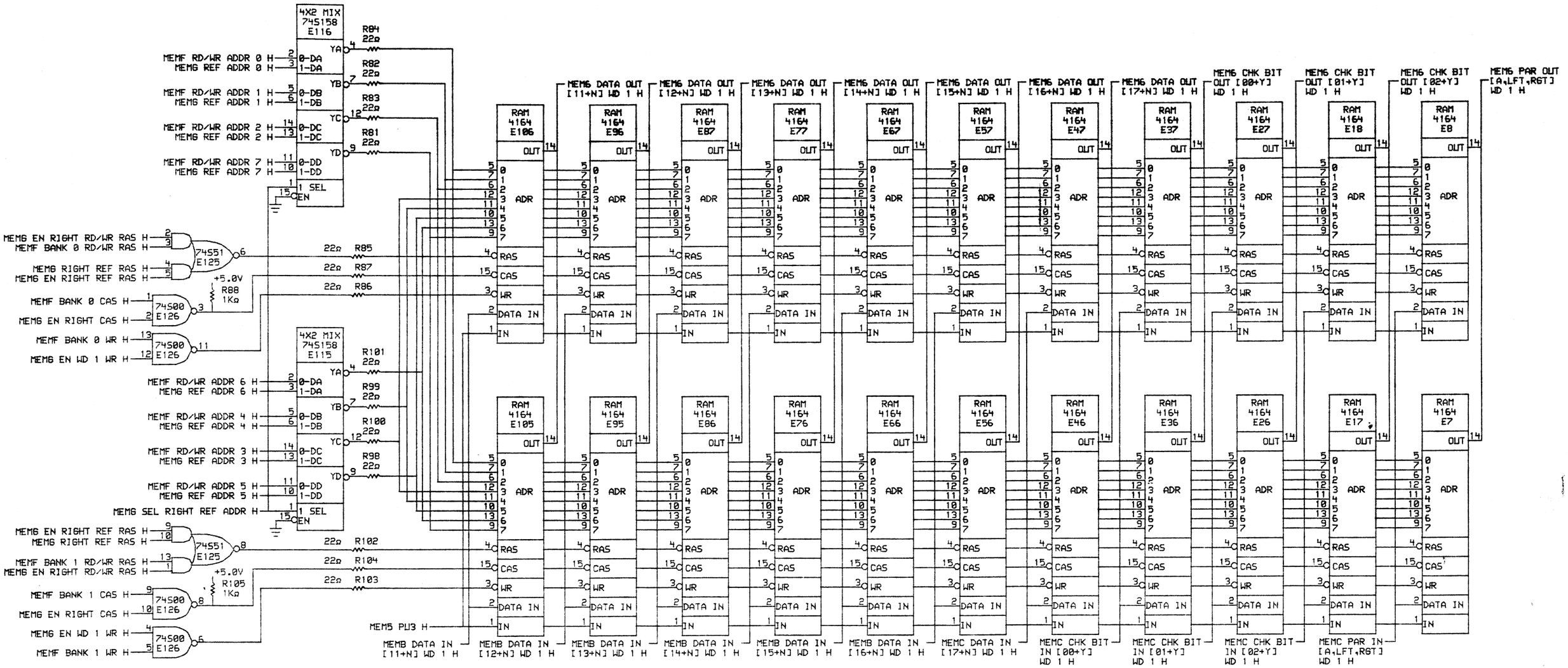
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	CHK'D.	DATE	BOARD LOCATION: SHEET OF		
VILCAN:<2080.L0030>TARR3.DRW 29-DEC-80 10:01			NEXT HIGHER ASSEMBLY:	SIZE D	CODE CS NUMBER L0030-0-MEM3 REV.
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0030-0		



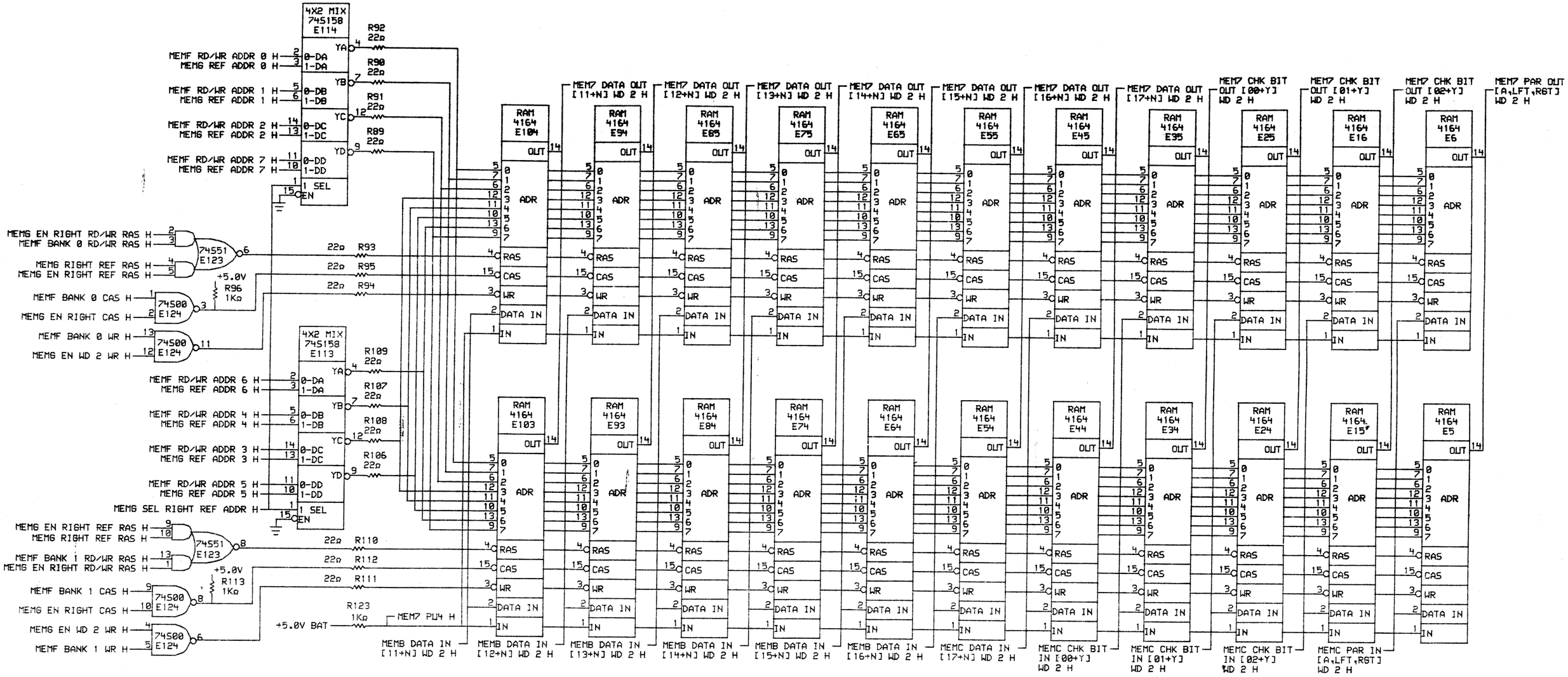
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	CHK'D.	DATE	BOARD LOCATION: SHEET OF		
VULCAN:<2080.L0030>TARR4.DRW 29-DEC-80 10:06			NEXT HIGHER ASSEMBLY:	SIZE D	CODE CS
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0030-0	NUMBER L0030-0-MEM4	REV.



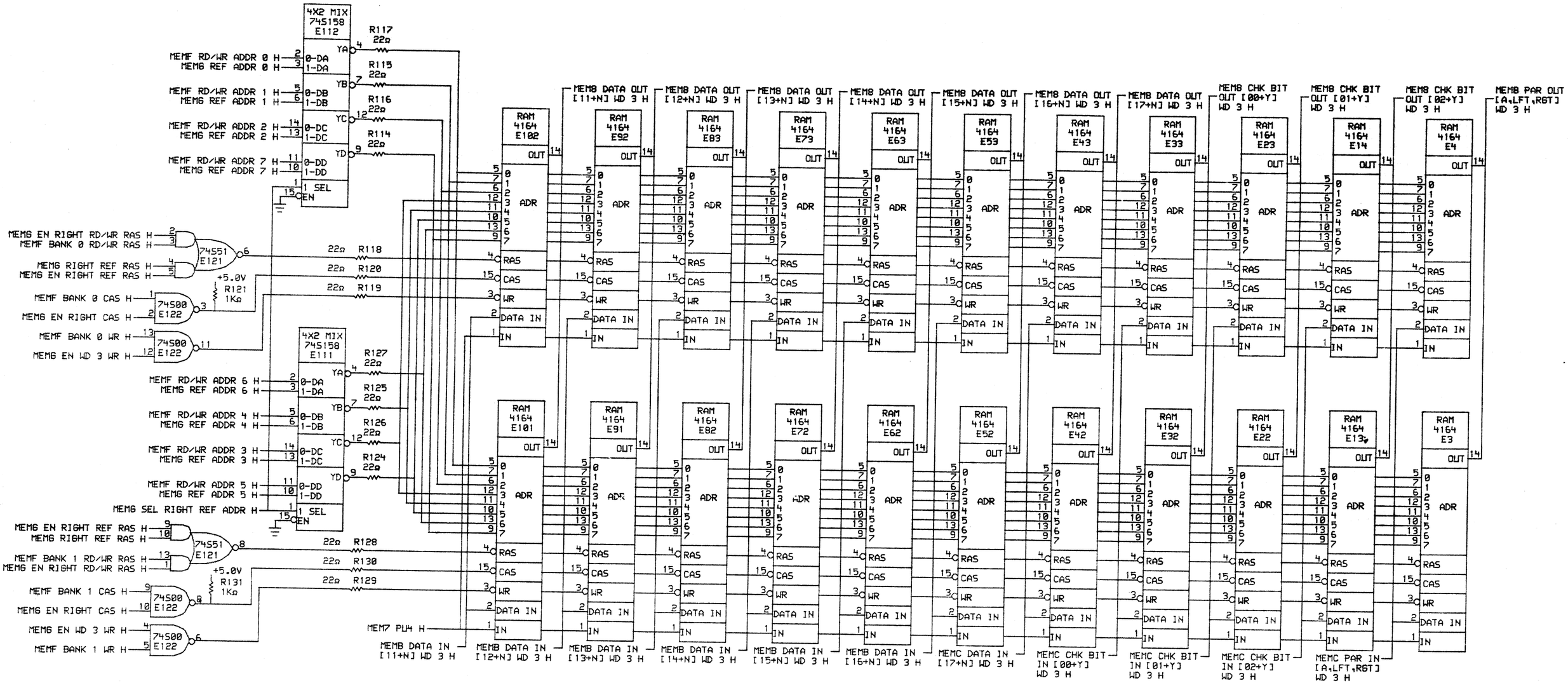
	DRN.	DATE 13-JAN-82	ENG.	DATE	TITLE: DATA 11-PAR WORD 0
	CHK'D.	DATE	BOARD LOCATION: SHEET OF		
VULCAN:<2080.L0030>TARR5.DRW 29-DEC-80 10:26			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0030-0-MEM5 / REV.
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0030-0		



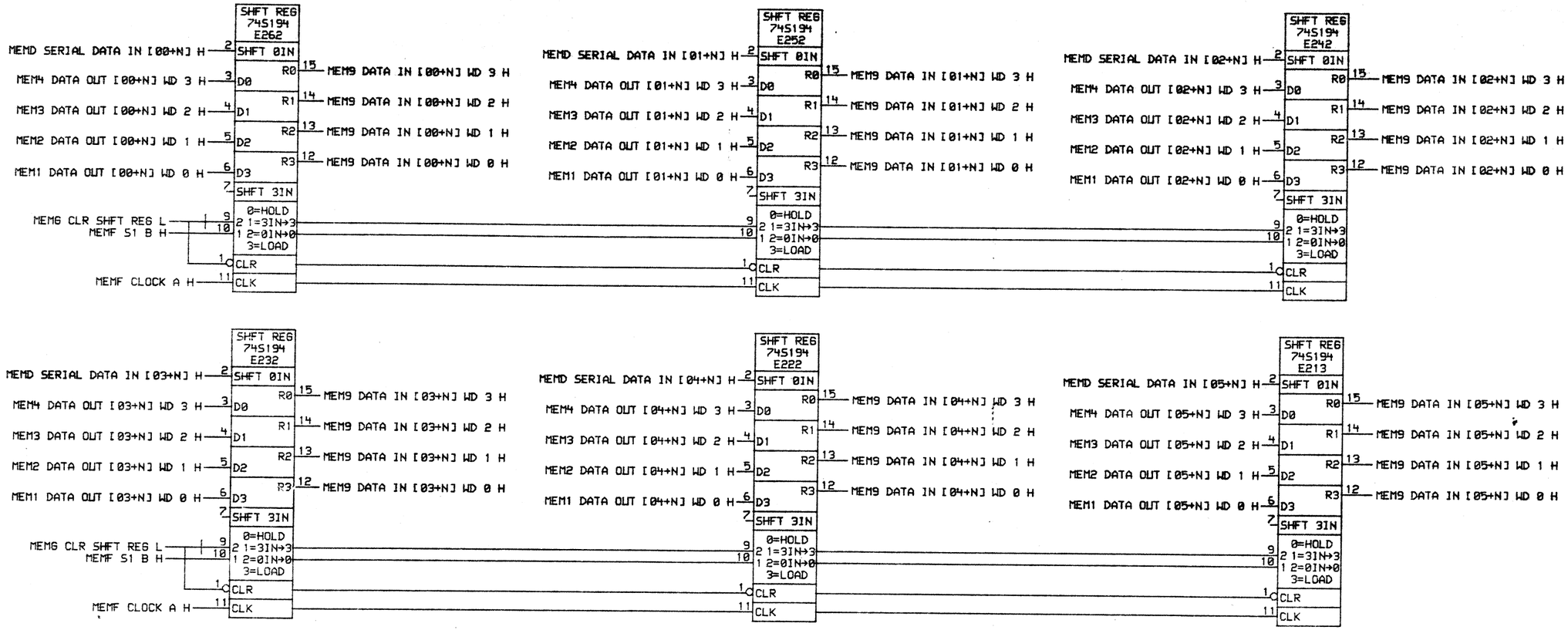
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	CHK'D.	DATE	BOARD LOCATION: SHEET OF			
VULCAN:<2080.L0030>TARR6.DRW 29-DEC-80 10:32			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS	NUMBER L0030-0-MEM6 / REV.
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0030-0			



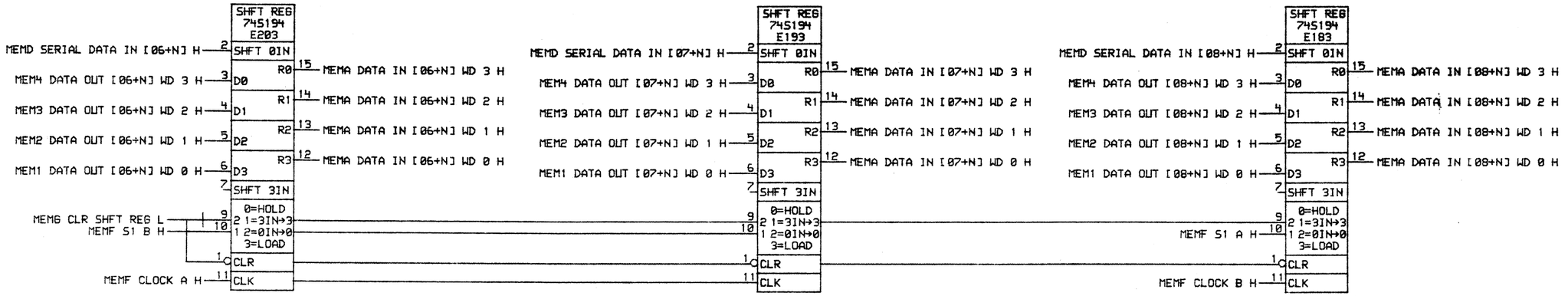
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	CHK'D.	DATE	BOARD LOCATION: SHEET OF				
VLI CAM:<2080.L0030>TARM7.DRW 29-DEC-80 10:34			NEXT HIGHER ASSEMBLY:		SIZE D	CODE CS	NUMBER L0030-0-MEM7	REV.
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0030-0					



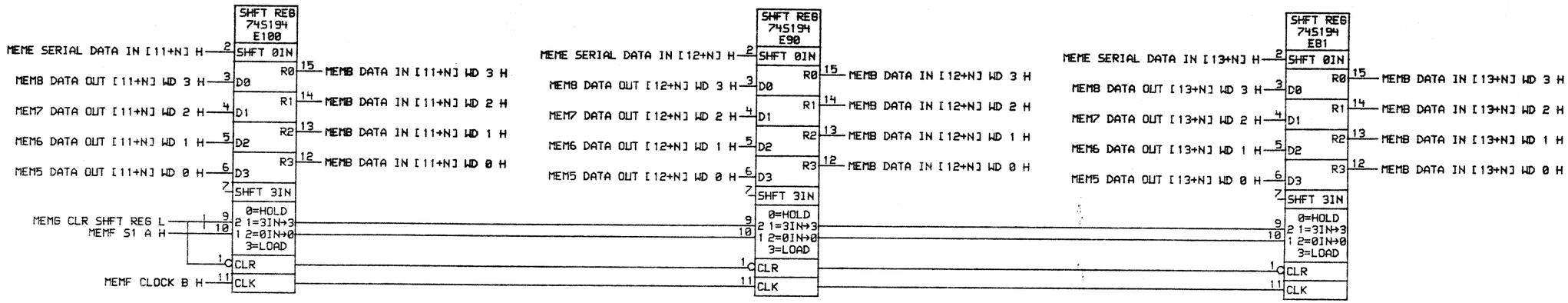
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	CHK'D.	13-JAN-82 DATE			BOARD LOCATION: SHEET OF			
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FIRST USED ON OPTION/MODEL: 2080			B-DD-L0030-0					



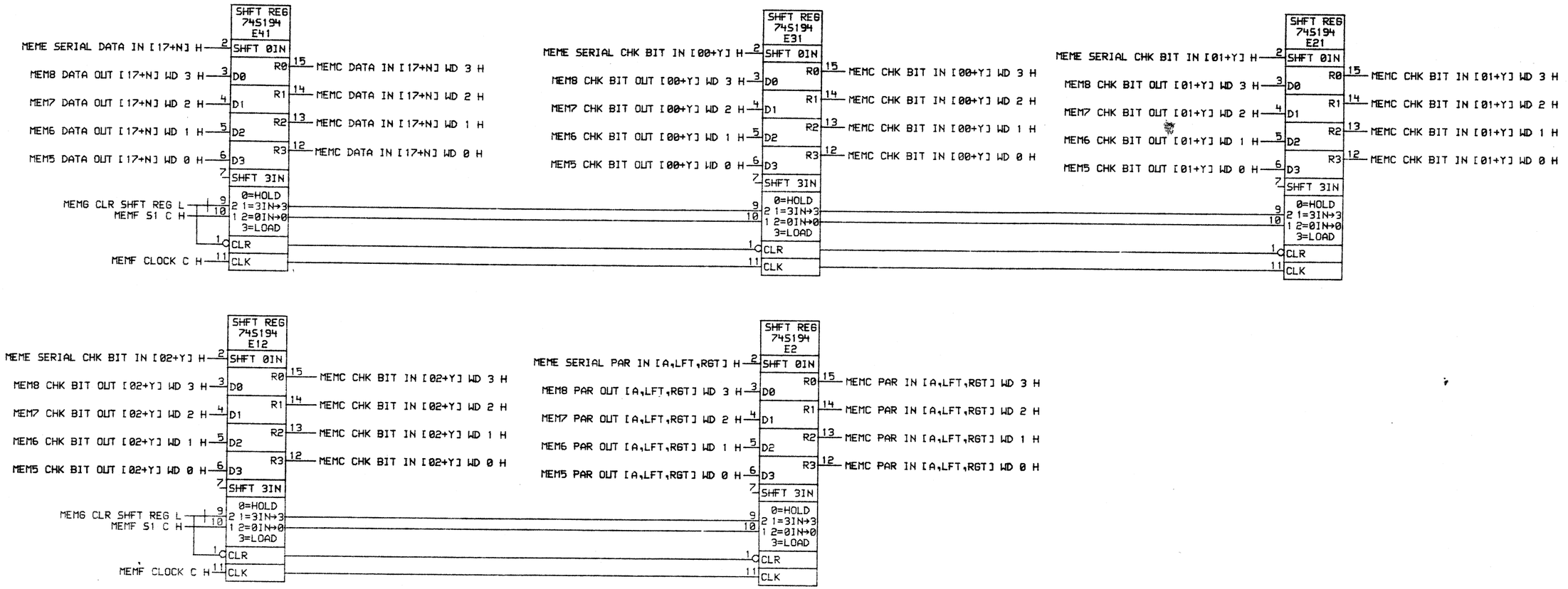
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	CHK'D.	DATE	BOARD LOCATION:		
			SHEET OF		
VULCAN: <2080.L0030>TARR9.DRW 24-AUG-80 09:12			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0030-0-MEM9 / REV.
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0030-0		



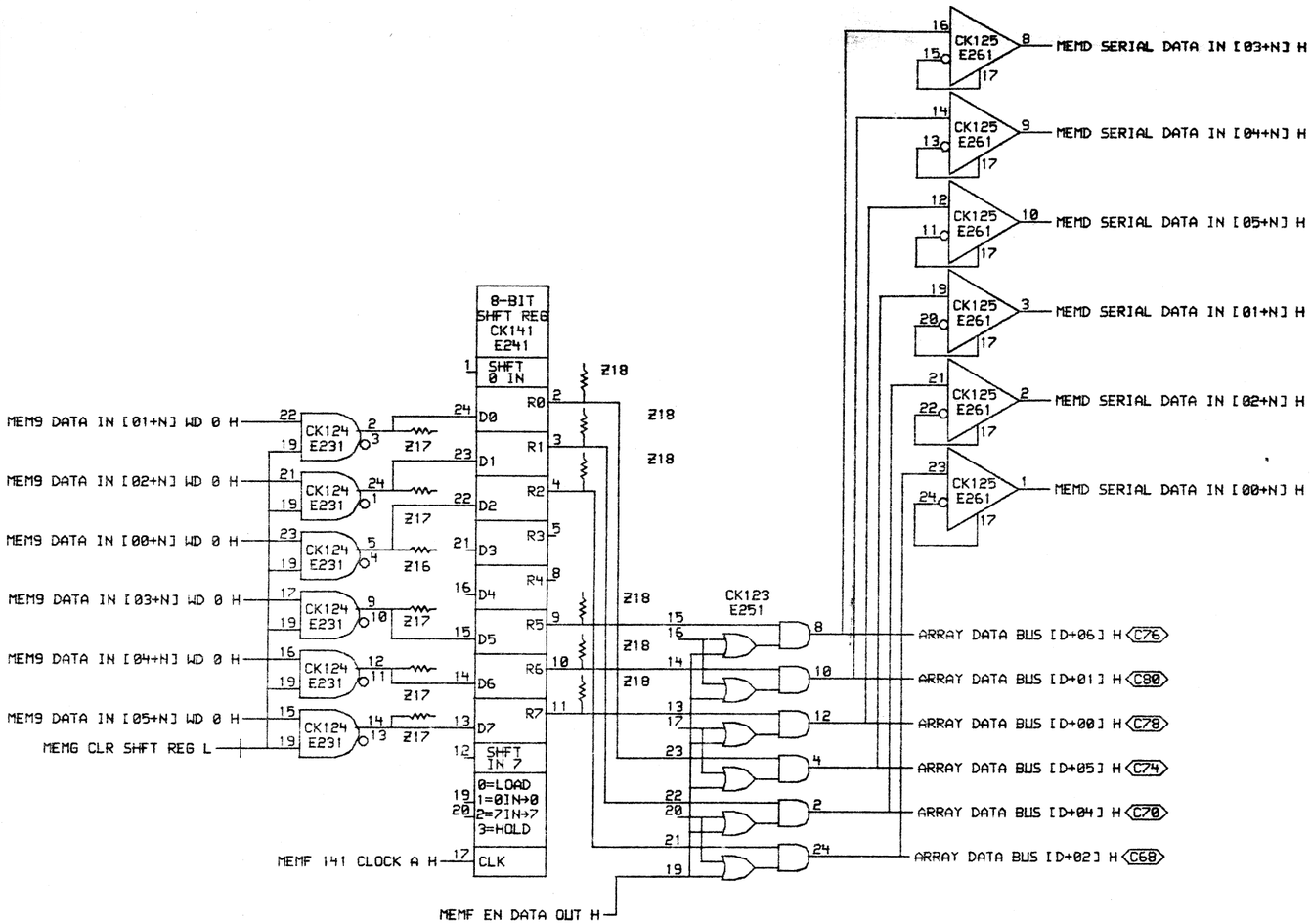
	DRN.	DATE 13-JAN-82	ENG.	DATE	TITLE: DATA REG 6-10
	CHK'D.	DATE	BOARD LOCATION:		
			SHEET OF		
VULCAN:<2080.L0030>TAPRA.DRW 24-AUG-80 09:13			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0030-0-MEMA / REV.
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0030-0		

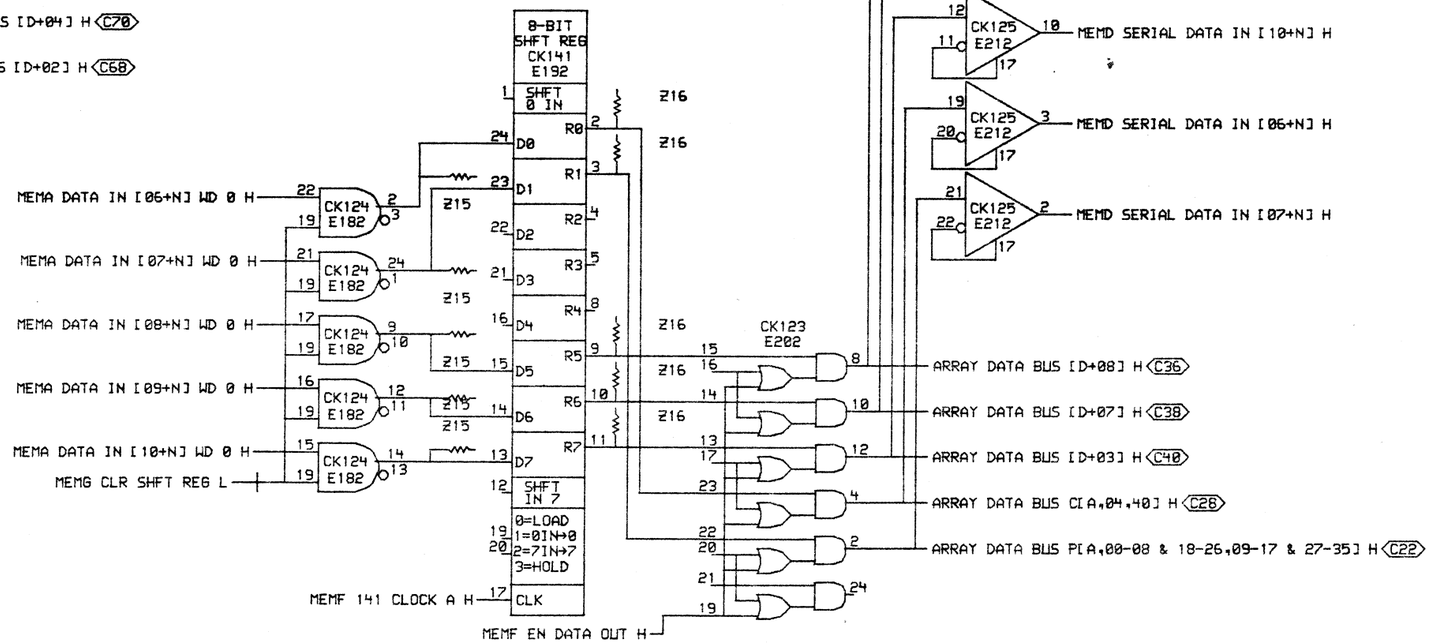


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FIRST USED ON OPTION/MODEL: 2080			B-DD-L0030-0					

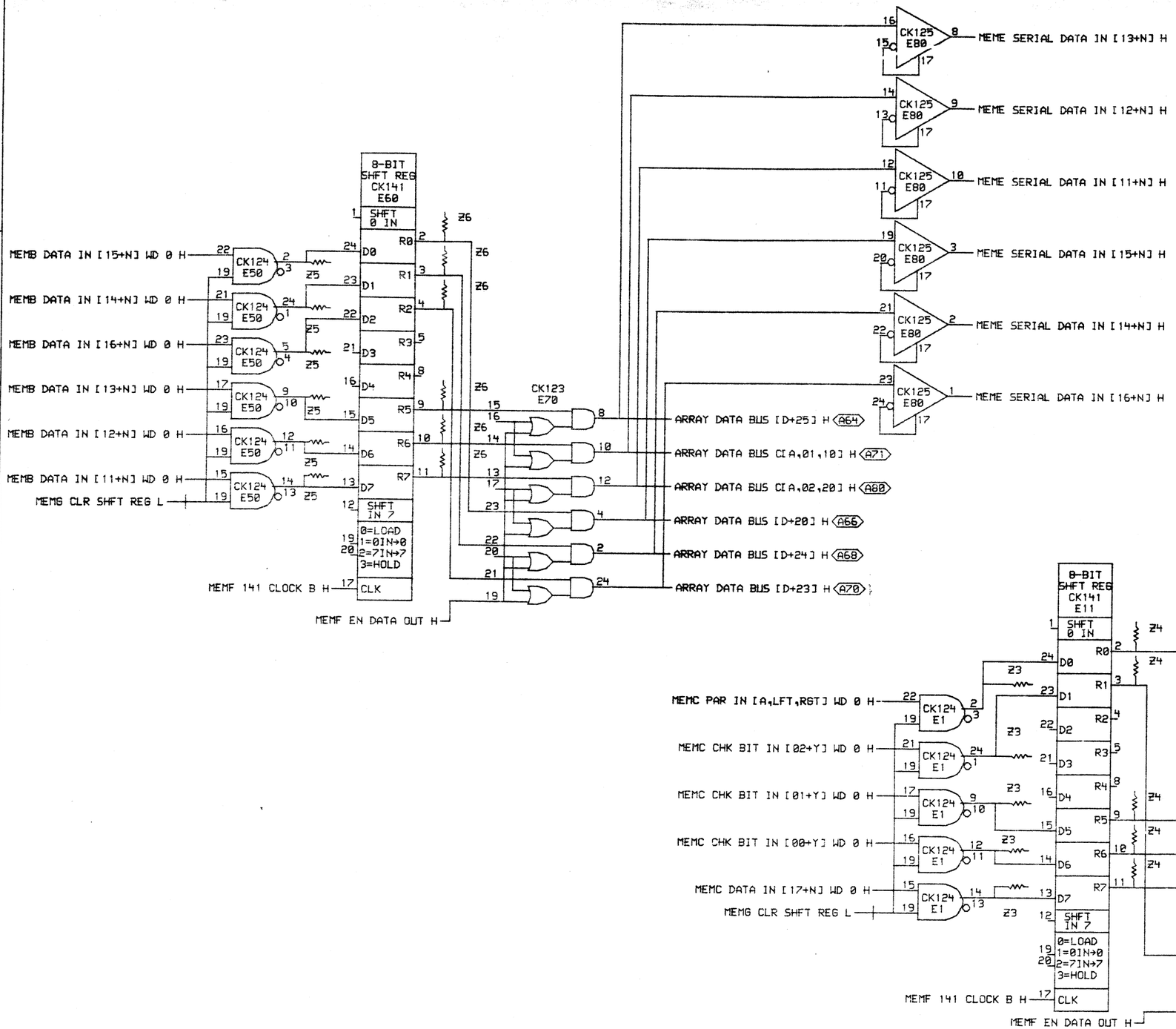


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	CHK'D.	DATE	BOARD LOCATION:		
			SHEET OF		
VULCAN:<2080.L0030>TARRC.DRW 24-AUG-80 09:15			NEXT HIGHER ASSEMBLY:		SIZE D / CODE CS / NUMBER L0030-0-MEMC / REV.
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0030-0		

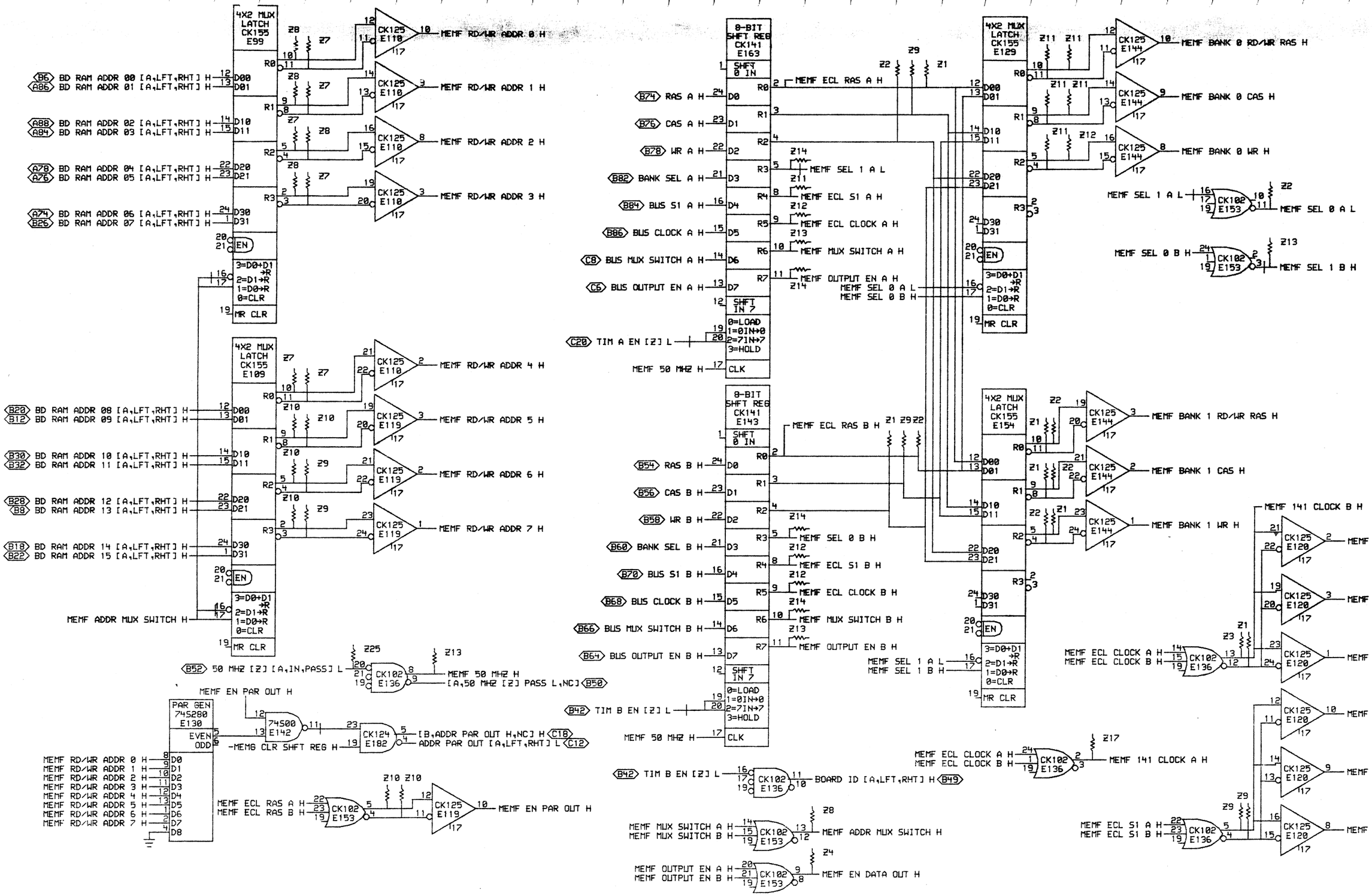




	DRN.	DATE 13-JAN-82	ENG.	DATE	TITLE: BUS 0-10
	CHK'D.	DATE	BOARD LOCATION: SHEET OF		
VULCAN:<2080.L0030>TARRD.DRW 29-DEC-80 10:50			NEXT HIGHER ASSEMBLY:	SIZE D	CODE CS
FIRST USED ON OPTION/MODEL: 2080			B-DD-L0030-0	NUMBER L0030-0-MEMD	REV.



	DRN.	DATE 19-JAN-82	ENG.	DATE	TITLE: BUS 11-17 CHK 0-2,PAR
	CHK'D.	DATE	BOARD LOCATION:		
VULCAN:<2000.L0030>TARRE.DRW 29-DEC-80 10:56			SHEET OF		
FIRST USED ON OPTION/MODEL: 2080			NEXT HIGHER ASSEMBLY: B-DD-L0030-0	SIZE D	CODE CS NUMBER L0030-0-MEME REV.



	DRN.	DATE	ENG.	DATE	TITLE: ECL CONTROL LOGIC		
	CHK'D.	13-JAN-82 DATE	BOARD LOCATION: SHEET OF				
VULCAN:<2000.L0030>TARRF.DRW 29-DEC-80 11:21		NEXT HIGHER ASSEMBLY:			SIZE D	CODE CS	NUMBER L0030-0-MEMF	REV.
FIRST USED ON OPTION/MODEL:								

+5.0V BAT
R122
1KΩ
MEMG CLR SHFT REG L

R153
2.2KΩ
MEMG BATT MODE H

IB,MEMG CLR SHFT REG L,NCJ A18
IB,MEMG CLR SHFT REG L,NCJ C84

+5.0V BAT
D1
1N746A
3.3V5%
.4W
R151
68Ω
R145
100Ω

+5.0V BAT
R148
1KΩ
R146
47Ω
R147
47Ω

+5.0V BAT
R149
1KΩ
MEMG EN LEFT CAS H
B34 SYS REF PRES H
CK125
E110
74L500
E135
B40 BAD PWR L

+5.0V BAT
R150
1KΩ
MEMG EN RIGHT CAS H

+5.0V BAT
R137
750Ω
R135
390Ω
D2
D672
C273
.1μF
50V
R136
1.2KΩ
MEMG TTL SYS REF L
74500
E142
74504
E141

+5.0V BAT
R152
1KΩ
C279
10NF
50V
R132
1KΩ
74574
E131
+V1

MEMG SYS MODE L
MEMG SYS MODE H
MEMG BATT MODE H
MEMG BATT MODE L

ECL SYS REF H B38
CK125
E119
74504
E134
MEMG TTL SYS REF H
MEMG TTL SYS REF L
74500
E142
MEMG SEL RIGHT REF ADDR H
MEMG SEL LEFT REF ADDR H

MEMG SYS MODE L
MEMG EN LEFT RD/WR RAS H
MEMG EN RIGHT RD/WR RAS H
MEMG EN LEFT CAS H
MEMG EN RIGHT CAS H

MEMG EN RIGHT REF RAS H
MEMG EN LEFT REF RAS H

B36 WR WORD EN [A,LFT,RHT] H
CK125
E119
SHFT REG
74S194
E138
SHFT 0IN
D0
D1
D2
D3
SHFT 3IN
0=HOLD
1 1=3IN→3
2 1=0IN→0
3=LOAD
CLR
CLK
R0
R1
R2
R3
MEMG EN WD 3 WR H
MEMG EN WD 2 WR H
MEMG EN WD 1 WR H
MEMG EN WD 0 WR H
+V1
MEMF S1 B H
MEMG BATT MODE L
MEMF CLOCK A H

R144
1KΩ
+5.0V BAT
+V1

+5.0V BAT
R141
62KΩ
C277
10nF
50V
GND
R142
2.7KΩ
C276
180pF
100V
TIMER
555
E140
OUTPUT
TRIGGER
RESET
CONTROL
THRSHLD
DISCHG
VCC

R143
100Ω
R139
120Ω
C278
1NF
100V

R138
5.1KΩ
C274
150PF
100V
74LS133
E133

MEMG RIGHT REF RAS H
MEMG LEFT REF RAS H

MEMG RIGHT REF RAS H
MEMG BATT MODE H
MEMG TTL SYS REF H
MEMG SYS MODE H
74LS00
E135

4-BIT
BIN CTR
74LS393
E132
00R
01R
02R
03R
0 CLK
0 CLR
10R
11R
12R
13R
1 CLK
1 CLR

MEMG REF ADDR 0 H
MEMG REF ADDR 1 H
MEMG REF ADDR 2 H
MEMG REF ADDR 3 H
MEMG REF ADDR 4 H
MEMG REF ADDR 5 H
MEMG REF ADDR 6 H
MEMG REF ADDR 7 H
74S240
E139

	DRN.	DATE 13-JAN-82	ENG.	DATE	TITLE: TTL CONTROL LOGIC
	CHK'D.	DATE	BOARD LOCATION: SHEET OF		
VULCAN:<2000.L0030>TARRS.DRW 27-AUG-80 05:11			NEXT HIGHER ASSEMBLY:	SIZE D	CODE CS NUMBER L0030-0-MEMG REV.
FIRST USED ON OPTION/MODEL:					

A21 [A,[C,NC,NC,ARRAY DATA BUS 21 H,NC],[C,NC,NC,NC,ARRAY DATA BUS 30 H]] — Z19

A29 [A,[C,NC,NC,ARRAY DATA BUS 19 H,NC],[C,NC,NC,NC,ARRAY DATA BUS 28 H]] — Z19

A37 [A,[C,NC,NC,ARRAY DATA BUS 22 H,NC],[C,NC,NC,NC,ARRAY DATA BUS 31 H]] — Z19

A39 [A,[C,NC,NC,ARRAY DATA BUS 26 H,NC],[C,NC,NC,NC,ARRAY DATA BUS 35 H]] — Z19

A41 [A,[C,NC,NC,ARRAY DATA BUS 18 H,NC],[C,NC,NC,NC,ARRAY DATA BUS 27 H]] — Z19

A45 [A,[C,NC,NC,ARRAY DATA BUS 25 H,NC],[C,NC,NC,NC,ARRAY DATA BUS 34 H]] — Z20

A49 [A,[C,NC,NC,ARRAY DATA BUS 24 H,NC],[C,NC,NC,NC,ARRAY DATA BUS 33 H]] — Z20

A53 [A,[C,NC,NC,ARRAY DATA BUS 20 H,NC],[C,NC,NC,NC,ARRAY DATA BUS 29 H]] — Z20

A55 [A,[C,NC,NC,ARRAY DATA BUS 23 H,NC],[C,NC,NC,NC,ARRAY DATA BUS 32 H]] — Z20

A59 [A,[C,NC,NC,ARRAY DATA BUS C01 H,NC],[C,NC,NC,NC,ARRAY DATA BUS C10 H]] — Z20

A63 [A,[C,NC,NC,ARRAY DATA BUS C02 H,NC],[C,NC,NC,NC,ARRAY DATA BUS C20 H]] — Z20

A73 [A,[C,NC,NC,BD RAM ADDR 06 LFT H,NC],
[C,NC,NC,BD RAM ADDR 06 RHT H]] — Z21

A75 [A,[C,NC,NC,BD RAM ADDR 05 LFT H,NC],
[C,NC,NC,BD RAM ADDR 05 RHT H]] — Z21

A77 [A,[C,NC,NC,BD RAM ADDR 04 LFT H,NC],
[C,NC,NC,BD RAM ADDR 04 RHT H]] — Z21

A83 [A,[C,NC,NC,BD RAM ADDR 03 LFT H,NC],
[C,NC,NC,BD RAM ADDR 03 RHT H]] — Z21

A85 [A,[C,NC,NC,BD RAM ADDR 01 LFT H,NC],
[C,NC,NC,BD RAM ADDR 01 RHT H]] — Z21

A87 [A,[C,NC,NC,BD RAM ADDR 02 LFT H,NC],
[C,NC,NC,BD RAM ADDR 02 RHT H]] — Z21

B5 [A,[C,NC,NC,BD RAM ADDR 00 LFT H,NC],
[C,NC,NC,BD RAM ADDR 00 RHT H]] — Z22

B7 [A,[C,NC,NC,BD RAM ADDR 13 LFT H,NC],
[C,NC,NC,BD RAM ADDR 13 RHT H]] — Z22

B11 [A,[C,NC,NC,BD RAM ADDR 09 LFT H,NC],
[C,NC,NC,BD RAM ADDR 09 RHT H]] — Z22

B17 [A,[C,NC,NC,BD RAM ADDR 14 LFT H,NC],
[C,NC,NC,BD RAM ADDR 14 RHT H]] — Z22

B19 [A,[C,NC,NC,BD RAM ADDR 08 LFT H,NC],
[C,NC,NC,BD RAM ADDR 08 RHT H]] — Z22

B21 [A,[C,NC,NC,BD RAM ADDR 15 LFT H,NC],
[C,NC,NC,BD RAM ADDR 15 RHT H]] — Z22

B25 [A,[C,NC,NC,BD RAM ADDR CTL,NC,ARRAY DATA BUS P00-08 & 18-26 H,NC],[C,NC,NC,ARRAY DATA BUS P09-17 & 27-35 H]] — Z23
[C,NC,NC,BD RAM ADDR 07 RHT H]]

B27 [A,[C,NC,NC,BD RAM ADDR 12 LFT H,NC],
[C,NC,NC,BD RAM ADDR 12 RHT H]] — Z23

B29 [A,[C,NC,NC,BD RAM ADDR 10 LFT H,NC],
[C,NC,NC,BD RAM ADDR 10 RHT H]] — Z23

B31 [A,[C,NC,NC,BD RAM ADDR 11 LFT H,NC],
[C,NC,NC,BD RAM ADDR 11 RHT H]] — Z23

B35 [C,NC,WR WORD EN LFT,WR WORD EN RHT] H — Z23

B37 [C,NC,NC,ECL SYS REF H] — Z23

B41 [A,NC,TIM B EN [Z] L] — Z19

B53 [C,NC,NC,RAS B H] — Z24

B55 [C,NC,NC,CAS B H] — Z25

B57 [C,NC,NC,WR B H] — Z24

B59 [C,NC,NC,BANK SEL B H] — Z25

B63 [C,NC,NC,BUS OUTPUT EN B H] — Z24

B65 [C,NC,NC,BUS MUX SWITCH B H] — Z25

B67 [C,NC,NC,BUS CLOCK B H] — Z24

B69 [C,NC,NC,BUS S1 B H] — Z24

B71 [C,NC,SYS REF PRES H,NC] — Z25

B73 [C,NC,NC,RAS A H] — Z24

B75 [C,NC,NC,CAS A H] — Z25

B77 [C,NC,NC,WR A H] — Z26

B81 [C,NC,NC,BANK SEL A H] — Z26

B83 [C,NC,NC,BUS S1 A H] — Z26

B85 [C,NC,NC,BUS CLOCK A H] — Z26

C5 [C,NC,NC,BUS OUTPUT EN A H] — Z26

C7 [C,NC,NC,BUS MUX SWITCH A H] — Z26

C19 [A,NC,TIM A EN [Z] L] — Z27

B33 [A,[C,NC,NC,ARRAY DATA BUS 03 H,NC],[C,NC,NC,ARRAY DATA BUS 12 H]] — Z27

B39 [A,[C,NC,NC,ARRAY DATA BUS 07 H,NC],[C,NC,NC,ARRAY DATA BUS 16 H]] — Z27

B43 [A,[C,NC,NC,ARRAY DATA BUS 08 H,NC],[C,NC,NC,ARRAY DATA BUS 17 H]] — Z27

B61 [A,[C,NC,NC,ARRAY DATA BUS P00-08 & 18-26 H,NC],[C,NC,NC,ARRAY DATA BUS P09-17 & 27-35 H]] — Z27

B79 [A,[C,NC,NC,ARRAY DATA BUS C04 H,NC],[C,NC,NC,ARRAY DATA BUS C40 H]] — Z27

B45 [A,[C,NC,NC,ARRAY DATA BUS 00 H,NC],[C,NC,NC,ARRAY DATA BUS 09 H]] — Z28

B47 [A,[C,NC,NC,ARRAY DATA BUS 01 H,NC],[C,NC,NC,ARRAY DATA BUS 10 H]] — Z28

B51 [A,[C,NC,NC,ARRAY DATA BUS 06 H,NC],[C,NC,NC,ARRAY DATA BUS 15 H]] — Z28

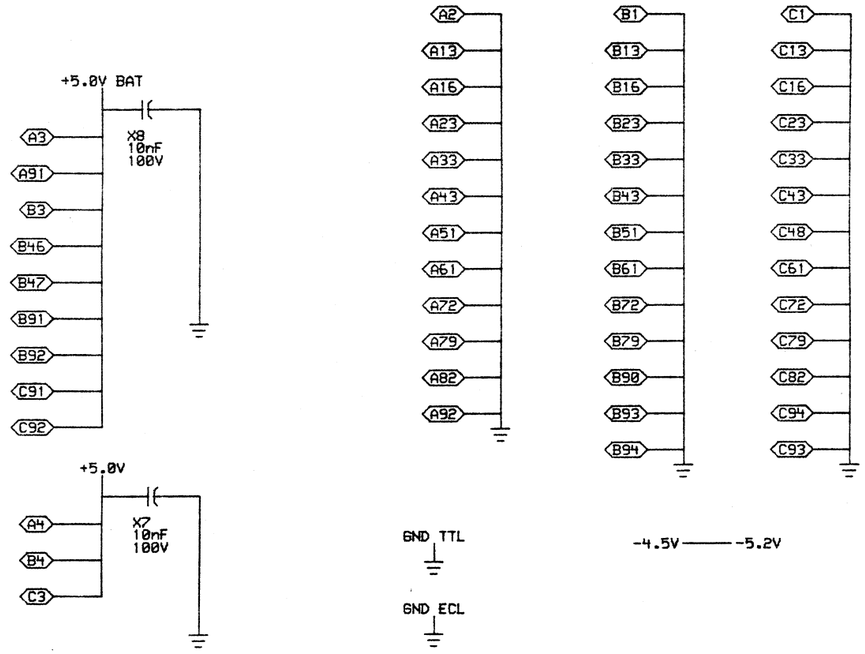
B61 [A,[C,NC,NC,ARRAY DATA BUS 04 H,NC],[C,NC,NC,ARRAY DATA BUS 13 H]] — Z28

B49 [A,[C,NC,NC,ARRAY DATA BUS 02 H,NC],[C,NC,NC,ARRAY DATA BUS 11 H]] — Z28

B43 [A,[C,NC,NC,ARRAY DATA BUS 05 H,NC],[C,NC,NC,ARRAY DATA BUS 14 H]] — Z28

C11 [C,NC,ADDR PAR OUT LFT L,
ADDR PAR OUT RHT L] — Z15

	DRN.	DATE 13-JAN-82	ENG.	DATE	TITLE: TERMINATORS
	CHK'D.	DATE	BOARD LOCATION: SHEET OF		
VULCAN:<2000.L0030>TARRH.DRW 29-DEC-80 09:38			NEXT HIGHER ASSEMBLY:	SIZE D	CODE CS NUMBER L0030-0-MEMH REV.
FIRST USED ON OPTION/MODEL:					



	DRN.	DATE 13-JAN-82	ENG.	DATE	TITLE: POWER GND,CAPS
	CHK'D.	DATE	BOARD LOCATION: SHEET OF		
VULCAN:<2000.L0030>TARRI.DRW 24-AUG-80 11:06	NEXT HIGHER ASSEMBLY:				SIZE D / CODE CS / NUMBER L0030-0-MEMI / REV.
FIRST USED ON OPTION/MODEL:					