

FIELD MAINTENANCE PRINT SET
TABLE OF CONTENTS

UNIT VARIATIONS
COVERED BY THIS
PRINT SET

MCA25
MCA25-AA
MCA25-AB

MCA25
FIELD MAINTENANCE
PRINT SET

DIGITAL EQUIPMENT
CORPORATION

PRINT SET ORDER NO.
MP01910-01

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REVISIONS		
CHK	CHANGE NO.	REV

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digital	DRW. <i>J. J. Jones</i>	DATE 26-JUL-84	ENG. C. A. JENS	DATE 26-JUL-84	TITLE: MCA25 CACHE PAGER
	CHK'D. G. ALSTON	DATE 26-JUL-84	BOARD LOCATION: SHEET 1 OF 4	N/A	
DSK:MCATC.T2P[4,21]		26-JUL-84 15:41		NEXT HIGHER ASSEMBLY:	
FIRST USED ON OPTION/MODEL: MCA25		KL10-PW		SIZE D TC	CODE MCA25-0-1
				NUMBER	REV. A

D

C

B

A

D

C

B

A

DOCUMENT NUMBER	REV	DESCRIPTION
D-TC-MCA25-0-1	A	MCA25 FIELD MAINT. PRINT SET (TC)
D-MU-KL10-PW-CPU	A	MODULE UTILIZATION
D-SP-KL10-PW-2	A	OPTION/SERIAL NUMBER CHART

OPTION MODULES (C.S.)

DOCUMENT NUMBER	REV	DESCRIPTION
D-DD-M852-0	A	CACHE DIRECTORY
E-UA-M852-0-0	A	CACHE DIRECTORY
D-CS-M852-0-CHA1	A	CACHE DIRECTORY
D-CS-M852-0-CHA2	A	CACHE DIRECTORY
D-CS-M852-0-CHA3	A	CACHE DIRECTORY
D-CS-M852-0-RES	A	CACHE DIRECTORY
D-DD-M853-0	A	CACHE EXTENSION
D-UA-M853-0-0	A	CACHE EXTENSION
D-CS-M853-CHX1	A	CACHE EXTENSION
D-CS-M853-CHX2	A	CACHE EXTENSION
D-CS-M853-CHX3	A	CACHE EXTENSION
D-CS-M853-CHX4	A	CACHE EXTENSION
D-CS-M853-CHX5	A	CACHE EXTENSION
D-CS-M853-RES	A	CACHE EXTENSION
D-DD-M854-0	A	PHYSICAL MEM ADR
D-UA-M854-0-0	A	PHYSICAL MEM ADR
D-CS-M854-0-PMA1	A	PHYSICAL MEM ADR
D-CS-M854-0-PMA2	A	PHYSICAL MEM ADR
D-CS-M854-0-PMA3	A	PHYSICAL MEM ADR
D-CS-M854-0-PMA4	A	PHYSICAL MEM ADR
D-CS-M854-0-PMA5	A	PHYSICAL MEM ADR
D-CS-M854-0-PMA6	A	PHYSICAL MEM ADR
D-CS-M854-0-RES	A	PHYSICAL MEM ADR

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DR. J. J. J.	DATE	ENG.	DATE	TITLE:		
	CHK'D.	26-JUL-84	C. A. JENS	26-JUL-84	MCA25		
	G. ALSTON	26-JUL-84 15:41	BOARD LOCATION: N/A	2 OF 4	CACHE PAGER		
DSK:MCA25.T2P(4,21)		NEXT HIGHER ASSEMBLY:		SIZE	CODE	NUMBER	REV.
FIRST USED ON OPTION/MODEL: MCA25		KL10-PW		D	TC	MCA25-0-1	A

OPTION MODULES (C.S.) CONTINUED

DOCUMENT NUMBER	REV	DESCRIPTION
D-DD-M855-0	A	PAGING BOARD SHEET 1
E-LA-M855-0-0	A1	PAGING BOARD (MULTIWIRE)
D-CS-M855-0-PAG1	A1	PAGING BOARD
D-CS-M855-0-PAG2	A1	PAGING BOARD
D-CS-M855-0-PAG3	A1	PAGING BOARD
D-CS-M855-0-PAG4	A1	PAGING BOARD
D-CS-M855-0-PAG5	A1	PAGING BOARD
D-CS-M855-0-PAG6	A1	PAGING BOARD
D-CS-M855-0-PAG7	A1	PAGING BOARD
D-CS-M855-0-PAG8	A1	PAGING BOARD
D-CS-M855-0-PAG9	A1	PAGING BOARD
D-CS-M855-0-PAGA	A1	PAGING BOARD
D-CS-M855-0-PAGB	A1	PAGING BOARD
D-CS-M855-0-RES	A1	PAGING BOARD
D-DD-M855-0	A	PAGING BOARD SHEET 2
D-LA-M855-0-0	B	PAGING BOARD (ETCH)
D-CS-M855-0-PAG1	B	PAGING BOARD
D-CS-M855-0-PAG2	B	PAGING BOARD
D-CS-M855-0-PAG3	B	PAGING BOARD
D-CS-M855-0-PAG4	B	PAGING BOARD
D-CS-M855-0-PAG5	B	PAGING BOARD
D-CS-M855-0-PAG6	B	PAGING BOARD
D-CS-M855-0-PAG7	B	PAGING BOARD
D-CS-M855-0-PAG8	B	PAGING BOARD
D-CS-M855-0-PAG9	B	PAGING BOARD
D-CS-M855-0-PAGA	B	PAGING BOARD
D-CS-M855-0-PAGB	B	PAGING BOARD
D-CS-M855-0-RES	B	PAGING BOARD
D-DD-M856-0	A	CACHE DATA
D-LA-M856-0-0	A	CACHE DATA
D-CS-M856-0-CHD1	A	CACHE DATA
D-CS-M856-0-CHD2	A	CACHE DATA
D-CS-M856-0-CHD3	A	CACHE DATA
D-CS-M856-0-CHD4	A	CACHE DATA
D-CS-M856-0-CHD5	A	CACHE DATA
D-CS-M856-0-CHD6	A	CACHE DATA
D-CS-M856-0-CHD7	A	CACHE DATA
D-CS-M856-0-CHD8	A	CACHE DATA
D-CS-M856-0-RES	A	CACHE DATA

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRW. J. J. J.	DATE 26-JUL-84	ENG. C. A. JENS	DATE 26-JUL-84	TITLE: MCA25
	CHK'D. G. ALSTON	DATE 26-JUL-84	BOARD LOCATION: N/A	SHEET 3 OF 4	CACHE PAGER
DSK1MCA25.T2P(4,21)		26-JUL-84 15:42		NEXT HIGHER ASSEMBLY:	KL10-PW
FIRST USED ON OPTION/MODEL: MCA25		KL10-PW		SIZE CODE D TC	NUMBER MCA25-0-1
					REV. A

8	7	6	5	4	3	2	1
D		C		B		A	
OPTION MODULES (C.S) CONTINUED							
DOCUMENT NUMBER REV DESCRIPTION							
D-DD-M857-0 A MBC MBOX CONTROL							
D-UA-M857-0-0 A MBC MBOX CONTROL							
D-CS-M857-0-MBC1 A MBC MBOX CONTROL							
D-CS-M857-0-MBC2 A MBC MBOX CONTROL							
D-CS-M857-0-MBC3 A MBC MBOX CONTROL							
D-CS-M857-0-MBC4 A MBC MBOX CONTROL							
D-CS-M857-0-MBC5 A MBC MBOX CONTROL							
D-CS-M857-0-MBC6 A MBC MBOX CONTROL							
D-CS-M857-0-RES A MBC MBOX CONTROL							
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REVISIONS							
CHK CHANGE NO. REV							
digital							
DATE 26-JUL-84 ENG. C. A. JENS DATE 26-JUL-84							
DATE 26-JUL-84 BOARD LOCATION: N/A							
DATE 26-JUL-84 SHEET 4 OF 4							
DSK:MCATC.TEP(4,21) 26-JUL-84 15:42 NEXT HIGHER ASSEMBLY:							
FIRST USED ON OPTION/MODEL: MCA25 KL10-PW							
SIZE CODE NUMBER REV.							
D TC MCA25-0-1 A							
TITLE: MCA25 CACHE PAGER							
MR							

1

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FOR EACH OPTION OR SERIAL NUMBER WIRE THE SOURCE LISTED ABOVE THE OPTION OR SERIAL NUMBER BIT TO THE SOURCE TO MAKE THE BIT TRUE.

OPTIONS:

SOURCE:
OPTIONS:

50HZ
CACHE AVAIL
CHANNEL AVAIL
EXTENDED ADDRESSING CPU
MASTER OSCILLATOR
MCA 25

4D44E1
4E43M2
4E43A1
4E43E1
4D43D2
4D43E1
4E43F2

SERIAL NO'S

SOURCE:

2048
1024
512
256
128
64

4D42E1
4E41M2
4E41A1
4E41E1
4D41D2
4D41E1
4E41F2

SERIAL NO'S

SOURCE:

32
16
8
4
2
1

4D40E1
4E39M2
4E39A1
4E39E1
4D39D2
4D39E1
4E39F2

DESCRIPTION		DRAWING NO.		PART NO.		REV. NO.	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC-STD 114)							
INCH TOLERANCES		ANGLES		APPLICABLE DIMENSION		DIMENSION RANGES IN INCHES	
X = ± .1		± 0° 30'		RANGE		OVER OVER OVER OVER OVER OVER OVER	
XX = ± .02		SURFACE QUALITY		CHECK ONE		10 0.2 1.2 4.0 12.0 40.0 100.0	
XXX = ± .005		✓		□		1.0 0.5 0.2 0.1 0.05 0.02 0.01	
QUANTITY & VARIATION		MICROINCHES		□		0.004 0.003 0.002 0.001 0.0005 0.0002 0.0001	
THIRD ANGLE PROJECTION		DATE		DATE		TITLE	
DO NOT SCALE DRAWING		6-18-84		6-29-84		OPTION/SERIAL NUMBER CHART	
REMOVE SURFS AND BREAK SHARP CORNERS		DATE		DATE		DOCUMENT NUMBER	
MATERIAL		DATE		DATE		D SP KL10-PW-2	
FINISH		DATE		DATE		SCALE	
++		9/12/84		24 JUN 84		1 OF 1	
++		B-DD-KL10-PW				MR	

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NOTES: 1. THIS BOARD MUST MEET SAFETY REQ. FOR HPWR.
2. SPARE COMPONENT LOCATIONS ARE: E30-E35, E37, E41, E42, E44-E52.

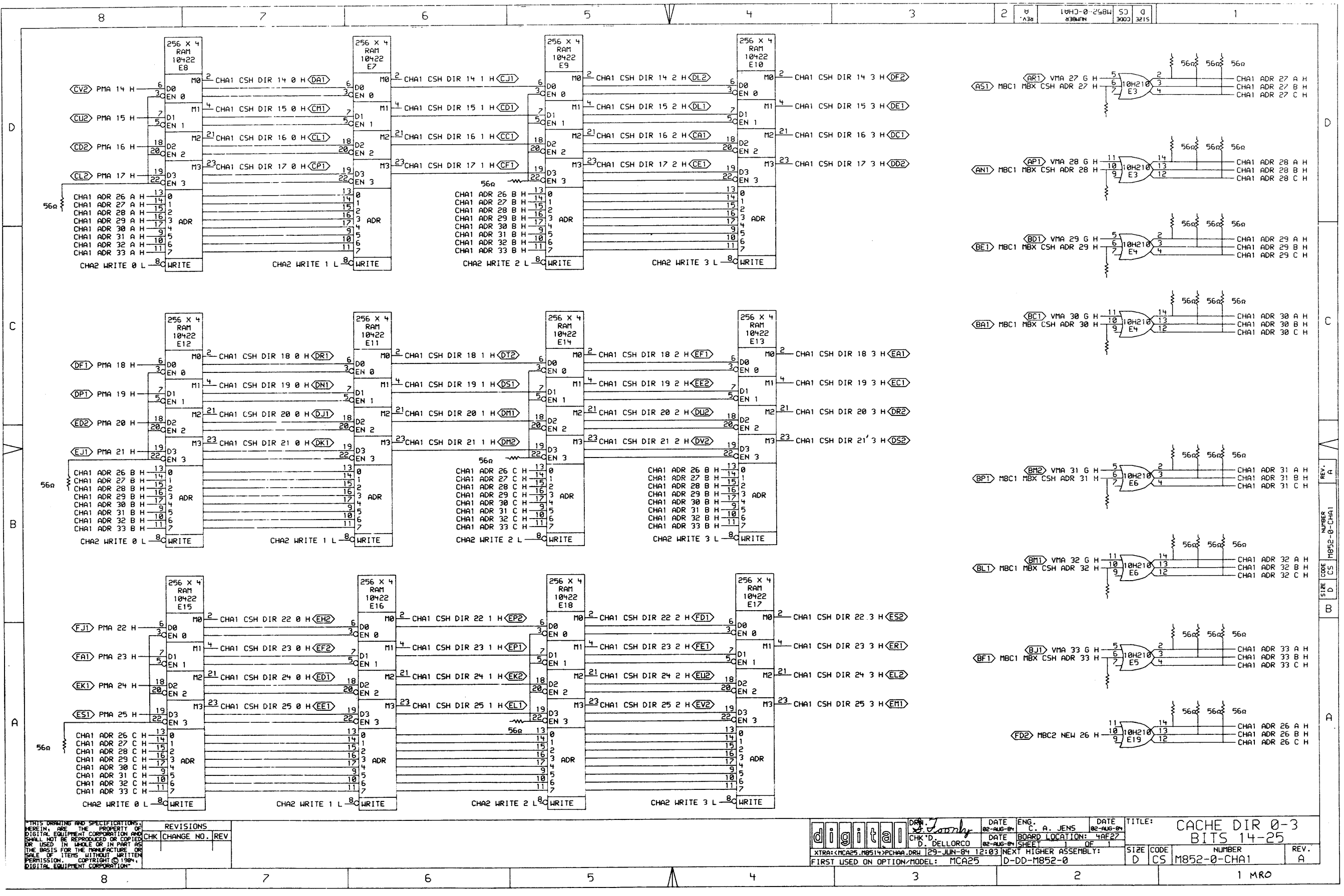
0-0-3584 VN 3

8.41
REF

COMPONENT SIDE

15.688
REF

DRW	MARY JO BUTLER	DATE	13MAR84	TITLE	CHA
CHK'D	S. S. S. S. S.	DATE	9-Jul-84		
DES. ENG.		DATE	25-Jul-84		
RESP. ENG.		DATE	25-Jul-84		
APP. ENG.		DATE	26-Jul-84		
NEXT HIGHER DOC.	0-00-M852-0				
DOCUMENT NUMBER				SIZE CODE NUMBER	REV
E UA M852-0-0					A
SCALE 2 / 1				MRO	



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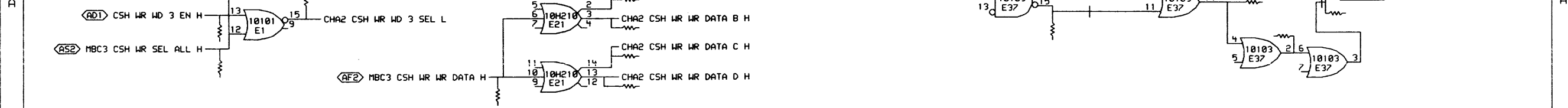
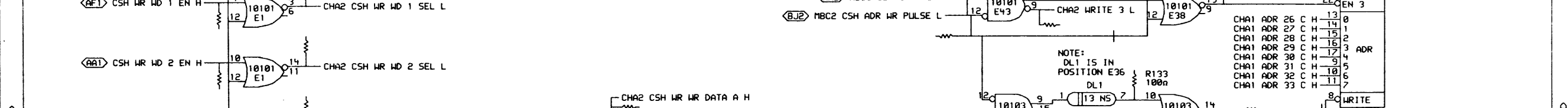
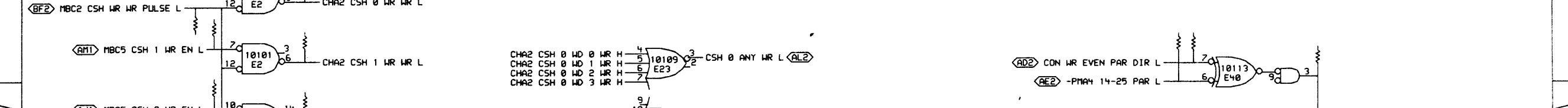
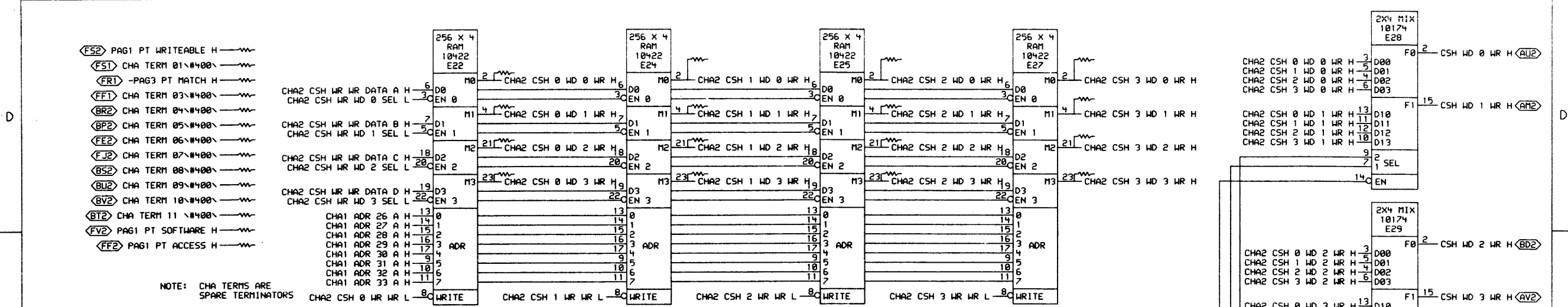
REVISIONS		
CHK	CHANGE NO.	REV

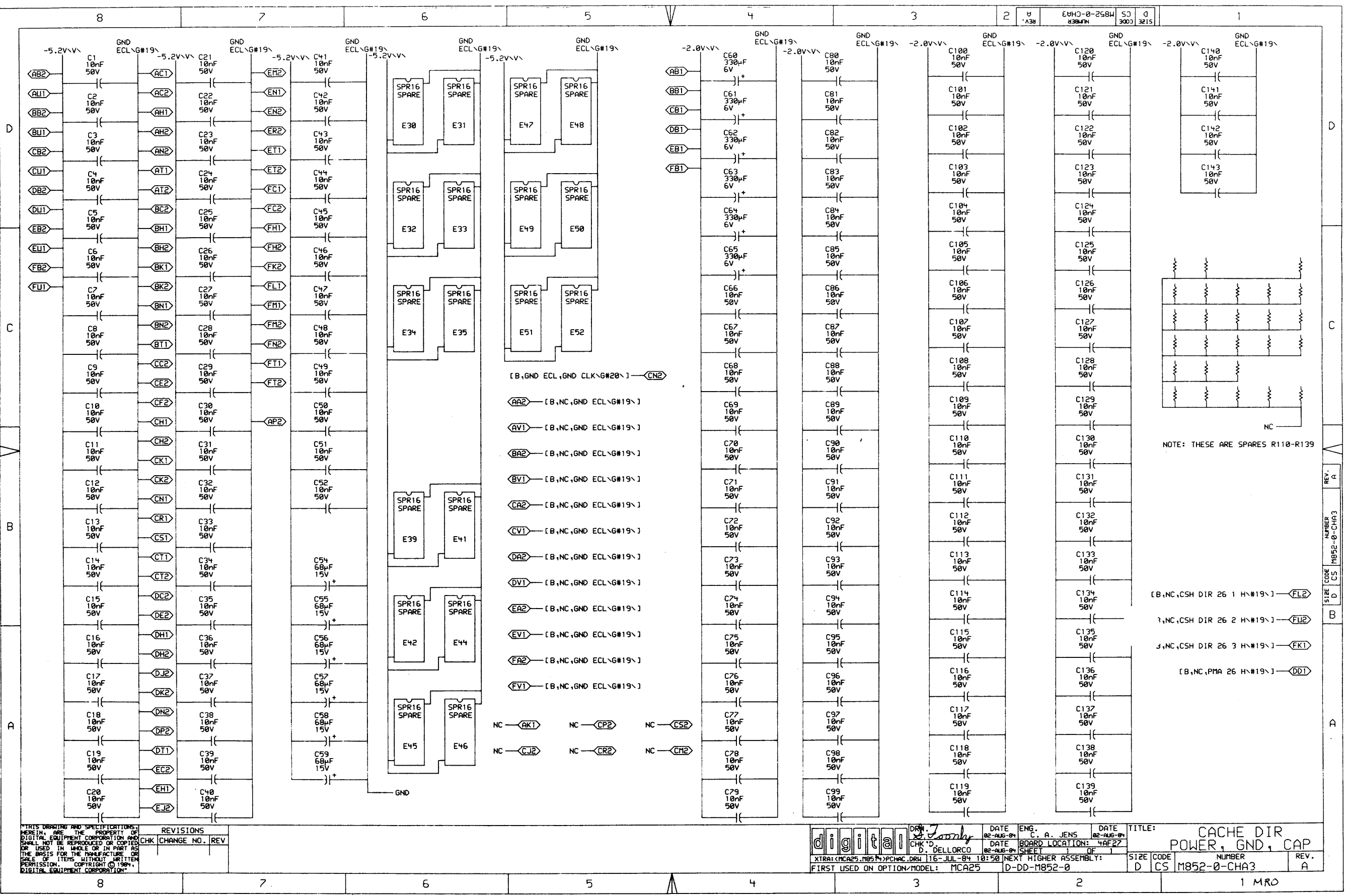
digital	DATE	ENG.	DATE	TITLE:			
	02-AUG-84	C. A. JENS	02-AUG-84				
CHK'D	DATE	BOARD LOCATION:	4AF27	SIZE	CODE	NUMBER	REV.
D. DELLORCO	02-AUG-84	SHEET	1 OF 1				
FIRST USED ON OPTION/MODEL: MCA25				D-DD-M852-0			

CACHE DIR 0-3
BITS 14-25

SIZE	CODE	NUMBER	REV.
D	CS	M852-0-CHA1	A

1 MRO





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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE:
	02-AUG-84	C. A. JENS	02-AUG-84	CACHE DIR
CHK'D	DATE	BOARD LOCATION:	44F27	POWER, GND, CAP
D. DELLORCO	02-AUG-84	SHEET	1 OF 1	
XTRA: MCA25.M852-0-CHA3.DRW 116-JUL-84 10:50		NEXT HIGHER ASSEMBLY:		
FIRST USED ON OPTION/MODEL: MCA25		D-DD-M852-0		
SIZE	CODE	NUMBER	REV.	
D	CS	M852-0-CHA3	A	

NOTE: THESE ARE SPARES R110-R139

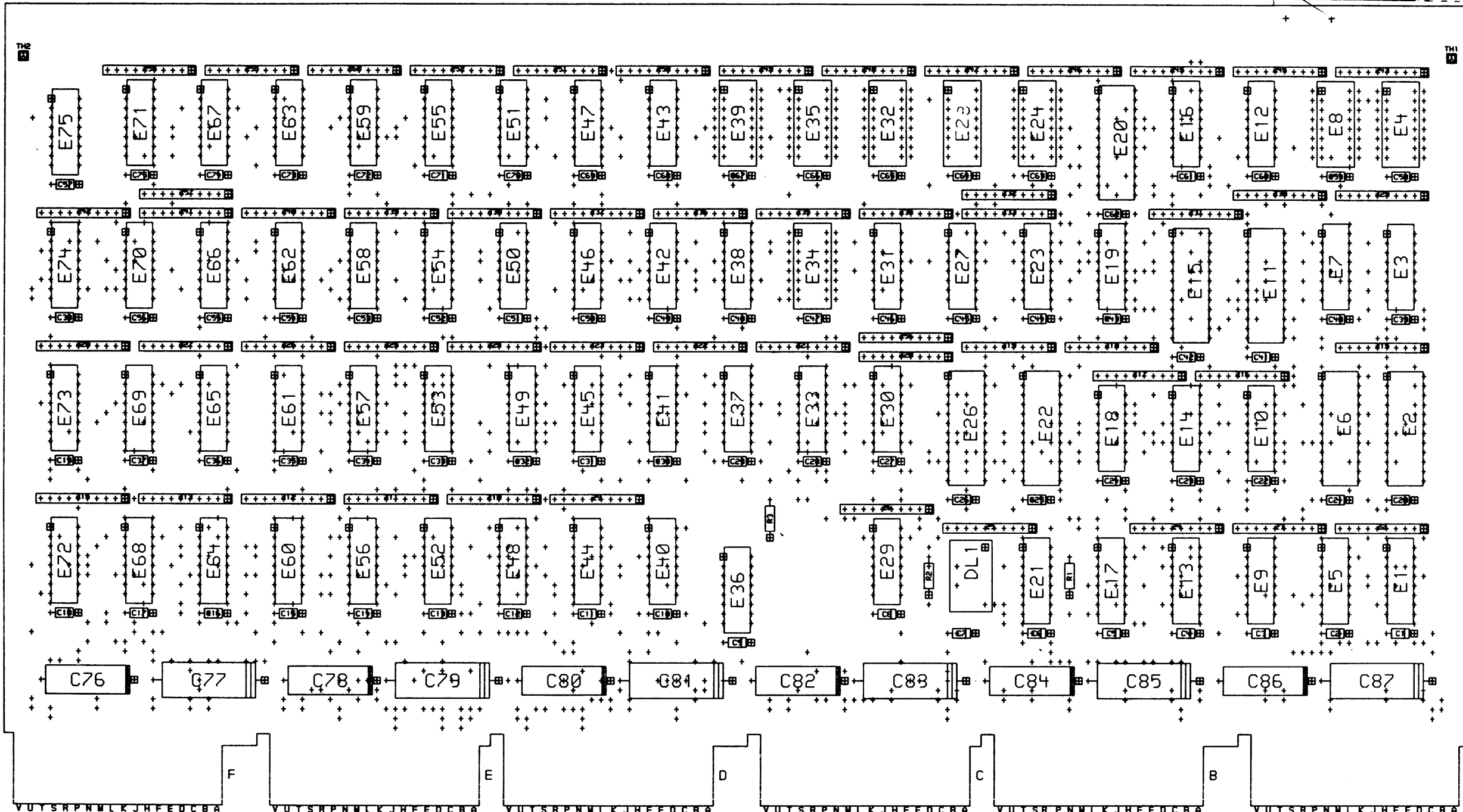
RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R133(1)	CHA2	A3	100n	%DL1(7)	R21(1)	CHA1	C1	56n	CHA1 ADR 29 B H	R84(1)	CHA2	A5	68n	CHA2 CSH WR WR DATA C H	R125(1)	CHA3	C1	56n	NC
R11(1)	CHA1	B8	56n	%E11(3)	R22(1)	CHA1	C1	56n	CHA1 ADR 29 C H	R86(1)	CHA2	A5	68n	CHA2 CSH WR WR DATA D H	R126(1)	CHA3	C1	56n	NC
R107(1)	CHA1	B5	56n	%E13(3)	R23(1)	CHA1	C1	56n	CHA1 ADR 30 A H	R56(1)	CHA2	B3	68n	-CHA2 WRITE 0 H	R127(1)	CHA3	C1	56n	NC
R13(1)	CHA1	A8	56n	%E15(3)	R24(1)	CHA1	C1	56n	CHA1 ADR 30 B H	R58(1)	CHA2	B3	68n	-CHA2 WRITE 1 H	R128(1)	CHA3	C1	56n	NC
R109(1)	CHA1	A5	56n	%E17(3)	R25(1)	CHA1	C1	56n	CHA1 ADR 30 C H	R61(1)	CHA2	B3	68n	-CHA2 WRITE 2 H	R129(1)	CHA3	C1	56n	NC
R112(1)	CHA2	A2	68n	%E37(14)	R26(1)	CHA1	B1	56n	CHA1 ADR 31 A H	R66(1)	CHA2	A3	68n	-CHA2 WRITE 3 H	R130(1)	CHA3	C1	56n	NC
R36(1)	CHA2	A3	68n	%E37(15)	R27(1)	CHA1	B1	56n	CHA1 ADR 31 B H	R91(1)	CHA2	C2	68n	-CON WR EVEN PAR DIR H	R131(1)	CHA3	C1	56n	NC
R113(1)	CHA2	A2	68n	%E37(2)	R28(1)	CHA1	B1	56n	CHA1 ADR 31 C H	R76(1)	CHA2	C1	68n	-CSH WR OUT EN H	R132(1)	CHA3	C1	56n	NC
R54(1)	CHA2	A2	68n	%E37(3)	R9(1)	CHA1	B1	56n	CHA1 ADR 32 A H	R39(1)	CHA2	B7	68n	CSH WR WD 0 EN H	R135(1)	CHA3	C1	56n	NC
R65(1)	CHA2	B2	68n	%E38(14)	R8(1)	CHA1	B1	56n	CHA1 ADR 32 B H	R41(1)	CHA2	A7	68n	CSH WR WD 1 EN H	R136(1)	CHA3	C1	56n	NC
R72(1)	CHA2	A2	68n	%E38(15)	R7(1)	CHA1	B1	56n	CHA1 ADR 32 C H	R43(1)	CHA2	A7	68n	CSH WR WD 2 EN H	R137(1)	CHA3	C1	56n	NC
R57(1)	CHA2	B2	68n	%E38(2)	R6(1)	CHA1	A1	56n	CHA1 ADR 33 A H	R44(1)	CHA2	A7	68n	CSH WR WD 3 EN H	R138(1)	CHA3	C1	56n	NC
R60(1)	CHA2	B2	68n	%E38(3)	R5(1)	CHA1	A1	56n	CHA1 ADR 33 B H	R30(1)	CHA1	D2	68n	MBC1 MBX CSH ADR 27 H	R139(1)	CHA3	C1	56n	NC
R88(1)	CHA2	C2	68n	%E40(3)	R4(1)	CHA1	A1	56n	CHA1 ADR 33 C H	R29(1)	CHA1	D2	68n	MBC1 MBX CSH ADR 28 H	R106(1)	CHA2	C7	68n	PAG1 PT ACCESS H
R64(1)	CHA2	B3	68n	%E43(14)	R63(1)	CHA2	D6	68n	CHA2 CSH 0 WD 0 WR H	R31(1)	CHA1	C2	68n	MBC1 MBX CSH ADR 29 H	R105(1)	CHA2	C7	68n	PAG1 PT SOFTWARE H
R69(1)	CHA2	B3	68n	%E43(15)	R62(1)	CHA2	D6	68n	CHA2 CSH 0 WD 1 WR H	R32(1)	CHA1	C2	68n	MBC1 MBX CSH ADR 30 H	R93(1)	CHA2	D7	68n	PAG1 PT WRITEABLE H
R55(1)	CHA2	B3	68n	%E43(2)	R73(1)	CHA2	D6	68n	CHA2 CSH 0 WD 2 WR H	R34(1)	CHA1	B2	68n	MBC1 MBX CSH ADR 31 H	R95(1)	CHA2	D7	68n	-PAG3 PT MATCH H
R59(1)	CHA2	B3	68n	%E43(3)	R71(1)	CHA2	D6	68n	CHA2 CSH 0 WD 3 WR H	R33(1)	CHA1	B2	68n	MBC1 MBX CSH ADR 32 H	R89(1)	CHA2	C2	68n	PM43 14-25 PAR H
R10(1)	CHA1	D8	56n	%E7(3)	R90(1)	CHA2	C7	68n	-CHA2 CSH 0 WR WR H	R35(1)	CHA1	A2	68n	MBC1 MBX CSH ADR 33 H					
R108(1)	CHA1	D5	56n	%E9(3)	R74(1)	CHA2	D5	68n	CHA2 CSH 1 WD 0 WR H	R134(1)	CHA2	A4	68n	-MBC2 CSH ADR WR PULSE H					
R94(1)	CHA2	D7	68n	CHA TERM 01\#400\	R70(1)	CHA2	D5	68n	CHA2 CSH 1 WD 1 WR H	R46(1)	CHA2	C8	68n	-MBC2 CSH WR WR PULSE H					
R96(1)	CHA2	D7	68n	CHA TERM 03\#400\	R68(1)	CHA2	D5	68n	CHA2 CSH 1 WD 2 WR H	R37(1)	CHA2	A7	68n	MBC3 CSH WR SEL ALL H					
R97(1)	CHA2	D7	68n	CHA TERM 04\#400\	R67(1)	CHA2	D5	68n	CHA2 CSH 1 WD 3 WR H	R79(1)	CHA2	A6	68n	MBC3 CSH WR WR DATA H					
R98(1)	CHA2	D7	68n	CHA TERM 05\#400\	R51(1)	CHA2	C7	68n	-CHA2 CSH 1 WR WR H	R92(1)	CHA2	C7	68n	-MBC5 CSH 0 WR EN H					
R99(1)	CHA2	D7	68n	CHA TERM 06\#400\	R81(1)	CHA2	D4	68n	CHA2 CSH 2 WD 0 WR H	R52(1)	CHA2	C7	68n	-MBC5 CSH 1 WR EN H					
R100(1)	CHA2	D7	68n	CHA TERM 07\#400\	R75(1)	CHA2	D4	68n	CHA2 CSH 2 WD 1 WR H	R50(1)	CHA2	B7	68n	-MBC5 CSH 2 WR EN H					
R101(1)	CHA2	D7	68n	CHA TERM 08\#400\	R77(1)	CHA2	D4	68n	CHA2 CSH 2 WD 2 WR H	R48(1)	CHA2	B7	68n	-MBC5 CSH 3 WR EN H					
R102(1)	CHA2	D7	68n	CHA TERM 09\#400\	R78(1)	CHA2	D4	68n	CHA2 CSH 2 WD 3 WR H	R110(1)	CHA3	C1	56n	NC					
R103(1)	CHA2	D7	68n	CHA TERM 10\#400\	R49(1)	CHA2	B7	68n	-CHA2 CSH 2 WR WR H	R111(1)	CHA3	C1	56n	NC					
R104(1)	CHA2	D7	68n	CHA TERM 11\#400\	R53(1)	CHA2	D3	68n	CHA2 CSH 3 WD 0 WR H	R114(1)	CHA3	C1	56n	NC					
R3(1)	CHA1	A1	56n	CHA1 ADR 26 A H	R83(1)	CHA2	D3	68n	CHA2 CSH 3 WD 1 WR H	R115(1)	CHA3	C1	56n	NC					
R2(1)	CHA1	A1	56n	CHA1 ADR 26 B H	R85(1)	CHA2	D3	68n	CHA2 CSH 3 WD 2 WR H	R116(1)	CHA3	C1	56n	NC					
R1(1)	CHA1	A1	56n	CHA1 ADR 26 C H	R87(1)	CHA2	D3	68n	CHA2 CSH 3 WD 3 WR H	R117(1)	CHA3	C1	56n	NC					
R14(1)	CHA1	D1	56n	CHA1 ADR 27 A H	R47(1)	CHA2	B7	68n	-CHA2 CSH 3 WR WR H	R118(1)	CHA3	C1	56n	NC					
R15(1)	CHA1	D1	56n	CHA1 ADR 27 B H	R38(1)	CHA2	B7	68n	-CHA2 CSH WR WD 0 SEL H	R119(1)	CHA3	C1	56n	NC					
R16(1)	CHA1	D1	56n	CHA1 ADR 27 C H	R40(1)	CHA2	A7	68n	-CHA2 CSH WR WD 1 SEL H	R120(1)	CHA3	C1	56n	NC					
R17(1)	CHA1	D1	56n	CHA1 ADR 28 A H	R42(1)	CHA2	A7	68n	-CHA2 CSH WR WD 2 SEL H	R121(1)	CHA3	C1	56n	NC					
R18(1)	CHA1	D1	56n	CHA1 ADR 28 B H	R45(1)	CHA2	A7	68n	-CHA2 CSH WR WD 3 SEL H	R122(1)	CHA3	C1	56n	NC					
R19(1)	CHA1	D1	56n	CHA1 ADR 28 C H	R80(1)	CHA2	A5	68n	CHA2 CSH WR WR DATA A H	R123(1)	CHA3	C1	56n	NC					
R20(1)	CHA1	C1	56n	CHA1 ADR 29 A H	R82(1)	CHA2	A5	68n	CHA2 CSH WR WR DATA B H	R124(1)	CHA3	C1	56n	NC					

NOTE:
1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES OUTPUT OF DIP LOC AND () INDICATES PIN NUMBER

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2 0UA M853-0-0 1

20 (QTY 12) COMPONENT SIDE VIEW

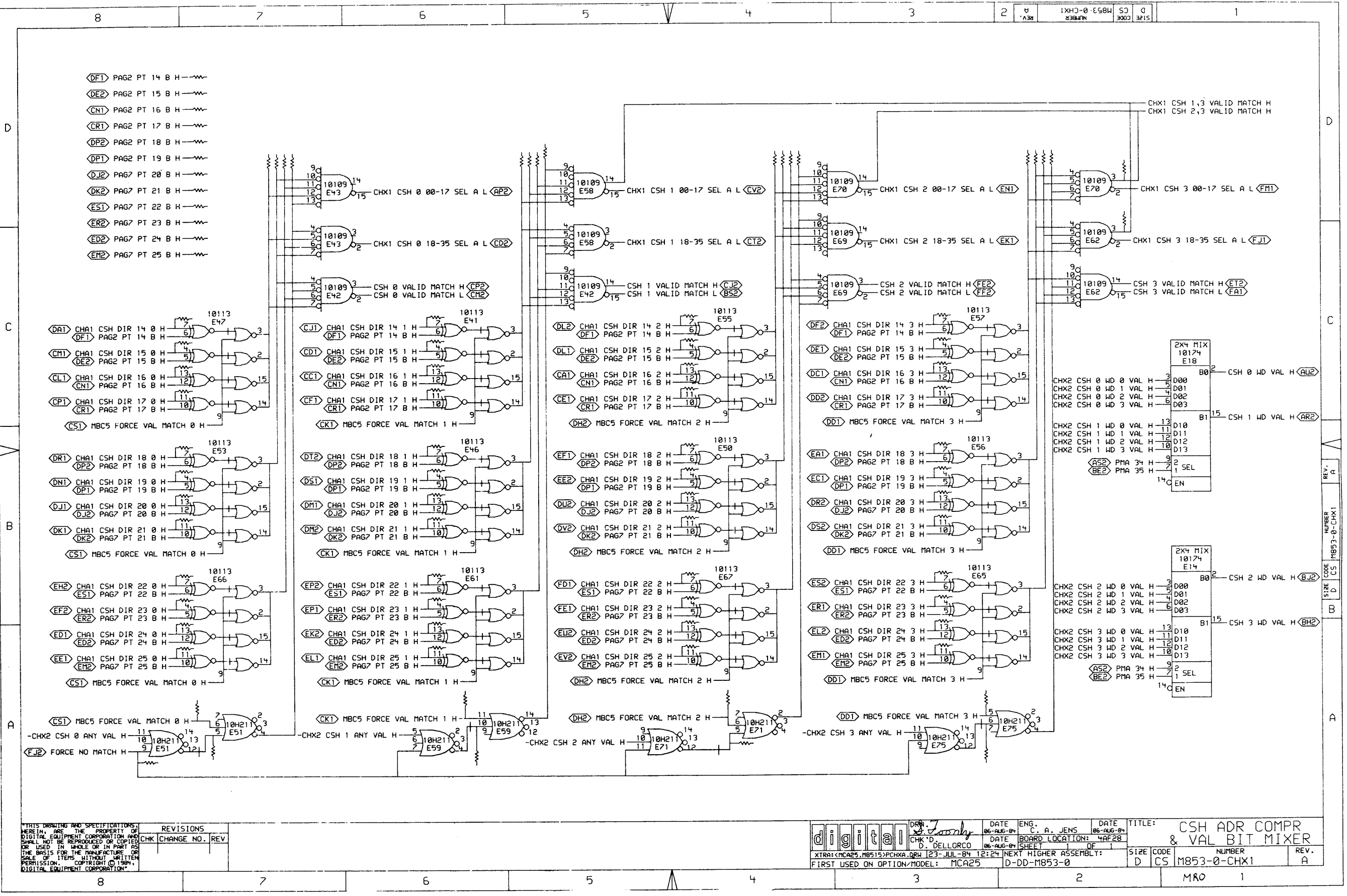


NOTES:
SPARE COMPONENTS: E4, E8, E24, E28, E32, E34, E35, E39, Z34, Z43, Z44, Z47 THRU Z49
STEP E + Y AXIS 0 STEP 0 TIMES
REPEAT + X AXIS 0 STEP 0 TIMES

CHANGE NO	REV

ETCH REV. A1

SIGNATURES	DATE	digital	TITLE	CACHE EXTENSION	REV
DRN.	4/27/84				
CHK'D. R.W. Cantor	5/1/84				
MECH. ENG. [Signature]	5/1/84				
PROJ. ENG. [Signature]	5/1/84				
PROD. [Signature]	8-27-84	SIZE	CODE	NUMBER	REV
SCALE 2/1	OF	d	UA	M853-0-0	A
TOP DOC. NO: D-00-M853-0					

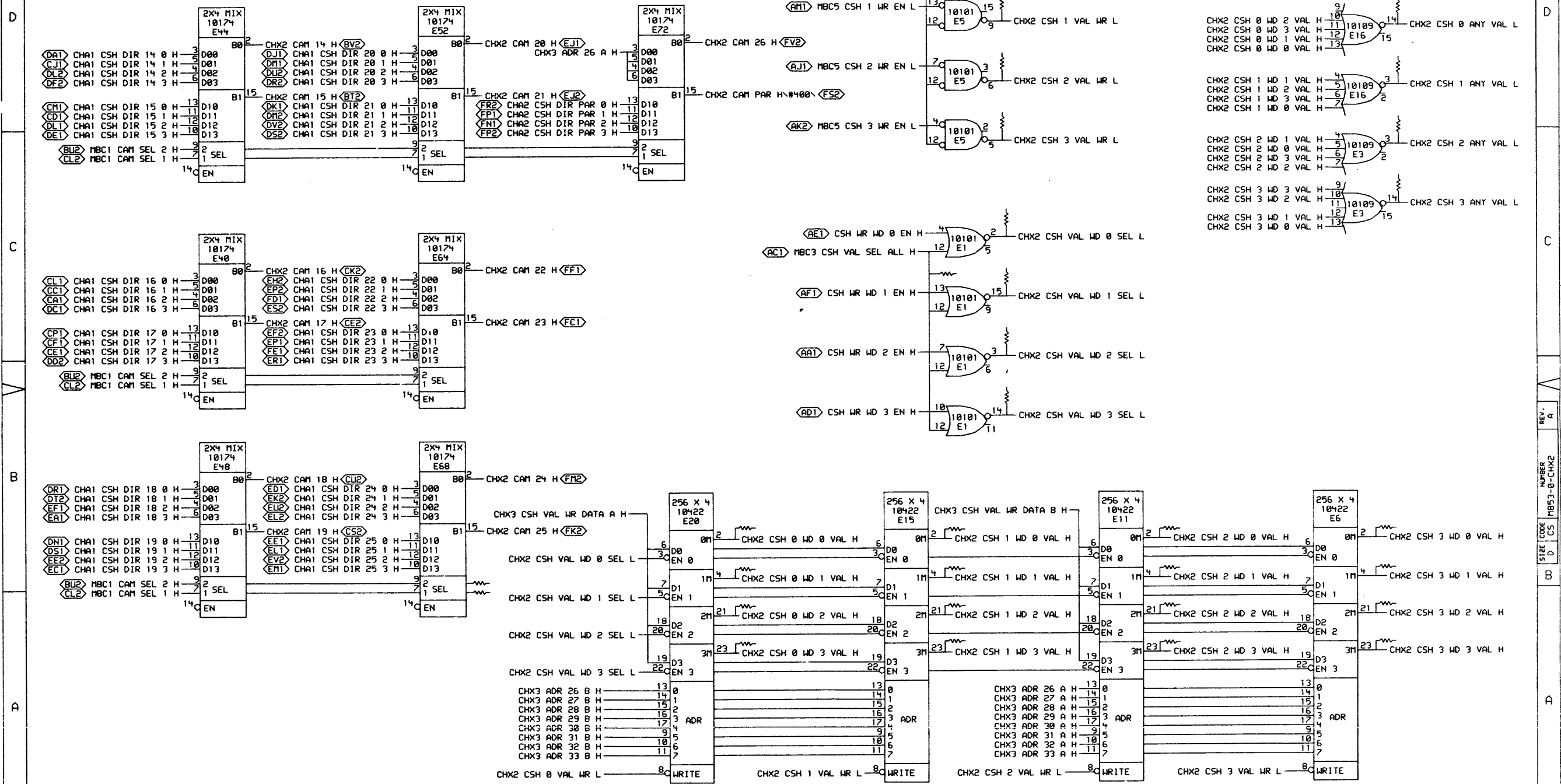


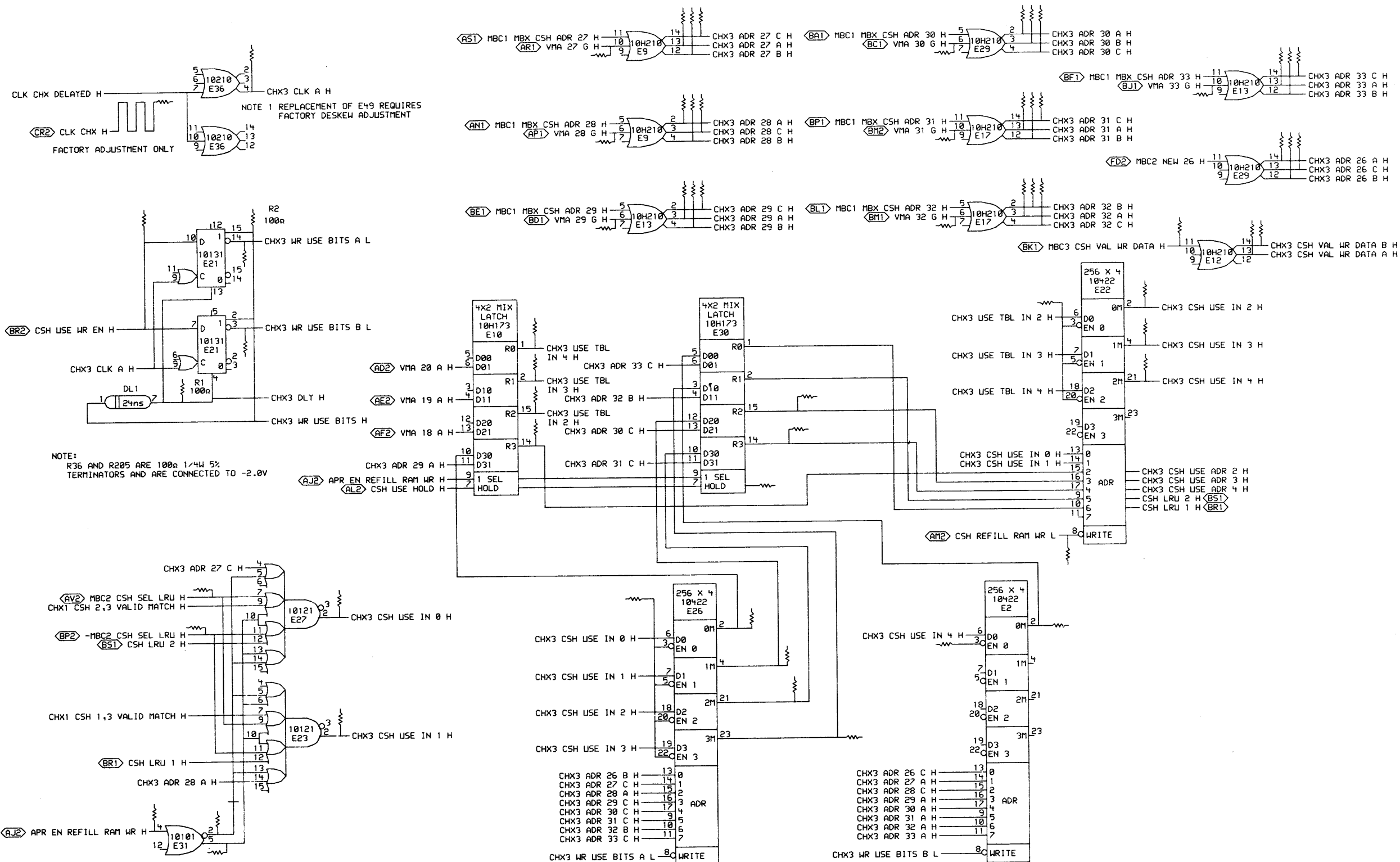
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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE:
	06-AUG-84	C. A. JENS	06-AUG-84	CSH ADR COMPR & VAL BIT MIXER
CHK'D	DATE	BOARD LOCATION:	SIZE	CODE
D. DELLORCO	23-JUL-84	4AF28	D	CS
EXTRA: MCA25, M8515, PCHXA, QW	23-JUL-84	12:24	NEXT HIGHER ASSEMBLY:	NUMBER
FIRST USED ON OPTION/MODEL:	MCA25	D-DD-M853-0		REV.
				A

MRO 1





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REVISIONS		
CHK	CHANGE NO.	REV

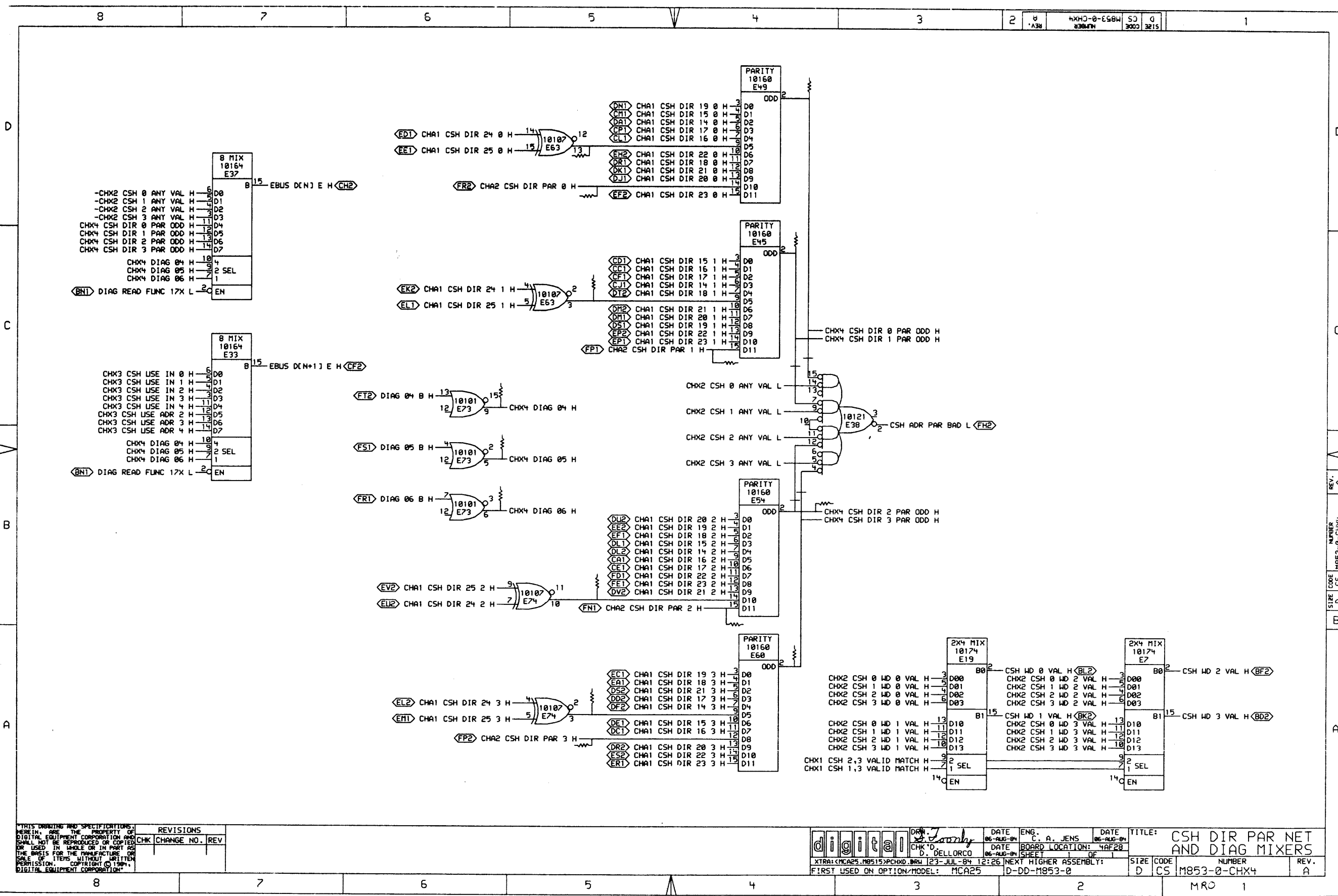
digital

XTRA: <MCA25.M8515>PCHXC.DRW	23-JUL-84 12:26	NEXT HIGHER ASSEMBLY:
FIRST USED ON OPTION/MODEL: MCA25		D-DD-M853-0

TITLE: CACHE USE BITS			
ADR BUF & LRU BITS			
REV	NO	DATE	BY

SIZE	CODE	NUMBER	REV.
D	CS	M853-0-CHX3	A

MRO	1
-----	---



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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE:
	06-AUG-84	C. A. JENS	06-AUG-84	CSH DIR PAR NET AND DIAG MIXERS
CHK'D	DATE	BOARD LOCATION:	4AF28	
D. DELLORCO	06-AUG-84	SHEET	1	OF
NEXT HIGHER ASSEMBLY:		D-DD-M853-0		
FIRST USED ON OPTION/MODEL:		MCA25		
SIZE	CODE	NUMBER	REV.	
D	CS	M853-0-CHX4	A	

MRO 1

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
220(8)	CHX3	B3	68n	%E2(2)	29(1)	CHX1	C6	68n	CHA1 CSH DIR 15 1 H	254(5)	CHX1	A6	68n	CHA1 CSH DIR 25 1 H	216(1)	CHX3	D5	68n	CHX3 ADR 27 A H
215(5)	CHX3	B3	68n	%E2(3)	252(10)	CHX1	C4	68n	CHA1 CSH DIR 15 2 H	257(1)	CHX1	A4	68n	CHA1 CSH DIR 25 2 H	245(10)	CHX3	D4	68n	CHX3 ADR 27 B H
219(3)	CHX3	C3	68n	%E22(3)	225(10)	CHX1	C3	68n	CHA1 CSH DIR 15 3 H	220(10)	CHX1	A3	68n	CHA1 CSH DIR 25 3 H	256(3)	CHX3	D4	68n	CHX3 ADR 27 C H
23(6)	CHX3	B4	68n	%E26(2)	251(1)	CHX1	C7	68n	CHA1 CSH DIR 16 0 H	224(1)	CHX4	D5	68n	CHA2 CSH DIR PAR 0 H	233(5)	CHX3	D5	68n	CHX3 ADR 28 A H
26(6)	CHX3	A4	68n	%E26(21)	29(6)	CHX1	C6	68n	CHA1 CSH DIR 16 1 H	223(3)	CHX4	C4	68n	CHA2 CSH DIR PAR 1 H	245(6)	CHX3	D4	68n	CHX3 ADR 28 B H
220(10)	CHX3	A4	68n	%E26(23)	252(5)	CHX1	C4	68n	CHA1 CSH DIR 16 2 H	224(10)	CHX4	B4	68n	CHA2 CSH DIR PAR 2 H	22(1)	CHX3	D4	68n	CHX3 ADR 28 C H
220(1)	CHX3	B5	68n	%E26(3)	225(6)	CHX1	C3	68n	CHA1 CSH DIR 16 3 H	212(5)	CHX4	A5	68n	CHA2 CSH DIR PAR 3 H	230(5)	CHX3	D5	68n	CHX3 ADR 29 A H
220(5)	CHX3	A4	68n	%E26(4)	250(10)	CHX1	C7	68n	CHA1 CSH DIR 17 0 H	230(3)	CHX1	C2	68n	CHX1 CSH 1,3 VALID MATCH H	246(5)	CHX3	D4	68n	CHX3 ADR 29 B H
233(6)	CHX3	A7	68n	%E31(2)	29(8)	CHX1	C6	68n	CHA1 CSH DIR 17 1 H	217(5)	CHX1	D2	68n	CHX1 CSH 2,3 VALID MATCH H	219(10)	CHX3	D4	68n	CHX3 ADR 29 C H
232(3)	CHX3	A7	68n	%E31(5)	252(6)	CHX1	C4	68n	CHA1 CSH DIR 17 2 H	235(10)	CHX2	D1	68n	-CHX2 CSH 0 ANY VAL H	215(1)	CHX3	D3	68n	CHX3 ADR 30 A H
239(10)	CHX1	D5	68n	%E41(2)	212(10)	CHX1	C3	68n	CHA1 CSH DIR 17 3 H	246(10)	CHX2	D3	68n	-CHX2 CSH 0 VAL WR H	246(3)	CHX3	D3	68n	CHX3 ADR 30 B H
240(1)	CHX1	D5	68n	%E46(2)	211(3)	CHX1	B7	68n	CHA1 CSH DIR 18 0 H	217(8)	CHX2	B5	68n	CHX2 CSH 0 WD 0 VAL H	256(6)	CHX3	D3	68n	CHX3 ADR 30 C H
222(10)	CHX1	D7	68n	%E47(2)	223(10)	CHX1	B6	68n	CHA1 CSH DIR 18 1 H	218(6)	CHX2	B5	68n	CHX2 CSH 0 WD 1 VAL H	216(5)	CHX3	D3	68n	CHX3 ADR 31 A H
213(8)	CHX1	D4	68n	%E50(2)	239(3)	CHX1	B4	68n	CHA1 CSH DIR 18 2 H	217(6)	CHX2	A5	68n	CHX2 CSH 0 WD 2 VAL H	218(1)	CHX3	D3	68n	CHX3 ADR 31 B H
252(1)	CHX1	A7	68n	%E51(12)	210(3)	CHX1	B3	68n	CHA1 CSH DIR 18 3 H	218(5)	CHX2	A5	68n	CHX2 CSH 0 WD 3 VAL H	256(5)	CHX3	D3	68n	CHX3 ADR 31 C H
237(1)	CHX1	D7	68n	%E51(4)	225(3)	CHX1	B7	68n	CHA1 CSH DIR 19 0 H	235(8)	CHX2	D1	68n	-CHX2 CSH 1 ANY VAL H	216(3)	CHX3	D3	68n	CHX3 ADR 32 A H
250(6)	CHX1	D7	68n	%E53(2)	223(8)	CHX1	B6	68n	CHA1 CSH DIR 19 1 H	218(3)	CHX2	D3	68n	-CHX2 CSH 1 VAL WR H	221(1)	CHX3	D3	68n	CHX3 ADR 32 B H
213(10)	CHX1	D4	68n	%E55(2)	239(1)	CHX1	B4	68n	CHA1 CSH DIR 19 2 H	231(5)	CHX2	B4	68n	CHX2 CSH 1 WD 0 VAL H	24(10)	CHX3	D3	68n	CHX3 ADR 32 C H
241(10)	CHX1	D2	68n	%E56(2)	210(5)	CHX1	B3	68n	CHA1 CSH DIR 19 3 H	245(5)	CHX2	B4	68n	CHX2 CSH 1 WD 1 VAL H	216(8)	CHX3	D1	68n	CHX3 ADR 33 A H
228(3)	CHX1	D2	68n	%E57(2)	211(1)	CHX1	B7	68n	CHA1 CSH DIR 20 0 H	245(3)	CHX2	A4	68n	CHX2 CSH 1 WD 2 VAL H	217(3)	CHX3	D1	68n	CHX3 ADR 33 B H
222(8)	CHX1	D5	68n	%E59(14)	237(5)	CHX1	B6	68n	CHA1 CSH DIR 20 1 H	231(3)	CHX2	A4	68n	CHX2 CSH 1 WD 3 VAL H	256(10)	CHX3	D1	68n	CHX3 ADR 33 C H
268(3)	CHX1	A6	68n	%E59(4)	224(6)	CHX1	B4	68n	CHA1 CSH DIR 20 2 H	222(5)	CHX2	C1	68n	-CHX2 CSH 2 ANY VAL H	25(3)	CHX3	D7	68n	CHX3 CLK A H
240(3)	CHX1	D5	68n	%E61(2)	212(3)	CHX1	B3	68n	CHA1 CSH DIR 20 3 H	216(6)	CHX2	D3	68n	-CHX2 CSH 2 VAL WR H	25(1)	CHX3	B5	68n	CHX3 CSH USE ADR 2 H
210(1)	CHX4	D5	68n	%E63(13)	210(10)	CHX1	B7	68n	CHA1 CSH DIR 21 0 H	233(1)	CHX2	B2	68n	CHX2 CSH 2 WD 0 VAL H	218(8)	CHX3	C4	68n	CHX3 CSH USE ADR 3 H
29(5)	CHX4	C5	68n	%E63(3)	223(5)	CHX1	B6	68n	CHA1 CSH DIR 21 1 H	217(1)	CHX2	B2	68n	CHX2 CSH 2 WD 1 VAL H	217(10)	CHX3	C4	68n	CHX3 CSH USE ADR 4 H
242(3)	CHX1	D2	68n	%E65(2)	224(3)	CHX1	B4	68n	CHA1 CSH DIR 21 2 H	230(1)	CHX2	A2	68n	CHX2 CSH 2 WD 2 VAL H	220(3)	CHX3	B6	68n	CHX3 CSH USE IN 0 H
250(8)	CHX1	D7	68n	%E66(2)	212(8)	CHX1	B3	68n	CHA1 CSH DIR 21 3 H	215(8)	CHX2	A2	68n	CHX2 CSH 2 WD 3 VAL H	26(1)	CHX3	A6	68n	CHX3 CSH USE IN 1 H
228(1)	CHX1	D4	68n	%E67(2)	213(3)	CHX1	B7	68n	CHA1 CSH DIR 22 0 H	222(3)	CHX2	C1	68n	-CHX2 CSH 3 ANY VAL H	221(8)	CHX3	C2	68n	CHX3 CSH USE IN 2 H
255(8)	CHX1	A4	68n	%E71(12)	213(1)	CHX1	B6	68n	CHA1 CSH DIR 22 1 H	23(3)	CHX2	C3	68n	-CHX2 CSH 3 VAL WR H	221(6)	CHX3	C2	68n	CHX3 CSH USE IN 3 H
214(1)	CHX1	D4	68n	%E71(2)	257(5)	CHX1	B4	68n	CHA1 CSH DIR 22 2 H	233(3)	CHX2	B1	68n	CHX2 CSH 3 WD 0 VAL H	215(3)	CHX3	C2	68n	CHX3 CSH USE IN 4 H
238(8)	CHX4	B5	68n	%E74(10)	213(5)	CHX1	B3	68n	CHA1 CSH DIR 22 3 H	231(8)	CHX2	B1	68n	CHX2 CSH 3 WD 1 VAL H	246(8)	CHX3	C1	68n	CHX3 CSH VAL WR DATA A H
212(1)	CHX4	A5	68n	%E74(3)	241(5)	CHX1	B7	68n	CHA1 CSH DIR 23 0 H	229(3)	CHX2	A1	68n	CHX2 CSH 3 WD 2 VAL H	23(1)	CHX3	C1	68n	CHX3 CSH VAL WR DATA B H
242(8)	CHX1	A3	68n	%E75(12)	29(3)	CHX1	B6	68n	CHA1 CSH DIR 23 1 H	215(10)	CHX2	A1	68n	CHX2 CSH 3 WD 3 VAL H	R1(1)	CHX3	C7	100n	CHX3 DLY H
240(5)	CHX1	D2	68n	%E75(4)	255(1)	CHX1	B4	68n	CHA1 CSH DIR 23 2 H	245(1)	CHX2	C3	68n	-CHX2 CSH VAL WD 0 SEL H	219(6)	CHX3	C5	68n	CHX3 USE TBL IN 2 H
26(5)	CHX3	A7	68n	APR EN REFILL RAM WR H	227(5)	CHX1	B3	68n	CHA1 CSH DIR 23 3 H	246(6)	CHX2	C3	68n	-CHX2 CSH VAL WD 1 SEL H	219(5)	CHX3	C5	68n	CHX3 USE TBL IN 3 H
238(1)	CHX1	C7	68n	CHA1 CSH DIR 14 0 H	254(1)	CHX1	A7	68n	CHA1 CSH DIR 24 0 H	245(8)	CHX2	C3	68n	-CHX2 CSH VAL WD 2 SEL H	218(10)	CHX3	C5	68n	CHX3 USE TBL IN 4 H
29(10)	CHX1	C6	68n	CHA1 CSH DIR 14 1 H	254(3)	CHX1	A6	68n	CHA1 CSH DIR 24 1 H	246(1)	CHX2	B3	68n	-CHX2 CSH VAL WD 3 SEL H	R2(1)	CHX3	C7	100n	CHX3 WR USE BITS H
252(8)	CHX1	C4	68n	CHA1 CSH DIR 14 2 H	254(8)	CHX1	A4	68n	CHA1 CSH DIR 24 2 H	22(8)	CHX3	D1	68n	CHX3 ADR 26 A H	25(10)	CHX3	C7	68n	-CHX3 WR USE BITS A H
212(6)	CHX1	C3	68n	CHA1 CSH DIR 14 3 H	228(8)	CHX1	A3	68n	CHA1 CSH DIR 24 3 H	231(10)	CHX3	D1	68n	CHX3 ADR 26 B H	22(5)	CHX3	C7	68n	-CHX3 WR USE BITS B H
251(5)	CHX1	C7	68n	CHA1 CSH DIR 15 0 H	268(10)	CHX1	A7	68n	CHA1 CSH DIR 25 0 H	22(6)	CHX3	D1	68n	CHX3 ADR 26 C H	236(1)	CHX4	D4	68n	CHX4 CSH DIR 0 PAR ODD H

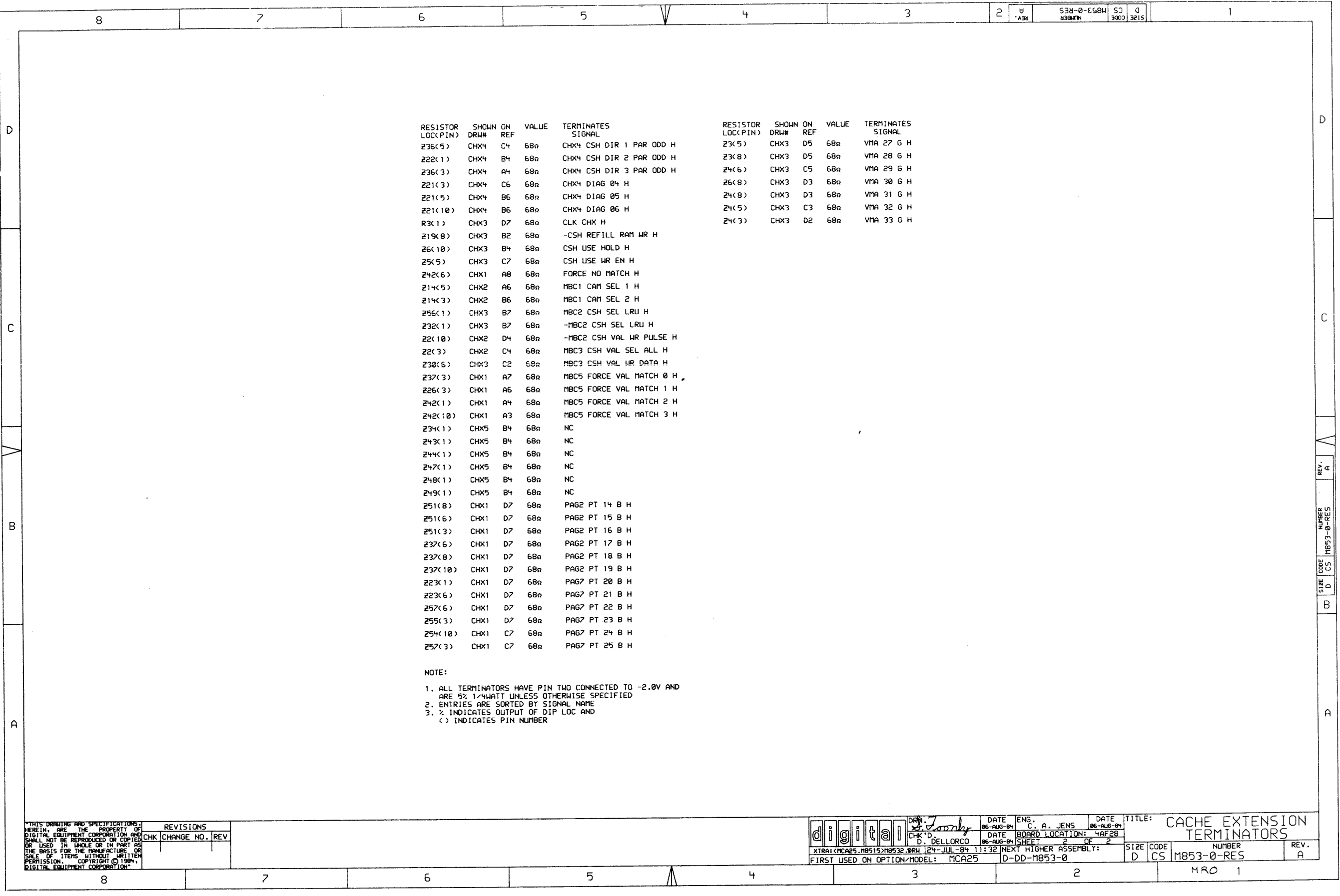
NOTE:

1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES OUTPUT OF DIP LOC AND
() INDICATES PIN NUMBER

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REVISIONS	CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE:
	24-JUL-84	C. A. JENS	06-AUG-84	CACHE EXTENSION TERMINATORS
CHK'D	DATE	BOARD LOCATION:		
D. DELLORCO	06-AUG-84	4A2B		
XTRA: MCA25.M8515.M8531.DRW		NEXT HIGHER ASSEMBLY:		
FIRST USED ON OPTION/MODEL: MCA25		D-DD-M853-0		
SIZE	CODE	NUMBER	REV.	
D	CS	M853-0-RES	A	



RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
Z36<5>	CHX4	C4	68Ω	CHX4 CSH DIR 1 PAR ODD H	Z3<5>	CHX3	D5	68Ω	VMA 27 G H
Z22<1>	CHX4	B4	68Ω	CHX4 CSH DIR 2 PAR ODD H	Z3<8>	CHX3	D5	68Ω	VMA 28 G H
Z36<3>	CHX4	A4	68Ω	CHX4 CSH DIR 3 PAR ODD H	Z4<6>	CHX3	C5	68Ω	VMA 29 G H
Z21<3>	CHX4	C6	68Ω	CHX4 DIAG 04 H	Z6<8>	CHX3	D3	68Ω	VMA 30 G H
Z21<5>	CHX4	B6	68Ω	CHX4 DIAG 05 H	Z4<8>	CHX3	D3	68Ω	VMA 31 G H
Z21<10>	CHX4	B6	68Ω	CHX4 DIAG 06 H	Z4<5>	CHX3	C3	68Ω	VMA 32 G H
R3<1>	CHX3	D7	68Ω	CLK CHX H	Z4<3>	CHX3	D2	68Ω	VMA 33 G H
Z19<8>	CHX3	B2	68Ω	-CSH REFILL RAM WR H					
Z6<10>	CHX3	B4	68Ω	CSH USE HOLD H					
Z5<5>	CHX3	C7	68Ω	CSH USE WR EN H					
Z42<6>	CHX1	A8	68Ω	FORCE NO MATCH H					
Z14<5>	CHX2	A6	68Ω	MBC1 CAM SEL 1 H					
Z14<3>	CHX2	B6	68Ω	MBC1 CAM SEL 2 H					
Z56<1>	CHX3	B7	68Ω	MBC2 CSH SEL LRU H					
Z32<1>	CHX3	B7	68Ω	-MBC2 CSH SEL LRU H					
Z2<10>	CHX2	D4	68Ω	-MBC2 CSH VAL WR PULSE H					
Z2<3>	CHX2	C4	68Ω	MBC3 CSH VAL SEL ALL H					
Z30<6>	CHX3	C2	68Ω	MBC3 CSH VAL WR DATA H					
Z37<3>	CHX1	A7	68Ω	MBC5 FORCE VAL MATCH 0 H					
Z26<3>	CHX1	A6	68Ω	MBC5 FORCE VAL MATCH 1 H					
Z42<1>	CHX1	A4	68Ω	MBC5 FORCE VAL MATCH 2 H					
Z42<10>	CHX1	A3	68Ω	MBC5 FORCE VAL MATCH 3 H					
Z34<1>	CHX5	B4	68Ω	NC					
Z43<1>	CHX5	B4	68Ω	NC					
Z44<1>	CHX5	B4	68Ω	NC					
Z47<1>	CHX5	B4	68Ω	NC					
Z48<1>	CHX5	B4	68Ω	NC					
Z49<1>	CHX5	B4	68Ω	NC					
Z51<8>	CHX1	D7	68Ω	PAG2 PT 14 B H					
Z51<6>	CHX1	D7	68Ω	PAG2 PT 15 B H					
Z51<3>	CHX1	D7	68Ω	PAG2 PT 16 B H					
Z37<6>	CHX1	D7	68Ω	PAG2 PT 17 B H					
Z37<8>	CHX1	D7	68Ω	PAG2 PT 18 B H					
Z37<10>	CHX1	D7	68Ω	PAG2 PT 19 B H					
Z23<1>	CHX1	D7	68Ω	PAG7 PT 20 B H					
Z23<6>	CHX1	D7	68Ω	PAG7 PT 21 B H					
Z57<6>	CHX1	D7	68Ω	PAG7 PT 22 B H					
Z55<3>	CHX1	D7	68Ω	PAG7 PT 23 B H					
Z54<10>	CHX1	C7	68Ω	PAG7 PT 24 B H					
Z57<3>	CHX1	C7	68Ω	PAG7 PT 25 B H					

NOTE:

- ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
- ENTRIES ARE SORTED BY SIGNAL NAME
- % INDICATES OUTPUT OF DIP LOC AND
< > INDICATES PIN NUMBER

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRW. <i>J. J. J.</i>	DATE 06-AUG-84	ENG. C. A. JENS	DATE 06-AUG-84	TITLE: CACHE EXTENSION TERMINATORS
	CHK'D D. DELLORCO	DATE 06-AUG-84	BOARD LOCATION: 4AF28	SHEET 2 OF 2	
XTRA: MCA25, M8515, M8532, 0RM 124-JUL-84 11:32		NEXT HIGHER ASSEMBLY: D-DD-M853-0		SIZE CODE D CS	NUMBER M853-0-RES
FIRST USED ON OPTION/MODEL: MCA25				REV. A	

MRO 1

DRAWING NUMBER	NO.OF SHT	PART NUMBER	DESCRIPTION	REVISION
		M854-00	MODULE REVISION	A1
D-UA-M854-0-0	1		PHYSICAL MEMORY ADDRESS	A
K-PL-M854-0-DBP	2		PART LIST, M854	A
D-CS-M854-0-PMA1	1		EBR & UBR REG UEBR MIX & VA BUF	A
D-CS-M854-0-PMA2	1		CACHE CLEARER ADR CTR	A
D-CS-M854-0-PMA3	1		PHY ADR MIX PA 14-31	A
D-CS-M854-0-PMA4	1		ERA REG, ADR PAR, PA 32-35 MIX	A
D-CS-M854-0-PMA5	1		PHY MEM ADR CONTROL LOGIC	A
D-CS-M854-0-PMA6	1		PHYSICAL MEM ADR PWR, GND,CAP	A
D-CS-M854-0-RES	2		PHYSICAL MEMORY ADDRESS TERMINATORS	A
K-PC-M854-0-DBI	-		P.C. DESIGN DATA BASE TAPE	A
D-DD-5017660-0	1		DRAWING DIRECTORY, 5017660	REF

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REVISIONS

CHK	CHANGE NO.	REV
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d	i	g	i	t	a
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I	DRN.
	D. DELLORCO
	CHK'D.
	D. DELLORCO

	DATE 09-JUL-1
	DATE 09-JUL-1

ENG.
C.
BOARD
SHEET

KACZOR
LOCATION
1

DATE	89-JUL
N:	N/A
OF	1

64	TITLE:
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M854
DRAWING DIRECTORY

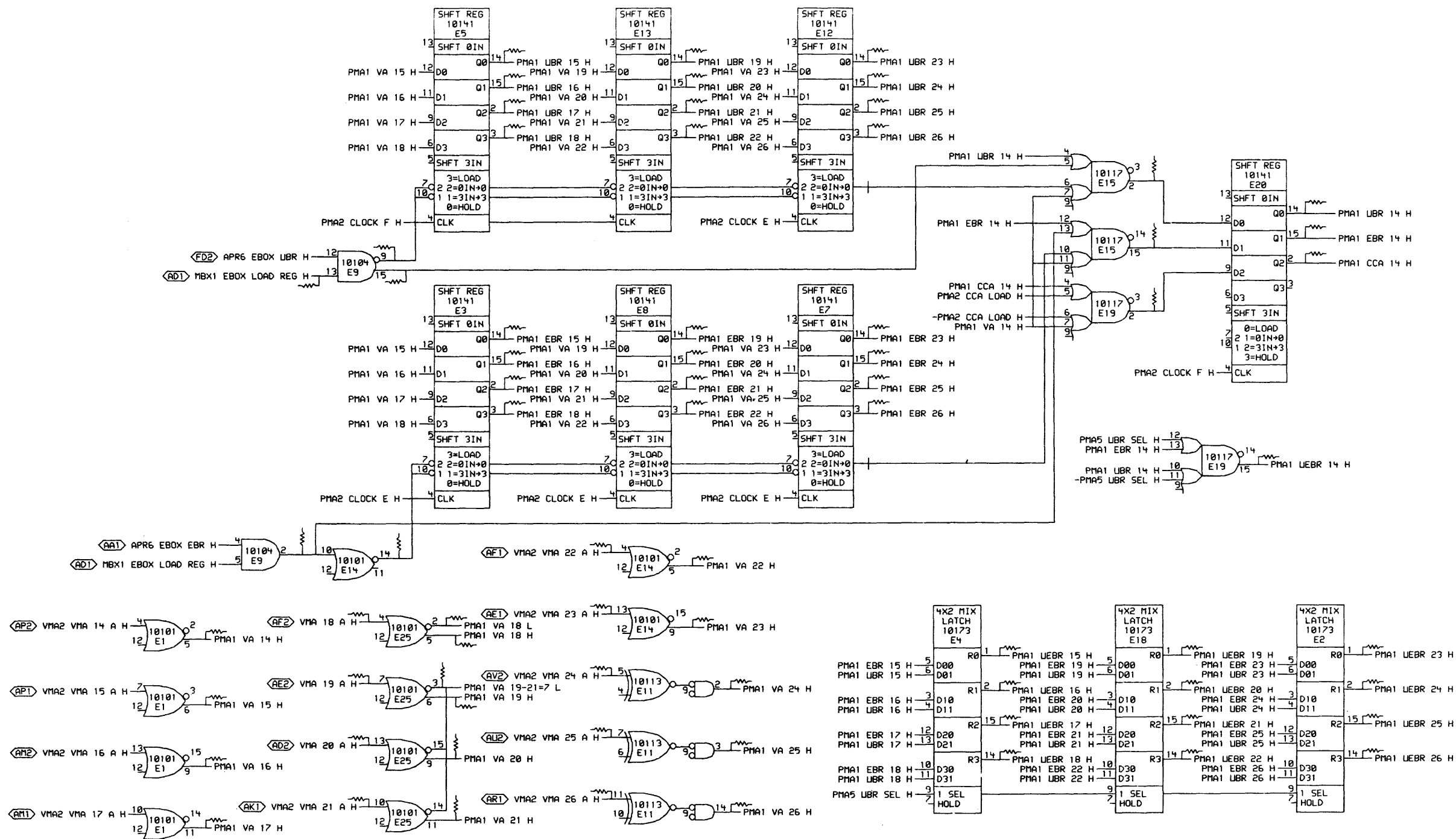
DSK:M854A.T2P(4,21)	09-JUL-84
FIRST USED ON OPTION MODEL: K110	

16:40	NEXT HIGHER ASSEMBLY:
DU	MC025

SIZE	CODE
D	DD

MOEL 2 N

NUMBER	REV.
	1



1 OF 6

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REVISIONS		
CHK	CHANGE NO.	REV

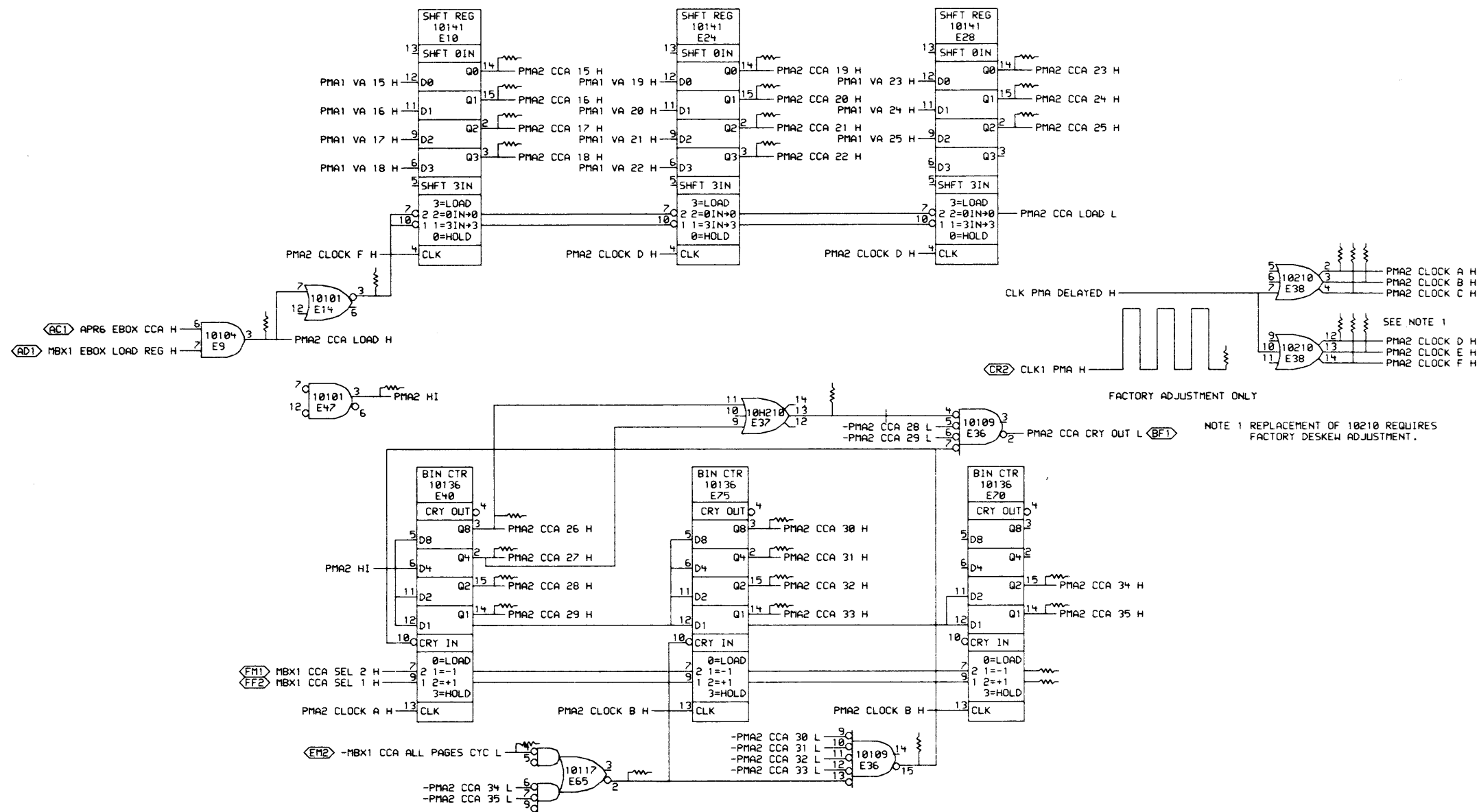
digital
 CHK'D
 D. DELLORCO

DATE 05-JUL-84
 DATE 05-JUL-84
 SHEET 1 OF 1

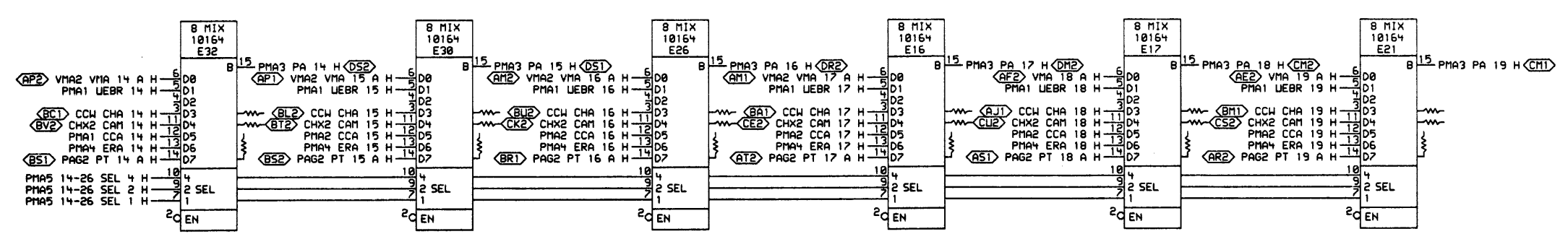
ENG. C. KACZOR
 BOARD LOCATION: 4AF29
 NEXT HIGHER ASSEMBLY:
 D-DD-M854-0

TITLE: EBR & UBR REG
 UEBR MIX & VA BUF

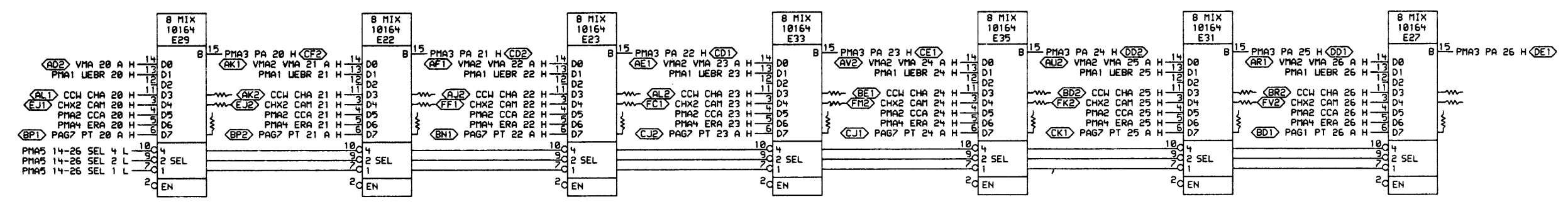
SIZE	CODE	NUMBER	REV.
D	CS	M854-0-PMA1	A



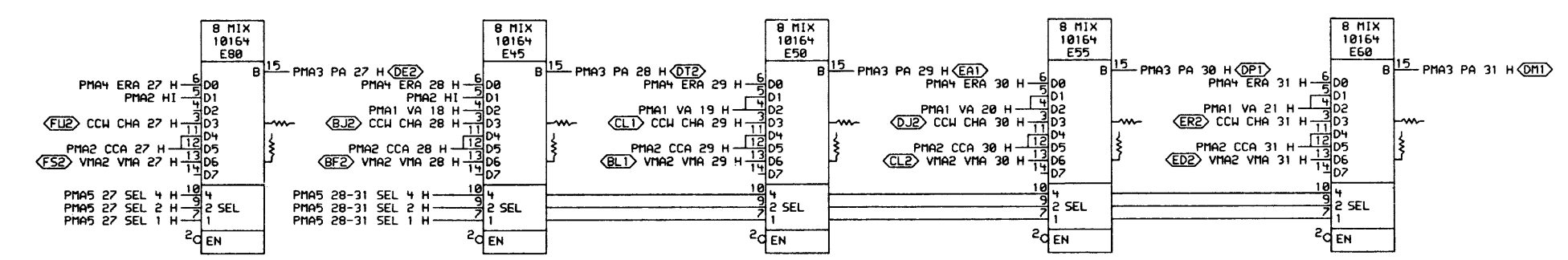
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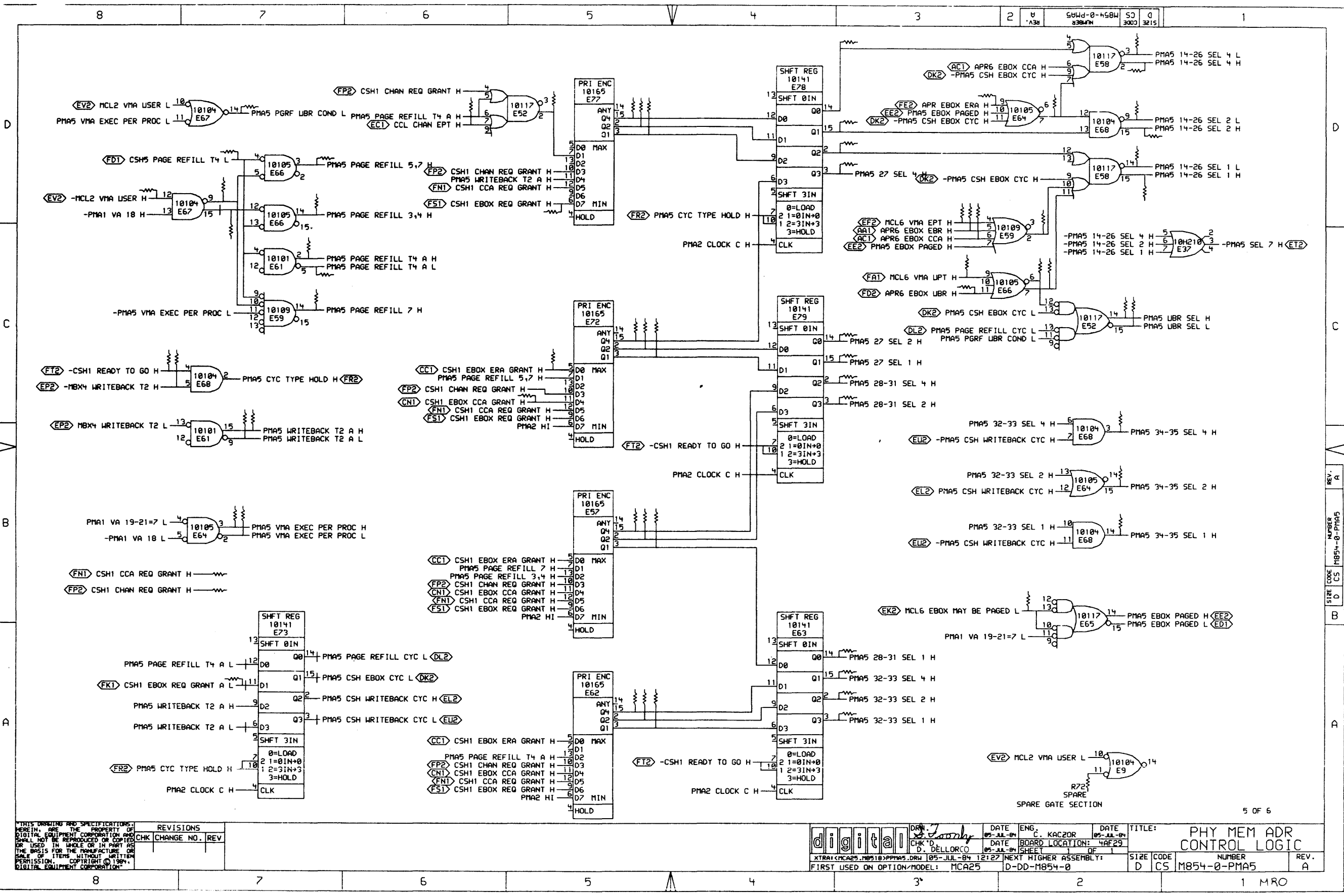
C



B



A



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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE:	
	05-JUL-84	C. KACZOR	05-JUL-84	PHY MEM ADR CONTROL LOGIC	
CHK'D	DATE	BOARD LOCATION:	44F29		
D. DELLORCO	05-JUL-84	SHEET	1		
NEXT HIGHER ASSEMBLY:		SIZE	CODE	NUMBER	
FIRST USED ON OPTION/MODEL: MCA25		D-DD-M854-0	D	CS	M854-0-PMA5
				REV.	A
				1	MRO

D

C

B

A

RESISTOR LOC(PIN)	SHOWN DRAW#	ON REF	VALUE	TERMINATES SIGNAL
R171(1)	PMA1	B6	68n	%E14(14)
R11(1)	PMA1	C2	68n	%E15(15)
R12(1)	PMA1	D2	68n	%E15(2)
R8(1)	PMA1	C2	68n	%E19(2)
R30(1)	PMA2	A4	68n	%E36(15)
R29(1)	PMA2	C4	68n	%E37(13)
R226(1)	PMA4	C2	68n	%E43(2)
R219(1)	PMA5	D5	68n	%E52(2)
R163(1)	PMA5	B5	68n	%E57(15)
R160(1)	PMA5	B5	68n	%E57(2)
R147(1)	PMA5	B5	68n	%E57(3)
R144(1)	PMA5	C2	68n	%E59(2)
R150(1)	PMA5	A5	68n	%E62(15)
R153(1)	PMA5	A5	68n	%E62(2)
R152(1)	PMA5	A5	68n	%E62(3)
R145(1)	PMA5	D2	68n	%E64(7)
R28(1)	PMA2	A5	68n	%E65(2)
R204(1)	PMA5	C2	68n	%E66(6)
R146(1)	PMA5	C2	68n	%E66(7)
R251(1)	PMA5	D7	68n	%E67(15)
R262(1)	PMA5	D7	68n	%E67(9)
R159(1)	PMA5	C5	68n	%E72(15)
R114(1)	PMA5	C5	68n	%E72(2)
R119(1)	PMA5	C5	68n	%E72(3)
R158(1)	PMA5	D5	68n	%E77(15)
R162(1)	PMA5	D5	68n	%E77(2)
R161(1)	PMA5	D5	68n	%E77(3)
R140(1)	PMA5	D3	68n	%E78(14)
R151(1)	PMA5	D3	68n	%E78(15)
R143(1)	PMA5	D3	68n	%E78(2)
R72(1)	PMA5	A2	68n	%E9(11)
R9(1)	PMA1	C6	68n	%E9(15)
R5(1)	PMA1	B7	68n	%E9(2)
R170(1)	PMA1	C6	68n	%E9(9)
R104(1)	PMA5	D3	68n	APR EBOX ERA H
R272(1)	PMA5	D3	68n	APR6 EBOX CCA H
R103(1)	PMA5	D3	68n	APR6 EBOX EBR H
R71(1)	PMA5	C3	68n	APR6 EBOX UBR H
R205(1)	PMA5	D6	68n	CCL CHAN EPT H
R135(1)	PMA3	D7	68n	CCW CHA 14 H

- NOTE:
1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
 2. ENTRIES ARE SORTED BY SIGNAL NAME
 3. % INDICATES OUTPUT OF DIP LOC AND () INDICATES PIN NUMBER

RESISTOR LOC(PIN)	SHOWN DRAW#	ON REF	VALUE	TERMINATES SIGNAL
R247(1)	PMA3	D6	68n	CCW CHA 15 H
R243(1)	PMA3	D5	68n	CCW CHA 16 H
R232(1)	PMA3	D4	68n	CCW CHA 17 H
R177(1)	PMA3	D3	68n	CCW CHA 18 H
R238(1)	PMA3	D2	68n	CCW CHA 19 H
R84(1)	PMA3	C7	68n	CCW CHA 20 H
R187(1)	PMA3	C6	68n	CCW CHA 21 H
R80(1)	PMA3	C5	68n	CCW CHA 22 H
R202(1)	PMA3	C4	68n	CCW CHA 23 H
R93(1)	PMA3	C3	68n	CCW CHA 24 H
R196(1)	PMA3	C2	68n	CCW CHA 25 H
R189(1)	PMA3	C1	68n	CCW CHA 26 H
R60(1)	PMA3	B7	68n	CCW CHA 27 H
R34(1)	PMA3	B6	68n	CCW CHA 28 H
R44(1)	PMA3	B5	68n	CCW CHA 29 H
R48(1)	PMA3	B4	68n	CCW CHA 30 H
R52(1)	PMA3	B3	68n	CCW CHA 31 H
R255(1)	PMA4	C7	68n	CCW CHA 32 H
R218(1)	PMA4	C6	68n	CCW CHA 33 H
R89(1)	PMA3	D7	68n	CHX2 CAM 14 H
R245(1)	PMA3	D6	68n	CHX2 CAM 15 H
R241(1)	PMA3	D5	68n	CHX2 CAM 16 H
R230(1)	PMA3	D4	68n	CHX2 CAM 17 H
R175(1)	PMA3	D3	68n	CHX2 CAM 18 H
R234(1)	PMA3	D2	68n	CHX2 CAM 19 H
R19(1)	PMA3	C7	68n	CHX2 CAM 20 H
R183(1)	PMA3	C6	68n	CHX2 CAM 21 H
R81(1)	PMA3	C5	68n	CHX2 CAM 22 H
R23(1)	PMA3	C4	68n	CHX2 CAM 23 H
R97(1)	PMA3	C3	68n	CHX2 CAM 24 H
R92(1)	PMA3	C2	68n	CHX2 CAM 25 H
R190(1)	PMA3	C1	68n	CHX2 CAM 26 H
R141(1)	PMA2	C2	68n	CLK1 PMA H
R210(1)	PMA5	B7	68n	CSH1 CCA REQ GRANT H
R209(1)	PMA5	B7	68n	CSH1 CHAN REQ GRANT H
R211(1)	PMA5	C5	68n	CSH1 EBOX CCA GRANT H
R213(1)	PMA5	C5	68n	CSH1 EBOX ERA GRANT H
R212(1)	PMA5	D5	68n	CSH1 EBOX REQ GRANT H
R157(1)	PMA5	A7	68n	-CSH1 EBOX REQ GRANT A H
R149(1)	PMA5	C8	68n	-CSH1 READY TO GO H

RESISTOR LOC(PIN)	SHOWN DRAW#	ON REF	VALUE	TERMINATES SIGNAL
R263(1)	PMA5	D7	68n	-CSH5 PAGE REFILL T4 H
R100(1)	PMA4	C7	68n	HOLD ERA H
R54(1)	PMA2	A6	68n	MBX1 CCA ALL PAGES CYC H
R32(1)	PMA2	B3	68n	MBX1 CCA SEL 1 H
R99(1)	PMA2	B3	68n	MBX1 CCA SEL 2 H
R70(1)	PMA1	C7	68n	MBX1 EBOX LOAD REG H
R115(1)	PMA4	C5	68n	MBX2 CACHE TO MB 34 H
R111(1)	PMA4	C4	68n	MBX2 CACHE TO MB 35 H
R155(1)	PMA5	C7	68n	-MBX4 WRITEBACK T2 H
R73(1)	PMA5	D8	68n	-MCL2 VMA USER H
R55(1)	PMA5	B2	68n	-MCL6 EBOX MAY BE PAGED H
R102(1)	PMA5	D3	68n	MCL6 VMA EPT H
R252(1)	PMA5	C3	68n	MCL6 VMA UPT H
R49(1)	PMA6	C1	68n	NC
R88(1)	PMA6	C1	68n	NC
R101(1)	PMA6	C1	68n	NC
R154(1)	PMA6	C1	68n	NC
R195(1)	PMA6	C1	68n	NC
R197(1)	PMA6	C1	68n	NC
R203(1)	PMA6	C1	68n	NC
R217(1)	PMA6	C1	68n	NC
R254(1)	PMA6	C1	68n	NC
R140(1)	PMA3	C1	68n	PAG1 PT 26 A H
R137(1)	PMA3	D7	68n	PAGE PT 14 A H
R249(1)	PMA3	D6	68n	PAGE PT 15 A H
R237(1)	PMA3	D5	68n	PAGE PT 16 A H
R228(1)	PMA3	D4	68n	PAGE PT 17 A H
R173(1)	PMA3	D3	68n	PAGE PT 18 A H
R231(1)	PMA3	D2	68n	PAGE PT 19 A H
R83(1)	PMA3	C7	68n	PAGE PT 20 A H
R106(1)	PMA3	C6	68n	PAGE PT 21 A H
R133(1)	PMA3	C5	68n	PAGE PT 22 A H
R21(1)	PMA3	C4	68n	PAGE PT 23 A H
R96(1)	PMA3	C3	68n	PAGE PT 24 A H
R201(1)	PMA3	C2	68n	PAGE PT 25 A H
R86(1)	PMA1	C2	68n	PMA1 CCA 14 H
R75(1)	PMA1	C2	68n	PMA1 EBR 14 H
R69(1)	PMA1	C6	68n	PMA1 EBR 15 H
R66(1)	PMA1	C6	68n	PMA1 EBR 16 H
R65(1)	PMA1	C6	68n	PMA1 EBR 17 H

RESISTOR LOC(PIN)	SHOWN DRAW#	ON REF	VALUE	TERMINATES SIGNAL
R120(1)	PMA1	C6	68n	PMA1 EBR 18 H
R130(1)	PMA1	C5	68n	PMA1 EBR 19 H
R127(1)	PMA1	C5	68n	PMA1 EBR 20 H
R123(1)	PMA1	C5	68n	PMA1 EBR 21 H
R125(1)	PMA1	C5	68n	PMA1 EBR 22 H
R169(1)	PMA1	C4	68n	PMA1 EBR 23 H
R166(1)	PMA1	C4	68n	PMA1 EBR 24 H
R165(1)	PMA1	C4	68n	PMA1 EBR 25 H
R225(1)	PMA1	C4	68n	PMA1 EBR 26 H
R76(1)	PMA1	D2	68n	PMA1 UBR 14 H
R68(1)	PMA1	D6	68n	PMA1 UBR 15 H
R67(1)	PMA1	D6	68n	PMA1 UBR 16 H
R64(1)	PMA1	D6	68n	PMA1 UBR 17 H
R122(1)	PMA1	D6	68n	PMA1 UBR 18 H
R129(1)	PMA1	D5	68n	PMA1 UBR 19 H
R128(1)	PMA1	D5	68n	PMA1 UBR 20 H
R124(1)	PMA1	D5	68n	PMA1 UBR 21 H
R126(1)	PMA1	D5	68n	PMA1 UBR 22 H
R168(1)	PMA1	D4	68n	PMA1 UBR 23 H
R167(1)	PMA1	D4	68n	PMA1 UBR 24 H
R164(1)	PMA1	D4	68n	PMA1 UBR 25 H
R224(1)	PMA1	D4	68n	PMA1 UBR 26 H
R138(1)	PMA1	B2	68n	PMA1 UEBR 14 H
R248(1)	PMA1	B3	68n	PMA1 UEBR 15 H
R244(1)	PMA1	A3	68n	PMA1 UEBR 16 H
R233(1)	PMA1	A3	68n	PMA1 UEBR 17 H
R178(1)	PMA1	A3	68n	PMA1 UEBR 18 H
R239(1)	PMA1	B2	68n	PMA1 UEBR 19 H
R85(1)	PMA1	A2	68n	PMA1 UEBR 20 H
R176(1)	PMA1	A2	68n	PMA1 UEBR 21 H
R79(1)	PMA1	A2	68n	PMA1 UEBR 22 H
R18(1)	PMA1	B1	68n	PMA1 UEBR 23 H
R90(1)	PMA1	A1	68n	PMA1 UEBR 24 H
R193(1)	PMA1	A1	68n	PMA1 UEBR 25 H
R188(1)	PMA1	A1	68n	PMA1 UEBR 26 H
R7(1)	PMA1	B7	68n	PMA1 VA 14 H
R6(1)	PMA1	A7	68n	PMA1 VA 15 H
R1(1)	PMA1	A7	68n	PMA1 VA 16 H
R3(1)	PMA1	A7	68n	PMA1 VA 17 H
R105(1)	PMA1	B6	68n	PMA1 VA 18 H

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R215(1)	PMA1	B6	68n	-PMA1 VA 18 H
R42(1)	PMA1	A6	68n	PMA1 VA 19 H
R107(1)	PMA1	A6	68n	-PMA1 VA 19-21=7 H
R46(1)	PMA1	A6	68n	PMA1 VA 20 H
R50(1)	PMA1	A6	68n	PMA1 VA 21 H
R256(1)	PMA1	B5	68n	PMA1 VA 22 H
R222(1)	PMA1	B5	68n	PMA1 VA 23 H
R87(1)	PMA1	B4	68n	PMA1 VA 24 H
R131(1)	PMA1	A4	68n	PMA1 VA 25 H
R265(1)	PMA1	A4	68n	PMA1 VA 26 H
R246(1)	PMA2	D6	68n	PMA2 CCA 15 H
R240(1)	PMA2	D6	68n	PMA2 CCA 16 H
R229(1)	PMA2	D6	68n	PMA2 CCA 17 H
R174(1)	PMA2	D6	68n	PMA2 CCA 18 H
R236(1)	PMA2	D5	68n	PMA2 CCA 19 H
R20(1)	PMA2	D5	68n	PMA2 CCA 20 H
R184(1)	PMA2	D5	68n	PMA2 CCA 21 H
R132(1)	PMA2	D5	68n	PMA2 CCA 22 H
R24(1)	PMA2	D3	68n	PMA2 CCA 23 H
R94(1)	PMA2	D3	68n	PMA2 CCA 24 H
R199(1)	PMA2	D3	68n	PMA2 CCA 25 H
R194(1)	PMA2	B6	68n	PMA2 CCA 26 H
R50(1)	PMA2	B6	68n	PMA2 CCA 27 H
R36(1)	PMA2	B6	68n	PMA2 CCA 28 H
R37(1)	PMA2	B6	68n	PMA2 CCA 29 H
R31(1)	PMA2	B5	68n	PMA2 CCA 30 H
R25(1)	PMA2	B5	68n	PMA2 CCA 31 H
R26(1)	PMA2	B5	68n	PMA2 CCA 32 H
R27(1)	PMA2	B5	68n	PMA2 CCA 33 H
R113(1)	PMA2	B3	68n	PMA2 CCA 34 H
R109(1)	PMA2	B3	68n	PMA2 CCA 35 H
R74(1)	PMA2	C7	68n	PMA2 CCA LOAD H
R4(1)	PMA2	C6	68n	-PMA2 CCA LOAD H
R142(1)	PMA2	C2	68n	PMA2 CLOCK A H
R56(1)	PMA2	C2	68n	PMA2 CLOCK B H
R63(1)	PMA2	C2	68n	PMA2 CLOCK C H
R16(1)	PMA2	C2	68n	PMA2 CLOCK D H
R121(1)	PMA2	C2	68n	PMA2 CLOCK E H
R2(1)	PMA2	C2	68n	PMA2 CLOCK F H
R268(1)	PMA2	C6	68n	PMA2 HI

NOTE:

1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES OUTPUT OF DIP LOC AND
() INDICATES PIN NUMBER

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R136(1)	PMA4	D7	68n	PMA4 ERA 14 H
R250(1)	PMA4	D7	68n	PMA4 ERA 15 H
R242(1)	PMA4	D7	68n	PMA4 ERA 16 H
R227(1)	PMA4	D7	68n	PMA4 ERA 17 H
R172(1)	PMA4	D6	68n	PMA4 ERA 18 H
R235(1)	PMA4	D6	68n	PMA4 ERA 19 H
R02(1)	PMA4	D6	68n	PMA4 ERA 20 H
R185(1)	PMA4	D6	68n	PMA4 ERA 21 H
R134(1)	PMA4	D5	68n	PMA4 ERA 22 H
R22(1)	PMA4	D5	68n	PMA4 ERA 23 H
R95(1)	PMA4	D5	68n	PMA4 ERA 24 H
R200(1)	PMA4	D5	68n	PMA4 ERA 25 H
R139(1)	PMA4	D4	68n	PMA4 ERA 26 H
R61(1)	PMA4	D4	68n	PMA4 ERA 27 H
R39(1)	PMA4	D4	68n	PMA4 ERA 28 H
R43(1)	PMA4	D4	68n	PMA4 ERA 29 H
R47(1)	PMA4	D3	68n	PMA4 ERA 30 H
R51(1)	PMA4	D3	68n	PMA4 ERA 31 H
R257(1)	PMA4	D3	68n	PMA4 ERA 32 H
R221(1)	PMA4	D3	68n	PMA4 ERA 33 H
R116(1)	PMA4	D1	68n	PMA4 ERA 34 H
R108(1)	PMA4	D1	68n	PMA4 ERA 35 H
R181(1)	PMA5	D2	68n	PMA5 14-26 SEL 1 H
R271(1)	PMA5	D2	68n	-PMA5 14-26 SEL 1 H
R180(1)	PMA5	D2	68n	PMA5 14-26 SEL 2 H
R270(1)	PMA5	D2	68n	-PMA5 14-26 SEL 2 H
R179(1)	PMA5	D2	68n	PMA5 14-26 SEL 4 H
R269(1)	PMA5	D2	68n	-PMA5 14-26 SEL 4 H
R62(1)	PMA5	C3	68n	PMA5 27 SEL 1 H
R53(1)	PMA5	C3	68n	PMA5 27 SEL 2 H
R57(1)	PMA5	D3	68n	PMA5 27 SEL 4 H
R40(1)	PMA5	A3	68n	PMA5 28-31 SEL 1 H
R98(1)	PMA5	C3	68n	PMA5 28-31 SEL 2 H
R35(1)	PMA5	C3	68n	PMA5 28-31 SEL 4 H
R220(1)	PMA5	A3	68n	PMA5 32-33 SEL 1 H
R258(1)	PMA5	A3	68n	PMA5 32-33 SEL 2 H
R259(1)	PMA5	A3	68n	PMA5 32-33 SEL 4 H
R117(1)	PMA5	B2	68n	PMA5 34-35 SEL 1 H
R118(1)	PMA5	B2	68n	PMA5 34-35 SEL 2 H
R112(1)	PMA5	B2	68n	PMA5 34-35 SEL 4 H

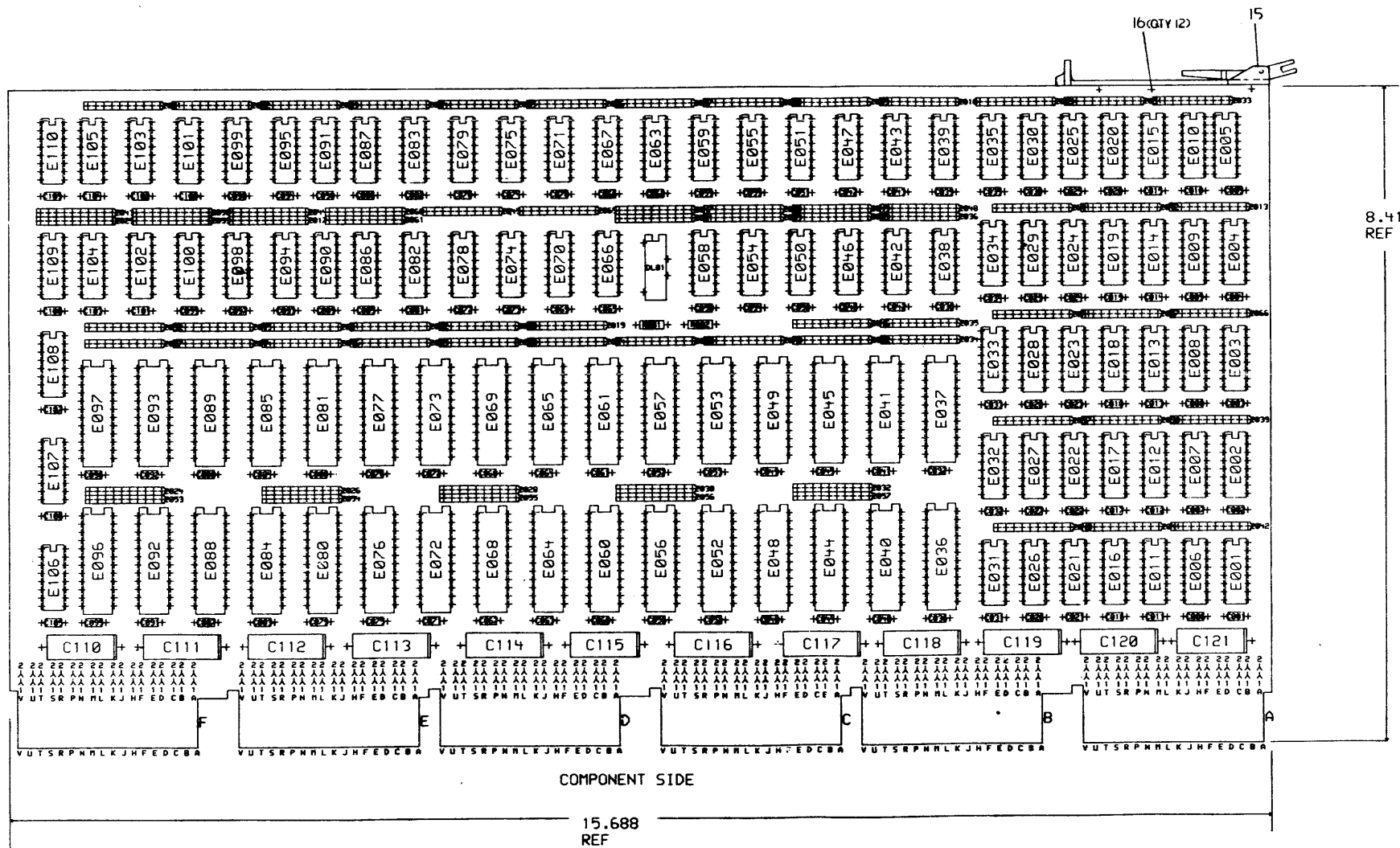
RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R208(1)	PMA5	D7	68n	PMA5 PAGE REFILL 3,4 H
R216(1)	PMA5	D7	68n	PMA5 PAGE REFILL 5,7 H
R264(1)	PMA5	C7	68n	PMA5 PAGE REFILL 7 H
R206(1)	PMA5	C7	68n	PMA5 PAGE REFILL T4 A H
R266(1)	PMA5	C7	68n	-PMA5 PAGE REFILL T4 A H
R207(1)	PMA5	D7	68n	-PMA5 PGRF UBR COND H
R223(1)	PMA5	C2	68n	PMA5 UBR SEL H
R77(1)	PMA5	C2	68n	-PMA5 UBR SEL H
R261(1)	PMA5	B7	68n	PMA5 VMA EXEC PER PROC H
R214(1)	PMA5	B7	68n	-PMA5 VMA EXEC PER PROC H
R156(1)	PMA5	B7	68n	PMA5 WRITEBACK T2 A H
R267(1)	PMA5	B7	68n	-PMA5 WRITEBACK T2 A H
R13(1)	PMA1	B6	68n	VMA 18 A H
R14(1)	PMA1	B6	68n	VMA 19 A H
R10(1)	PMA1	A6	68n	VMA 20 A H
R15(1)	PMA1	A6	68n	VMA2 VMA 21 A H
R78(1)	PMA1	B5	68n	VMA2 VMA 22 A H
R17(1)	PMA1	B5	68n	VMA2 VMA 23 A H
R91(1)	PMA1	B5	68n	VMA2 VMA 24 A H
R192(1)	PMA1	A5	68n	VMA2 VMA 25 A H
R182(1)	PMA1	A5	68n	VMA2 VMA 26 A H
R59(1)	PMA3	A7	68n	VMA2 VMA 27 H
R33(1)	PMA3	A6	68n	VMA2 VMA 28 H
R38(1)	PMA3	A5	68n	VMA2 VMA 29 H
R41(1)	PMA3	A4	68n	VMA2 VMA 30 H
R45(1)	PMA3	A3	68n	VMA2 VMA 31 H
R253(1)	PMA4	C7	68n	VMA2 VMA 32 H
R260(1)	PMA4	C6	68n	VMA2 VMA 33 H
R110(1)	PMA4	C5	68n	VMA2 VMA 34 H
R106(1)	PMA4	C4	68n	VMA2 VMA 35 H

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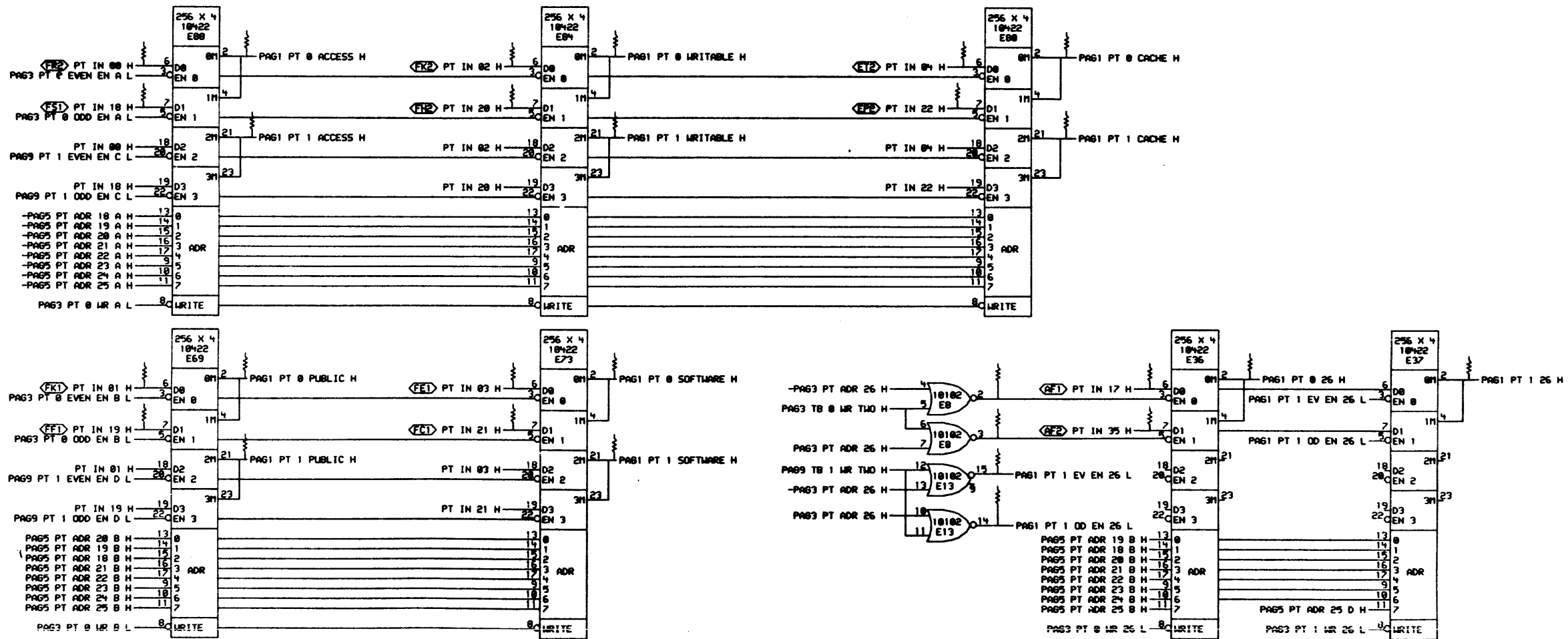
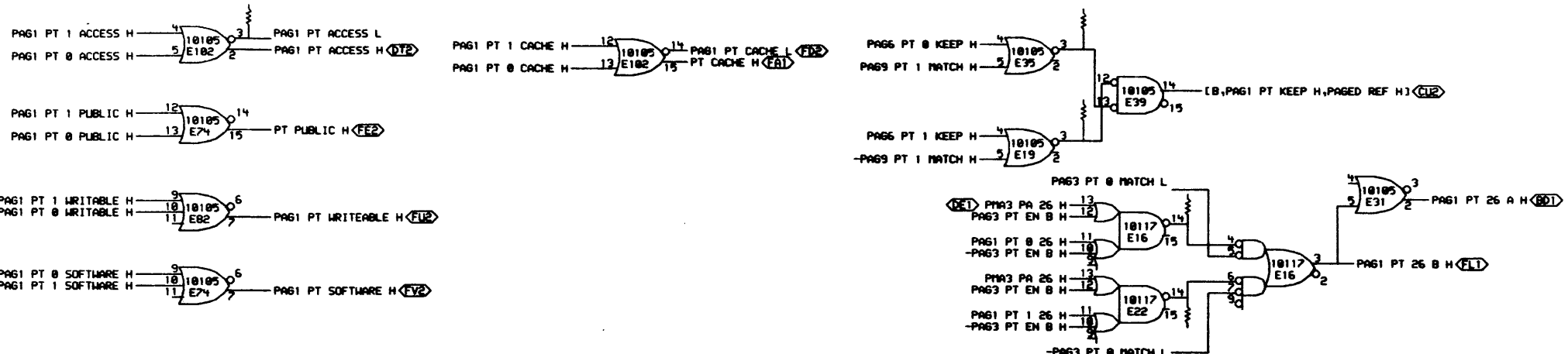
REVISIONS
CHK CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE: PHYSICAL MEMORY ADDRESS--TERMINATORS
	05-JUL-84	C. KACZOR	05-JUL-84	
CHK'D	DATE	BOARD LOCATION:	DATE	
D. DELLORCO	05-JUL-84	4AF29	05-JUL-84	
XTRAI<MCA25.M8518>M8542.DRW		12:28		NEXT HIGHER ASSEMBLY:
FIRST USED ON OPTION/MODEL: MCA25		D-DD-M854-0		
SIZE	CODE	NUMBER	REV.	
D	CS	M854-0-RES	A	

NOTES: THIS BOARD MUST MEET SAFETY REQ. FOR HPWR.
2. SPARE COMPONENT LOCATIONS ARE: E12, E43, E103, E105.



DPN P. LASSEN	DATE 08MARG4	TITLE PAG
CHK'D J. SELLERS	DATE 14 Jun 84	DOCUMENT NUMBER M855-0-0
DES. ENG. C. Hage	DATE 7-25-84	
RESP. ENG. C. Hage	DATE 7-25-84	
DATE 26 July 84		
NEXT HIGHER DOD 0-00-M855-0	SCALE 2 / 1	SHEET 1

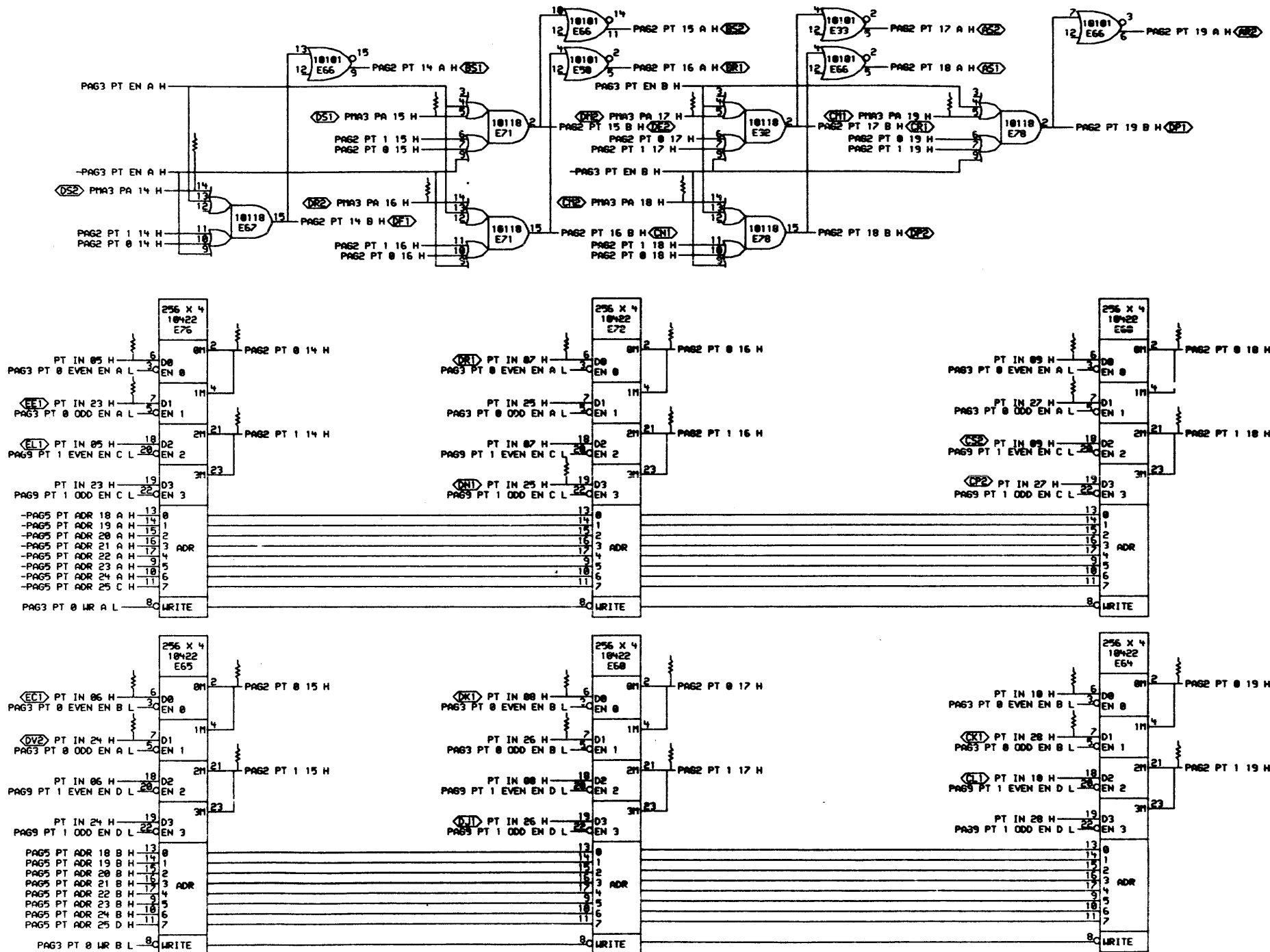


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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE: PAGE TABLE DATA PT ACCESS-PT 17
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CHK'D	DATE	BOARD LOCATION	46F38	
23-JUL-84	23-JUL-84	23-JUL-84	23-JUL-84	
FIRST USED ON OPTION MODEL: MCA25				
D-DD-M855-0				
SIZE	CODE	NUMBER	REV.	
D	CS	M855-0-PAG1	A1	

REV. A1
NUMBER M855-0-PAG1
SIZE D
CODE CS
B



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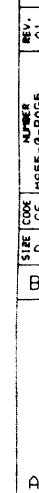
REVISIONS
CHK CHANGE NO. REV

digital
D. DELLORCO
FIRST USED ON OPTION MODEL: MCA25

DATE: 23-JUL-84
DATE: 23-JUL-84
DATE: 23-JUL-84

ENG. C. KACZOR
DATE: 23-JUL-84
DATE: 23-JUL-84
DATE: 23-JUL-84

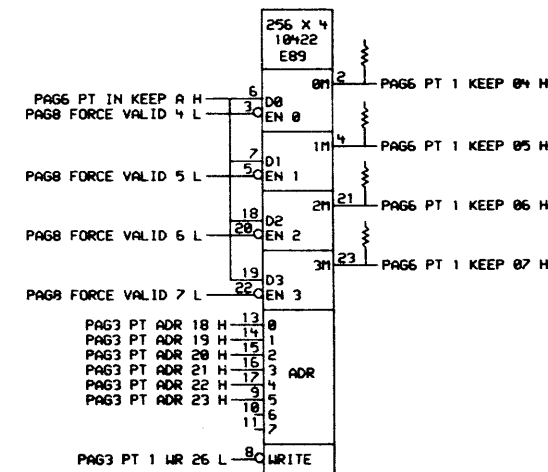
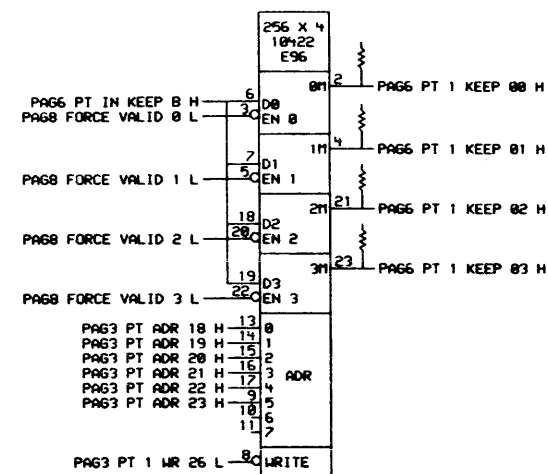
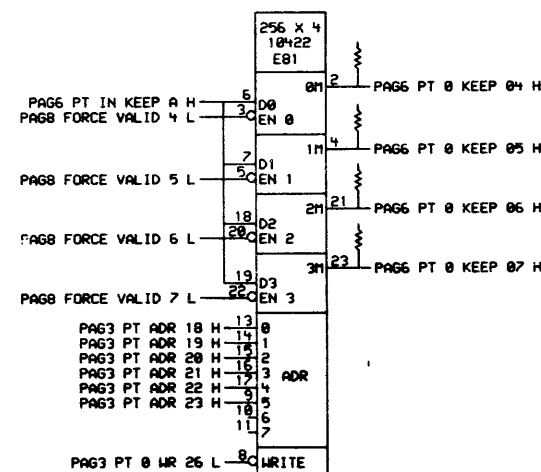
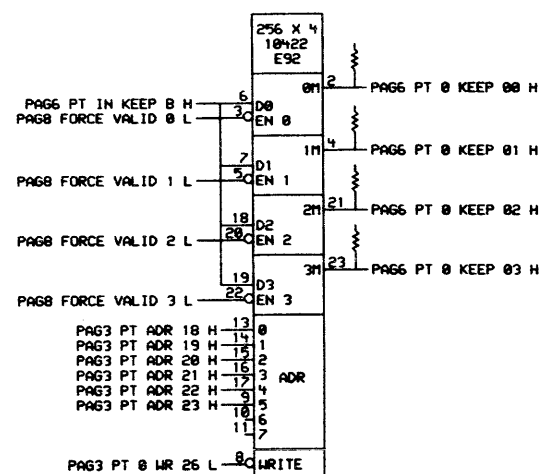
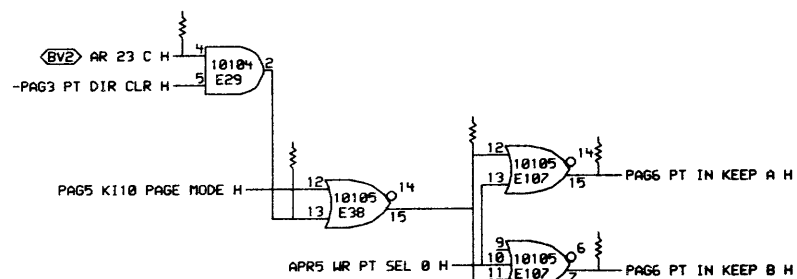
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PT 16 - PT 26
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REV. A1

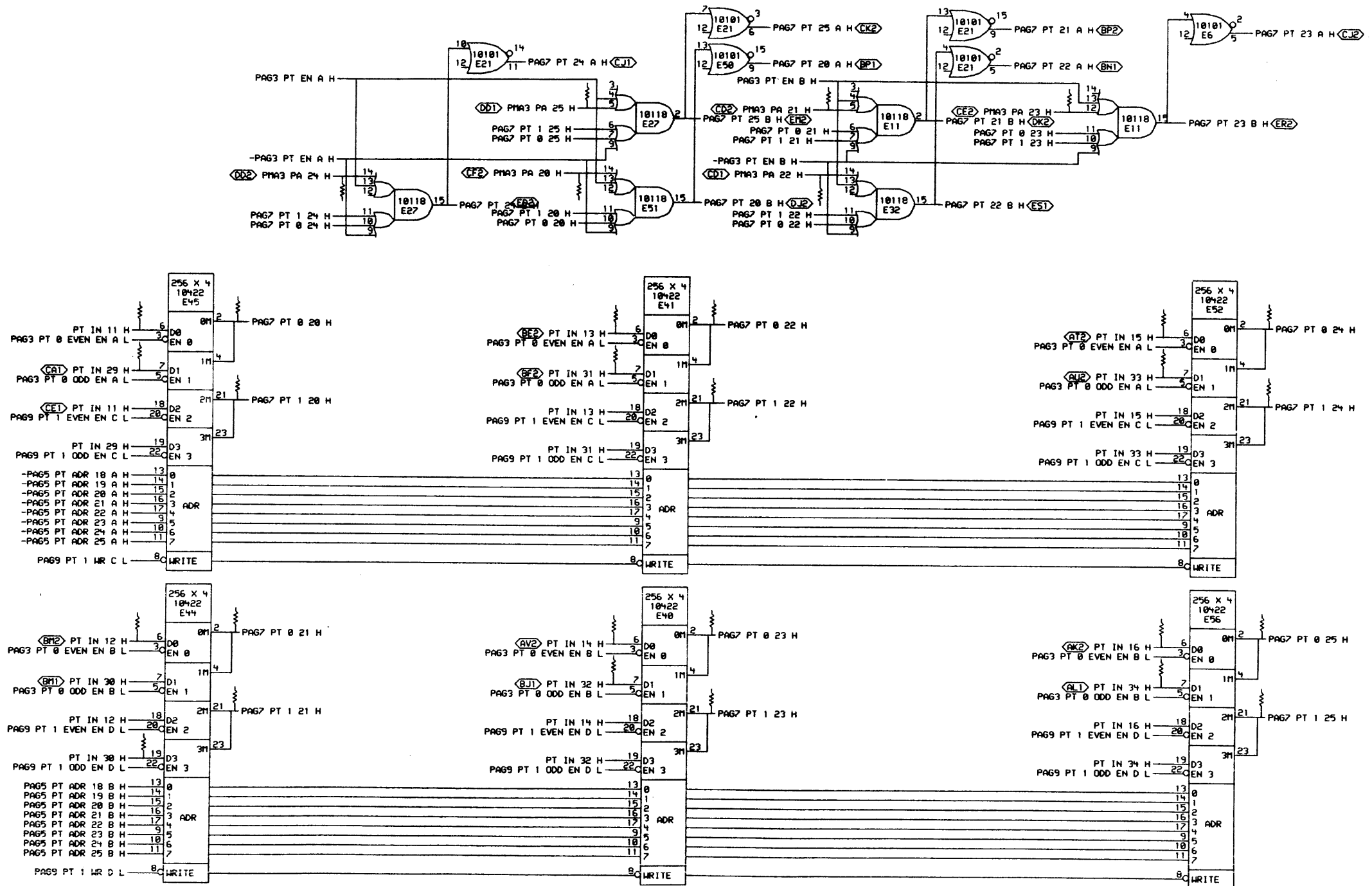


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CHANGE NO.	REV

TITLE:	PAGE TABLE PARITY LOGIC
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SIZE	CODE	NUMBER	REV.
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REVISIONS
CHK CHANGE NO. REV

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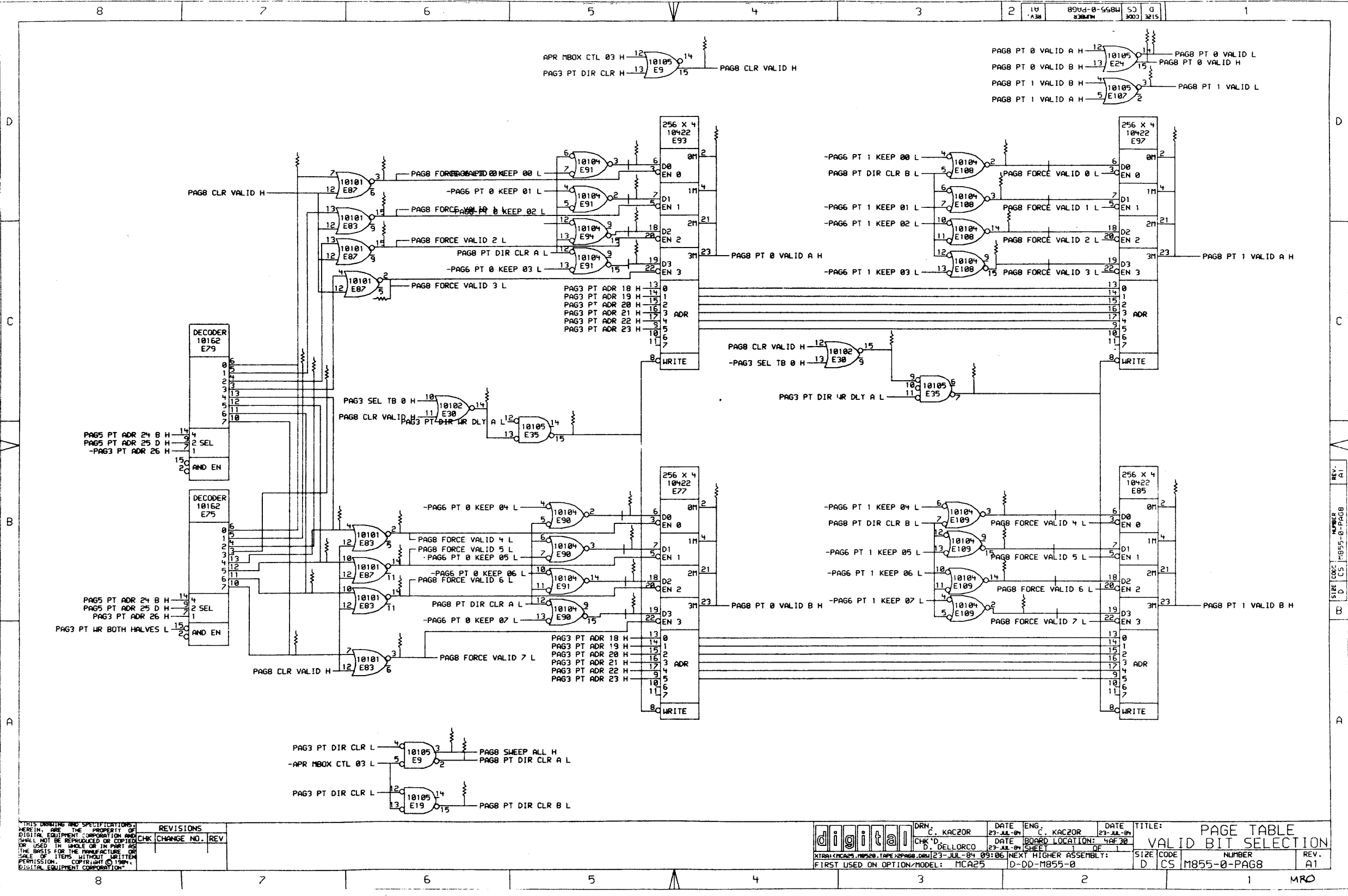
DRN.
C. KACZOR
CHK'D
D. DELLORCO

DATE
23-JUL-84
DATE
23-JUL-84
SHEET
OF 1

ENG.
C. KACZOR
DATE
23-JUL-84
BOARD LOCATION
TAF-30
TITLE:
PAGE TABLE DATA
PT 20 - 25

EXTN: (MCA25, M855, TAPE) 22PAG7.DWG 23-JUL-84 09:05 NEXT HIGHER ASSEMBLY:
FIRST USED ON OPTION/MODEL: MCA25 D-DD-M855-0

SIZE CODE
D CS
NUMBER
M855-0-PAG7
REV.
A1



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REVISIONS		
CHK	CHANGE NO.	REV

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	CHK'D D. DELLORCO	DATE 23-JUL-84	BOARD LOCATION: 4AF30	SHEET 1 OF 1	
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D

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D

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REV. A1

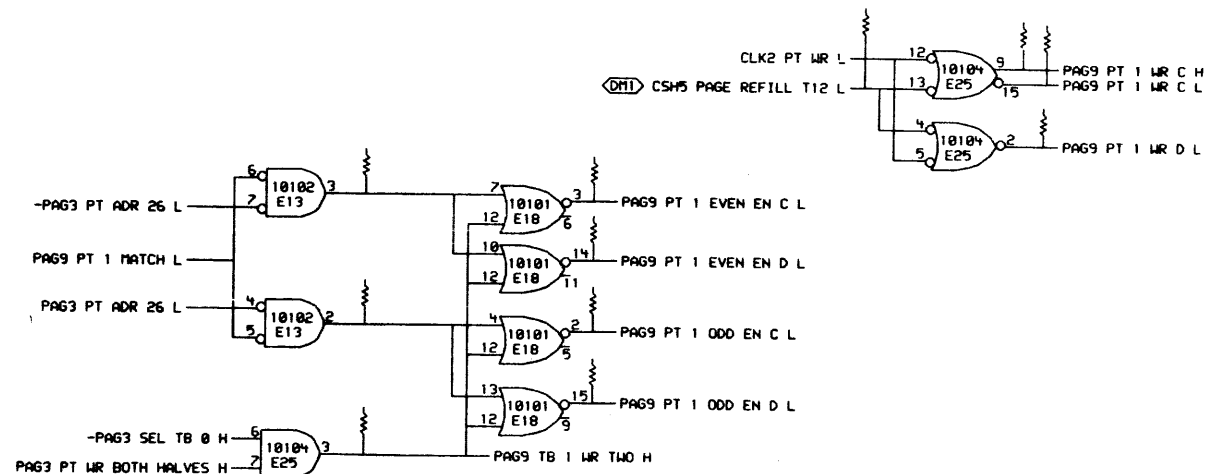
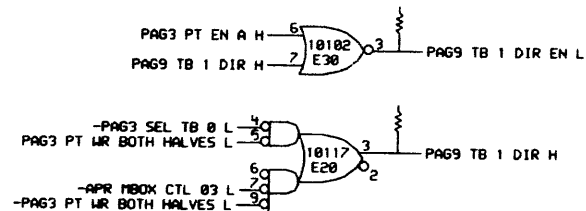
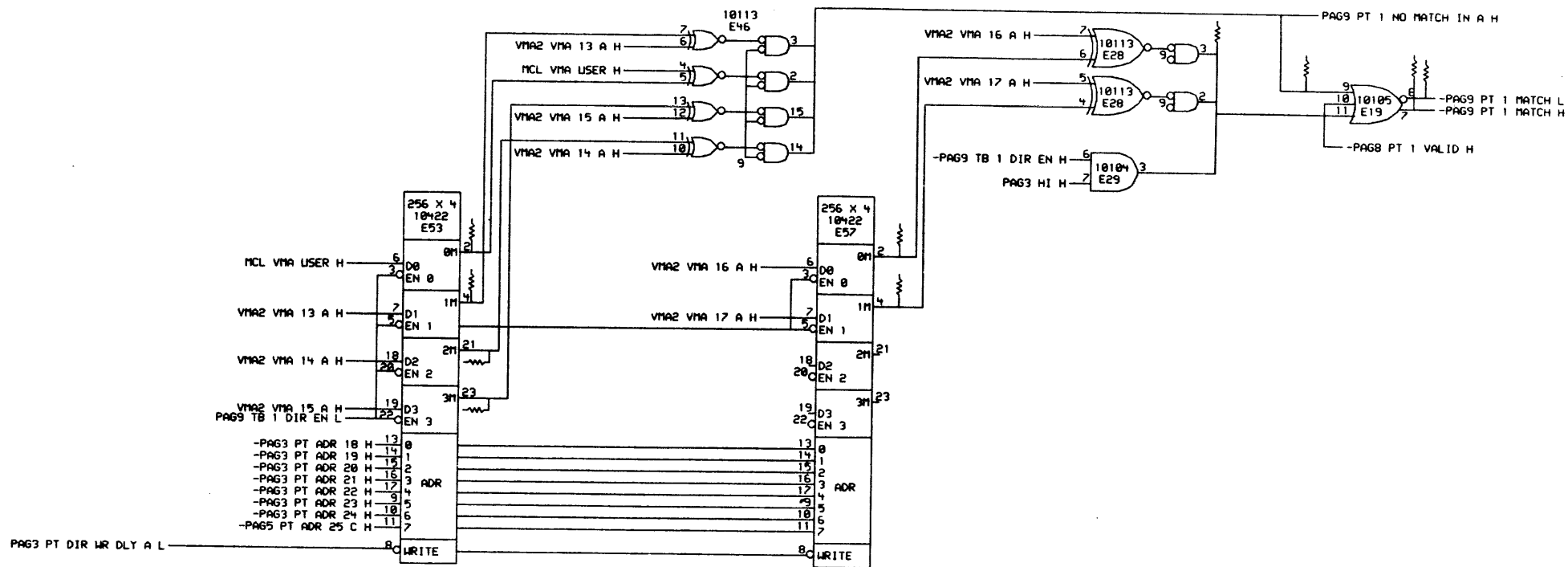
PAGE 9

CS M855-0-PAG9

CS

B

A



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REVISIONS
CHK CHANGE NO. REV.

digital

DRN. C. KACZOR
CHK'D. D. DELLORCO

DATE 23-JUL-84
DATE 23-JUL-84

ENG. C. KACZOR
BOARD LOCATION: YAF30

DATE 23-JUL-84
SHEET 1 OF 1

TITLE: PAGE TABLE 1
DIRECTORY

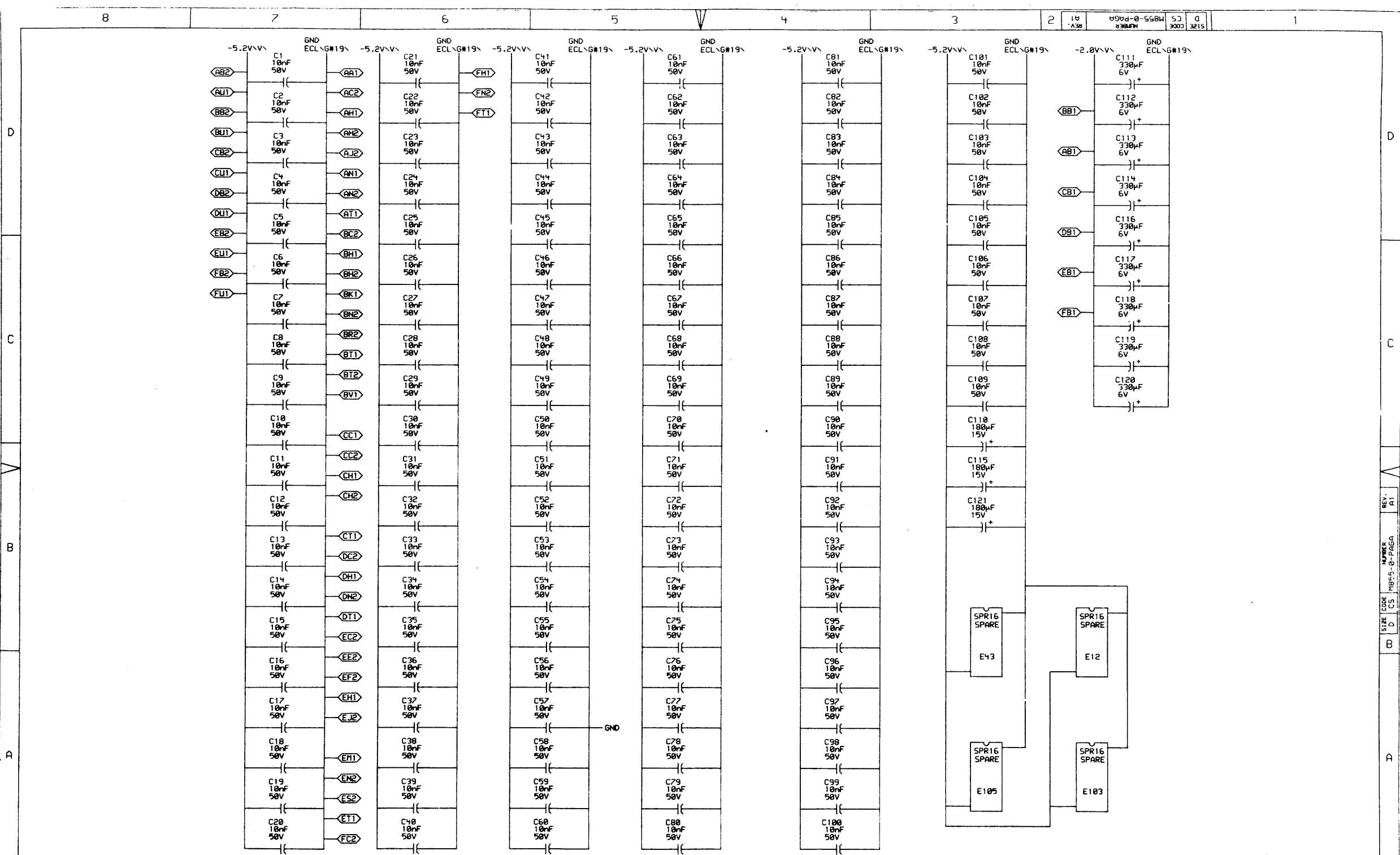
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NEXT HIGHER ASSEMBLY: D-DD-M855-0

SIZE CODE NUMBER

REV. A1

MRC



[B,GND,ECL, GND CLK\G#20\] —CNE

[B,NC,GND ECL\G#19\] —AA2

[B,NC,GND ECL\G#19\] —AV1

[B,NC,GND ECL\G#19\] —BA2

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[B,NC,GND ECL\G#19\] —FA2

[B,NC,GND ECL\G#19\] —FV1

[B,GND ECL\G#19\,NC] —EK2

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REVISIONS		
CHK	CHANGE NO.	REV

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NEXT HIGHER ASSEMBLY: D-DD-M855-0			SIZE CODE NUMBER REV. D CS M855-0-PAGB A1		

FIRST USED ON OPTION/MODEL: MCA25

MRO

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
212(3)	PAG3	C2	68a	2E53(2)	223(5)	PAG9	C5	68a	2E53(21)	212(8)	PAG3	B2	68a	-CLK2 PT WR H	247(10)	PAG2	C6	68a	PAG2 PT 0 14 H
253(8)	PAG4	C2	68a	2E102(6)	223(1)	PAG9	C5	68a	2E53(23)	238(6)	PAG5	B4	68a	-CON KI10 PAGING MODE H	265(8)	PAG2	B6	68a	PAG2 PT 0 15 H
259(3)	PAG4	A3	68a	2E104(15)	223(3)	PAG9	C5	68a	2E53(4)	211(3)	PAG3	B5	68a	CSH1 PGRF CYC A H	265(6)	PAG2	C4	68a	PAG2 PT 0 16 H
252(5)	PAG8	C2	68a	2E100(14)	246(6)	PAG9	C5	68a	2E57(2)	231(8)	PAG9	B2	68a	-CSH5 PAGE REFILL T12 H	232(8)	PAG2	B4	68a	PAG2 PT 0 17 H
227(6)	PAG8	C3	68a	2E100(15)	234(3)	PAG9	C4	68a	2E59(9)	27(3)	PAG3	C7	68a	-CSH6 MBOX PT DIR WR H	250(5)	PAG2	C2	68a	PAG2 PT 0 18 H
227(10)	PAG8	D2	68a	2E100(2)	235(5)	PAG9	C4	68a	2E61(2)	237(3)	PAG3	B5	68a	-CSH6 PAGE FAIL HOLD H	250(10)	PAG2	B2	68a	PAG2 PT 0 19 H
252(8)	PAG8	D2	68a	2E100(3)	249(5)	PAG4	C5	68a	2E61(4)	250(1)	PAG4	C2	68a	CSH6 PAGE REFILL ERROR H	27(8)	PAG2	C6	68a	PAG2 PT 1 14 H
216(5)	PAG8	B2	68a	2E109(14)	235(6)	PAG3	D4	68a	2E63(15)	250(3)	PAG4	C4	68a	-CSH6 PAGE REFILL ERROR H	265(10)	PAG2	A6	68a	PAG2 PT 1 15 H
216(8)	PAG8	B2	68a	2E109(15)	234(1)	PAG3	C4	68a	2E63(2)	239(1)	PAG5	D8	68a	MB 00-05 PAR ODD H	26(6)	PAG2	C4	68a	PAG2 PT 1 16 H
216(6)	PAG8	B2	68a	2E109(2)	27(6)	PAG4	B7	68a	2E63(4)	239(3)	PAG5	D7	68a	MB 06-11 PAR ODD H	232(1)	PAG2	A4	68a	PAG2 PT 1 17 H
251(8)	PAG8	B2	68a	2E109(3)	261(3)	PAG4	B6	68a	2E63(5)	239(5)	PAG5	D8	68a	MB 12-17 PAR ODD H	250(3)	PAG2	C2	68a	PAG2 PT 1 18 H
267(10)	PAG9	A4	68a	2E13(2)	238(3)	PAG5	C7	68a	2E63(10)	230(5)	PAG5	C8	68a	MB 18-23 PAR ODD H	250(8)	PAG2	A2	68a	PAG2 PT 1 19 H
271(5)	PAG9	A4	68a	2E13(3)	221(1)	PAG5	C3	68a	2E70(2)	239(10)	PAG5	C7	68a	MB 24-29 PAR ODD H	271(10)	PAG3	A7	68a	PAG3 HI H
241(10)	PAG1	D3	68a	2E16(14)	260(3)	PAG8	A7	68a	2E75(11)	242(1)	PAG5	C7	68a	MB 30-35 PAR ODD H	214(5)	PAG3	B2	68a	-PAG3 PT 0 EVEN EN A H
230(3)	PAG5	B3	68a	2E17(15)	245(10)	PAG8	B7	68a	2E75(12)	251(5)	PAG3	A5	68a	MB SEL 1 H	263(1)	PAG3	B2	68a	-PAG3 PT 0 EVEN EN B H
256(1)	PAG5	B3	68a	2E17(2)	260(5)	PAG8	B7	68a	2E75(13)	217(8)	PAG3	A5	68a	MB SEL 2 H	213(6)	PAG3	C4	68a	-PAG3 PT 0 LRU H
210(3)	PAG1	D3	68a	2E19(3)	24(5)	PAG8	B6	68a	2E75(3)	271(8)	PAG4	D8	68a	MCL VMA USER H	244(8)	PAG3	D1	68a	PAG3 PT 0 MATCH H
230(1)	PAG5	C7	68a	2E2(10)	24(10)	PAG8	C7	68a	2E75(4)	269(3)	PAG3	B7	68a	MCL2 VMA EXEC H	240(1)	PAG3	D1	68a	-PAG3 PT 0 MATCH H
239(6)	PAG5	D7	68a	2E2(13)	24(8)	PAG8	C7	68a	2E75(5)	27(1)	PAG4	A4	68a	-MCL2 VMA WRITE H	266(10)	PAG3	D2	68a	PAG3 PT 0 NO MATCH IN A H
239(8)	PAG5	D7	68a	2E2(3)	24(1)	PAG8	C7	68a	2E75(6)	224(6)	PAG4	C2	68a	MCL3 PAGE ADDRESS COND H	215(3)	PAG3	A2	68a	-PAG3 PT 0 ODD EN A H
240(3)	PAG1	C3	68a	2E22(14)	260(6)	PAG8	D7	68a	2E8(15)	29(5)	PAG4	D7	68a	MCL3 PAGE ILL ENTRY H	263(3)	PAG3	A2	68a	-PAG3 PT 0 ODD EN B H
266(6)	PAG3	D2	68a	2E23(3)	266(1)	PAG3	B3	68a	2E8(2)	211(6)	PAG4	C4	68a	-MCL3 PAGE ILL ENTRY H	216(3)	PAG3	D2	68a	-PAG3 PT 0 WR 26 H
269(5)	PAG5	D2	68a	2E26(2)	266(5)	PAG3	A3	68a	2E8(3)	236(6)	PAG4	B6	68a	MCL3 PAGE TEST PRIVATE H	214(1)	PAG3	B1	68a	-PAG3 PT 0 WR A H
212(5)	PAG9	C2	68a	2E20(2)	257(3)	PAG1	B3	68a	2E86(2)	253(10)	PAG4	C2	68a	MCL5 VMA ADR ERR H	263(5)	PAG3	B1	68a	-PAG3 PT 0 WR B H
240(5)	PAG5	D3	68a	2E29(14)	257(5)	PAG1	A3	68a	2E86(5)	240(1)	PAG4	D7	68a	MCL6 PAGE UEBR REF H	253(6)	PAG3	C2	68a	-PAG3 PT 1 WR 26 H
236(3)	PAG6	B7	68a	2E29(2)	244(10)	PAG3	A7	68a	2E90(15)	241(8)	PAG1	B2	68a	PAG1 PT 0 26 H	214(6)	PAG3	D7	68a	PAG3 PT ADR 18 H
229(8)	PAG8	C6	68a	2E30(14)	217(10)	PAG3	A7	68a	2E90(2)	243(1)	PAG1	C7	68a	PAG1 PT 0 ACCESS H	230(10)	PAG3	D7	68a	-PAG3 PT ADR 18 H
211(10)	PAG8	C3	68a	2E30(15)	263(6)	PAG8	B5	68a	2E90(3)	259(8)	PAG1	C3	68a	PAG1 PT 0 CACHE H	224(3)	PAG3	B7	68a	PAG3 PT ADR 19 H
22(8)	PAG4	C4	68a	2E34(15)	263(8)	PAG8	B5	68a	2E91(14)	260(3)	PAG1	B7	68a	PAG1 PT 0 PUBLIC H	230(8)	PAG3	B7	68a	-PAG3 PT ADR 19 H
260(1)	PAG4	C7	68a	2E34(3)	263(10)	PAG8	B5	68a	2E91(15)	219(10)	PAG1	B5	68a	PAG1 PT 0 SOFTWARE H	214(8)	PAG3	B7	68a	PAG3 PT ADR 20 H
227(1)	PAG8	B5	68a	2E35(15)	262(8)	PAG8	B5	68a	2E91(2)	262(5)	PAG1	C5	68a	PAG1 PT 0 WRITABLE H	220(6)	PAG3	B7	68a	-PAG3 PT ADR 20 H
210(1)	PAG1	D3	68a	2E35(3)	215(10)	PAG8	C5	68a	2E91(3)	237(1)	PAG1	B1	68a	PAG1 PT 1 26 H	215(8)	PAG3	D7	68a	PAG3 PT ADR 21 H
227(8)	PAG8	C3	68a	2E35(7)	227(3)	PAG8	D5	68a	2E94(15)	259(10)	PAG1	C7	68a	PAG1 PT 1 ACCESS H	264(1)	PAG3	D7	68a	-PAG3 PT ADR 21 H
224(10)	PAG6	B7	68a	2E38(15)	252(3)	PAG8	D5	68a	APR5 WR PT SEL 0 H	259(6)	PAG1	C3	68a	PAG1 PT 1 CACHE H	227(5)	PAG3	D7	68a	PAG3 PT ADR 22 H
238(10)	PAG5	B4	68a	2E4(2)	214(10)	PAG8	C5	68a	APR5 WR PT SEL 1 H	235(10)	PAG1	A3	68a	-PAG1 PT 1 EV EN 26 H	220(8)	PAG3	D7	68a	-PAG3 PT ADR 22 H
29(6)	PAG4	D4	68a	2E42(2)	211(5)	PAG3	B7	68a	AR 23 C H	246(1)	PAG1	A3	68a	-PAG1 PT 1 OD EN 26 H	253(5)	PAG3	C7	68a	PAG3 PT ADR 23 H
235(1)	PAG3	D5	68a	2E49(2)	251(1)	PAG3	B7	68a	-CLK2 PT DIR WR H	260(6)	PAG1	A7	68a	PAG1 PT 1 PUBLIC H	264(5)	PAG3	C7	68a	-PAG3 PT ADR 23 H
235(3)	PAG3	C5	68a	2E49(21)	237(5)	PAG3	B5	68a		219(8)	PAG1	A5	68a	PAG1 PT 1 SOFTWARE H	246(8)	PAG3	A5	68a	PAG3 PT ADR 24 H
271(1)	PAG3	C5	68a	2E49(23)	236(1)	PAG6	B7	68a		262(3)	PAG1	C5	68a	PAG1 PT 1 WRITABLE H	270(5)	PAG3	A5	68a	-PAG3 PT ADR 24 H
271(6)	PAG3	C5	68a	2E49(4)	247(1)	PAG3	C7	68a		243(3)	PAG1	D7	68a	-PAG1 PT ACCESS H	22(3)	PAG5	B6	68a	PAG3 PT ADR 25 A IN H

NOTE:

1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES OUTPUT OF DIP LOC AND
() INDICATES PIN NUMBER

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REVISIONS:
CHK CHANGE NO. REV

digital	DRN	C.KACZOR	DATE	02-AUG-84	ENG	C.KACZOR	DATE	02-AUG-84	TITLE:	MULTIWIRE PAGER TERMINATORS
	CHK'D	D. DELLORCO	DATE	02-AUG-84	BOARD LOCATION:		OF	3	SIZE	D
FIRST USED ON OPTION MODEL: MCA25									CODE	CS
NEXT HIGHER ASSEMBLY:									NUMBER	M855-0-RES
									REV.	A1

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
22(5)	PAG3	A5	68a	PAG3 PT ADR 25 B IN H	262(10)	PAG4	B3	68a	-PAG4 PF CODE 2X H	21(8)	PAG6	B1	68a	PAG6 PT 1 KEEP 04 H	267(3)	PAG9	D1	68a	-PAG9 PT 1 MATCH H
267(5)	PAG3	A7	68a	PAG3 PT ADR 26 H	238(8)	PAG5	A7	68a	PAG5 K110 PAGE MODE H	225(10)	PAG6	B1	68a	PAG6 PT 1 KEEP 05 H	267(6)	PAG9	D2	68a	PAG9 PT 1 NO MATCH IN A H
266(8)	PAG3	A7	68a	-PAG3 PT ADR 26 H	240(6)	PAG5	B1	68a	PAG5 PT 0 EV PG PAR H	21(10)	PAG6	B1	68a	PAG6 PT 1 KEEP 06 H	226(10)	PAG9	A3	68a	-PAG9 PT 1 ODD EN C H
268(1)	PAG3	B7	68a	PAG3 PT ADR 26 A IN H	257(1)	PAG5	B1	68a	PAG5 PT 0 OD PG PAR H	243(10)	PAG6	A1	68a	PAG6 PT 1 KEEP 07 H	218(10)	PAG9	A3	68a	-PAG9 PT 1 ODD EN D H
213(3)	PAG3	B5	68a	PAG3 PT DIR CLR H	240(10)	PAG5	B1	68a	PAG5 PT 1 EV PG PAR H	216(1)	PAG6	B6	68a	PAG6 PT IN KEEP A H	212(1)	PAG9	B2	68a	PAG9 PT 1 WR C H
213(10)	PAG3	B5	68a	-PAG3 PT DIR CLR H	240(8)	PAG5	B1	68a	PAG5 PT 1 OD PG PAR H	253(1)	PAG6	A6	68a	PAG6 PT IN KEEP B H	256(5)	PAG9	B1	68a	-PAG9 PT 1 WR C H
R2(1)	PAG3	B7	100a	-PAG3 PT DIR WR H	226(3)	PAG5	C7	68a	-PAG5 PT ADR 18 A H	249(8)	PAG7	C7	68a	PAG7 PT 0 20 H	256(8)	PAG9	B1	68a	-PAG9 PT 1 WR D H
R1(1)	PAG3	B7	100a	-PAG3 PT DIR WR DLY H	218(3)	PAG5	C7	68a	PAG5 PT ADR 18 B H	241(5)	PAG7	B7	68a	PAG7 PT 0 21 H	211(8)	PAG9	B7	68a	PAG9 TB 1 DIR H
229(6)	PAG3	C7	68a	-PAG3 PT DIR WR DLY A H	254(5)	PAG5	B7	68a	-PAG5 PT ADR 19 A H	232(5)	PAG7	C4	68a	PAG7 PT 0 22 H	220(5)	PAG9	B7	68a	-PAG9 TB 1 DIR EN H
26(8)	PAG3	A1	68a	PAG3 PT EN A H	228(6)	PAG5	B7	68a	PAG5 PT ADR 19 B H	241(1)	PAG7	B4	68a	PAG7 PT 0 23 H	267(8)	PAG9	A4	68a	PAG9 TB 1 WR TWO H
265(5)	PAG3	A1	68a	-PAG3 PT EN A H	226(5)	PAG5	B7	68a	-PAG5 PT ADR 20 A H	232(3)	PAG7	C1	68a	PAG7 PT 0 24 H	27(10)	PAG2	D7	68a	PM3 PA 14 H
245(6)	PAG3	A1	68a	PAG3 PT EN B H	228(8)	PAG5	B7	68a	PAG5 PT ADR 20 B H	232(6)	PAG7	B2	68a	PAG7 PT 0 25 H	26(10)	PAG2	D5	68a	PM3 PA 15 H
258(6)	PAG3	A1	68a	-PAG3 PT EN B H	254(6)	PAG5	B7	68a	-PAG5 PT ADR 21 A H	223(8)	PAG7	C7	68a	PAG7 PT 1 20 H	26(5)	PAG2	D5	68a	PM3 PA 16 H
27(5)	PAG3	D2	68a	PAG3 PT MATCH H	218(5)	PAG5	B7	68a	PAG5 PT ADR 21 B H	241(6)	PAG7	A7	68a	PAG7 PT 1 21 H	274(10)	PAG2	D4	68a	PM3 PA 17 H
231(1)	PAG3	B5	68a	PAG3 PT WR BOTH HALVES H	246(5)	PAG5	C5	68a	-PAG5 PT ADR 22 A H	235(8)	PAG7	C4	68a	PAG7 PT 1 22 H	245(3)	PAG2	D4	68a	PM3 PA 18 H
25(1)	PAG3	B5	68a	-PAG3 PT WR BOTH HALVES H	234(8)	PAG5	C5	68a	PAG5 PT ADR 22 B H	242(8)	PAG7	A4	68a	PAG7 PT 1 23 H	245(8)	PAG2	D3	68a	PM3 PA 19 H
228(10)	PAG3	C1	68a	PAG3 SEL TB 0 H	216(10)	PAG5	B5	68a	-PAG5 PT ADR 23 A H	237(8)	PAG7	C1	68a	PAG7 PT 1 24 H	29(8)	PAG7	D5	68a	PM3 PA 20 H
229(3)	PAG3	C1	68a	-PAG3 SEL TB 0 H	262(6)	PAG5	B5	68a	PAG5 PT ADR 23 B H	234(6)	PAG7	A2	68a	PAG7 PT 1 25 H	241(3)	PAG7	D4	68a	PM3 PA 21 H
229(5)	PAG3	B5	68a	PAG3 TB 0 DIR H	226(8)	PAG5	B5	68a	-PAG5 PT ADR 24 A H	24(6)	PAG8	D4	68a	PAG8 CLR VALID H	234(5)	PAG7	D4	68a	PM3 PA 22 H
264(3)	PAG3	B5	68a	-PAG3 TB 0 DIR EN H	25(3)	PAG5	B5	68a	PAG5 PT ADR 24 B H	224(5)	PAG8	D6	68a	-PAG8 FORCE VALID 0 H	242(10)	PAG7	D3	68a	PM3 PA 23 H
266(3)	PAG3	A2	68a	PAG3 TB 0 WR TWO H	222(8)	PAG5	B5	68a	-PAG5 PT ADR 25 A H	253(3)	PAG8	D6	68a	-PAG8 FORCE VALID 1 H	237(6)	PAG7	D6	68a	PM3 PA 24 H
233(1)	PAG4	D1	68a	PAG4 FORCE TB CLK H	257(6)	PAG5	B5	68a	PAG5 PT ADR 25 B H	224(1)	PAG8	C6	68a	-PAG8 FORCE VALID 2 H	237(10)	PAG7	D5	68a	PM3 PA 25 H
233(8)	PAG4	A6	68a	-PAG4 FORCE TO TB 0 H	264(6)	PAG5	A5	68a	-PAG5 PT ADR 25 C H	252(6)	PAG8	C6	68a	-PAG8 FORCE VALID 3 H	226(6)	PAG1	C8	68a	PT IN 00 H
233(3)	PAG4	A6	68a	PAG4 FORCE TO TB 1 H	222(3)	PAG5	A5	68a	PAG5 PT ADR 25 D H	215(6)	PAG8	B6	68a	-PAG8 FORCE VALID 4 H	218(1)	PAG1	B8	68a	PT IN 01 H
228(1)	PAG4	D1	68a	-PAG4 MBOX CTL 03 H	22(10)	PAG5	C1	68a	PAG5 PT PAR ODD H	215(5)	PAG8	B6	68a	-PAG8 FORCE VALID 5 H	226(1)	PAG1	C6	68a	PT IN 02 H
268(5)	PAG4	D7	68a	PAG4 PAGE EXEC PAGED REF H	249(3)	PAG5	C2	68a	-PAG5 PT PAR ODD H	215(1)	PAG8	B6	68a	-PAG8 FORCE VALID 6 H	218(6)	PAG1	B6	68a	PT IN 03 H
225(3)	PAG4	D7	68a	-PAG4 PAGE EXEC PAGED REF H	229(10)	PAG6	D6	68a	PAG6 PT 0 KEEP H	214(3)	PAG8	A6	68a	-PAG8 FORCE VALID 7 H	228(10)	PAG1	C4	68a	PT IN 04 H
248(3)	PAG4	D7	68a	PAG4 PAGE EXEC REF H	244(1)	PAG6	D4	68a	PAG6 PT 0 KEEP 00 H	233(5)	PAG8	D2	68a	PAG8 PT 0 VALID H	255(8)	PAG2	C7	68a	PT IN 05 H
29(1)	PAG4	D7	68a	-PAG4 PAGE EXEC REF H	23(5)	PAG6	D4	68a	PAG6 PT 0 KEEP 01 H	271(3)	PAG8	D2	68a	-PAG8 PT 0 VALID H	264(10)	PAG2	B7	68a	PT IN 06 H
26(1)	PAG4	C3	68a	PAG4 PAGE FAIL A H	244(3)	PAG6	D4	68a	PAG6 PT 0 KEEP 02 H	211(1)	PAG8	D4	68a	PAG8 PT 0 VALID A H	255(6)	PAG2	C5	68a	PT IN 07 H
225(6)	PAG4	C3	68a	-PAG4 PAGE FAIL A H	23(1)	PAG6	C4	68a	PAG6 PT 0 KEEP 03 H	212(10)	PAG8	B4	68a	PAG8 PT 0 VALID B H	228(1)	PAG2	B5	68a	PT IN 08 H
265(3)	PAG4	B6	68a	PAG4 PAGE TEST PRIVATE H	23(10)	PAG6	B4	68a	PAG6 PT 0 KEEP 04 H	233(6)	PAG8	D2	68a	-PAG8 PT 1 VALID H	255(5)	PAG2	C2	68a	PT IN 09 H
225(5)	PAG4	B6	68a	-PAG4 PAGE TEST PRIVATE H	244(5)	PAG6	B4	68a	PAG6 PT 0 KEEP 05 H	224(8)	PAG8	D1	68a	PAG8 PT 1 VALID A H	255(1)	PAG2	B2	68a	PT IN 10 H
259(1)	PAG4	C6	68a	-PAG4 PAGE TEST WRITE H	23(8)	PAG6	B4	68a	PAG6 PT 0 KEEP 06 H	252(10)	PAG8	B1	68a	PAG8 PT 1 VALID B H	222(10)	PAG7	C7	68a	PT IN 11 H
29(3)	PAG4	C7	68a	-PAG4 PAGE UNPAGED REF H	244(6)	PAG6	A4	68a	PAG6 PT 0 KEEP 07 H	268(10)	PAG8	A6	68a	-PAG8 PT DIR CLR A H	257(10)	PAG7	B7	68a	PT IN 12 H
28(3)	PAG4	D7	68a	PAG4 PAGE USER PAGED REF H	212(6)	PAG6	C6	68a	PAG6 PT 1 KEEP H	225(8)	PAG8	A6	68a	-PAG8 PT DIR CLR B H	222(5)	PAG7	C5	68a	PT IN 13 H
225(1)	PAG4	D7	68a	-PAG4 PAGE USER PAGED REF H	243(8)	PAG6	D1	68a	PAG6 PT 1 KEEP 00 H	267(1)	PAG8	A6	68a	PAG8 SWEEP ALL H	257(8)	PAG7	B5	68a	PT IN 14 H
259(5)	PAG4	D4	68a	PAG4 PAGE WRITE OK H	243(6)	PAG6	D1	68a	PAG6 PT 1 KEEP 01 H	254(10)	PAG9	A3	68a	-PAG9 PT 1 EVEN EN C H	256(3)	PAG7	C2	68a	PT IN 15 H
249(1)	PAG4	D4	68a	-PAG4 PAGE WRITE OK H	21(6)	PAG6	D1	68a	-PAG6 PT 1 KEEP 02 H	218(8)	PAG9	A3	68a	-PAG9 PT 1 EVEN EN D H	238(6)	PAG7	B2	68a	PT IN 16 H
252(1)	PAG4	A2	68a	-PAG4 PAGED REF H	243(5)	PAG6	C1	68a	PAG6 PT 1 KEEP 03 H	22(1)	PAG9	D1	68a	PAG9 PT 1 MATCH H	222(1)	PAG1	B3	68a	PT IN 17 H

NOTE:

1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES OUTPUT OF DIP LOC AND
() INDICATES PIN NUMBER

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REVISIONS
CHK CHANGE NO. REV

digital	DRN C.KACZOR	DATE 02-AUG-84	ENG C.KACZOR	DATE 02-AUG-84	TITLE MULTIWIRED PAGER TERMINATORS
	CHK'D D. DELLORCO	DATE 02-AUG-84	BOARD LOCATION SHEET 2 OF 3	SIZE D	CODE CS
FIRST USED ON OPTION/MODEL: MCA25			NEXT HIGHER ASSEMBLY: D-DD-M855-0		NUMBER M855-0-RES
					REV. A1

RESISTOR LOC(PIN)	SHOWN ON DRAW	REF	VALUE	TERMINATES SIGNAL
Z54(8)	PAG1	C8	68Ω	PT IN 18 H
Z58(1)	PAG1	A8	68Ω	PT IN 19 H
Z54(3)	PAG1	C6	68Ω	PT IN 20 H
Z62(1)	PAG1	A6	68Ω	PT IN 21 H
Z54(1)	PAG1	C4	68Ω	PT IN 22 H
Z55(10)	PAG2	C7	68Ω	PT IN 23 H
Z64(8)	PAG2	A7	68Ω	PT IN 24 H
Z28(5)	PAG2	B5	68Ω	PT IN 25 H
Z56(10)	PAG2	A5	68Ω	PT IN 26 H
Z55(3)	PAG2	C2	68Ω	PT IN 27 H
Z28(3)	PAG2	A2	68Ω	PT IN 28 H
Z78(1)	PAG7	C7	68Ω	PT IN 29 H
Z38(1)	PAG7	A7	68Ω	PT IN 30 H
Z22(6)	PAG7	C5	68Ω	PT IN 31 H
Z32(10)	PAG7	B5	68Ω	PT IN 32 H
Z38(5)	PAG7	C2	68Ω	PT IN 33 H
Z56(6)	PAG7	B2	68Ω	PT IN 34 H
Z46(3)	PAG1	A3	68Ω	PT IN 35 H
Z13(5)	PAG5	A4	68Ω	SH AR PAR ODD A H
Z28(3)	PAG3	C6	68Ω	VMA2 VMA 13 A H
Z78(10)	PAG3	C6	68Ω	VMA2 VMA 14 A H
Z78(8)	PAG3	C6	68Ω	VMA2 VMA 15 A H
Z19(3)	PAG3	D4	68Ω	VMA2 VMA 16 A H
Z78(3)	PAG3	B7	68Ω	VMA2 VMA 17 H
Z19(5)	PAG3	C4	68Ω	VMA2 VMA 17 A H
Z42(6)	PAG3	D8	68Ω	VMA2 VMA 18 H
Z69(1)	PAG3	B7	68Ω	VMA2 VMA 19 H
Z78(6)	PAG3	B7	68Ω	VMA2 VMA 20 H
Z42(5)	PAG3	D8	68Ω	VMA2 VMA 21 H
Z36(8)	PAG3	D8	68Ω	VMA2 VMA 22 H
Z42(3)	PAG3	C8	68Ω	VMA2 VMA 23 H
Z51(10)	PAG3	A5	68Ω	VMA2 VMA 24 H
Z51(6)	PAG3	A7	68Ω	VMA2 VMA 25 H
Z61(1)	PAG3	A7	68Ω	VMA2 VMA 26 H

NOTES

1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.8V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
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3. % INDICATES OUTPUT OF DIP LOC AND
() INDICATES PIN NUMBER

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REVISIONS

CHK CHANGE NO. REV

digital	DRN	C. KACZOR	DATE	ENG	C. KACZOR	DATE	FILE: MULTIWIRED PAGER	
	CHK	D. DELLORCO	DATE	DATE	DATE	DATE	TERMINATORS	
XTRA: (MCA25, M8550, TAPE, M8553, DAW)		02-AUG-84 12:15		NEXT HIGHER ASSEMBLY:		SIZE CODE NUMBER REV.		
FIRST USED ON OPTION/MODEL: MCA25		D-DD M855-0		D CS M855-0 RES		1 MRC		

DRAWING NUMBER	NO.OF SHT	PART NUMBER	DESCRIPTION	REVISION
		M855-00	MODULE REVISION	A2
D-UA-M855-0-0	2		2 WAY ASSOCIATIVE PAGER	B
K-PL-M855-0-DBP	2		PART LIST, M855	B
D-CS-M855-0-PAG1	1		PAGE TABLE DATA PT ACCESS-PT 17	B
D-CS-M855-0-PAG2	1		PAGE TABLE DATA PT 16 - PT 26	B
D-CS-M855-0-PAG3	1		PAGE TABLE DIRECTORY	B
D-CS-M855-0-PAG4	1		PAGE TABLE CONTROL LOGIC	B
D-CS-M855-0-PAG5	1		PAGE TABLE PARITY LOGIC	B
D-CS-M855-0-PAG6	1		PAGING KEEP BIT SELECTION	B
D-CS-M855-0-PAG7	1		PAGE TABLE DATA PT 20 - 25	B
D-CS-M855-0-PAG8	1		PAGE TABLE VALID BIT SELECTION	B
D-CS-M855-0-PAG9	1		PAGE TABLE 1 DIRECTORY	B
D-CS-M855-0-PAGA	1		PAGING BOARD POWER, GND, CAP	B
D-CS-M855-0-PAGB	1		PAGING BOARD GND	B
D-CS-M855-0-RES	3		PAG TERMINATORS	B
K-PC-M855-0-DBI	-		P.C. DESIGN DATA BASE TAPE	B
D-DD-5017661-0	2		DRAWING DIRECTORY, 5017661	REF

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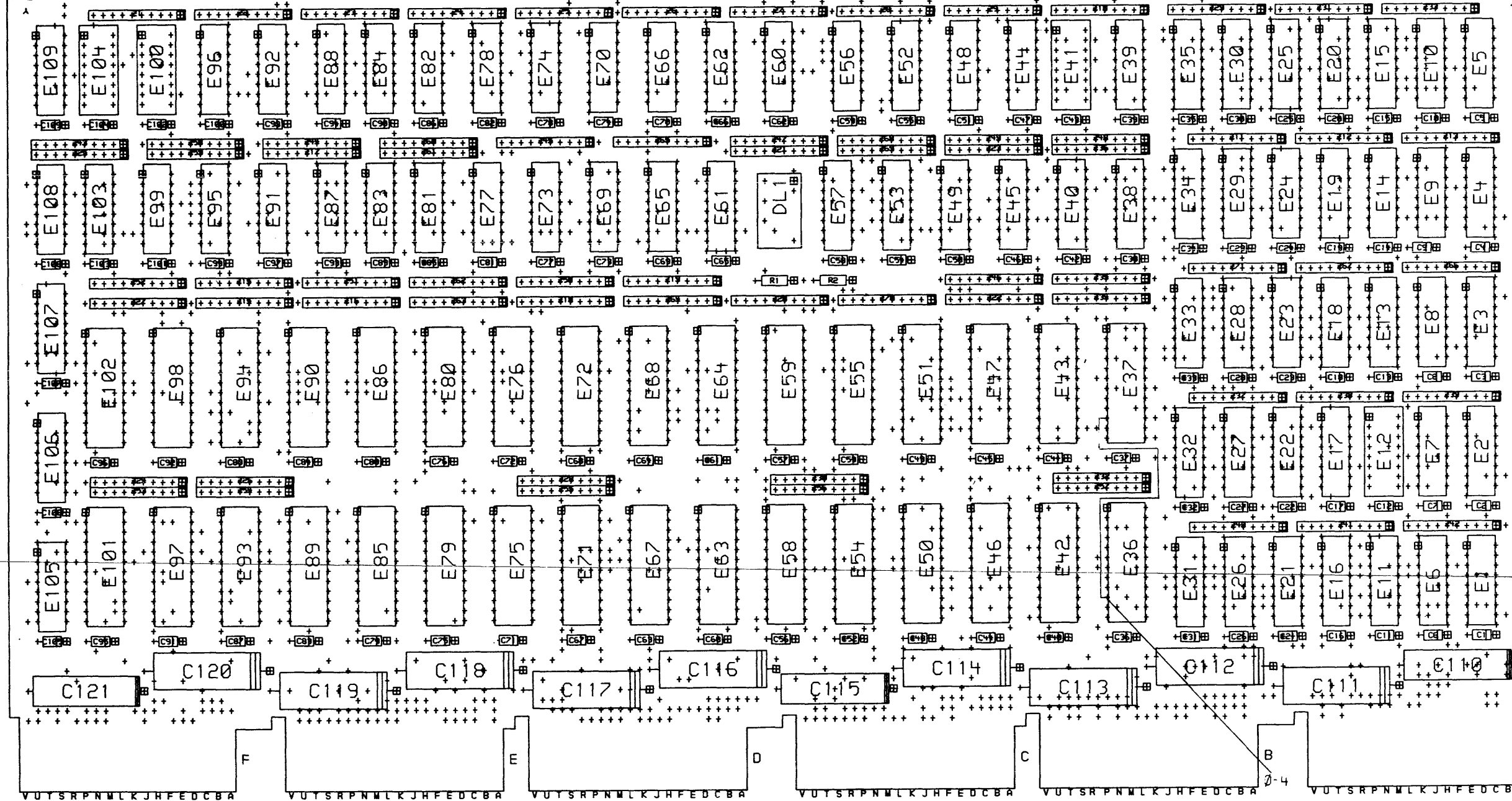
03(01712)

COMPONENT SIDE VIEW

22

TH2

TH1



NOTES: 1. SPARE COMP. ARE: E12, E41, E100, E104.
2. THIS BOARD MUST MEET SAFETY REQ. FOR HPWR.

STEP E + Y AXIS 0 STEP 0 TIMES
REPEAT + X AXIS 0 STEP 0 TIMES

CHK CHANGE NO REV

ETCH REV. B1

SIGNATURES		DATE	digital
DRN. J. MASON		4-12-84	
CHK'D. D. J. MASON		10-2-84	TITLE 2 WAY ASSOCIATIVE PAGER
MECH. ENG. J. MASON			
PROJ. ENG. J. MASON			SIZE CODE NUMBER 0 UAA M855-0-0
PROD. J. MASON		26667	
SCALE 2/1			REV B
SHT. 1 OF 2			
TCP DCC NC: D-DD-M855-0			

MRO 1 MS#410642B

REWORK INSTRUCTIONS

NEW RELEASE

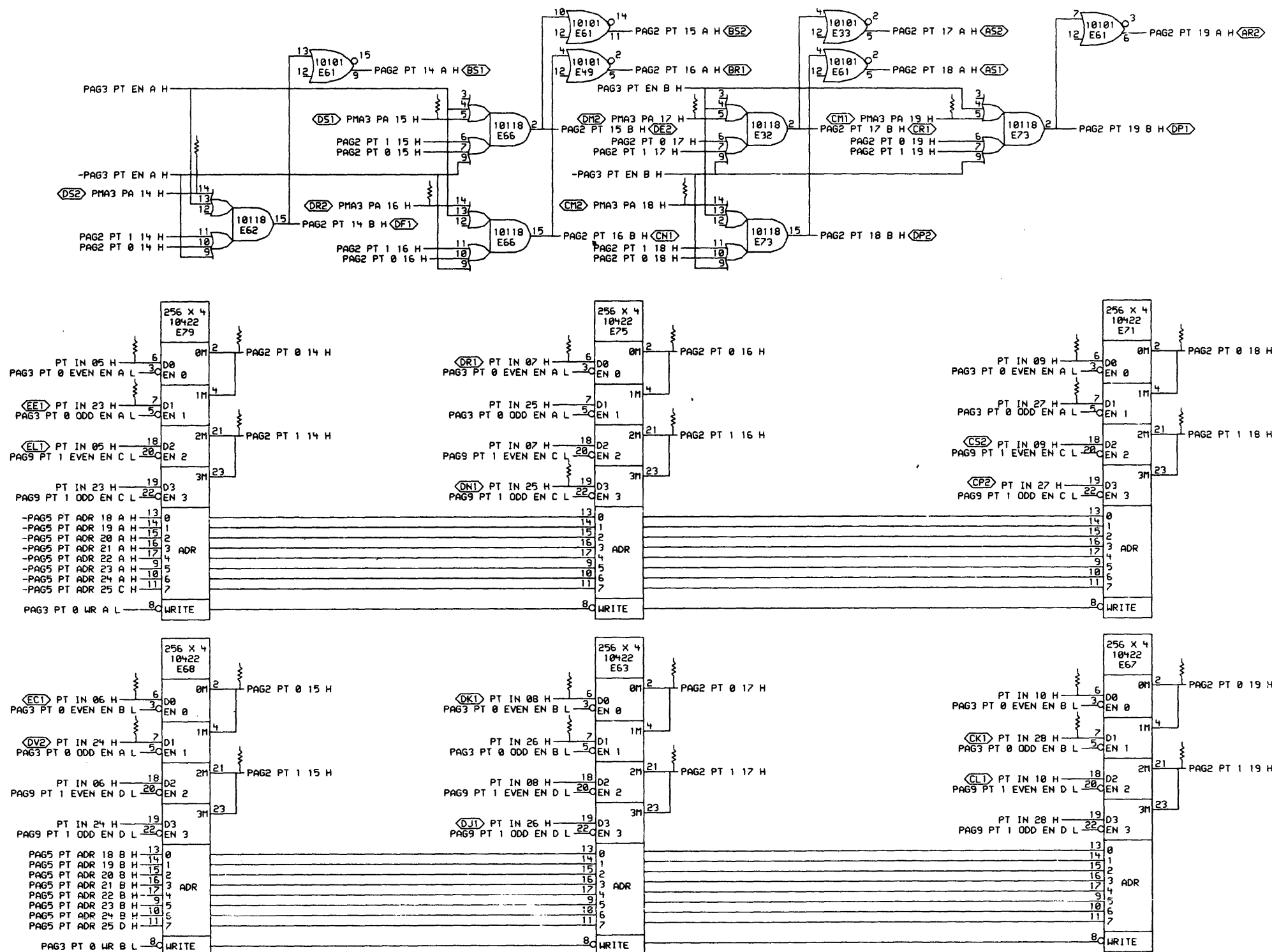
0-1 THRU 0-3 FOR ETCH CUTS REFER TO
D-EC-5017661-0-0

WIRE ADD, SIDE 1, AS SHOWN, (P/N 9105740-55)
0-4 E36-10 TO E37-10

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REVISIONS	
CHK	CHANGE NO. REV.

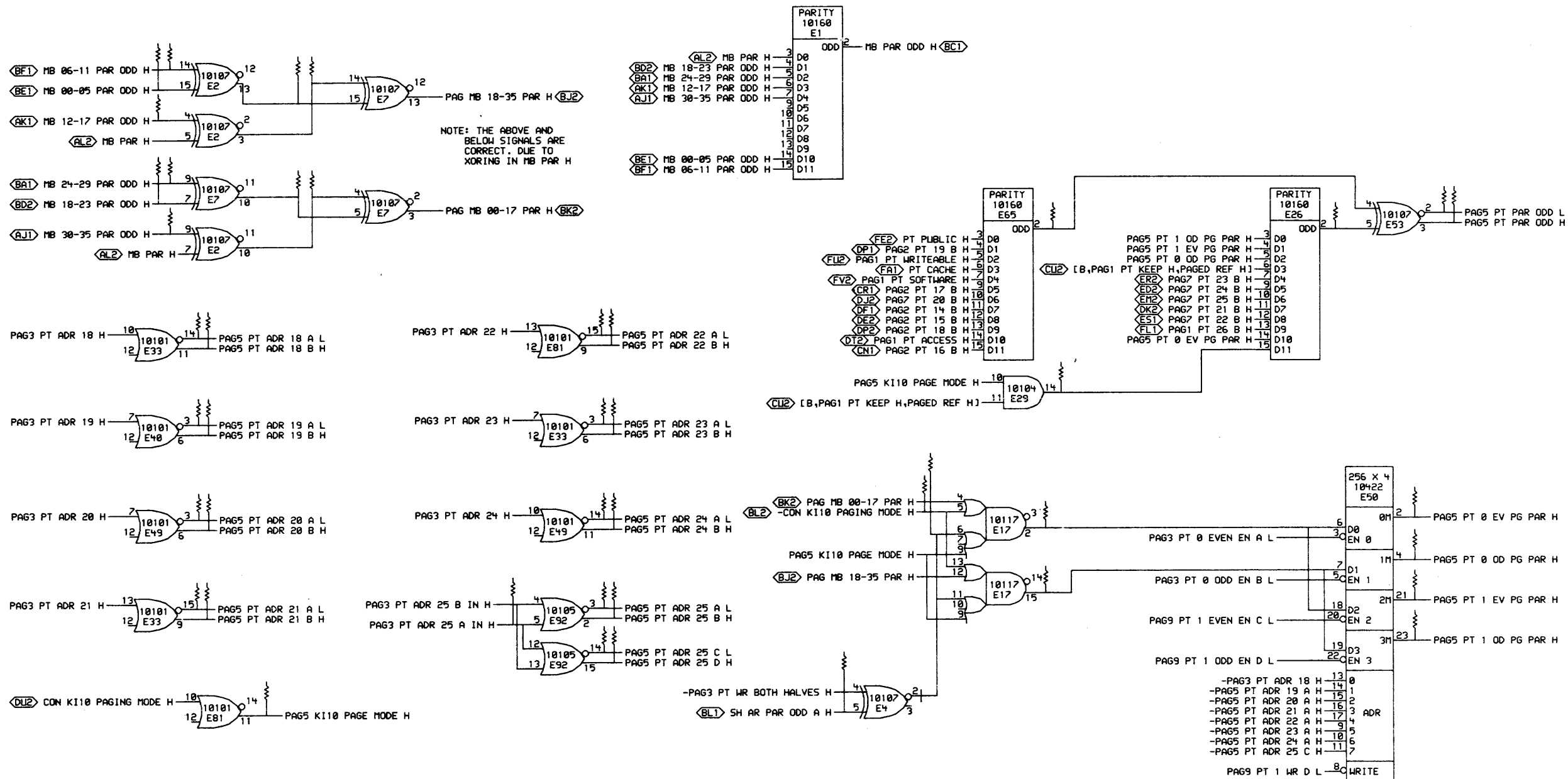
digital	DATE	ENG.	DATE	TITLE:
	23-JUL-84	C. J. J.	2/25/84	
CHK'D.	DATE	BOARD LOCATION:		
W. W. J.	23-JUL-84	2 OF 2		
FIRST USED ON OPTION/MODEL: KL10-PW D-DD-M855-0				
SIZE	CODE	NUMBER	REV.	
D	UA	M855-0-0	B	



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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN.	C. KACZOR	DATE	10-JUL-84	ENG.	C. KACZOR	DATE	10-JUL-84	TITLE:	PAGE TABLE DATA
	CHK'D	D. DELLORCO	DATE	10-JUL-84	BOARD LOCATION:	4AF30			PT 16 - PT 26	
FIRST USED ON OPTION/MODEL:									MCA25	D-DD-M855-0
SIZE	CODE	CS	D	NUMBER	M855-0-PAG2	REV.	B			



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REVISIONS

CHK	CHANGE NO.	REV
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digital

DRN.	C. KACZOR	DATE	20-JUL-84	ENG.	C. KACZOR	DATE	20-JUL-84
CHK'D	D. DELLORCO	DATE	20-JUL-84	BOARD LOCATION	4AF30	SHEET	1 OF 1

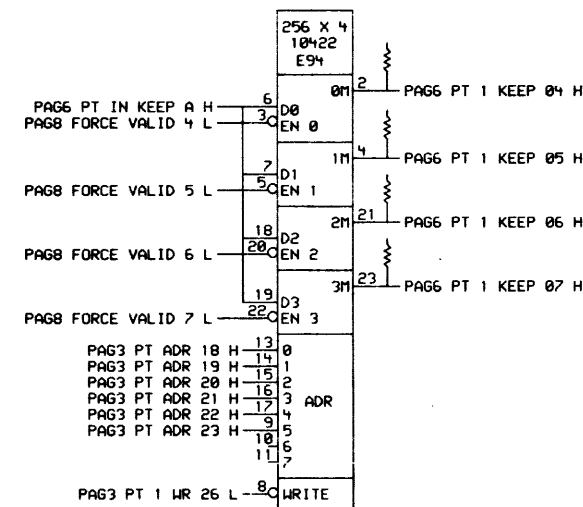
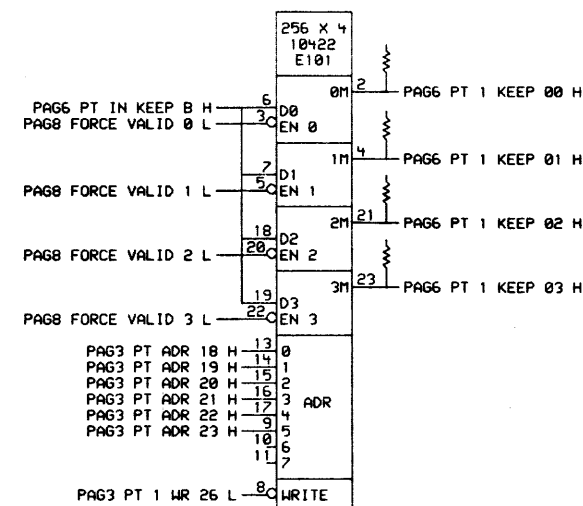
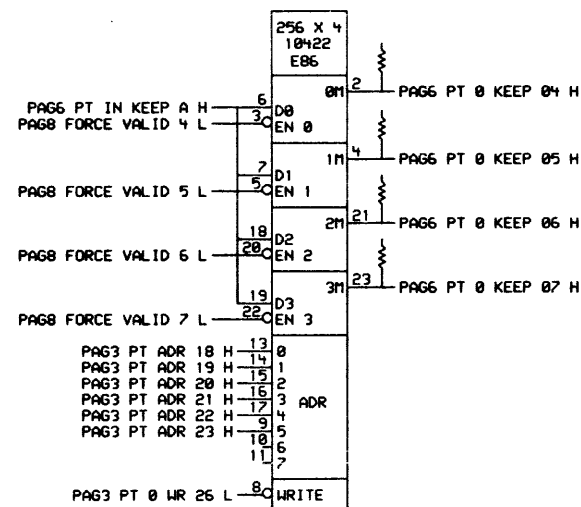
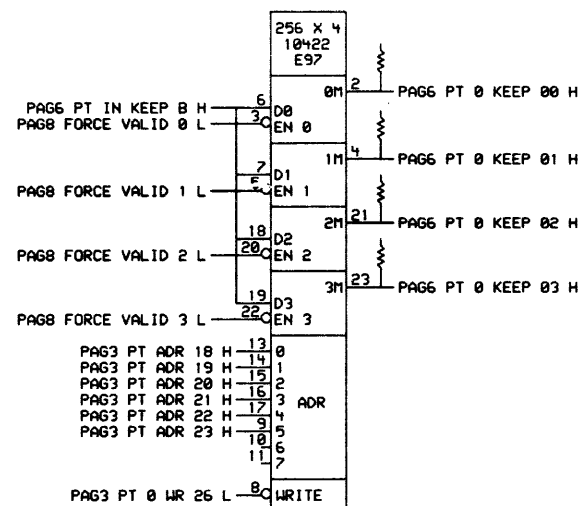
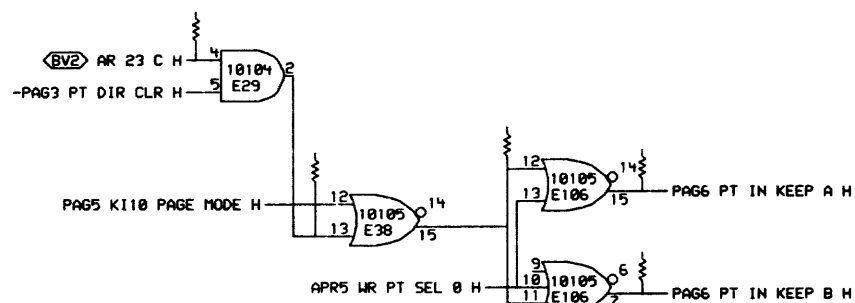
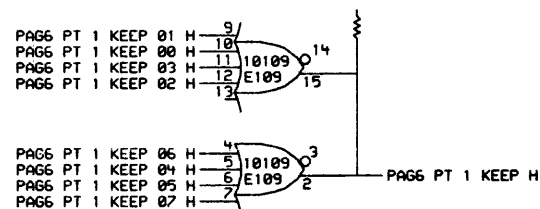
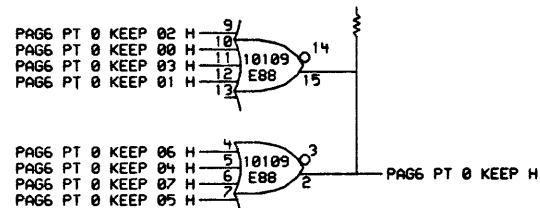
XTBA1:110225.M855-0-REV8-PAGE5.DWG 116-JUL-84 11:45 NEXT HIGHER ASSEMBLY:

FIRST USED ON OPTION/MODEL: MCA25 B-DD-M855-0

TITLE: PAGE TABLE PARITY LOGIC

SIZE	CODE	NUMBER	REV.
D	CS	M855-0-PAG5	B

MRO 1



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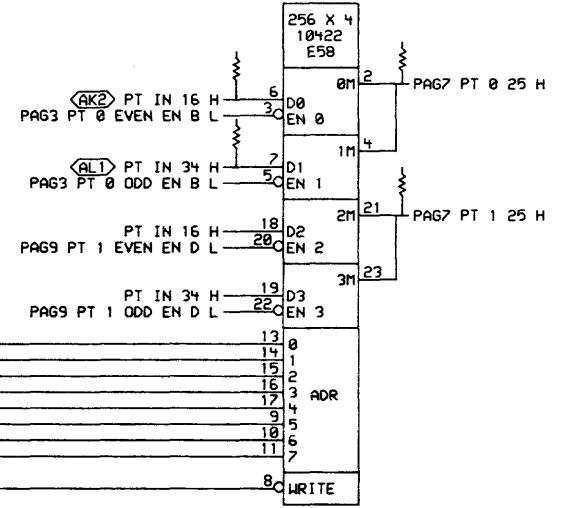
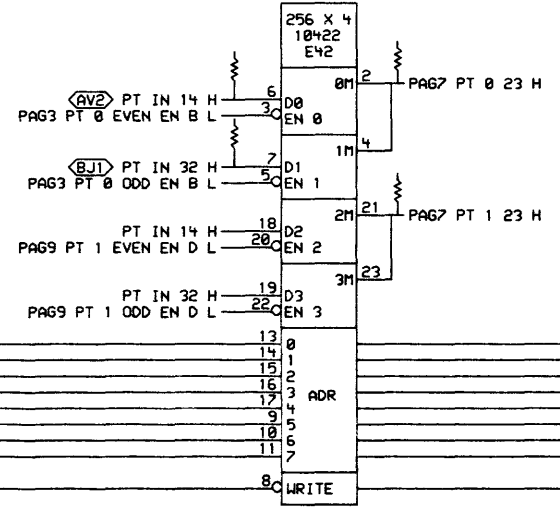
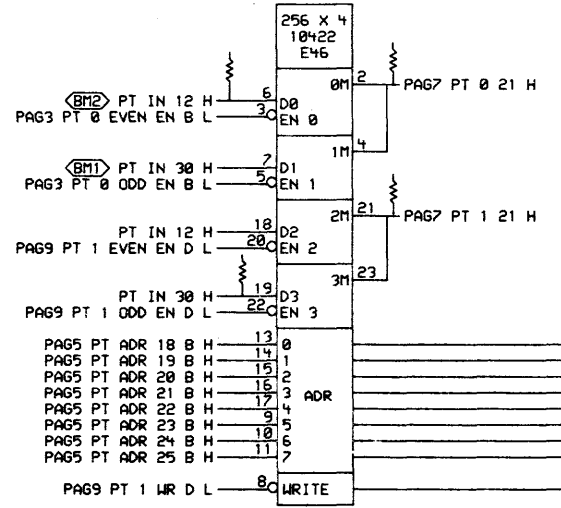
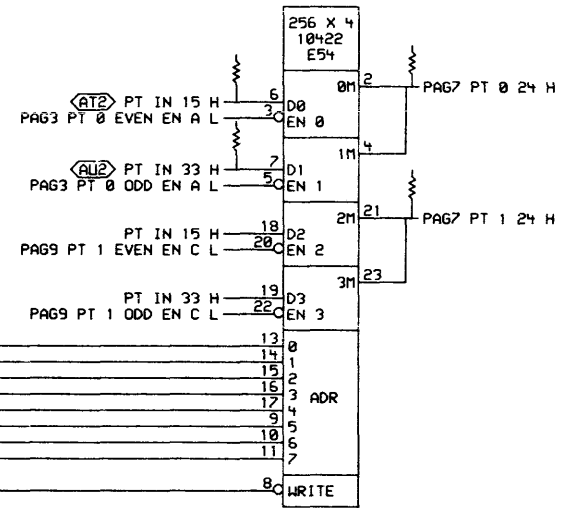
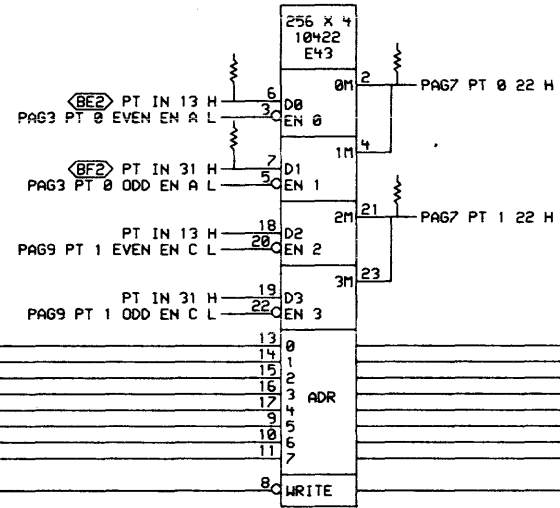
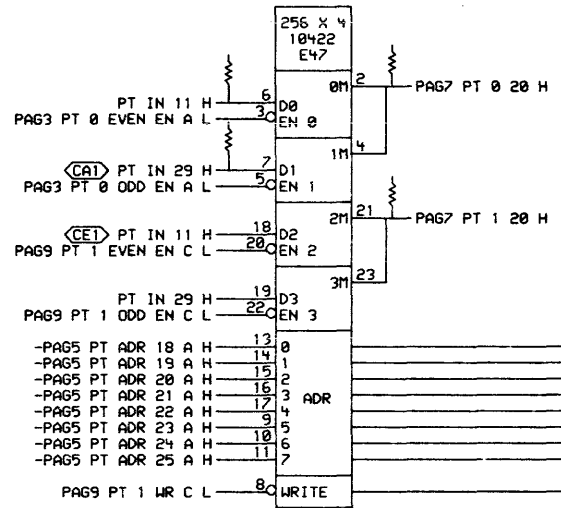
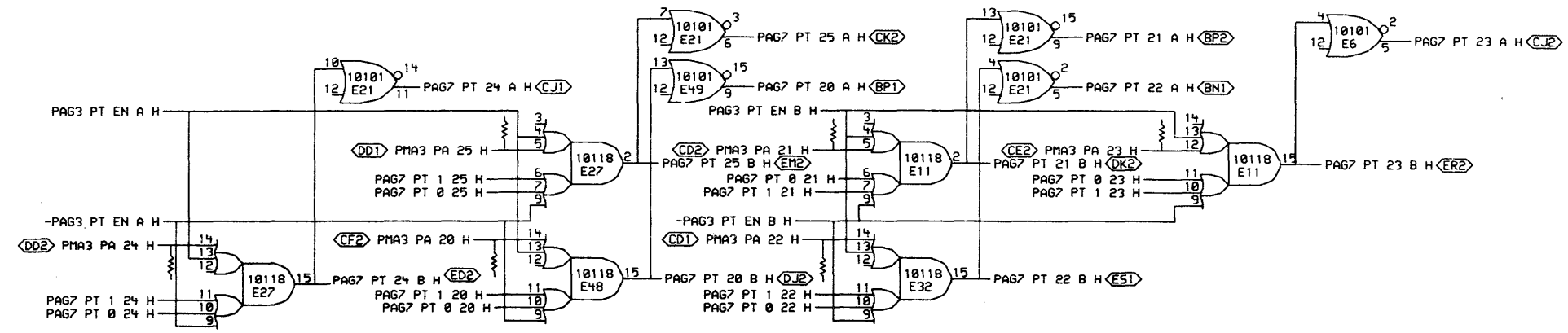
REVISIONS
CHK CHANGE NO. REV

digital
XTRA: CHANGES, REVISIONS, PAGESET, DATA
FIRST USED ON OPTION/MODEL: MCA25

DRN. C. KACZOR
CHK'D D. DELLORCO
DATE 18-JUL-84
SHEET 1 OF 1

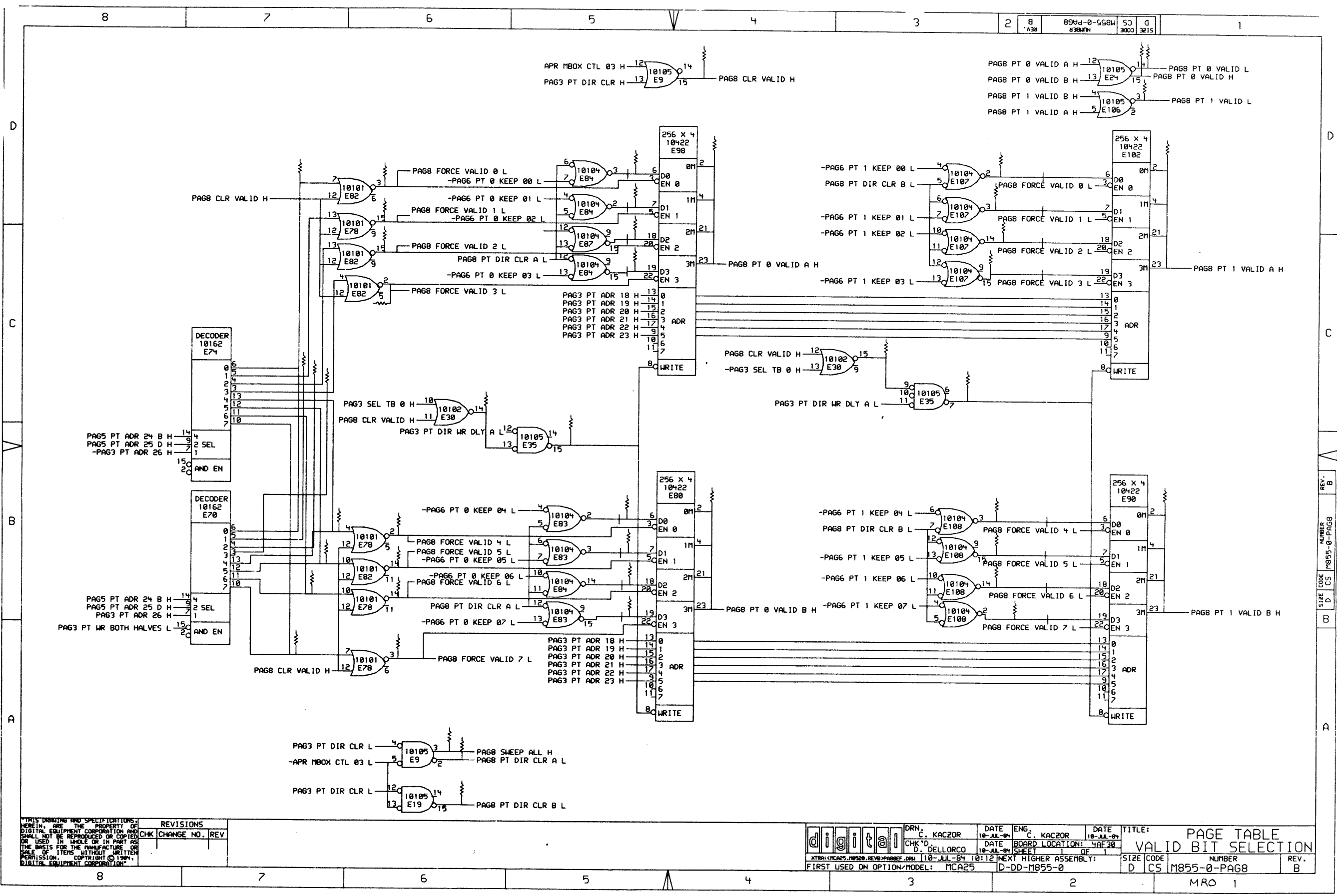
ENG. C. KACZOR
DATE 18-JUL-84
BOARD LOCATION: 4AF30
NEXT HIGHER ASSEMBLY: D-DD-M855-0

TITLE: PAGING
KEEP BIT SELECTION
SIZE CODE D CS
NUMBER M855-0-PAGE6
REV. B



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REVISIONS	
CHK	CHANGE NO. REV

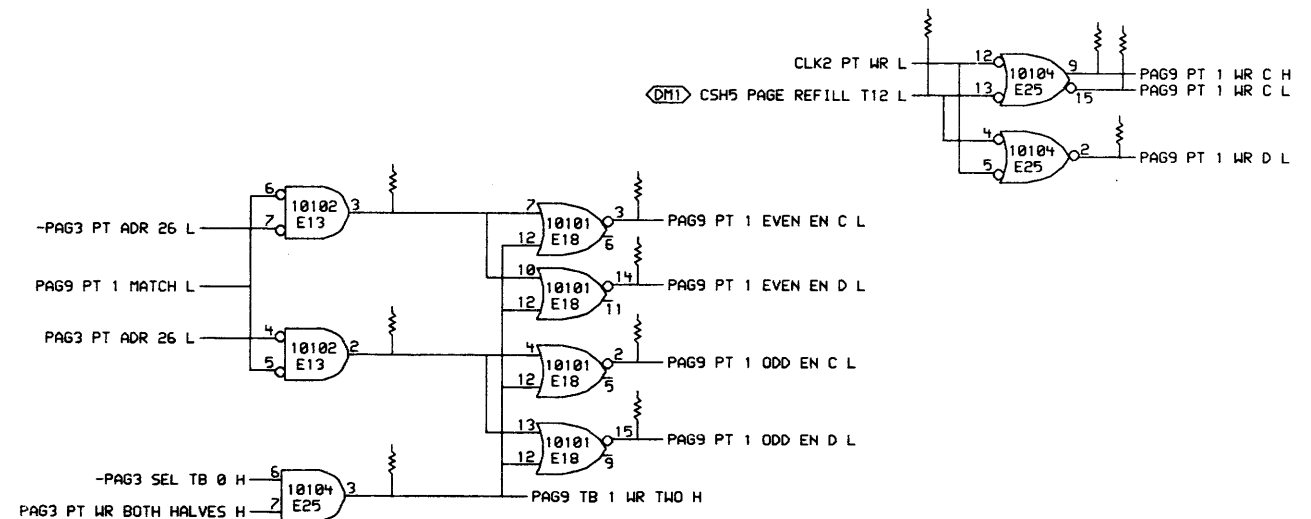
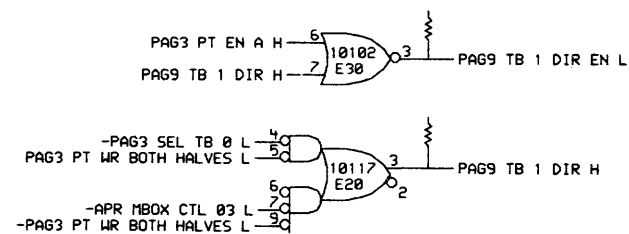
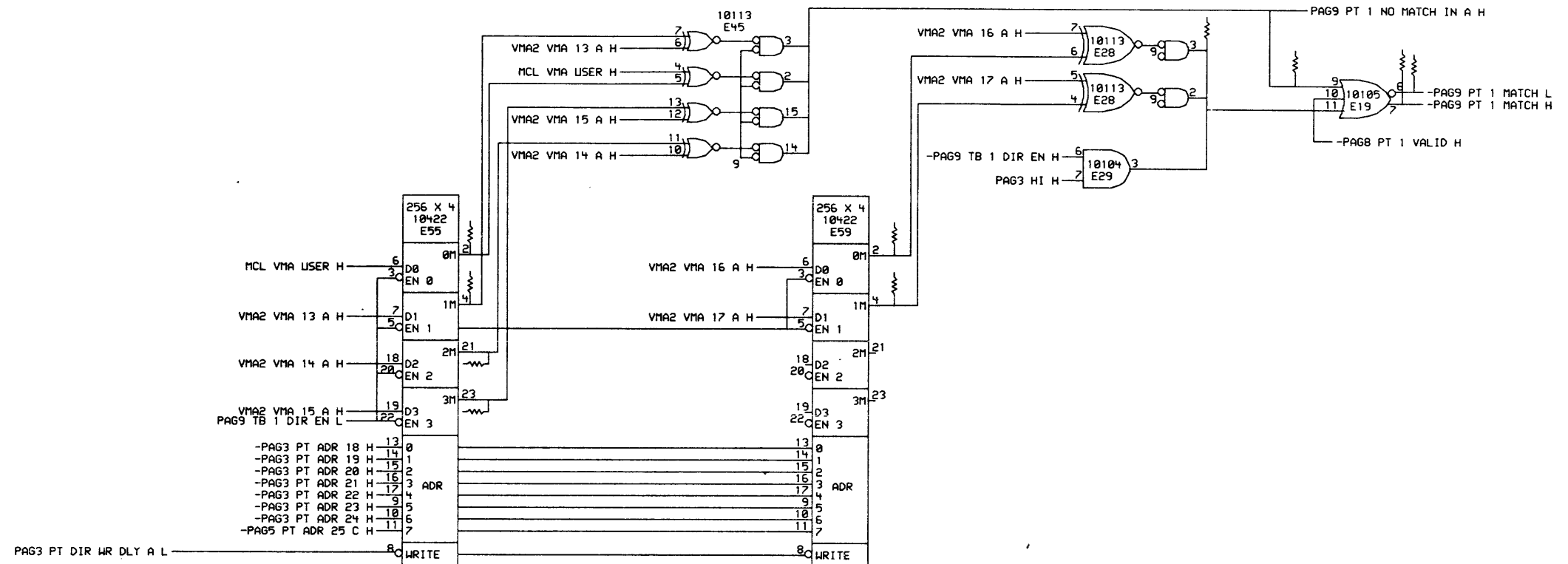


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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN.	C. KACZOR	DATE	10-JUL-84	ENG.	C. KACZOR	DATE	10-JUL-84	TITLE:	PAGE TABLE
	CHK'D	O. DELLORCO	DATE	10-JUL-84	BOARD LOCATION:	4AF30	VALID BIT SELECTION			
	FIRST USED ON OPTION/MODEL:		MCA25	D-DD-M855-0						
	SIZE		CODE	D	CS	NUMBER	M855-0-PAG8	REV.	B	

MRO 1



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REVISIONS
CHK CHANGE NO. REV.

digital

DRN: C. KACZOR
CHK'D: D. DELLORCO

DATE: 10-11-84
DATE: 10-11-84

ENG: C. KACZOR
SHEET 1 OF 1

DATE: 10-11-84
BOARD LOCATION: 4AF30

TITLE: PAGE TABLE 1
DIRECTORY

FIRST USED ON OPTION/MODEL: MCA25

DATE: 10-11-84
D-DD-M855-0

SIZE CODE: D CS
NUMBER: M855-0-PAG9
REV: B

D

C

B

D

D

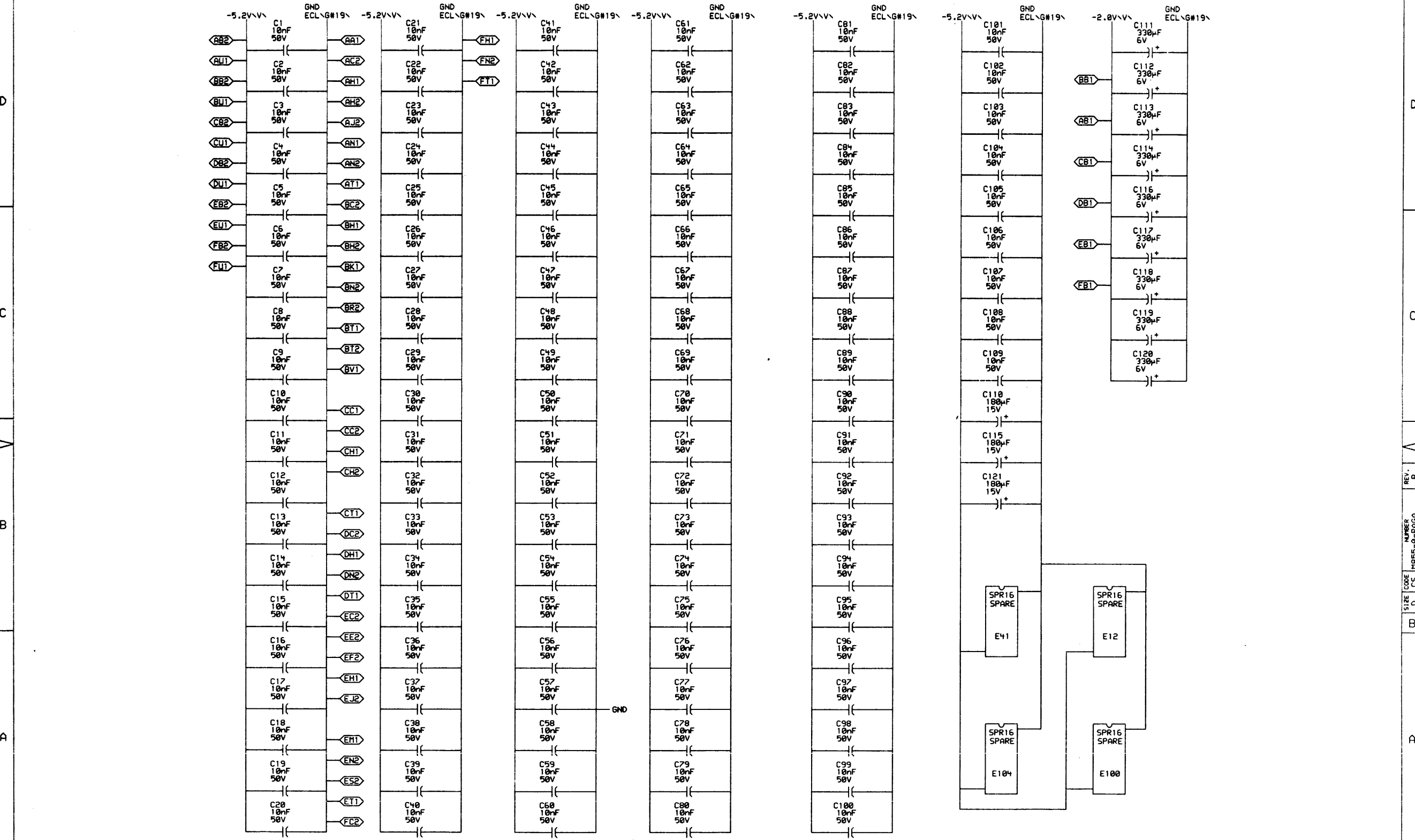
C

REV. B

NUMBER M855-0-PAGA

SIZE CODE CS D

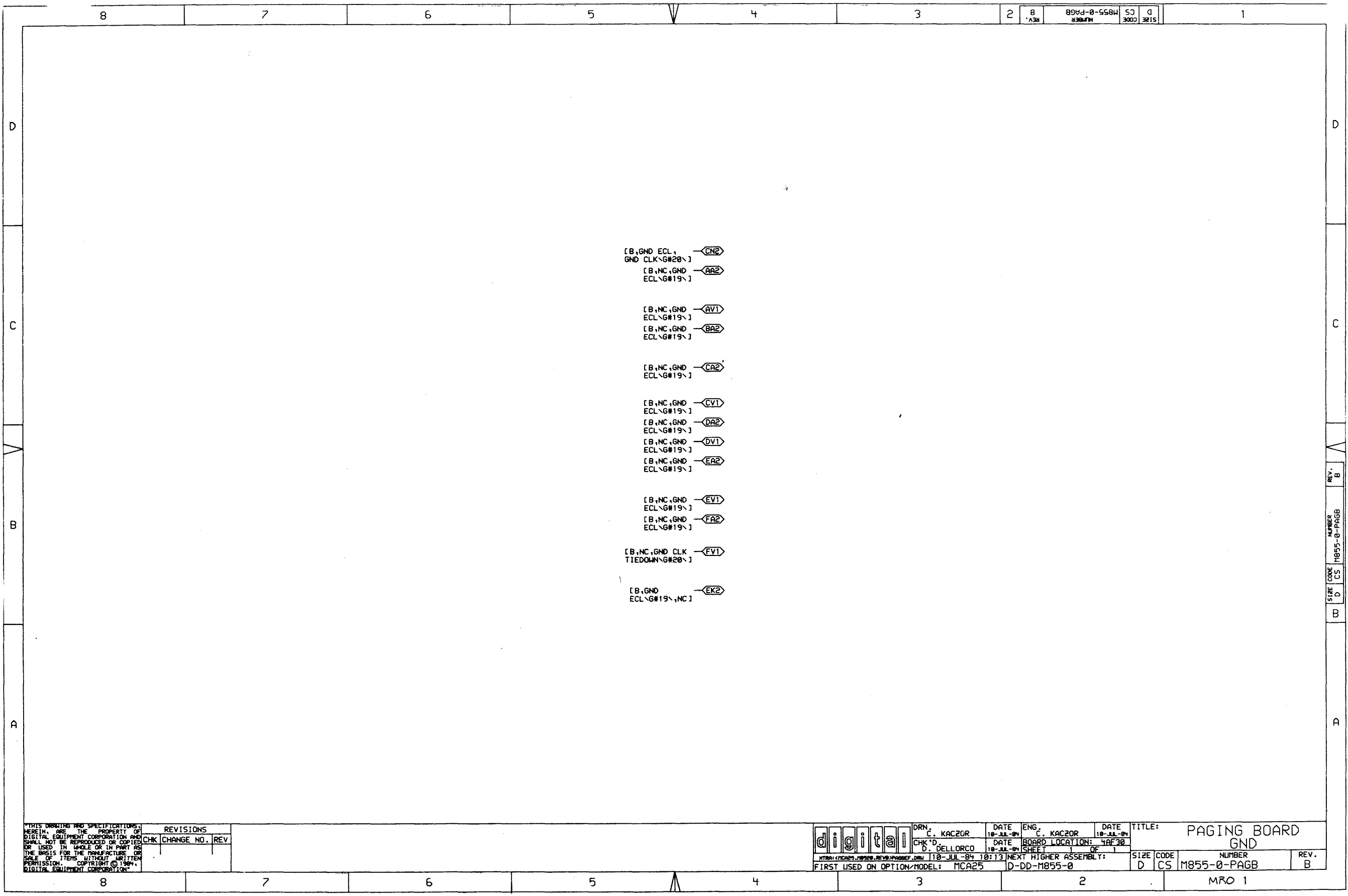
D



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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. C. KACZOR	DATE 10-JUL-84	ENG. C. KACZOR	DATE 10-JUL-84	TITLE: PAGING BOARD POWER, GND, CAP
	CHK. D. DELLORCO	DATE 10-JUL-84	BOARD LOCATION: 4AF30	SHEET 1 OF 1	SIZE CODE CS D
	FIRST USED ON OPTION/MODEL: MCA25		NEXT HIGHER ASSEMBLY: D-DD-M855-0		NUMBER M855-0-PAGA



[B,GND ECL , GND CLK\G#20\] —CN2

[B,NC,GND ECL\G#19\] —AA2

[B,NC,GND ECL\G#19\] —AV1

[B,NC,GND ECL\G#19\] —BA2

[B,NC,GND ECL\G#19\] —CA2

[B,NC,GND ECL\G#19\] —CV1

[B,NC,GND ECL\G#19\] —DA2

[B,NC,GND ECL\G#19\] —DV1

[B,NC,GND ECL\G#19\] —EA2

[B,NC,GND ECL\G#19\] —EV1

[B,NC,GND ECL\G#19\] —FA2

[B,NC,GND CLK TIEDOWN\G#20\] —FV1

[B,GND ECL\G#19\,NC] —EK2

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN: C. KACZOR	DATE: 18-JUL-84	ENG: C. KACZOR	DATE: 18-JUL-84	TITLE: PAGING BOARD GND		
	CHK'D: D. DELLORCO	DATE: 18-JUL-84	BOARD LOCATION: 4AF30	SHEET: 1 OF 1			
FIRST USED ON OPTION/MODEL: MCA25		NEXT HIGHER ASSEMBLY: D-DD-M855-0		SIZE: D	CODE: CS	NUMBER: M855-0-PAGB	REV.: B

RESISTOR LOC(PIN)	SHOWN ON DRAW#	VALUE	TERMINATES SIGNAL
212(3)	PAG3 C2	68n	%E10(3)
259(3)	PAG4 A3	68n	%E10(15)
252(5)	PAG8 C2	68n	%E10(14)
227(6)	PAG8 C3	68n	%E10(15)
227(10)	PAG8 D2	68n	%E10(2)
252(8)	PAG8 D2	68n	%E10(3)
216(5)	PAG8 B2	68n	%E100(14)
216(8)	PAG8 B2	68n	%E100(15)
216(6)	PAG8 B2	68n	%E100(2)
251(8)	PAG8 B2	68n	%E100(3)
267(10)	PAG9 A4	68n	%E13(2)
271(5)	PAG9 A4	68n	%E13(3)
241(10)	PAG1 D3	68n	%E16(14)
230(3)	PAG5 B3	68n	%E17(15)
256(1)	PAG5 B3	68n	%E17(2)
210(3)	PAG1 D3	68n	%E19(3)
230(1)	PAG5 C7	68n	%E2(10)
239(6)	PAG5 D7	68n	%E2(13)
239(8)	PAG5 D7	68n	%E2(3)
240(3)	PAG1 C3	68n	%E22(14)
266(6)	PAG3 D2	68n	%E23(3)
269(5)	PAG5 C2	68n	%E26(2)
212(5)	PAG9 D2	68n	%E28(2)
240(5)	PAG5 C3	68n	%E29(14)
236(3)	PAG6 B7	68n	%E29(2)
229(8)	PAG8 C6	68n	%E30(14)
211(10)	PAG8 C3	68n	%E30(15)
22(8)	PAG4 C4	68n	%E34(15)
260(1)	PAG4 C7	68n	%E34(3)
227(1)	PAG8 B5	68n	%E35(15)
210(1)	PAG1 D3	68n	%E35(3)
227(8)	PAG8 C3	68n	%E35(7)
224(10)	PAG6 B7	68n	%E38(15)
230(10)	PAG5 B4	68n	%E4(2)
29(6)	PAG4 D4	68n	%E40(2)
235(1)	PAG3 D5	68n	%E51(2)
235(3)	PAG3 C5	68n	%E51(21)
271(1)	PAG3 C5	68n	%E51(23)
271(6)	PAG3 C5	68n	%E51(4)
223(5)	PAG9 C5	68n	%E55(2)

RESISTOR LOC(PIN)	SHOWN ON DRAW#	VALUE	TERMINATES SIGNAL
223(1)	PAG9 C5	68n	%E55(21)
223(3)	PAG9 C5	68n	%E55(23)
246(6)	PAG9 C5	68n	%E55(4)
249(5)	PAG4 C5	68n	%E56(9)
234(3)	PAG9 C4	68n	%E59(2)
235(5)	PAG9 C4	68n	%E59(4)
27(6)	PAG4 B7	68n	%E60(15)
261(3)	PAG4 B6	68n	%E60(2)
235(6)	PAG3 D4	68n	%E64(2)
234(1)	PAG3 C4	68n	%E64(4)
221(1)	PAG5 C3	68n	%E65(2)
230(3)	PAG5 C7	68n	%E7(10)
260(3)	PAG8 A7	68n	%E70(10)
245(10)	PAG8 B7	68n	%E70(11)
260(5)	PAG8 B7	68n	%E70(12)
24(5)	PAG8 B6	68n	%E70(13)
24(10)	PAG8 C7	68n	%E70(3)
24(8)	PAG8 C7	68n	%E70(4)
24(1)	PAG8 C7	68n	%E70(5)
260(6)	PAG8 D7	68n	%E70(6)
266(1)	PAG3 B3	68n	%E8(14)
266(5)	PAG3 A3	68n	%E8(15)
257(3)	PAG1 B3	68n	%E8(2)
257(5)	PAG1 A3	68n	%E8(3)
244(10)	PAG3 A7	68n	%E81(2)
217(10)	PAG3 A7	68n	%E81(5)
263(6)	PAG8 B5	68n	%E83(15)
263(8)	PAG8 B5	68n	%E83(2)
263(10)	PAG8 B5	68n	%E83(3)
262(8)	PAG8 B5	68n	%E84(14)
215(10)	PAG8 C5	68n	%E84(15)
227(3)	PAG8 D5	68n	%E84(2)
252(3)	PAG8 D5	68n	%E84(3)
214(10)	PAG8 C5	68n	%E87(15)
253(8)	PAG4 C2	68n	%E99(6)
211(5)	PAG3 B7	68n	APR5 WR PT SEL 0 H
251(1)	PAG3 B7	68n	APR5 WR PT SEL 1 H
237(5)	PAG3 B5	68n	-APR5 WR PT SEL 1 H
236(1)	PAG6 B7	68n	AR 23 C H
247(1)	PAG3 C7	68n	-CLK2 PT DIR WR H

RESISTOR LOC(PIN)	SHOWN ON DRAW#	VALUE	TERMINATES SIGNAL
212(8)	PAG3 B2	68n	-CLK2 PT WR H
230(6)	PAG5 B4	68n	-CON K110 PAGING MODE H
211(3)	PAG3 B5	68n	CSH1 PRGR CYC A H
231(8)	PAG9 B2	68n	-CSH5 PAGE REFILL T12 H
27(3)	PAG3 C7	68n	-CSH6 MBOX PT DIR WR H
237(3)	PAG3 B5	68n	-CSH6 PAGE FAIL HOLD H
250(1)	PAG4 C2	68n	CSH6 PAGE REFILL ERROR H
250(3)	PAG4 C4	68n	-CSH6 PAGE REFILL ERROR H
239(1)	PAG5 D8	68n	MB 00-05 PAR ODD H
239(5)	PAG5 D8	68n	MB 06-11 PAR ODD H
230(5)	PAG5 C8	68n	MB 12-17 PAR ODD H
239(10)	PAG5 C7	68n	MB 18-23 PAR ODD H
242(1)	PAG5 C7	68n	MB 24-29 PAR ODD H
251(5)	PAG3 A5	68n	MB 30-35 PAR ODD H
217(8)	PAG3 A5	68n	MB SEL 1 H
271(8)	PAG4 D8	68n	MB SEL 2 H
269(3)	PAG3 B7	68n	MCL VMA USER H
27(1)	PAG4 A4	68n	MCL2 VMA EXEC H
224(6)	PAG4 C2	68n	-MCL2 VMA WRITE H
29(5)	PAG4 D7	68n	MCL3 PAGE ADDRESS COND H
211(6)	PAG4 C4	68n	MCL3 PAGE ILL ENTRY H
236(6)	PAG4 B6	68n	-MCL3 PAGE ILL ENTRY H
253(10)	PAG4 C2	68n	MCL3 PAGE TEST PRIVATE H
240(1)	PAG4 D7	68n	MCL5 VMA ADR ERR H
241(8)	PAG1 B2	68n	MCL6 PAGE LIEBR REF H
243(1)	PAG1 C7	68n	PAG1 PT 0 26 H
259(8)	PAG1 C3	68n	PAG1 PT 0 ACCESS H
260(3)	PAG1 B7	68n	PAG1 PT 0 CACHE H
219(10)	PAG1 B5	68n	PAG1 PT 0 PUBLIC H
262(5)	PAG1 C5	68n	PAG1 PT 0 SOFTWARE H
237(1)	PAG1 B1	68n	PAG1 PT 0 WRITABLE H
259(10)	PAG1 C7	68n	PAG1 PT 1 26 H
259(6)	PAG1 C3	68n	PAG1 PT 1 ACCESS H
235(10)	PAG1 A3	68n	PAG1 PT 1 CACHE H
246(1)	PAG1 A3	68n	-PAG1 PT 1 EV EN 26 H
260(6)	PAG1 A7	68n	-PAG1 PT 1 OD EN 26 H
219(8)	PAG1 A5	68n	PAG1 PT 1 PUBLIC H
262(3)	PAG1 C5	68n	PAG1 PT 1 SOFTWARE H
243(3)	PAG1 D7	68n	PAG1 PT 1 WRITABLE H
			-PAG1 PT ACCESS H

RESISTOR LOC(PIN)	SHOWN ON DRAW#	VALUE	TERMINATES SIGNAL
247(10)	PAG2 C6	68n	PAG2 PT 0 14 H
265(8)	PAG2 B6	68n	PAG2 PT 0 15 H
265(6)	PAG2 C4	68n	PAG2 PT 0 16 H
232(8)	PAG2 B4	68n	PAG2 PT 0 17 H
250(5)	PAG2 C2	68n	PAG2 PT 0 18 H
250(10)	PAG2 B2	68n	PAG2 PT 0 19 H
27(8)	PAG2 C6	68n	PAG2 PT 1 14 H
265(10)	PAG2 A6	68n	PAG2 PT 1 15 H
26(6)	PAG2 C4	68n	PAG2 PT 1 16 H
232(1)	PAG2 A4	68n	PAG2 PT 1 17 H
250(3)	PAG2 C2	68n	PAG2 PT 1 18 H
250(8)	PAG2 A2	68n	PAG2 PT 1 19 H
271(10)	PAG3 A7	68n	PAG3 HI H
214(5)	PAG3 B2	68n	-PAG3 PT 0 EVEN EN A H
263(1)	PAG3 B2	68n	-PAG3 PT 0 EVEN EN B H
213(6)	PAG3 C4	68n	-PAG3 PT 0 LRU H
244(8)	PAG3 D1	68n	PAG3 PT 0 MATCH H
240(1)	PAG3 D1	68n	-PAG3 PT 0 MATCH H
266(10)	PAG3 D2	68n	PAG3 PT 0 NO MATCH IN A H
215(3)	PAG3 A2	68n	-PAG3 PT 0 ODD EN A H
263(3)	PAG3 A2	68n	-PAG3 PT 0 ODD EN B H
216(3)	PAG3 D2	68n	-PAG3 PT 0 WR 26 H
214(1)	PAG3 B1	68n	-PAG3 PT 0 WR A H
263(5)	PAG3 B1	68n	-PAG3 PT 0 WR B H
253(6)	PAG3 C2	68n	-PAG3 PT 1 WR 26 H
214(6)	PAG3 D7	68n	PAG3 PT ADR 18 H
230(10)	PAG3 D7	68n	-PAG3 PT ADR 18 H
224(3)	PAG3 B7	68n	PAG3 PT ADR 19 H
230(8)	PAG3 B7	68n	-PAG3 PT ADR 19 H
214(8)	PAG3 B7	68n	PAG3 PT ADR 20 H
220(6)	PAG3 B7	68n	-PAG3 PT ADR 20 H
215(8)	PAG3 D7	68n	PAG3 PT ADR 21 H
264(1)	PAG3 D7	68n	-PAG3 PT ADR 21 H
227(5)	PAG3 D7	68n	PAG3 PT ADR 22 H
220(8)	PAG3 D7	68n	-PAG3 PT ADR 22 H
253(5)	PAG3 C7	68n	PAG3 PT ADR 23 H
264(5)	PAG3 C7	68n	-PAG3 PT ADR 23 H
246(8)	PAG3 A5	68n	PAG3 PT ADR 24 H
270(5)	PAG3 A5	68n	-PAG3 PT ADR 24 H
22(3)	PAG5 B6	68n	PAG3 PT ADR 25 A IN H

NOTE:

1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES OUTPUT OF DIP LOC AND (<) INDICATES PIN NUMBER

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE 06-AUG-84	ENG. C. KACZOR	DATE 06-AUG-84	TITLE: ETCH PAGER TERMINATORS	SIZE D	CODE CS	NUMBER M855-0-RES	REV. B
FIRST USED ON OPTION/MODEL: MCA25				D-DD-M855-0		MRO 1		

8

7

6

5

4

3

2

B

REV.

A3

NUMBER

M855-0-RES

CS

CODE

SIZE

D

REV.

B

1

RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL
22(5)	PAG3	A5	68n	PAG3 PT ADR 25 B IN H
267(5)	PAG3	A7	68n	PAG3 PT ADR 26 H
266(8)	PAG3	A7	68n	-PAG3 PT ADR 26 H
260(1)	PAG3	B7	68n	PAG3 PT ADR 26 A IN H
213(3)	PAG3	B5	68n	PAG3 PT DIR CLR H
213(10)	PAG3	B5	68n	-PAG3 PT DIR CLR H
R2(1)	PAG3	B7	100n	-PAG3 PT DIR WR H
R1(1)	PAG3	B7	100n	-PAG3 PT DIR WR DLY H
229(6)	PAG3	C7	68n	-PAG3 PT DIR WR DLY A H
26(8)	PAG3	A1	68n	PAG3 PT EN A H
265(5)	PAG3	A1	68n	-PAG3 PT EN A H
245(6)	PAG3	A1	68n	PAG3 PT EN B H
250(6)	PAG3	A1	68n	-PAG3 PT EN B H
27(5)	PAG3	D2	68n	PAG3 PT MATCH H
231(1)	PAG3	B5	68n	PAG3 PT WR BOTH HALVES H
25(1)	PAG3	B5	68n	-PAG3 PT WR BOTH HALVES H
220(10)	PAG3	C1	68n	PAG3 SEL TB 0 H
229(3)	PAG3	C1	68n	-PAG3 SEL TB 0 H
229(5)	PAG3	B5	68n	PAG3 TB 0 DIR H
264(3)	PAG3	B5	68n	-PAG3 TB 0 DIR EN H
266(3)	PAG3	A2	68n	PAG3 TB 0 WR TWO H
233(1)	PAG4	D1	68n	PAG4 FORCE TB CLK H
233(8)	PAG4	A6	68n	-PAG4 FORCE TO TB 0 H
233(3)	PAG4	A6	68n	PAG4 FORCE TO TB 1 H
220(1)	PAG4	D1	68n	-PAG4 MBOX CTL 03 H
268(5)	PAG4	D7	68n	PAG4 PAGE EXEC PAGED REF H
225(3)	PAG4	D7	68n	-PAG4 PAGE EXEC PAGED REF H
248(3)	PAG4	D7	68n	PAG4 PAGE EXEC REF H
29(1)	PAG4	D7	68n	-PAG4 PAGE EXEC REF H
26(1)	PAG4	C3	68n	PAG4 PAGE FAIL A H
225(6)	PAG4	C3	68n	-PAG4 PAGE FAIL A H
265(3)	PAG4	B6	68n	PAG4 PAGE TEST PRIVATE H
225(5)	PAG4	B6	68n	-PAG4 PAGE TEST PRIVATE H
259(1)	PAG4	C6	68n	-PAG4 PAGE TEST WRITE H
29(3)	PAG4	C7	68n	-PAG4 PAGE UNPAGED REF H
28(3)	PAG4	D7	68n	PAG4 PAGE USER PAGED REF H
225(1)	PAG4	D7	68n	-PAG4 PAGE USER PAGED REF H
259(5)	PAG4	D4	68n	PAG4 PAGE WRITE OK H
249(1)	PAG4	D4	68n	-PAG4 PAGE WRITE OK H
252(1)	PAG4	A2	68n	-PAG4 PAGED REF H

RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL
262(10)	PAG4	B3	68n	-PAG4 PF CODE 2X H
238(8)	PAG5	A7	68n	PAG5 KI10 PAGE MODE H
240(6)	PAG5	B1	68n	PAG5 PT 0 EV PG PAR H
257(1)	PAG5	B1	68n	PAG5 PT 0 OD PG PAR H
240(10)	PAG5	B1	68n	PAG5 PT 1 EV PG PAR H
240(8)	PAG5	B1	68n	PAG5 PT 1 OD PG PAR H
226(3)	PAG5	C7	68n	-PAG5 PT ADR 18 A H
218(3)	PAG5	C7	68n	PAG5 PT ADR 18 B H
254(5)	PAG5	B7	68n	-PAG5 PT ADR 19 A H
228(6)	PAG5	B7	68n	PAG5 PT ADR 19 B H
226(5)	PAG5	B7	68n	-PAG5 PT ADR 20 A H
228(8)	PAG5	B7	68n	PAG5 PT ADR 20 B H
254(6)	PAG5	B7	68n	-PAG5 PT ADR 21 A H
218(5)	PAG5	B7	68n	PAG5 PT ADR 21 B H
246(5)	PAG5	C5	68n	-PAG5 PT ADR 22 A H
234(8)	PAG5	C5	68n	PAG5 PT ADR 22 B H
216(10)	PAG5	B5	68n	-PAG5 PT ADR 23 A H
262(6)	PAG5	B5	68n	PAG5 PT ADR 23 B H
226(8)	PAG5	B5	68n	-PAG5 PT ADR 24 A H
25(3)	PAG5	B5	68n	PAG5 PT ADR 24 B H
222(8)	PAG5	B5	68n	-PAG5 PT ADR 25 A H
257(6)	PAG5	B5	68n	PAG5 PT ADR 25 B H
264(6)	PAG5	A5	68n	-PAG5 PT ADR 25 C H
222(3)	PAG5	A5	68n	PAG5 PT ADR 25 D H
22(10)	PAG5	C1	68n	PAG5 PT PAR ODD H
249(3)	PAG5	C1	68n	-PAG5 PT PAR ODD H
229(10)	PAG6	D6	68n	PAG6 PT 0 KEEP H
244(1)	PAG6	D4	68n	PAG6 PT 0 KEEP 00 H
23(5)	PAG6	D4	68n	PAG6 PT 0 KEEP 01 H
244(3)	PAG6	D4	68n	PAG6 PT 0 KEEP 02 H
23(1)	PAG6	C4	68n	PAG6 PT 0 KEEP 03 H
23(10)	PAG6	B4	68n	PAG6 PT 0 KEEP 04 H
244(5)	PAG6	B4	68n	PAG6 PT 0 KEEP 05 H
23(8)	PAG6	B4	68n	PAG6 PT 0 KEEP 06 H
244(6)	PAG6	A4	68n	PAG6 PT 0 KEEP 07 H
212(6)	PAG6	C6	68n	PAG6 PT 1 KEEP H
243(8)	PAG6	D1	68n	PAG6 PT 1 KEEP 00 H
243(6)	PAG6	D1	68n	PAG6 PT 1 KEEP 01 H
21(6)	PAG6	D1	68n	PAG6 PT 1 KEEP 02 H
243(5)	PAG6	C1	68n	PAG6 PT 1 KEEP 03 H

RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL
21(8)	PAG6	B1	68n	PAG6 PT 1 KEEP 04 H
225(10)	PAG6	B1	68n	PAG6 PT 1 KEEP 05 H
21(10)	PAG6	B1	68n	PAG6 PT 1 KEEP 06 H
243(10)	PAG6	A1	68n	PAG6 PT 1 KEEP 07 H
216(1)	PAG6	B6	68n	PAG6 PT IN KEEP A H
253(1)	PAG6	A6	68n	PAG6 PT IN KEEP B H
249(8)	PAG7	C7	68n	PAG7 PT 0 20 H
241(5)	PAG7	B7	68n	PAG7 PT 0 21 H
232(5)	PAG7	C4	68n	PAG7 PT 0 22 H
241(1)	PAG7	B4	68n	PAG7 PT 0 23 H
232(3)	PAG7	C1	68n	PAG7 PT 0 24 H
232(6)	PAG7	B2	68n	PAG7 PT 0 25 H
223(8)	PAG7	C7	68n	PAG7 PT 1 20 H
241(6)	PAG7	A7	68n	PAG7 PT 1 21 H
235(8)	PAG7	C4	68n	PAG7 PT 1 22 H
242(8)	PAG7	A4	68n	PAG7 PT 1 23 H
237(8)	PAG7	C1	68n	PAG7 PT 1 24 H
234(6)	PAG7	A2	68n	PAG7 PT 1 25 H
24(6)	PAG8	D4	68n	PAG8 CLR VALID H
224(5)	PAG8	D6	68n	-PAG8 FORCE VALID 0 H
253(3)	PAG8	D6	68n	-PAG8 FORCE VALID 1 H
224(1)	PAG8	C6	68n	-PAG8 FORCE VAL'D 2 H
252(6)	PAG8	C6	68n	-PAG8 FORCE VALID 3 H
215(6)	PAG8	B6	68n	-PAG8 FORCE VALID 4 H
215(5)	PAG8	B6	68n	-PAG8 FORCE VALID 5 H
215(1)	PAG8	B6	68n	-PAG8 FORCE VALID 6 H
214(3)	PAG8	A6	68n	-PAG8 FORCE VALID 7 H
233(5)	PAG8	D2	68n	PAG8 PT 0 VALID H
271(3)	PAG8	D2	68n	-PAG8 PT 0 VALID H
211(1)	PAG8	D4	68n	PAG8 PT 0 VALID A H
212(10)	PAG8	B4	68n	PAG8 PT 0 VALID B H
233(6)	PAG8	D2	68n	-PAG8 PT 1 VALID H
224(8)	PAG8	D1	68n	PAG8 PT 1 VALID A H
252(10)	PAG8	B1	68n	PAG8 PT 1 VALID B H
260(10)	PAG8	A6	68n	-PAG8 PT DIR CLR A H
225(8)	PAG8	A6	68n	-PAG8 PT DIR CLR B H
267(1)	PAG8	A6	68n	PAG8 SWEEP ALL H
254(10)	PAG9	A3	68n	-PAG9 PT 1 EVEN EN C H
218(8)	PAG9	A3	68n	-PAG9 PT 1 EVEN EN D H
22(1)	PAG9	D1	68n	PAG9 PT 1 MATCH H

RESISTOR LOC(PIN)	SHOWN ON DRW#	REF	VALUE	TERMINATES SIGNAL
267(3)	PAG9	D1	68n	-PAG9 PT 1 MATCH H
267(6)	PAG9	D2	68n	PAG9 PT 1 NO MATCH IN A H
226(10)	PAG9	A3	68n	-PAG9 PT 1 ODD EN C H
218(10)	PAG9	A3	68n	-PAG9 PT 1 ODD EN D H
212(1)	PAG9	B2	68n	PAG9 PT 1 WR C H
256(5)	PAG9	B1	68n	-PAG9 PT 1 WR C H
256(8)	PAG9	B1	68n	-PAG9 PT 1 WR D H
211(8)	PAG9	B7	68n	PAG9 TB 1 DIR H
220(5)	PAG9	B7	68n	-PAG9 TB 1 DIR EN H
267(8)	PAG9	A4	68n	PAG9 TB 1 WR TWO H
27(10)	PAG2	D7	68n	PMA3 PA 14 H
26(10)	PAG2	D5	68n	PMA3 PA 15 H
26(5)	PAG2	D5	68n	PMA3 PA 16 H
234(10)	PAG2	D4	68n	PMA3 PA 17 H
245(3)	PAG2	D4	68n	PMA3 PA 18 H
245(8)	PAG2	D3	68n	PMA3 PA 19 H
29(8)	PAG2	D5	68n	PMA3 PA 20 H
241(3)	PAG2	D4	68n	PMA3 PA 21 H
234(5)	PAG2	D4	68n	PMA3 PA 22 H
242(10)	PAG2	D3	68n	PMA3 PA 23 H
237(6)	PAG2	D6	68n	PMA3 PA 24 H
237(10)	PAG2	D5	68n	PMA3 PA 25 H
226(6)	PAG1	C8	68n	PT IN 00 H
218(1)	PAG1	B8	68n	PT IN 01 H
226(1)	PAG1	C6	68n	PT IN 02 H
218(6)	PAG1	B6	68n	PT IN 03 H
228(10)	PAG1	C4	68n	PT IN 04 H
255(8)	PAG2	C7	68n	PT IN 05 H
264(10)	PAG2	B7	68n	PT IN 06 H
255(6)	PAG2	C5	68n	PT IN 07 H
228(1)	PAG2	B5	68n	PT IN 08 H
255(5)	PAG2	C2	68n	PT IN 09 H
255(1)	PAG2	B2	68n	PT IN 10 H
222(10)	PAG7	C7	68n	PT IN 11 H
257(10)	PAG7	B7	68n	PT IN 12 H
222(5)	PAG7	C5	68n	PT IN 13 H
257(8)	PAG7	B5	68n	PT IN 14 H
256(3)	PAG7	C2	68n	PT IN 15 H
230(6)	PAG7	B2	68n	PT IN 16 H
222(1)	PAG1	B3	68n	PT IN 17 H

NOTE:

1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES OUTPUT OF DIP LOC AND
() INDICATES PIN NUMBER

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REVISIONS

CHK CHANGE NO. REV

digital

DATE 06-AUG-84
CHK'D D. DELLORCO
DATE 06-AUG-84ENG. C. KACZOR
DATE 06-AUG-84
SHEET 2 OF 3BOARD LOCATION: 4AF30
NEXT HIGHER ASSEMBLY:TITLE: ETCH PAGER
TERMINATORSSIZE CODE NUMBER
D CS M855-0-RES

REV. B

8

7

6

5

4

3

2

MRO 1

RESISTOR LOC(PIN)	SHOWN ON DRW#	ON REF	VALUE	TERMINATES SIGNAL
254(8)	PAG1	C8	68n	PT IN 18 H
250(1)	PAG1	A8	68n	PT IN 19 H
254(3)	PAG1	C6	68n	PT IN 20 H
262(1)	PAG1	A6	68n	PT IN 21 H
254(1)	PAG1	C4	68n	PT IN 22 H
255(10)	PAG2	C7	68n	PT IN 23 H
264(8)	PAG2	A7	68n	PT IN 24 H
228(5)	PAG2	B5	68n	PT IN 25 H
256(10)	PAG2	A5	68n	PT IN 26 H
255(3)	PAG2	C2	68n	PT IN 27 H
228(3)	PAG2	A2	68n	PT IN 28 H
270(1)	PAG7	C7	68n	PT IN 29 H
230(1)	PAG7	A7	68n	PT IN 30 H
222(6)	PAG7	C5	68n	PT IN 31 H
232(10)	PAG7	B5	68n	PT IN 32 H
230(5)	PAG7	C2	68n	PT IN 33 H
256(6)	PAG7	B2	68n	PT IN 34 H
246(3)	PAG1	A3	68n	PT IN 35 H
213(5)	PAG5	A4	68n	SH AR PAR ODD A H
220(3)	PAG3	C6	68n	VMA2 VMA 13 A H
270(10)	PAG3	C6	68n	VMA2 VMA 14 A H
270(8)	PAG3	C6	68n	VMA2 VMA 15 A H
219(3)	PAG3	D4	68n	VMA2 VMA 16 A H
270(3)	PAG3	B7	68n	VMA2 VMA 17 H
219(5)	PAG3	C4	68n	VMA2 VMA 17 A H
242(6)	PAG3	D8	68n	VMA2 VMA 18 H
269(1)	PAG3	B7	68n	VMA2 VMA 19 H
270(6)	PAG3	B7	68n	VMA2 VMA 20 H
242(5)	PAG3	D8	68n	VMA2 VMA 21 H
236(8)	PAG3	D8	68n	VMA2 VMA 22 H
242(3)	PAG3	C8	68n	VMA2 VMA 23 H
251(10)	PAG3	A5	68n	VMA2 VMA 24 H
251(6)	PAG3	A7	68n	VMA2 VMA 25 H
261(1)	PAG3	A7	68n	VMA2 VMA 26 H

NOTE:

1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES OUTPUT OF DIP LOC AND (>) INDICATES PIN NUMBER

DRAWING NUMBER	NO.OF SHT	PART NUMBER	DESCRIPTION	REVISION
		M856-00	MODULE REVISION	A1
E-UA-M856-0-0	2		CHD	A
K-PL-M856-0-DBP	2		PART LIST, M856	A
D-CS-M856-0-CHD1	1		CACHE DATA BITS [N+00],[N+01]	A
D-CS-M856-0-CHD2	1		CACHE DATA BITS [N+02],[N+03]	A
D-CS-M856-0-CHD3	1		CACHE DATA BITS [N+04],[N+05]	A
D-CS-M856-0-CHD4	1		CACHE DATA BITS [N+06],[N+07]	A
D-CS-M856-0-CHD5	1		CACHE DATA BIT [N+08]	A
D-CS-M856-0-CHD6	1		CACHE DATA ADDRESSING LOGIC	A
D-CS-M856-0-CHD7	1		CACHE SEL LOGIC TERMINATORS	A
D-CS-M856-0-CHD8	1		CACHE DATA TERMINATORS	A
D-CS-M856-0-RES	1		CACHE DATA TERMINATORS	A
D-CS-M856-0-RES	1		CACHE DATA TERMINATORS	A
K-PC-M856-0-DBI	-		P.C. DESIGN DATA BASE TAPE	A
D-DD-5017662-0	1		DRAWING DIRECTORY, 5017662	REF

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REVISIONS		
CHK	CHANGE NO.	REV

d	i	g	i	t	a	l
---	---	---	---	---	---	---

DRW: *J. J. J. J.*
CHK'D: D. DELLORCO

DATE	13-SEP-84
DATE	13-SEP-84

ENG.
C.A. JENS
BOARD LOCATI
SHEET 1

DATE	13-SEP-
ON:	N/A
OF	1

84	TITLE:
----	--------

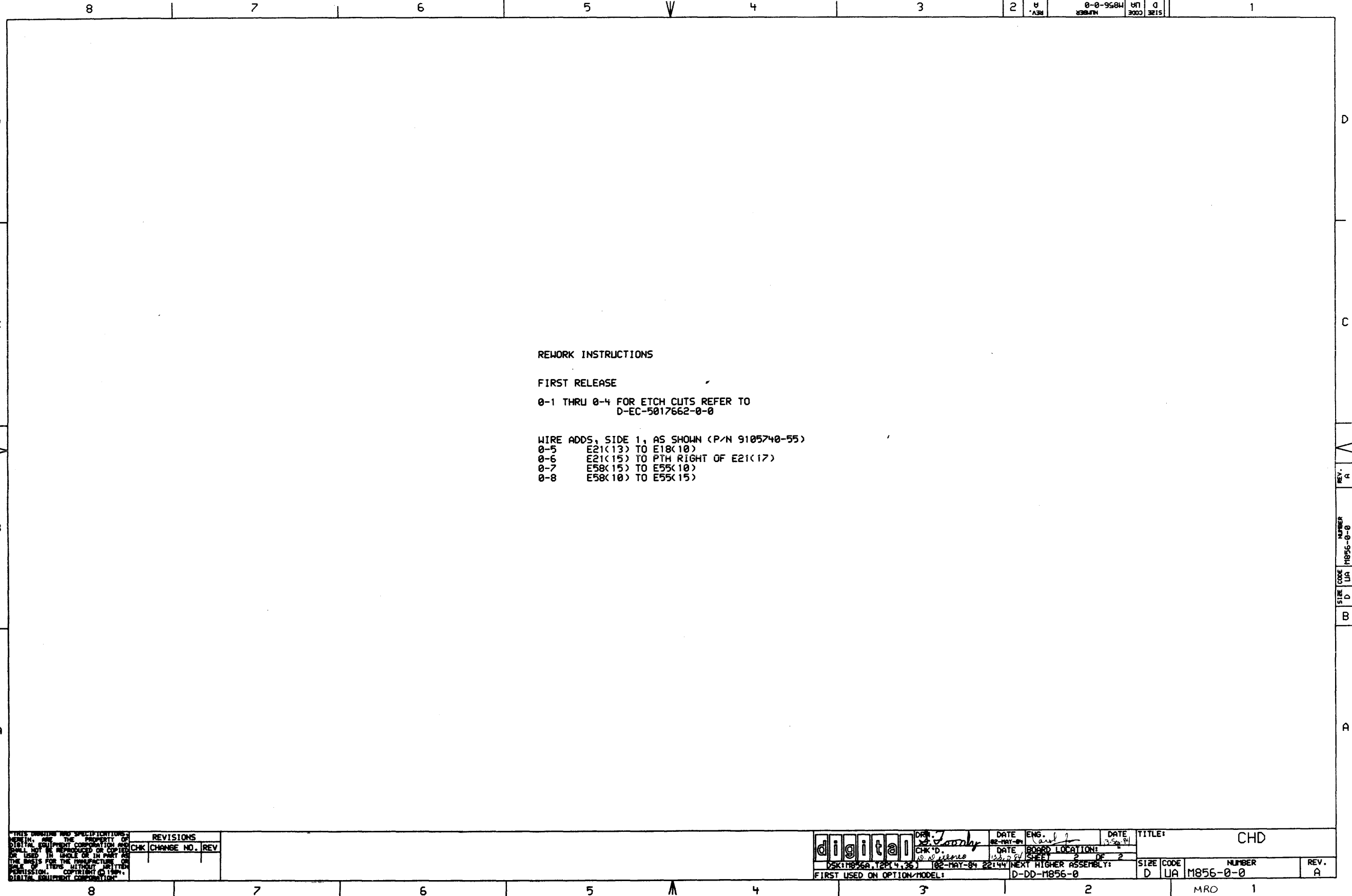
M856
DRAWING DIRECTORY

DSK:M856A.T2P[4,21] 13-SEP-84 1
FIRST USED ON OPTION/MODEL: KL10-P

4:24	NEXT HIGHER ASSEMBLY:
W	MCA25

SIZE	CODE	NUMBER	REV.
D	DD	M856-0	A

1



REWORK INSTRUCTIONS

FIRST RELEASE

0-1 THRU 0-4 FOR ETCH CUTS REFER TO
D-EC-5017662-0-0

WIRE ADDS, SIDE 1, AS SHOWN (P/N 9105740-55)
0-5 E21(13) TO E18(10)
0-6 E21(15) TO PTH RIGHT OF E21(17)
0-7 E58(15) TO E55(10)
0-8 E58(10) TO E55(15)

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8	7	6	5	4	3	2	MRO		1		

D

C

B

A

D

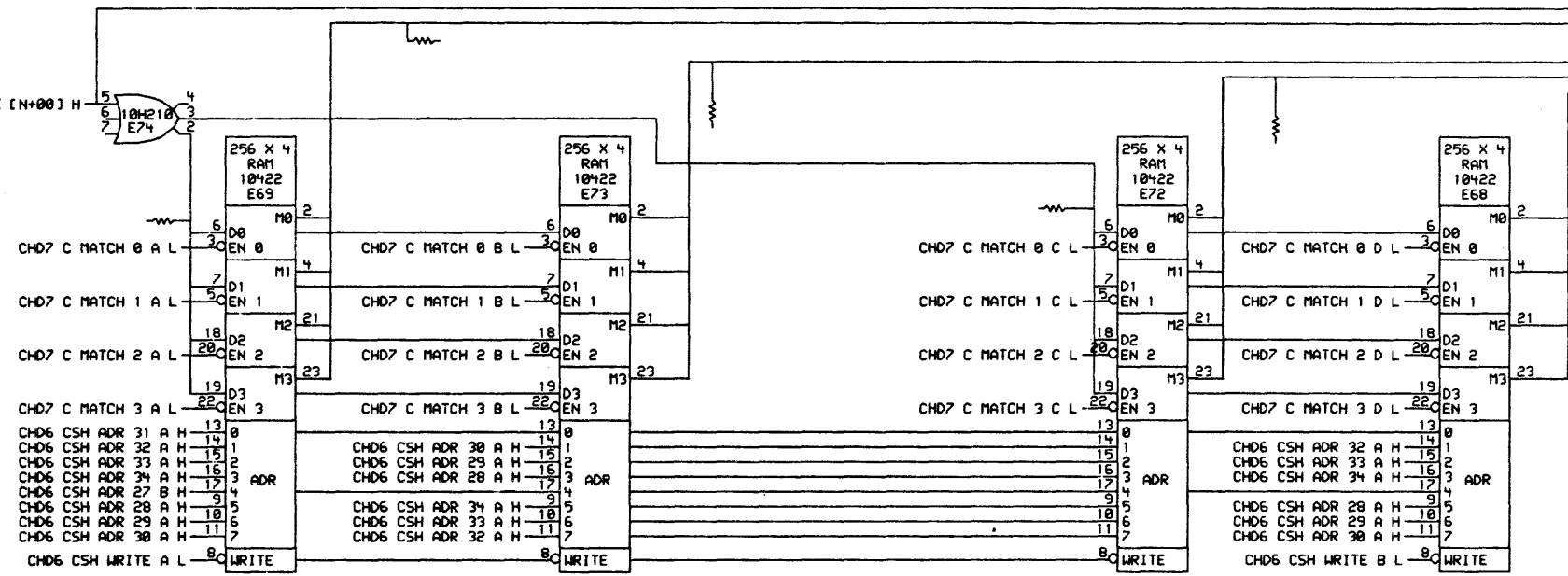
C

A

B

A

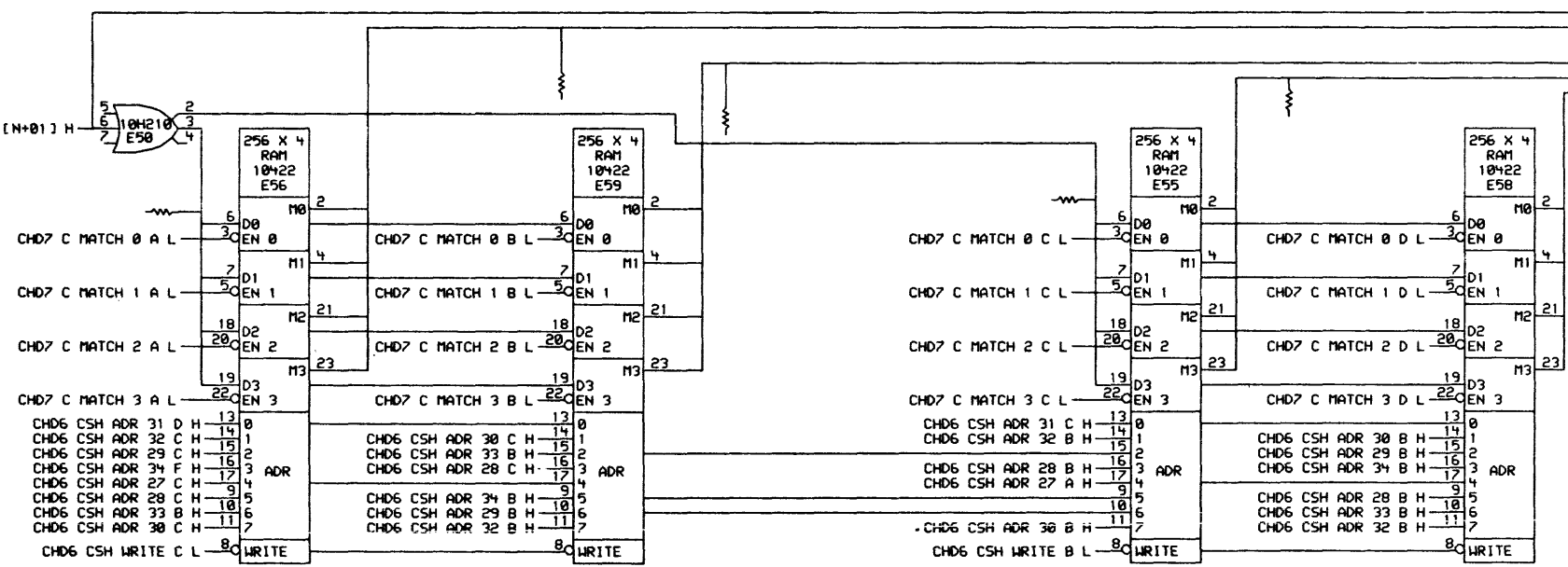
(FD2) MEM TO CACHE [N+00] H



N	SLOT
00	4AF25
09	4AF24
18	4AF19
27	4AF17

CACHE DATA [N+00] B H (FE2)
CACHE DATA [N+00] C H (FE1)
CACHE DATA [N+00] A H (FF1)

(ER2) MEM TO CACHE [N+01] H



CACHE DATA [N+01] C H (E71)
CACHE DATA [N+01] A H (EP2)
CACHE DATA [N+01] B H (E71)

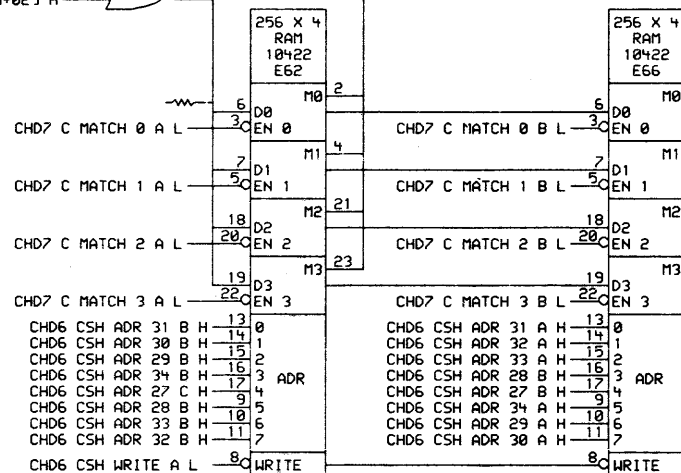
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REVISIONS
CHK CHANGE NO. REV

digital
DATE 06-AUG-84
ENG. C. A. JENS
DATE 06-AUG-84
CHK'D D. DELLORCO
DATE 06-AUG-84
SHEET 1 OF 1
FIRST USED ON OPTION/MODEL: MCA25
D-DD-M856-0

TITLE: CACHE DATA BITS [N+00], [N+01]
SIZE CODE NUMBER REV.
D CS M856-0-CHD1 A

(FF2) MEM TO CACHE [N+02] H



CHD7 C MATCH 0 C L

CHD7 C MATCH 1 C L

CHD7 C MATCH 2 C L

CHD7 C MATCH 3 C L

CHD6 CSH ADR 31 C H
CHD6 CSH ADR 30 B H
CHD6 CSH ADR 33 B HCHD6 CSH ADR 27 A H
CHD6 CSH ADR 34 B H
CHD6 CSH ADR 29 B H
CHD6 CSH ADR 32 B H
CHD6 CSH WRITE B L

CHD7 C MATCH 0 D L

CHD7 C MATCH 1 D L

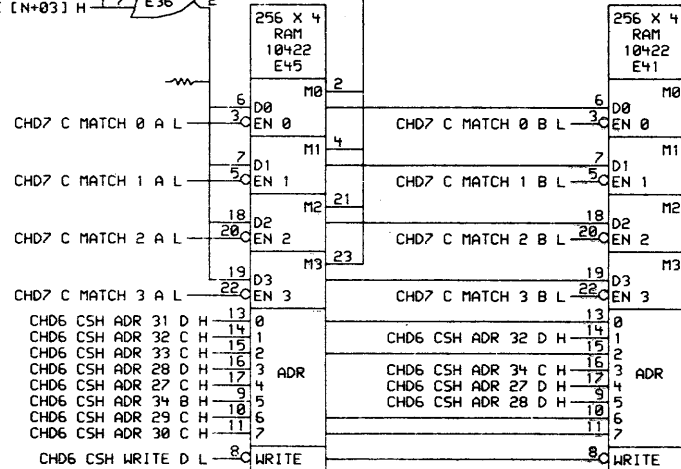
CHD7 C MATCH 2 D L

CHD7 C MATCH 3 D L

CHD6 CSH ADR 32 A H
CHD6 CSH ADR 29 B H
CHD6 CSH ADR 34 B HCHD6 CSH ADR 28 A H
CHD6 CSH ADR 33 A H
CHD6 CSH ADR 30 A H
CHD6 CSH WRITE D L

N	SLOT
00	4AF25
09	4AF24
18	4AF19
27	4AF12

(CH1) MEM TO CACHE [N+03] H



CHD7 C MATCH 0 C L

CHD7 C MATCH 1 C L

CHD7 C MATCH 2 C L

CHD7 C MATCH 3 C L

CHD6 CSH ADR 31 B H
CHD6 CSH ADR 30 D H
CHD6 CSH ADR 29 D H
CHD6 CSH ADR 34 F HCHD6 CSH ADR 27 C H
CHD6 CSH ADR 28 C H
CHD6 CSH ADR 33 D H
CHD6 CSH ADR 32 C H
CHD6 CSH WRITE C L

CHD7 C MATCH 0 D L

CHD7 C MATCH 1 D L

CHD7 C MATCH 2 D L

CHD7 C MATCH 3 D L

CHD6 CSH ADR 32 D H
CHD6 CSH ADR 33 D H
CHD6 CSH ADR 27 D HCHD6 CSH ADR 28 D H
CHD6 CSH ADR 29 D H
CHD6 CSH ADR 30 D H
CHD6 CSH WRITE D L

11	10H210	14	CACHE DATA [N+03] A H	DF1
10	10H210	13	CACHE DATA [N+03] B H	DF2
9	10H210	12	CACHE DATA [N+03] C H	DF3
8	10H210	11	CACHE DATA [N+03] D H	DF4

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REVISIONS
CHK CHANGE NO. REV

digital
DATE 06-AUG-84
ENG. C. A. JENS
DATE 06-AUG-84
SHEET 1 OF 1
XTRA: (MCA25, M856-0-9584) 23-JUL-84 14:09
FIRST USED ON OPTION MODEL: MCA25
D-DD-M856-0

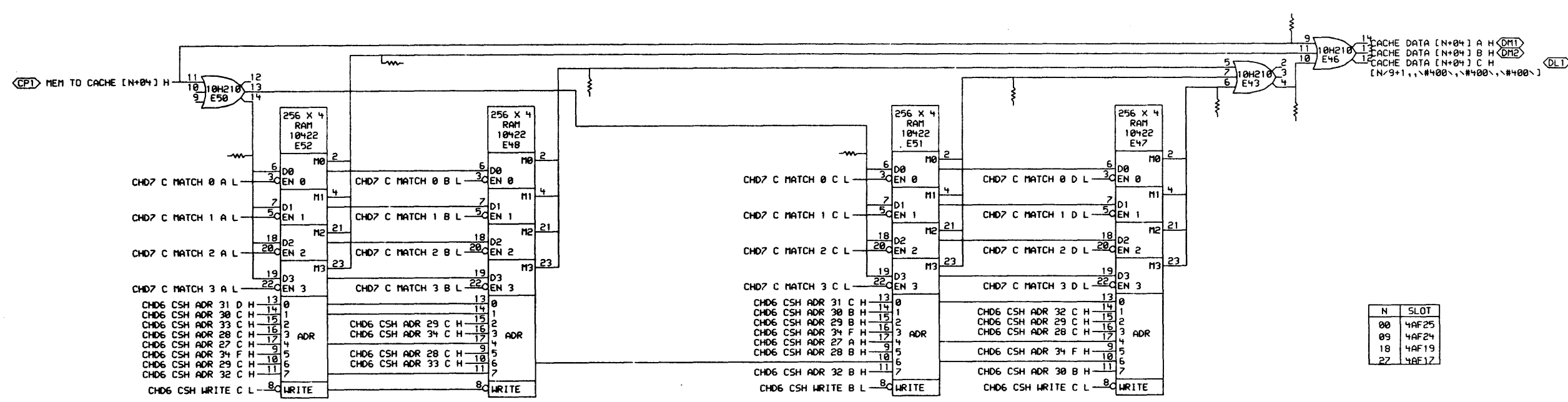
TITLE: CACHE DATA BITS
[N+02], [N+03]
SIZE CODE D CS
NUMBER M856-0-CHD2
REV. A

D

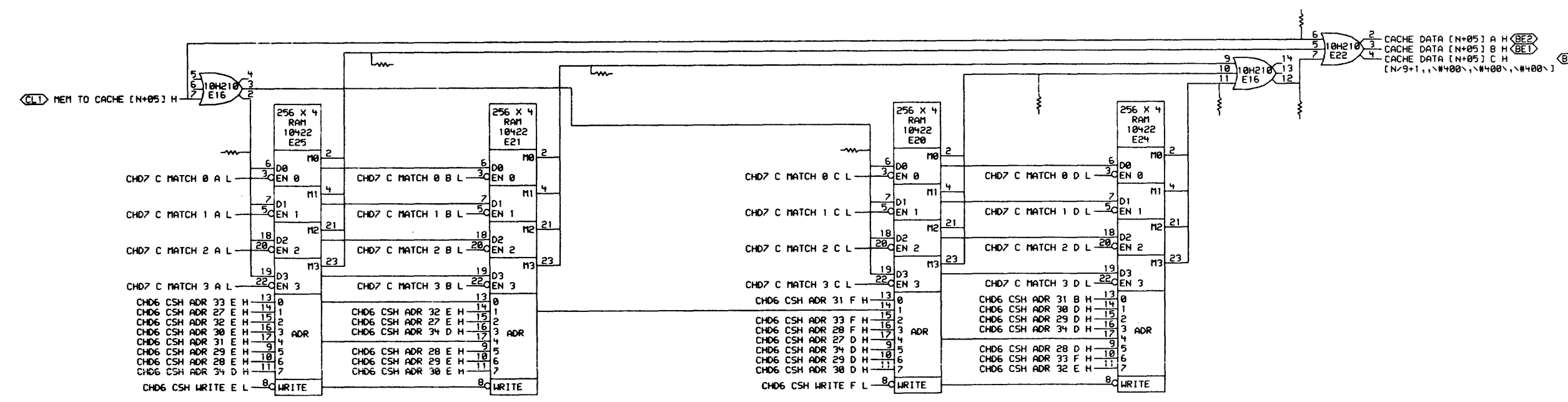
C

B

A



N	SLOT
00	4AF25
09	4AF24
18	4AF19
27	4AF17



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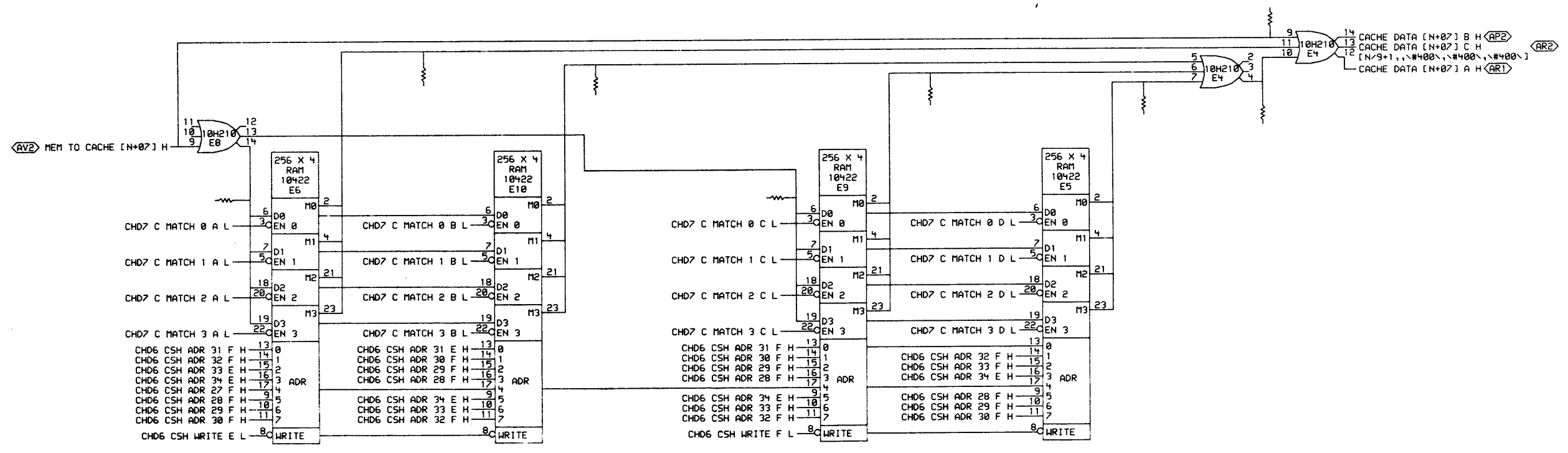
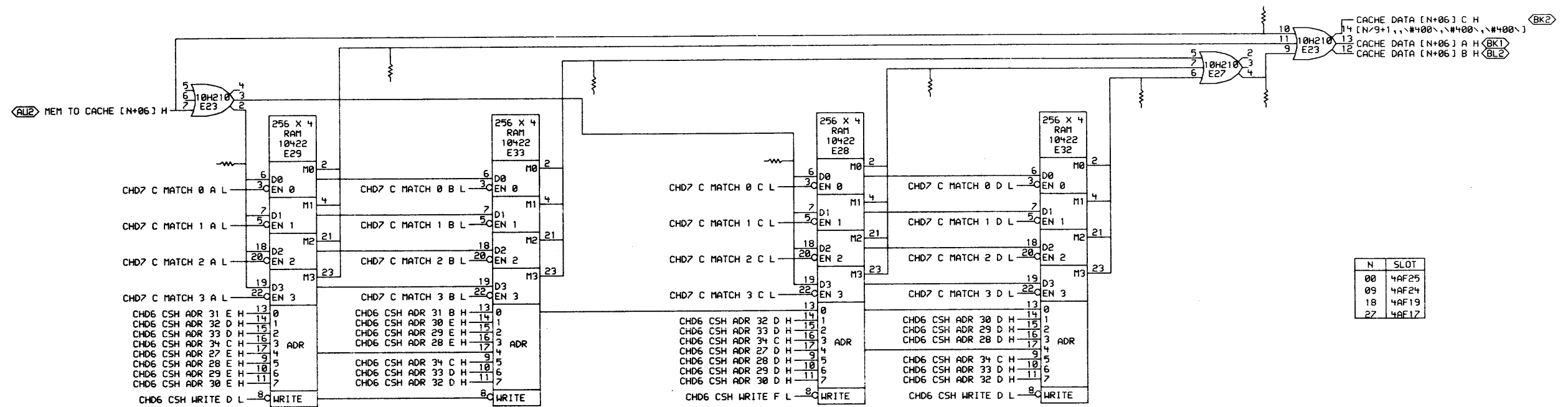
REVISIONS		
CHK	CHANGE NO.	REV

DATE: 06-AUG-84
ENG: C. A. JENS
DATE: 06-AUG-84
SHEET: 1 OF 1
NEXT HIGHER ASSEMBLY: D-DD-M856-0

TITLE: CACHE DATA BITS [N+04], [N+05]
NUMBER: M856-0-CHD3
REV: A

FIRST USED ON OPTION/MODEL: MCA25

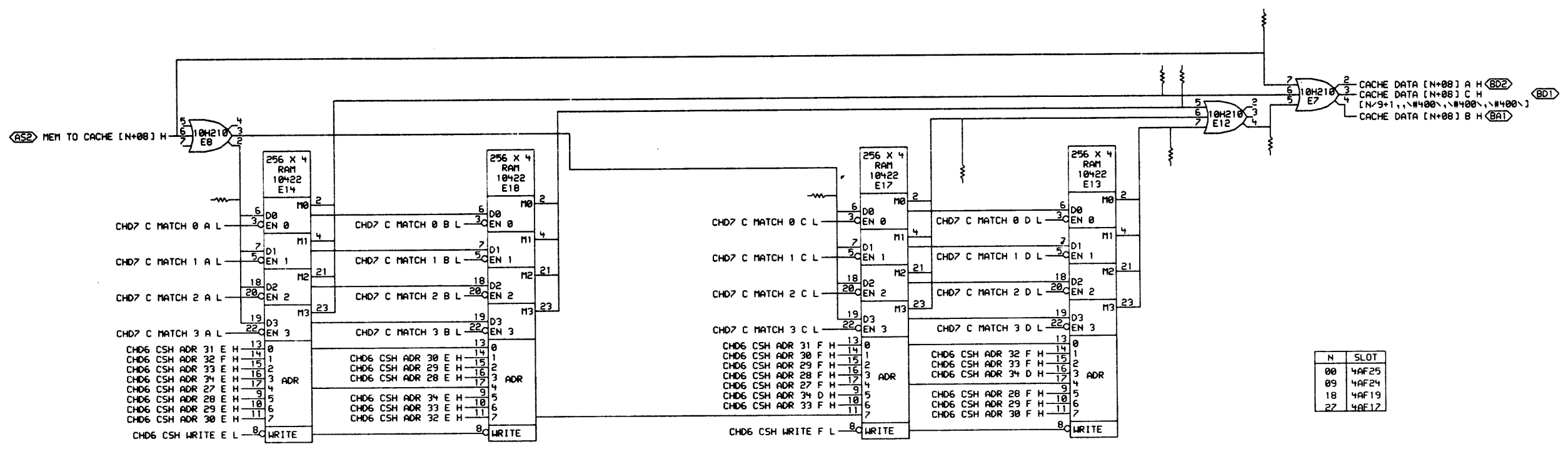
D
C
B
A



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REVISIONS		
CHK	CHANGE NO.	REV

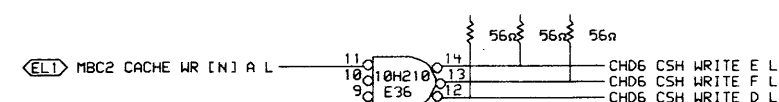
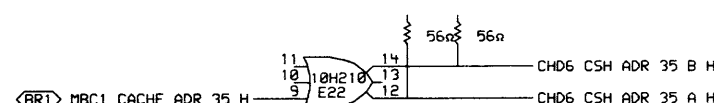
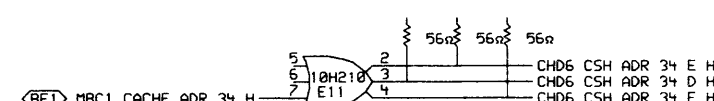
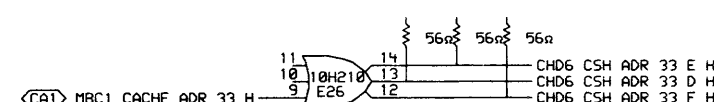
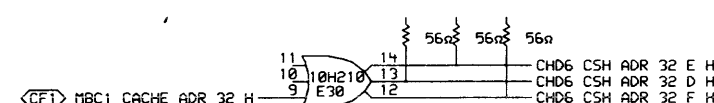
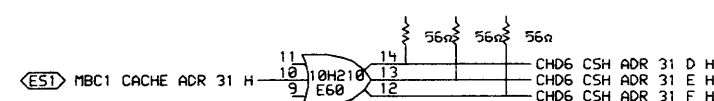
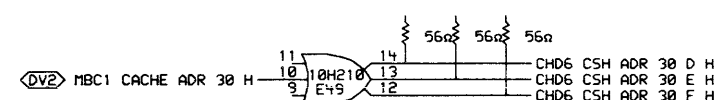
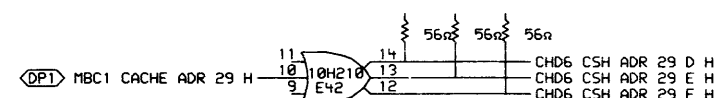
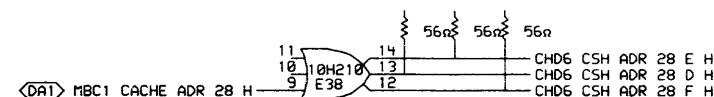
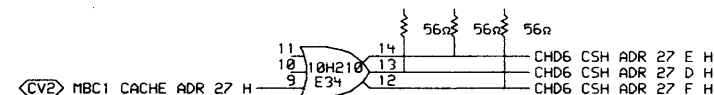
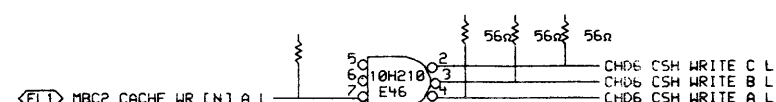
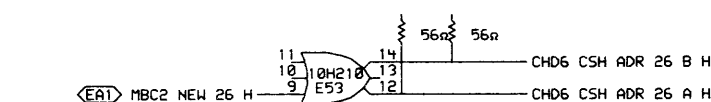
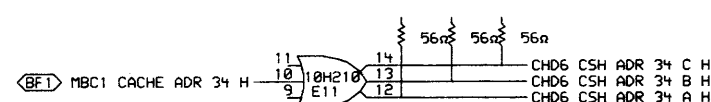
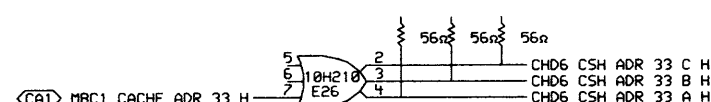
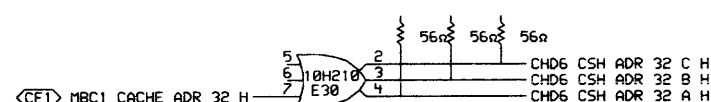
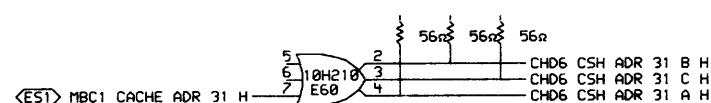
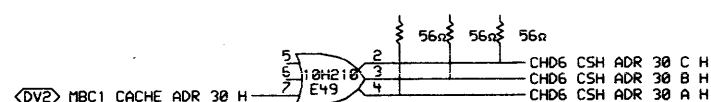
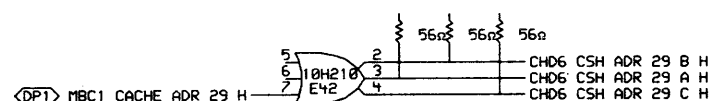
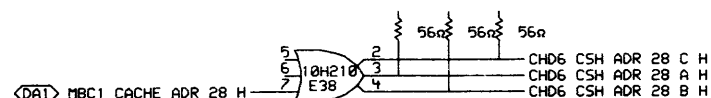
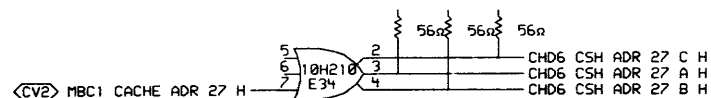
digital	DATE	ENG.	DATE	TITLE: CACHE DATA BITS [N+06], [N+07]
	23-JUL-84	C. A. JENS	06-AUG-84	
D. DELLORCO		DATE	BOARD LOCATION:	SIZE CODE NUMBER REV. D CS M856-0-CHD4 A
FIRST USED ON OPTION/MODEL: MCA25		06-AUG-84	SHEET 1 OF 1	



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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE 06-AUG-84	ENG. C. A. JENS	DATE 06-AUG-84	TITLE: CACHE DATA BIT [N+08]	
	DATE 06-AUG-84	BOARD LOCATION: 1	OF 1		
	NEXT HIGHER ASSEMBLY: D-DD-M856-0				
FIRST USED ON OPTION/MODEL: MCA25		SIZE CODE D CS		NUMBER M856-0-CHD5	REV. A



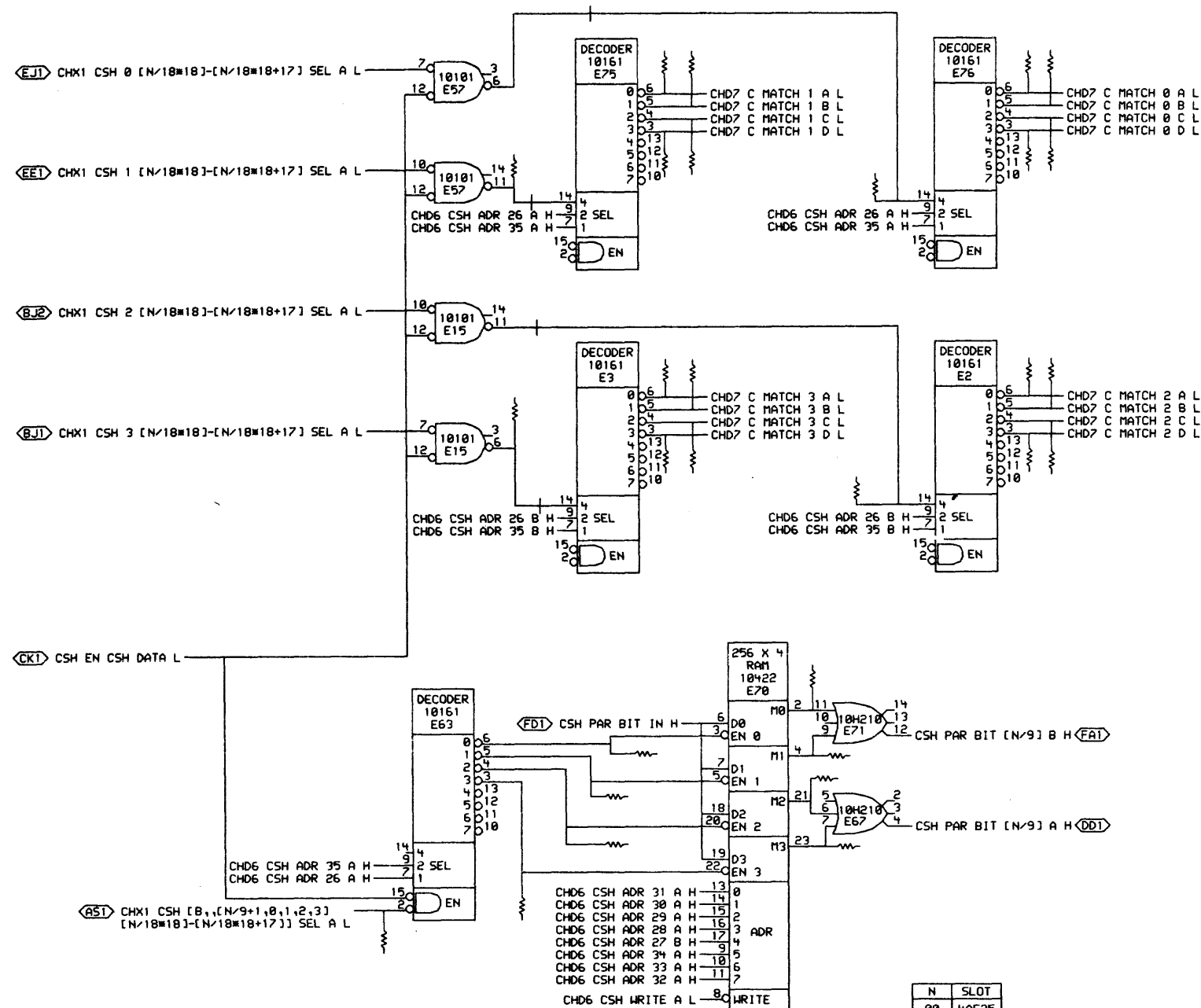
N	SLOT
00	4AF25
09	4AF24
18	4AF19
27	4AF17

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CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE:
	06-AUG-84	C. A. JENS	06-AUG-84	CACHE DATA ADDRESSING LOGIC
CHK'D	DATE	BOARD LOCATION:	SIZE	CODE
D. DELLORCO	06-AUG-84	SHEET 1 OF 1	D	CS
FIRST USED ON OPTION/MODEL: MCA25		NEXT HIGHER ASSEMBLY: D-DD-M856-0		NUMBER
				M856-0-CHD6

REV.
A



(CF2) [B,CHD7 TERM 00 [N] H, [N/27+1,CHD7 TERM 00 [N] H\#400\, MBC2 NEW 26 H]]

(FP2) [B,CHD7 TERM 01 H,[N/9+1,CHD7 TERM 01 00 H\#400\, CHD7 TERM 01 09 H\#400\,CCW BUF ADR 0 H, MBC1 CACHE ADR 28 H]]

(AF1) [B,CHD7 TERM 02 [N] H,[N/9+1, CHD7 TERM 02 00 H\#400\,CHX1 CSH 3 00-17 SEL A L, CHD7 TERM 02 18 H\#400\,MBC1 CACHE ADR 32 H]]

(FP1) [B,CHD7 TERM 03 [N] H,[N/9+1,CSH PAR BIT IN H, CHD7 TERM 03 09 H\#400\, CCW BUF ADR 1 H,MBC1 CACHE ADR 29 H]]

(AF2) [B,CHD7 TERM 04 [N] H,[N/27+1, CHD7 TERM 04 [N] H\#400\,MBC1 CACHE ADR 33 H]]

(FR1) [B,CHD7 TERM 05 [N] H,[N/27+1, CHD7 TERM 05 [N] H\#400\,MBC1 CACHE ADR 30 H]]

(AL1) [B,CHD7 TERM 06 [N] H,[N/9+1, CHD7 TERM 06 00 H\#400\,CHD7 TERM 06 09 H\#400\, PMA 34 H,MBC1 CACHE ADR 34 H]]

(AN1) [B,CHD7 TERM 07 [N] H,[N/27+1, CHD7 TERM 07 [N] H\#400\,MBC1 CACHE ADR 35 H]]

(AM2) [B,CHD7 TERM 08 [N] H,[N/27+1, CHD7 TERM 08 [N] H\#400\, MBC1 CACHE ADR 35 L]]

(AL2) CHD7 TERM 09 [N] H\#400\

(FS1) [B,CHD7 TERM 10 [N] H,[N/27+1, CHD7 TERM 10 [N] H\#400\, MBC1 CACHE ADR 31 H]]

(FS2) [B,CHD7 TERM 11 [N] H,[N/9+1, CHD7 TERM 11 00 H\#400\,CHD7 TERM 11 09 H\#400\, CACHE EXISTS L,CHX1 CSH 0 18-35 SEL A L]]

(FR2) [B,CHD7 TERM 12 [N] H,[N/9+1,PMA5 EBOX PAGED H, CHD7 TERM 12 09 H\#400\,CHX1 CSH 1 18-35 SEL A L, CCW BUF ADR 2 H]]

(AD2) CHD7 TERM 13 [N] H\#400\

(AE2) [B,CHD7 TERM 14 [N] H,[N/27+1, CHD7 TERM 14 [N] H\#400\, CCL CH BUF EN L]]

(AE1) CHD7 TERM 15 [N] H\#400\

(AD1) CHD7 TERM 16 [N] H\#400\

(AP1) [B,CHD7 TERM 17 H,[N/9+1, CSH EN CSH DATA L, CHX1 CSH 2 00-17 SEL A L, PMA 35 H,MBC1 CACHE ADR 27 H]]

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REVISIONS
CHK CHANGE NO. REV

digital
CHK'D
D. DELLORCO

DATE
06-AUG-84

ENG.
C. A. JENS

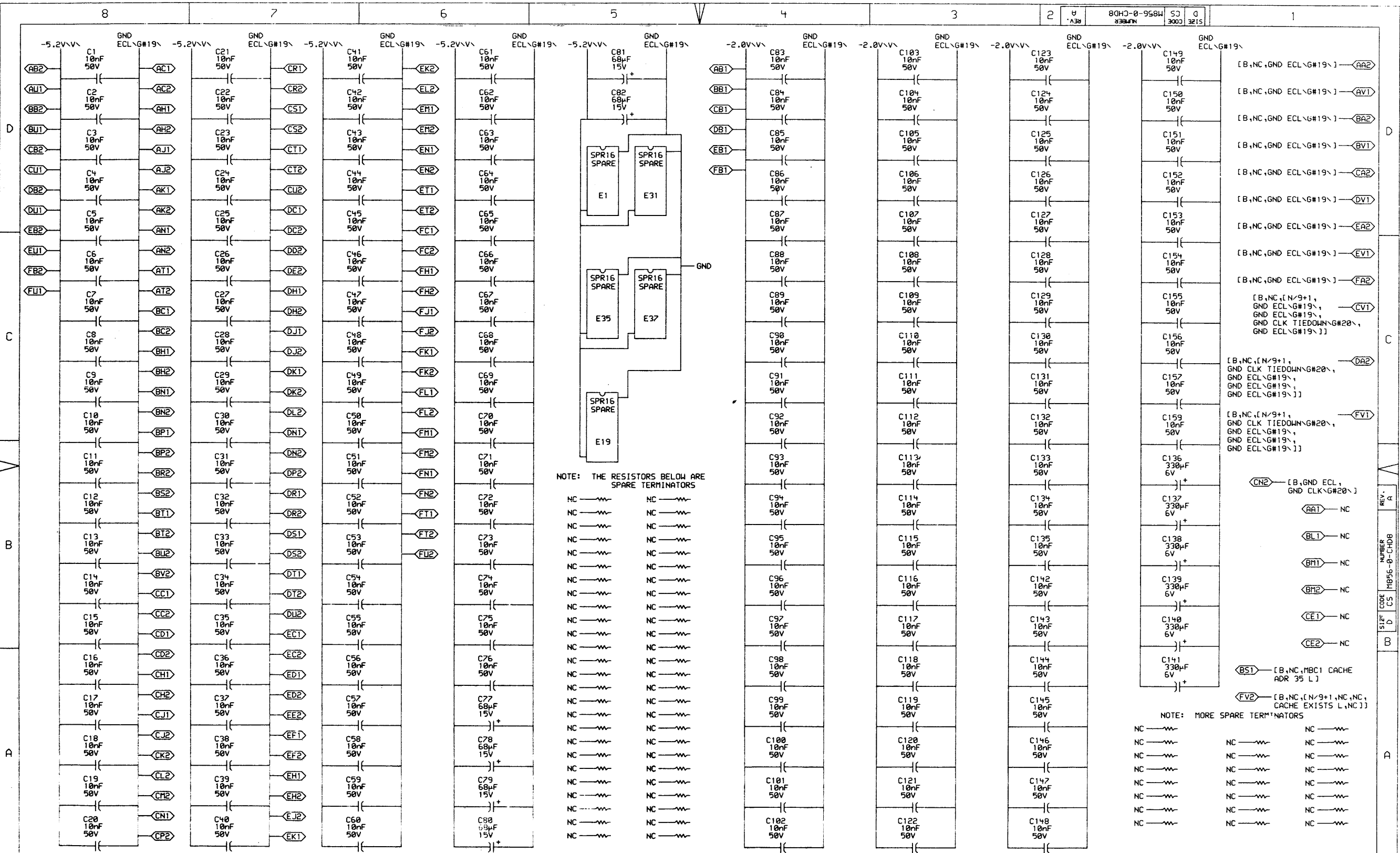
DATE
06-AUG-84

TITLE: CACHE SEL LOGIC TERMINATORS

FIRST USED ON OPTION/MODEL: MCA25

DATE
06-AUG-84

SIZE CODE NUMBER REV.
D CS M856-0-CHD7 A



RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R52(1)	CHD4	B5	68n	%E10(2)
R69(1)	CHD5	C2	68n	%E12(4)
R178(1)	CHD5	C3	68n	%E13(2)
R66(1)	CHD5	C3	68n	%E14(2)
R151(1)	CHD7	C5	68n	%E15(11)
R50(1)	CHD7	C6	68n	%E15(6)
R55(1)	CHD3	B2	68n	%E16(12)
R197(1)	CHD3	B7	68n	%E16(2)
R218(1)	CHD3	B4	68n	%E16(3)
R185(1)	CHD5	C4	68n	%E17(2)
R186(1)	CHD5	C3	68n	%E18(2)
R34(1)	CHD3	B3	68n	%E20(2)
R46(1)	CHD3	B5	68n	%E21(2)
R110(1)	CHD4	D7	68n	%E23(2)
R229(1)	CHD4	D4	68n	%E23(3)
R187(1)	CHD3	B2	68n	%E24(2)
R58(1)	CHD3	B7	68n	%E25(2)
R57(1)	CHD4	D2	68n	%E27(4)
R114(1)	CHD4	D4	68n	%E28(2)
R103(1)	CHD4	D6	68n	%E29(2)
R113(1)	CHD4	D3	68n	%E32(2)
R177(1)	CHD4	D5	68n	%E33(2)
R129(1)	CHD2	B7	68n	%E36(2)
R227(1)	CHD2	B4	68n	%E36(4)
R250(1)	CHD2	B2	68n	%E39(12)
R61(1)	CHD4	B2	68n	%E4(4)
R251(1)	CHD2	B2	68n	%E40(2)
R24(1)	CHD2	B5	68n	%E41(2)
R27(1)	CHD3	D2	68n	%E43(4)
R252(1)	CHD2	B4	68n	%E44(2)
R249(1)	CHD2	B6	68n	%E45(2)
R26(1)	CHD3	D2	68n	%E47(2)
R29(1)	CHD3	D5	68n	%E48(2)
R51(1)	CHD4	B3	68n	%E5(2)
R23(1)	CHD3	D4	68n	%E50(13)
R126(1)	CHD3	D7	68n	%E50(14)
R235(1)	CHD1	B4	68n	%E50(2)
R142(1)	CHD1	B7	68n	%E50(3)
R42(1)	CHD3	D3	68n	%E51(2)
R22(1)	CHD3	D7	68n	%E52(2)

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R82(1)	CHD1	B2	68n	%E54(4)
R14(1)	CHD1	B3	68n	%E55(2)
R2(1)	CHD1	B6	68n	%E56(2)
R242(1)	CHD7	D6	68n	%E57(11)
R173(1)	CHD7	D5	68n	%E57(6)
R44(1)	CHD1	B2	68n	%E58(2)
R120(1)	CHD1	B5	68n	%E59(2)
R182(1)	CHD4	B6	68n	%E6(2)
R190(1)	CHD2	D4	68n	%E61(2)
R188(1)	CHD2	D6	68n	%E62(2)
R20(1)	CHD7	A6	68n	%E63(3)
R101(1)	CHD7	B6	68n	%E63(4)
R18(1)	CHD7	B6	68n	%E63(5)
R102(1)	CHD7	B6	68n	%E63(6)
R99(1)	CHD2	D2	68n	%E64(4)
R15(1)	CHD2	D2	68n	%E65(2)
R189(1)	CHD2	D5	68n	%E66(2)
R236(1)	CHD2	D4	68n	%E67(13)
R150(1)	CHD2	D7	68n	%E67(14)
R245(1)	CHD1	D2	68n	%E68(2)
R12(1)	CHD1	D6	68n	%E69(2)
R7(1)	CHD7	B5	68n	%E70(2)
R192(1)	CHD7	B5	68n	%E70(21)
R11(1)	CHD7	B5	68n	%E70(23)
R10(1)	CHD7	B5	68n	%E70(4)
R8(1)	CHD1	D3	68n	%E72(2)
R247(1)	CHD1	D5	68n	%E73(2)
R248(1)	CHD1	D2	68n	%E74(12)
R174(1)	CHD1	D7	68n	%E74(2)
R239(1)	CHD1	D4	68n	%E74(3)
R157(1)	CHD4	B4	68n	%E8(13)
R210(1)	CHD4	B7	68n	%E8(14)
R143(1)	CHD5	C7	68n	%E8(2)
R217(1)	CHD5	C4	68n	%E8(3)
R181(1)	CHD4	B4	68n	%E9(2)
R240(1)	CHD6	A6	56n	CHD6 CSH ADR 26 A H
R201(1)	CHD6	A6	56n	CHD6 CSH ADR 26 B H
R238(1)	CHD6	D6	56n	CHD6 CSH ADR 27 A H
R31(1)	CHD6	D6	56n	CHD6 CSH ADR 27 B H
R156(1)	CHD6	D6	56n	CHD6 CSH ADR 27 C H

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R221(1)	CHD6	D2	56n	CHD6 CSH ADR 27 D H
R109(1)	CHD6	D2	56n	CHD6 CSH ADR 27 E H
R214(1)	CHD6	D2	56n	CHD6 CSH ADR 27 F H
R30(1)	CHD6	D6	56n	CHD6 CSH ADR 28 A H
R167(1)	CHD6	D6	56n	CHD6 CSH ADR 28 B H
R138(1)	CHD6	D6	56n	CHD6 CSH ADR 28 C H
R233(1)	CHD6	D2	56n	CHD6 CSH ADR 28 D H
R216(1)	CHD6	D2	56n	CHD6 CSH ADR 28 E H
R213(1)	CHD6	D2	56n	CHD6 CSH ADR 28 F H
R152(1)	CHD6	C6	56n	CHD6 CSH ADR 29 A H
R162(1)	CHD6	C6	56n	CHD6 CSH ADR 29 B H
R122(1)	CHD6	C6	56n	CHD6 CSH ADR 29 C H
R73(1)	CHD6	C2	56n	CHD6 CSH ADR 29 D H
R97(1)	CHD6	C2	56n	CHD6 CSH ADR 29 E H
R211(1)	CHD6	C2	56n	CHD6 CSH ADR 29 F H
R172(1)	CHD6	C6	56n	CHD6 CSH ADR 30 A H
R144(1)	CHD6	C6	56n	CHD6 CSH ADR 30 B H
R132(1)	CHD6	C6	56n	CHD6 CSH ADR 30 C H
R74(1)	CHD6	C2	56n	CHD6 CSH ADR 30 D H
R38(1)	CHD6	C2	56n	CHD6 CSH ADR 30 E H
R212(1)	CHD6	C2	56n	CHD6 CSH ADR 30 F H
R224(1)	CHD6	C6	56n	CHD6 CSH ADR 31 A H
R96(1)	CHD6	C6	56n	CHD6 CSH ADR 31 B H
R219(1)	CHD6	C6	56n	CHD6 CSH ADR 31 C H
R226(1)	CHD6	C2	56n	CHD6 CSH ADR 31 D H
R168(1)	CHD6	C2	56n	CHD6 CSH ADR 31 E H
R206(1)	CHD6	C2	56n	CHD6 CSH ADR 31 F H
R204(1)	CHD6	B6	56n	CHD6 CSH ADR 32 A H
R191(1)	CHD6	B6	56n	CHD6 CSH ADR 32 B H
R228(1)	CHD6	B6	56n	CHD6 CSH ADR 32 C H
R232(1)	CHD6	B2	56n	CHD6 CSH ADR 32 D H
R137(1)	CHD6	B2	56n	CHD6 CSH ADR 32 E H
R215(1)	CHD6	B2	56n	CHD6 CSH ADR 32 F H
R203(1)	CHD6	B6	56n	CHD6 CSH ADR 33 A H
R160(1)	CHD6	B6	56n	CHD6 CSH ADR 33 B H
R223(1)	CHD6	B6	56n	CHD6 CSH ADR 33 C H
R48(1)	CHD6	B2	56n	CHD6 CSH ADR 33 D H
R207(1)	CHD6	B2	56n	CHD6 CSH ADR 33 E H
R180(1)	CHD6	B2	56n	CHD6 CSH ADR 33 F H
R6(1)	CHD6	B6	56n	CHD6 CSH ADR 34 A H

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R237(1)	CHD6	B6	56n	CHD6 CSH ADR 34 B H
R171(1)	CHD6	B6	56n	CHD6 CSH ADR 34 C H
R222(1)	CHD6	B2	56n	CHD6 CSH ADR 34 D H
R67(1)	CHD6	B2	56n	CHD6 CSH ADR 34 E H
R118(1)	CHD6	B2	56n	CHD6 CSH ADR 34 F H
R205(1)	CHD6	A2	56n	CHD6 CSH ADR 35 A H
R139(1)	CHD6	A2	56n	CHD6 CSH ADR 35 B H
R25(1)	CHD6	A6	56n	-CHD6 CSH WRITE A H
R32(1)	CHD6	A6	56n	-CHD6 CSH WRITE B H
R121(1)	CHD6	A6	56n	-CHD6 CSH WRITE C H
R194(1)	CHD6	A2	56n	-CHD6 CSH WRITE D H
R200(1)	CHD6	A2	56n	-CHD6 CSH WRITE E H
R53(1)	CHD6	A2	56n	-CHD6 CSH WRITE F H
R125(1)	CHD7	D4	68n	-CHD7 C MATCH 0 A H
R199(1)	CHD7	D4	68n	-CHD7 C MATCH 0 B H
R209(1)	CHD7	D4	68n	-CHD7 C MATCH 0 C H
R145(1)	CHD7	D4	68n	-CHD7 C MATCH 0 D H
R128(1)	CHD7	D5	68n	-CHD7 C MATCH 1 A H
R68(1)	CHD7	D5	68n	-CHD7 C MATCH 1 B H
R253(1)	CHD7	D5	68n	-CHD7 C MATCH 1 C H
R170(1)	CHD7	D5	68n	-CHD7 C MATCH 1 D H
R140(1)	CHD7	C4	68n	-CHD7 C MATCH 2 A H
R146(1)	CHD7	C4	68n	-CHD7 C MATCH 2 B H
R244(1)	CHD7	C4	68n	-CHD7 C MATCH 2 C H
R179(1)	CHD7	C4	68n	-CHD7 C MATCH 2 D H
R134(1)	CHD7	C5	68n	-CHD7 C MATCH 3 A H
R158(1)	CHD7	C5	68n	-CHD7 C MATCH 3 B H
R241(1)	CHD7	C5	68n	-CHD7 C MATCH 3 C H
R243(1)	CHD7	C5	68n	-CHD7 C MATCH 3 D H
R91(1)	CHD7	B1	68n	CHD7 TERM 09 [N] H#400\
R81(1)	CHD7	B1	68n	CHD7 TERM 13 [N] H#400\
R79(1)	CHD7	A1	68n	CHD7 TERM 15 [N] H#400\
R75(1)	CHD7	A1	68n	CHD7 TERM 16 [N] H#400\
R19(1)	CHD7	A6	68n	-CHX1 CSH [B, [N+9+1,0,1,2,3] [N+18+18]-[N+18+18+17]] SEL A H
R220(1)	CHD6	A7	68n	-MBC2 CACHE WR [N] A H
R246(1)	CHD1	D2	68n	MEM TO CACHE [N+00] H
R193(1)	CHD1	B2	68n	MEM TO CACHE [N+01] H
R100(1)	CHD2	D2	68n	MEM TO CACHE [N+02] H
R230(1)	CHD2	B2	68n	MEM TO CACHE [N+03] H
R234(1)	CHD3	D2	68n	MEM TO CACHE [N+04] H

NOTE:

1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES OUTPUT OF DIP LOC AND
() INDICATES PIN NUMBER

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REVISIONS
CHK CHANGE NO. REV

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D. DELLORCO

DATE
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ENG.
C. A. JENS
DATE
06-AUG-84
SHEET
1 OF 2

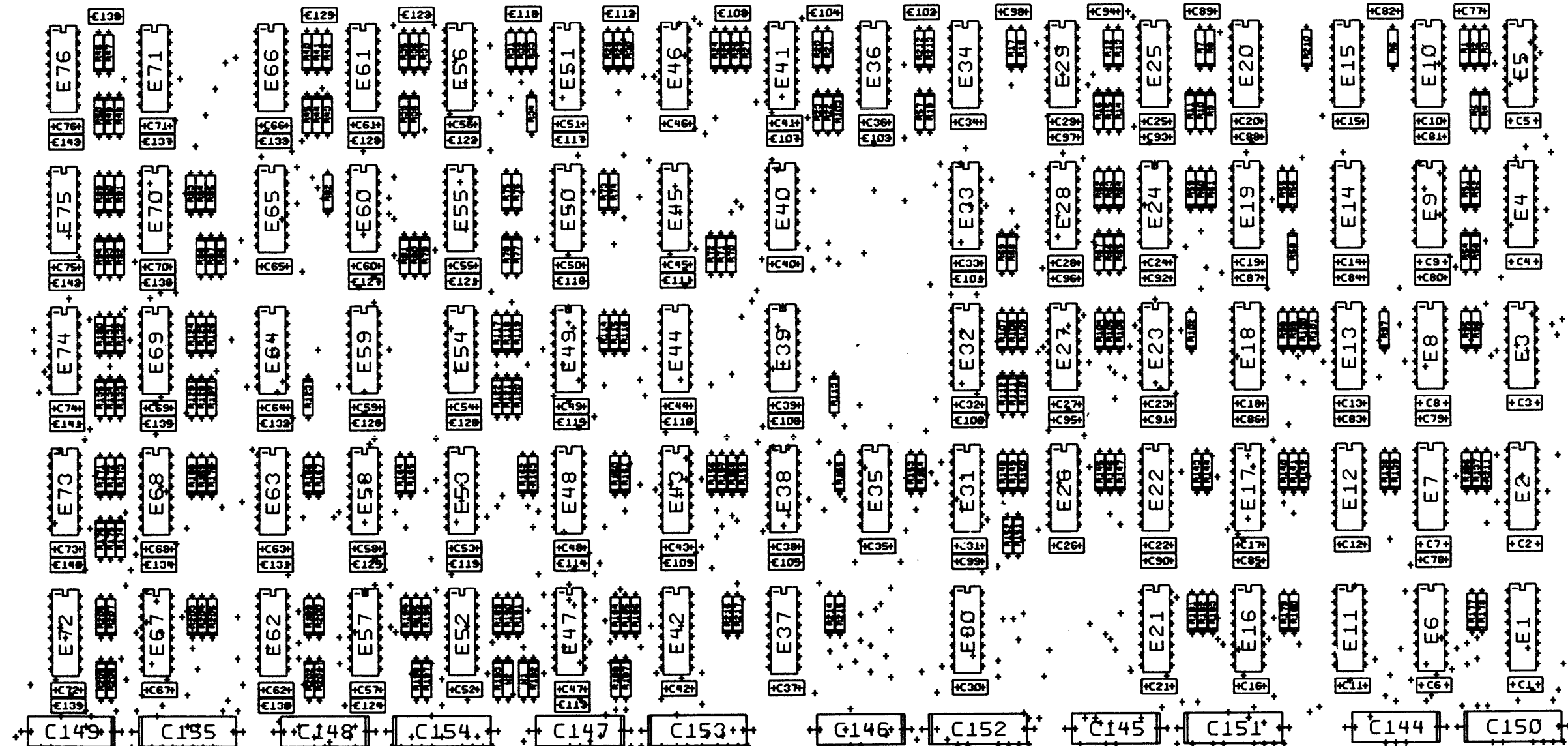
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TERMINATORS

SIZE	CODE	NUMBER	REV.
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<table><tr><td>RESISTOR LOC(PIN)</td><td>SHOWN ON DRW#</td><td>VALUE REF</td><td>TERMINATES SIGNAL</td><td>RESISTOR LOC(PIN)</td><td>SHOWN ON DRW#</td><td>VALUE REF</td><td>TERMINATES SIGNAL</td><td>RESISTOR LOC(PIN)</td><td>SHOWN ON DRW#</td><td>VALUE REF</td><td>TERMINATES SIGNAL</td></tr><tr><td>R64(1)</td><td>CHD3</td><td>B2</td><td>68Ω</td><td>MEM TO CACHE [N+05] H</td><td>R111(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R94(1)</td><td>CHD7</td><td>D1</td><td>68Ω [B,CHD7 TERM 01 H, [N/9+1,CHD7 TERM 01 00 H\#400\, CHD7 TERM 01 09 H\#400\,CCW BUF ADR 0 H, MBC1 CACHE ADR 28 H]]</td></tr><tr><td>R56(1)</td><td>CHD4</td><td>D2</td><td>68Ω</td><td>MEM TO CACHE [N+06] H</td><td>R112(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R86(1)</td><td>CHD7</td><td>D1</td><td>68Ω [B,CHD7 TERM 02 [N] H, [N/9+1, CHD7 TERM 02 00 H\#400\,CHX1 CSH 3 00-17 SEL A L, CHD7 TERM 02 18 H\#400\,MBC1 CACHE ADR 32 H]]</td></tr><tr><td>R72(1)</td><td>CHD4</td><td>B2</td><td>68Ω</td><td>MEM TO CACHE [N+07] H</td><td>R115(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R90(1)</td><td>CHD7</td><td>D1</td><td>68Ω [B,CHD7 TERM 03 [N] H, [N/9+1,CSH PAR BIT IN H, CHD7 TERM 03 09 H\#400\, CCW BUF ADR 1 H,MBC1 CACHE ADR 29 H]]</td></tr><tr><td>R71(1)</td><td>CHD5</td><td>C2</td><td>68Ω</td><td>MEM TO CACHE [N+08] H</td><td>R116(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R87(1)</td><td>CHD7</td><td>C1</td><td>68Ω [B,CHD7 TERM 04 [N] H, [N/27+1, CHD7 TERM 04 [N] H\#400\,MBC1 CACHE ADR 33 H]]</td></tr><tr><td>R1(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R117(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R92(1)</td><td>CHD7</td><td>C1</td><td>68Ω [B,CHD7 TERM 05 [N] H, [N/27+1, CHD7 TERM 05 [N] H\#400\,MBC1 CACHE ADR 30 H]]</td></tr><tr><td>R3(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R119(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R93(1)</td><td>CHD7</td><td>C1</td><td>68Ω [B,CHD7 TERM 06 [N] H, [N/9+1, CHD7 TERM 06 00 H\#400\,CHD7 TERM 06 09 H\#400\, PMA 34 H,MBC1 CACHE ADR 34 H]]</td></tr><tr><td>R4(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R123(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R45(1)</td><td>CHD7</td><td>C1</td><td>68Ω [B,CHD7 TERM 07 [N] H, [N/27+1, CHD7 TERM 07 [N] H\#400\,MBC1 CACHE ADR 35 H]]</td></tr><tr><td>R5(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R124(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R89(1)</td><td>CHD7</td><td>C1</td><td>68Ω [B,CHD7 TERM 08 [N] H, [N/27+1, CHD7 TERM 08 [N] H\#400\, MBC1 CACHE ADR 35 L]]</td></tr><tr><td>R9(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R127(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R85(1)</td><td>CHD7</td><td>B1</td><td>68Ω [B,CHD7 TERM 10 [N] H, [N/27+1, CHD7 TERM 10 [N] H\#400\, MBC1 CACHE ADR 31 H]]</td></tr><tr><td>R13(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R130(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R84(1)</td><td>CHD7</td><td>B1</td><td>68Ω [B,CHD7 TERM 11 [N] H, [N/9+1, CHD7 TERM 11 00 H\#400\,CHD7 TERM 11 09 H\#400\, CACHE EXISTS L,CHX1 CSH 0 18-35 SEL A L]]</td></tr><tr><td>R16(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R131(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R83(1)</td><td>CHD7</td><td>B1</td><td>68Ω [B,CHD7 TERM 12 [N] H, [N/9+1,PMA5 EBOX PAGED H, CHD7 TERM 12 09 H\#400\,CHX1 CSH 1 18-35 SEL A L, CCW BUF ADR 2 H]]</td></tr><tr><td>R17(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R133(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R77(1)</td><td>CHD7</td><td>A1</td><td>68Ω [B,CHD7 TERM 14 [N] H, [N/27+1, CHD7 TERM 14 [N] H\#400\, CCL CH BUF EN L]]</td></tr><tr><td>R21(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R135(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R39(1)</td><td>CHD7</td><td>A1</td><td>68Ω [B,CHD7 TERM 17 H, [N/9+1, CSH EN CSH DATA L, CHX1 CSH 2 00-17 SEL A L, PMA 35 H,MBC1 CACHE ADR 27 H]]</td></tr><tr><td>R28(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R136(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R33(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R141(1)</td><td>CHD8</td><td>A1</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R35(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R147(1)</td><td>CHD8</td><td>A1</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R36(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R148(1)</td><td>CHD8</td><td>A1</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R37(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R149(1)</td><td>CHD8</td><td>A1</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R40(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R153(1)</td><td>CHD8</td><td>A1</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R41(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R154(1)</td><td>CHD8</td><td>A1</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R43(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R155(1)</td><td>CHD8</td><td>A1</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R47(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R159(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R49(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R161(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R54(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R163(1)</td><td>CHD8</td><td>A1</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R59(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R164(1)</td><td>CHD8</td><td>A1</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R60(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R165(1)</td><td>CHD8</td><td>A1</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R62(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R166(1)</td><td>CHD8</td><td>A1</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R63(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R169(1)</td><td>CHD8</td><td>A1</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R65(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R175(1)</td><td>CHD8</td><td>A1</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R70(1)</td><td>CHD8</td><td>B5</td><td>68Ω</td><td>NC</td><td>R176(1)</td><td>CHD8</td><td>A1</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R76(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R183(1)</td><td>CHD8</td><td>A1</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R78(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R184(1)</td><td>CHD8</td><td>A2</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R80(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R195(1)</td><td>CHD8</td><td>A2</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R88(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R196(1)</td><td>CHD8</td><td>A2</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R98(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R198(1)</td><td>CHD8</td><td>A2</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R104(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R200(1)</td><td>CHD8</td><td>A2</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R105(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R202(1)</td><td>CHD8</td><td>A2</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R106(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R225(1)</td><td>CHD8</td><td>A2</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R107(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R231(1)</td><td>CHD8</td><td>A2</td><td>68Ω</td><td>NC</td><td></td><td></td><td></td><td></td></tr><tr><td>R108(1)</td><td>CHD8</td><td>A5</td><td>68Ω</td><td>NC</td><td>R95(1)</td><td>CHD7</td><td>D1</td><td>68Ω</td><td>[B,CHD7 TERM 00 [N] H, [N/27+1,CHD7 TERM 00 [N] H\#400\, MBC2 NEW 26 H]]</td><td></td><td></td><td></td><td></td></tr></table>								RESISTOR LOC(PIN)	SHOWN ON DRW#	VALUE REF	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	VALUE REF	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	VALUE REF	TERMINATES SIGNAL	R64(1)	CHD3	B2	68Ω	MEM TO CACHE [N+05] H	R111(1)	CHD8	A5	68Ω	NC	R94(1)	CHD7	D1	68Ω [B,CHD7 TERM 01 H, [N/9+1,CHD7 TERM 01 00 H\#400\, CHD7 TERM 01 09 H\#400\,CCW BUF ADR 0 H, MBC1 CACHE ADR 28 H]]	R56(1)	CHD4	D2	68Ω	MEM TO CACHE [N+06] H	R112(1)	CHD8	A5	68Ω	NC	R86(1)	CHD7	D1	68Ω [B,CHD7 TERM 02 [N] H, [N/9+1, CHD7 TERM 02 00 H\#400\,CHX1 CSH 3 00-17 SEL A L, CHD7 TERM 02 18 H\#400\,MBC1 CACHE ADR 32 H]]	R72(1)	CHD4	B2	68Ω	MEM TO CACHE [N+07] H	R115(1)	CHD8	A5	68Ω	NC	R90(1)	CHD7	D1	68Ω [B,CHD7 TERM 03 [N] H, [N/9+1,CSH PAR BIT IN H, CHD7 TERM 03 09 H\#400\, CCW BUF ADR 1 H,MBC1 CACHE ADR 29 H]]	R71(1)	CHD5	C2	68Ω	MEM TO CACHE [N+08] H	R116(1)	CHD8	A5	68Ω	NC	R87(1)	CHD7	C1	68Ω [B,CHD7 TERM 04 [N] H, [N/27+1, CHD7 TERM 04 [N] H\#400\,MBC1 CACHE ADR 33 H]]	R1(1)	CHD8	B5	68Ω	NC	R117(1)	CHD8	A5	68Ω	NC	R92(1)	CHD7	C1	68Ω [B,CHD7 TERM 05 [N] H, [N/27+1, CHD7 TERM 05 [N] H\#400\,MBC1 CACHE ADR 30 H]]	R3(1)	CHD8	B5	68Ω	NC	R119(1)	CHD8	A5	68Ω	NC	R93(1)	CHD7	C1	68Ω [B,CHD7 TERM 06 [N] H, [N/9+1, CHD7 TERM 06 00 H\#400\,CHD7 TERM 06 09 H\#400\, PMA 34 H,MBC1 CACHE ADR 34 H]]	R4(1)	CHD8	B5	68Ω	NC	R123(1)	CHD8	A5	68Ω	NC	R45(1)	CHD7	C1	68Ω [B,CHD7 TERM 07 [N] H, [N/27+1, CHD7 TERM 07 [N] H\#400\,MBC1 CACHE ADR 35 H]]	R5(1)	CHD8	B5	68Ω	NC	R124(1)	CHD8	A5	68Ω	NC	R89(1)	CHD7	C1	68Ω [B,CHD7 TERM 08 [N] H, [N/27+1, CHD7 TERM 08 [N] H\#400\, MBC1 CACHE ADR 35 L]]	R9(1)	CHD8	B5	68Ω	NC	R127(1)	CHD8	A5	68Ω	NC	R85(1)	CHD7	B1	68Ω [B,CHD7 TERM 10 [N] H, [N/27+1, CHD7 TERM 10 [N] H\#400\, MBC1 CACHE ADR 31 H]]	R13(1)	CHD8	B5	68Ω	NC	R130(1)	CHD8	A5	68Ω	NC	R84(1)	CHD7	B1	68Ω [B,CHD7 TERM 11 [N] H, [N/9+1, CHD7 TERM 11 00 H\#400\,CHD7 TERM 11 09 H\#400\, CACHE EXISTS L,CHX1 CSH 0 18-35 SEL A L]]	R16(1)	CHD8	B5	68Ω	NC	R131(1)	CHD8	A5	68Ω	NC	R83(1)	CHD7	B1	68Ω [B,CHD7 TERM 12 [N] H, [N/9+1,PMA5 EBOX PAGED H, CHD7 TERM 12 09 H\#400\,CHX1 CSH 1 18-35 SEL A L, CCW BUF ADR 2 H]]	R17(1)	CHD8	B5	68Ω	NC	R133(1)	CHD8	A5	68Ω	NC	R77(1)	CHD7	A1	68Ω [B,CHD7 TERM 14 [N] H, [N/27+1, CHD7 TERM 14 [N] H\#400\, CCL CH BUF EN L]]	R21(1)	CHD8	B5	68Ω	NC	R135(1)	CHD8	A5	68Ω	NC	R39(1)	CHD7	A1	68Ω [B,CHD7 TERM 17 H, [N/9+1, CSH EN CSH DATA L, CHX1 CSH 2 00-17 SEL A L, PMA 35 H,MBC1 CACHE ADR 27 H]]	R28(1)	CHD8	B5	68Ω	NC	R136(1)	CHD8	A5	68Ω	NC					R33(1)	CHD8	A5	68Ω	NC	R141(1)	CHD8	A1	68Ω	NC					R35(1)	CHD8	B5	68Ω	NC	R147(1)	CHD8	A1	68Ω	NC					R36(1)	CHD8	B5	68Ω	NC	R148(1)	CHD8	A1	68Ω	NC					R37(1)	CHD8	B5	68Ω	NC	R149(1)	CHD8	A1	68Ω	NC					R40(1)	CHD8	B5	68Ω	NC	R153(1)	CHD8	A1	68Ω	NC					R41(1)	CHD8	B5	68Ω	NC	R154(1)	CHD8	A1	68Ω	NC					R43(1)	CHD8	B5	68Ω	NC	R155(1)	CHD8	A1	68Ω	NC					R47(1)	CHD8	B5	68Ω	NC	R159(1)	CHD8	A5	68Ω	NC					R49(1)	CHD8	B5	68Ω	NC	R161(1)	CHD8	A5	68Ω	NC					R54(1)	CHD8	B5	68Ω	NC	R163(1)	CHD8	A1	68Ω	NC					R59(1)	CHD8	B5	68Ω	NC	R164(1)	CHD8	A1	68Ω	NC					R60(1)	CHD8	A5	68Ω	NC	R165(1)	CHD8	A1	68Ω	NC					R62(1)	CHD8	B5	68Ω	NC	R166(1)	CHD8	A1	68Ω	NC					R63(1)	CHD8	B5	68Ω	NC	R169(1)	CHD8	A1	68Ω	NC					R65(1)	CHD8	B5	68Ω	NC	R175(1)	CHD8	A1	68Ω	NC					R70(1)	CHD8	B5	68Ω	NC	R176(1)	CHD8	A1	68Ω	NC					R76(1)	CHD8	A5	68Ω	NC	R183(1)	CHD8	A1	68Ω	NC					R78(1)	CHD8	A5	68Ω	NC	R184(1)	CHD8	A2	68Ω	NC					R80(1)	CHD8	A5	68Ω	NC	R195(1)	CHD8	A2	68Ω	NC					R88(1)	CHD8	A5	68Ω	NC	R196(1)	CHD8	A2	68Ω	NC					R98(1)	CHD8	A5	68Ω	NC	R198(1)	CHD8	A2	68Ω	NC					R104(1)	CHD8	A5	68Ω	NC	R200(1)	CHD8	A2	68Ω	NC					R105(1)	CHD8	A5	68Ω	NC	R202(1)	CHD8	A2	68Ω	NC					R106(1)	CHD8	A5	68Ω	NC	R225(1)	CHD8	A2	68Ω	NC					R107(1)	CHD8	A5	68Ω	NC	R231(1)	CHD8	A2	68Ω	NC					R108(1)	CHD8	A5	68Ω	NC	R95(1)	CHD7	D1	68Ω	[B,CHD7 TERM 00 [N] H, [N/27+1,CHD7 TERM 00 [N] H\#400\, MBC2 NEW 26 H]]				
RESISTOR LOC(PIN)	SHOWN ON DRW#	VALUE REF	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	VALUE REF	TERMINATES SIGNAL	RESISTOR LOC(PIN)	SHOWN ON DRW#	VALUE REF	TERMINATES SIGNAL																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																								
R64(1)	CHD3	B2	68Ω	MEM TO CACHE [N+05] H	R111(1)	CHD8	A5	68Ω	NC	R94(1)	CHD7	D1	68Ω [B,CHD7 TERM 01 H, [N/9+1,CHD7 TERM 01 00 H\#400\, CHD7 TERM 01 09 H\#400\,CCW BUF ADR 0 H, MBC1 CACHE ADR 28 H]]																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																						
R56(1)	CHD4	D2	68Ω	MEM TO CACHE [N+06] H	R112(1)	CHD8	A5	68Ω	NC	R86(1)	CHD7	D1	68Ω [B,CHD7 TERM 02 [N] H, [N/9+1, CHD7 TERM 02 00 H\#400\,CHX1 CSH 3 00-17 SEL A L, CHD7 TERM 02 18 H\#400\,MBC1 CACHE ADR 32 H]]																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																						
R72(1)	CHD4	B2	68Ω	MEM TO CACHE [N+07] H	R115(1)	CHD8	A5	68Ω	NC	R90(1)	CHD7	D1	68Ω [B,CHD7 TERM 03 [N] H, [N/9+1,CSH PAR BIT IN H, CHD7 TERM 03 09 H\#400\, CCW BUF ADR 1 H,MBC1 CACHE ADR 29 H]]																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																						
R71(1)	CHD5	C2	68Ω	MEM TO CACHE [N+08] H	R116(1)	CHD8	A5	68Ω	NC	R87(1)	CHD7	C1	68Ω [B,CHD7 TERM 04 [N] H, [N/27+1, CHD7 TERM 04 [N] H\#400\,MBC1 CACHE ADR 33 H]]																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																						
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NOTE: 1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED 2. ENTRIES ARE SORTED BY SIGNAL NAME 3. % INDICATES OUTPUT OF DIP LOC AND (<) INDICATES PIN NUMBER																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																			
THIS DRAWING AND SPECIFICATIONS ARE THE PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION. COPYRIGHT © 1984, DIGITAL EQUIPMENT CORPORATION REVISIONS CHK CHANGE NO. REV digital DATE 06-AUG-84 D. DELLORCO ENG. C. A. JENS DATE 06-AUG-84 SHEET 2 OF 2 DATE 06-AUG-84 12:49 BOARD LOCATION: NEXT HIGHER ASSEMBLY: D-DD-M856-0 MODEL: MCA25 TITLE: CACHE DATA TERMINATORS SIZE CODE NUMBER REV. D CS M856-0-RES A																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																			
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22 (OTY 12) COMPONENT SIDE VIEW



NOTES:

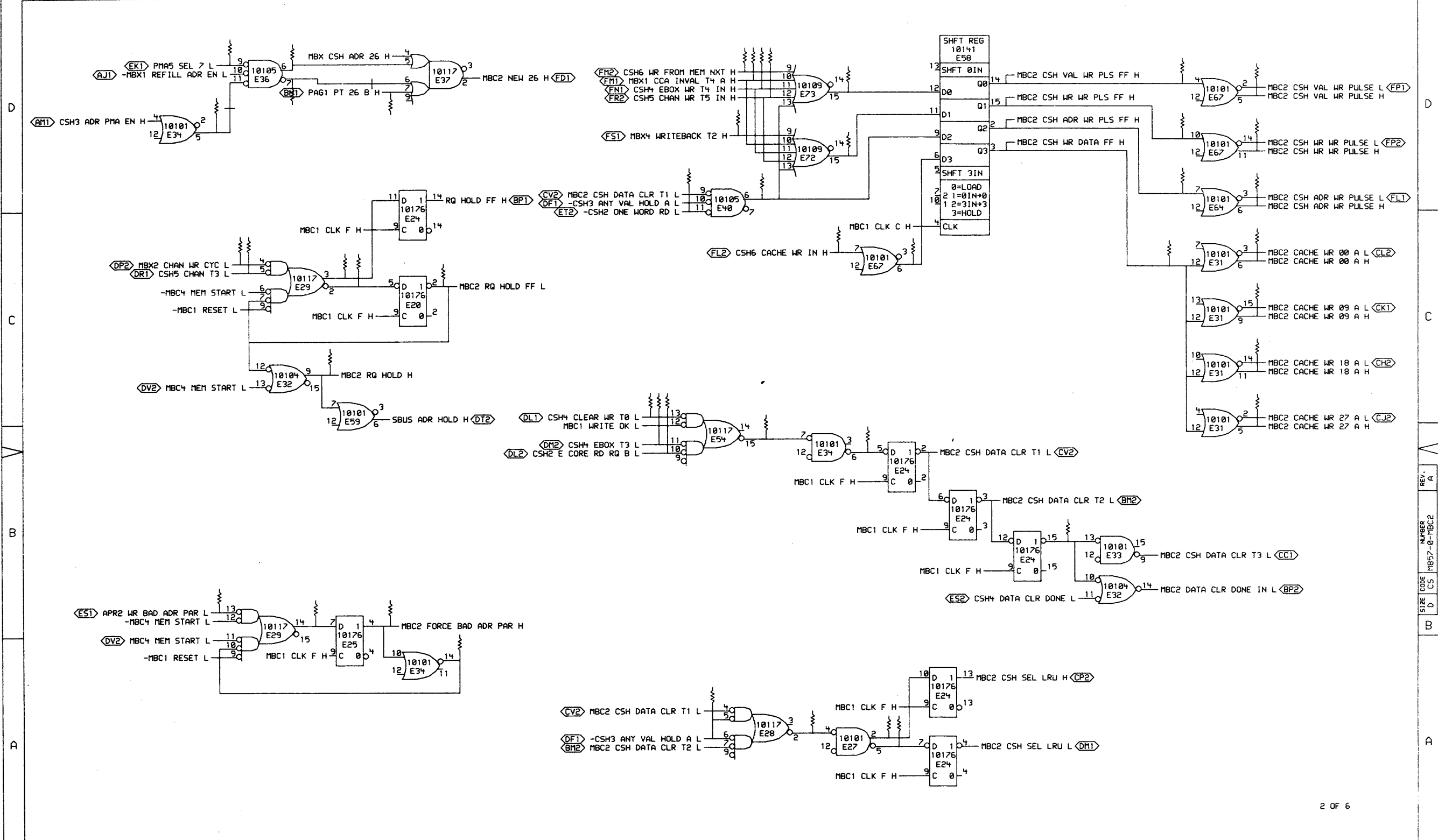
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REPEAT → X AXIS 0 STEP 0 TIMES

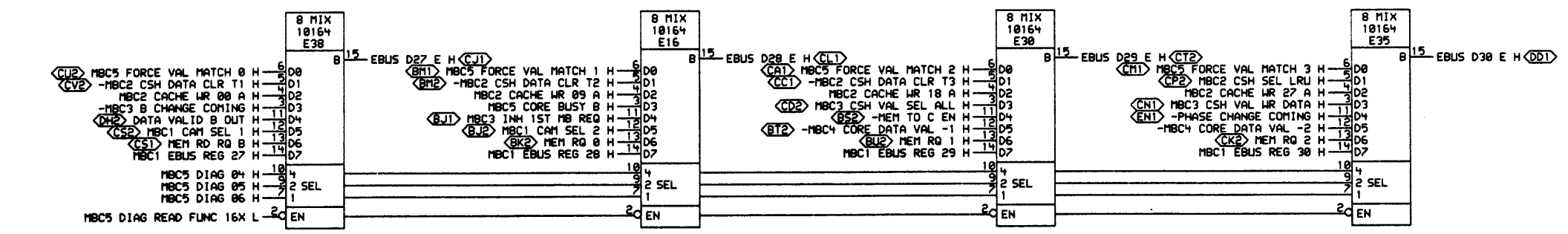
CHK CHANGE NO REV

ETCH REV: AI

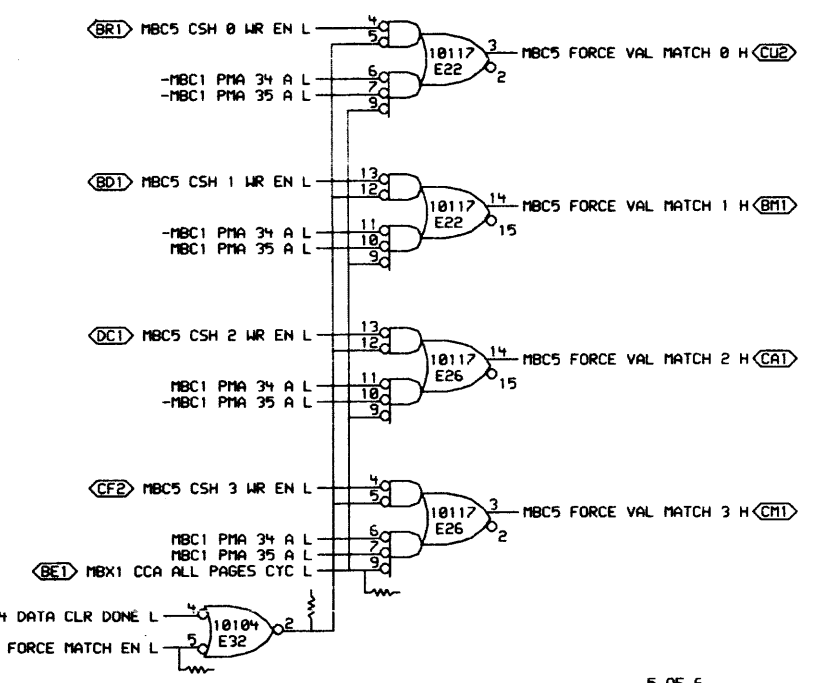
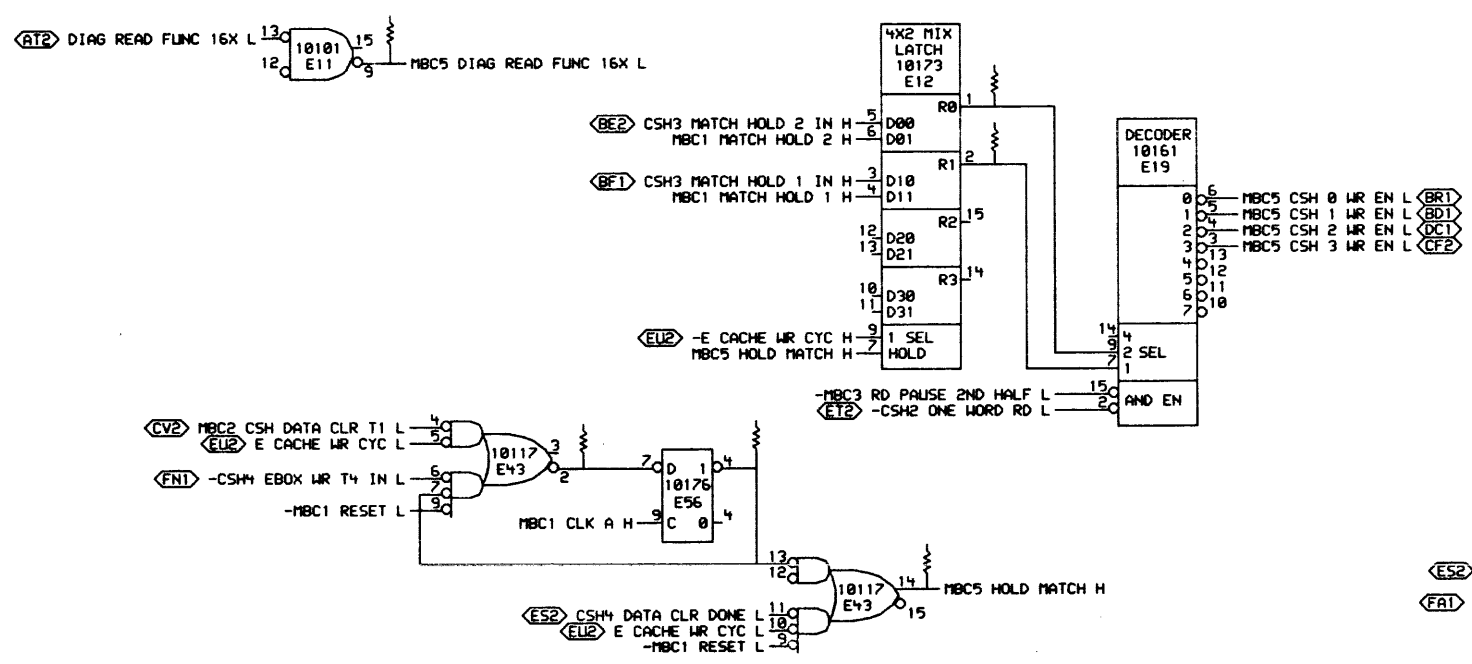
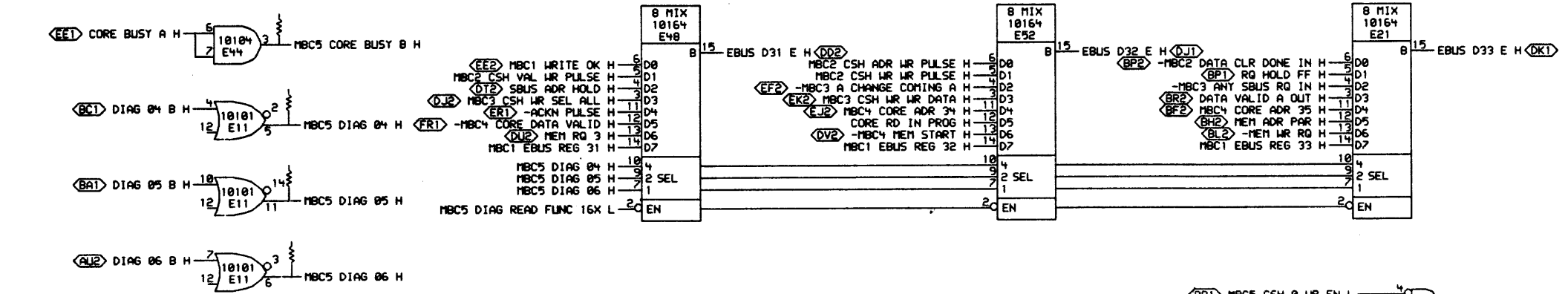
SIGNATURES		DATE	digital	TITLE	MBOX CONTROL 3	REV
DRN.		4/19/84				
CHK'D BY		20/8/84				
MECH. ENG.		7-Jun-84				
PROJ. ENG.		7-Jun-84				
PROD.		27-Aug-84	SIZE CODE	NUMBER		
SCALE 2/1			0	UA M857-0-0	A	
SHT. 1 OF 1			TOP DOC. NO: D-DD-M857-0			

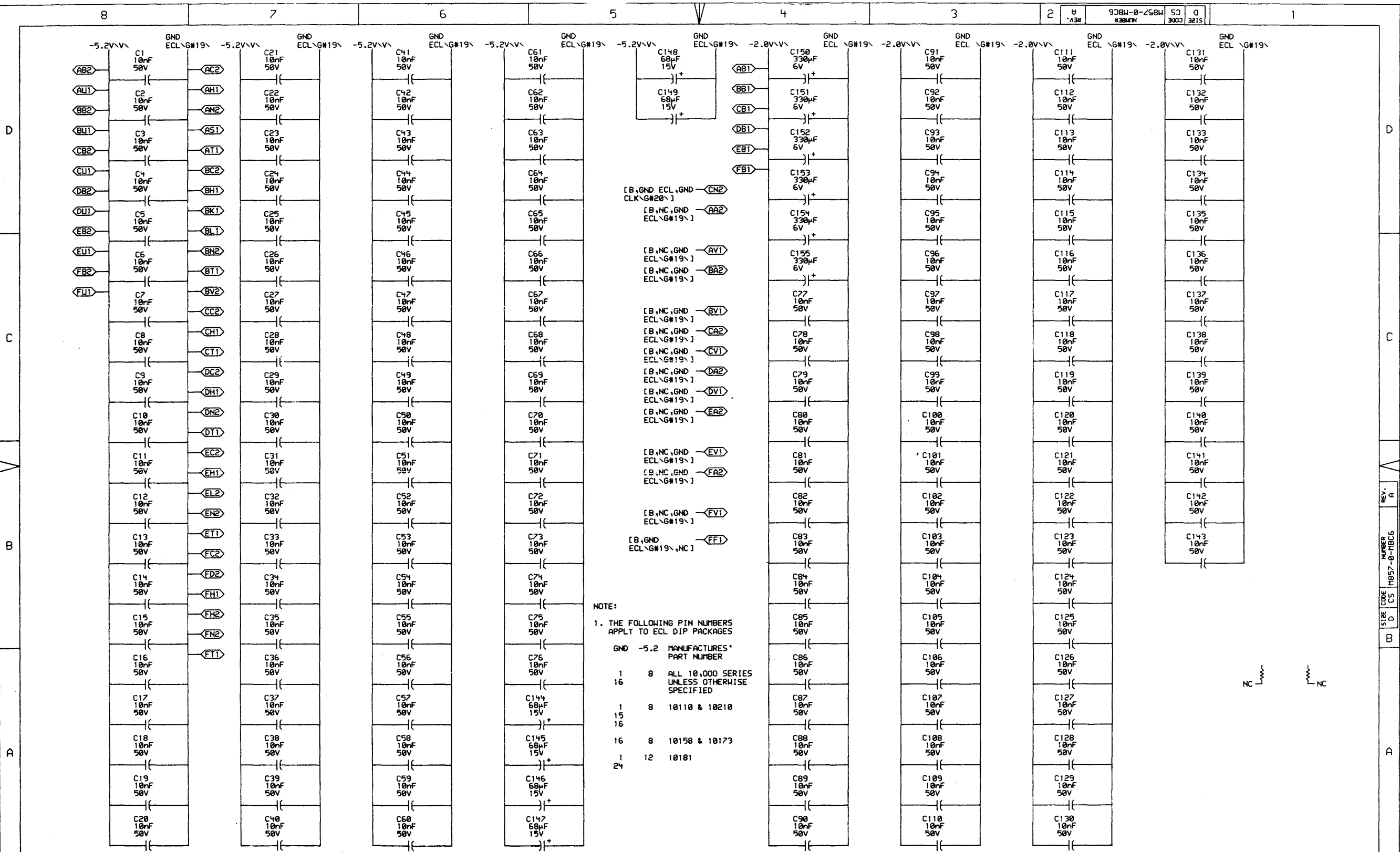
MRO 1 WO#410668 A





NOTE: DIAGNOSTIC MIXERS USE FUNCTIONS 160-167





RESISTOR LOC(PIN)	SHOWN ON DRAW#	ON REF	VALUE	TERMINATES SIGNAL
R56(1)	MBC5	B5	68n	%E12(1)
R60(1)	MBC5	B5	68n	%E12(2)
R21(1)	MBC1	B2	68n	%E17(15)
R75(1)	MBC3	D3	68n	%E20(15)
R110(1)	MBC1	A4	68n	%E23(15)
R112(1)	MBC2	B3	68n	%E24(15)
R66(1)	MBC2	A4	68n	%E27(2)
R65(1)	MBC2	A3	68n	%E27(5)
R11(1)	MBC3	B4	68n	%E28(15)
R109(1)	MBC2	A4	68n	%E28(2)
R14(1)	MBC2	B7	68n	%E29(14)
R8(1)	MBC2	C7	68n	%E29(2)
R59(1)	MBC2	C7	68n	%E29(3)
R143(1)	MBC5	A2	68n	%E32(2)
R16(1)	MBC2	A6	68n	%E34(14)
R213(1)	MBC2	D7	68n	%E34(5)
R64(1)	MBC2	B4	68n	%E34(6)
R215(1)	MBC2	D7	68n	%E36(6)
R214(1)	MBC2	D7	68n	%E36(7)
R32(1)	MBC3	C7	68n	%E40(14)
R35(1)	MBC3	C7	68n	%E40(15)
R206(1)	MBC2	D4	68n	%E40(6)
R27(1)	MBC4	C7	68n	%E41(14)
R36(1)	MBC4	C6	68n	%E41(2)
R37(1)	MBC4	C6	68n	%E41(3)
R185(1)	MBC3	A6	68n	%E42(2)
R38(1)	MBC5	A6	68n	%E43(2)
R28(1)	MBC3	C7	68n	%E44(15)
R13(1)	MBC3	B4	68n	%E46(15)
R31(1)	MBC3	A4	68n	%E46(2)
R9(1)	MBC3	C3	68n	%E49(15)
R7(1)	MBC3	D2	68n	%E5(2)
R33(1)	MBC4	D7	68n	%E50(14)
R34(1)	MBC4	D7	68n	%E50(3)
R120(1)	MBC4	C7	68n	%E53(3)
R119(1)	MBC4	C7	68n	%E53(6)
R19(1)	MBC2	B4	68n	%E54(15)
R84(1)	MBC3	D4	68n	%E55(2)
R76(1)	MBC3	D4	68n	%E55(3)
R93(1)	MBC4	B3	68n	%E55(7)

RESISTOR LOC(PIN)	SHOWN ON DRAW#	ON REF	VALUE	TERMINATES SIGNAL
R80(1)	MBC3	D5	68n	%E56(13)
R156(1)	MBC5	A6	68n	%E56(4)
R50(1)	MBC4	A3	68n	%E60(3)
R67(1)	MBC3	B5	68n	%E60(6)
R131(1)	MBC4	A4	68n	%E64(2)
R128(1)	MBC4	B4	68n	%E65(14)
R30(1)	MBC3	A5	68n	%E65(2)
R88(1)	MBC4	B5	68n	%E65(3)
R3(1)	MBC3	D4	68n	%E65(9)
R201(1)	MBC4	D2	68n	%E67(15)
R167(1)	MBC2	C3	68n	%E67(6)
R205(1)	MBC4	D2	68n	%E67(9)
R129(1)	MBC4	B4	68n	%E70(14)
R92(1)	MBC4	C4	68n	%E70(2)
R166(1)	MBC4	C4	68n	%E70(3)
R165(1)	MBC2	D4	68n	%E72(15)
R164(1)	MBC2	D4	68n	%E73(15)
R133(1)	MBC4	C4	68n	%E74(2)
R89(1)	MBC4	A4	68n	%E74(7)
R49(1)	MBC4	A3	68n	%E75(14)
R15(1)	MBC2	B7	68n	-APR2 WR BAD ADR PAR H
R186(1)	MBC3	A7	68n	-CCL START MEM H
R113(1)	MBC1	C6	68n	CLK1 MBC H
R121(1)	MBC2	C5	68n	-CSH2 E CORE RD RQ B H
R61(1)	MBC2	D5	68n	CSH2 ONE WORD RD H
R69(1)	MBC3	A5	68n	-CSH2 RD PAUSE 2ND HALF H
R57(1)	MBC1	B4	68n	CSH3 ADR PMA EN H
R68(1)	MBC2	A5	68n	CSH3 ANY VAL HOLD A H
R99(1)	MBC1	C2	68n	CSH3 MATCH HOLD 1 IN H
R98(1)	MBC1	C2	68n	CSH3 MATCH HOLD 2 IN H
R118(1)	MBC2	C5	68n	-CSH4 CLEAR WR T0 H
R107(1)	MBC3	A2	68n	-CSH4 DATA CLR DONE H
R122(1)	MBC2	C5	68n	-CSH4 EBOX T3 H
R161(1)	MBC2	D4	68n	CSH4 EBOX WR T4 IN H
R187(1)	MBC3	A7	68n	-CSH5 CHAN RD T5 H
R18(1)	MBC2	C7	68n	-CSH5 CHAN T3 H
R171(1)	MBC2	D4	68n	CSH5 CHAN WR T5 IN H
R188(1)	MBC3	A7	68n	-CSH5 PAGE REFILL T9 H
R208(1)	MBC2	C4	68n	CSH6 CACHE WR IN H
R148(1)	MBC3	B2	68n	-CSH6 CHAN WR CACHE H

RESISTOR LOC(PIN)	SHOWN ON DRAW#	ON REF	VALUE	TERMINATES SIGNAL
R174(1)	MBC2	D4	68n	CSH6 WR FROM MEM NXT H
R138(1)	MBC3	B2	68n	-E CACHE WR CYC H
R177(1)	MBC1	D8	68n	-LOAD EBUS REG H
R39(1)	MBC1	C5	68n	MBC1 CLK A H
R47(1)	MBC1	C5	68n	MBC1 CLK B H
R200(1)	MBC1	C5	68n	MBC1 CLK C H
R178(1)	MBC1	C5	68n	MBC1 CLK D H
R6(1)	MBC1	C5	68n	MBC1 CLK E H
R10(1)	MBC1	C5	68n	MBC1 CLK F H
R157(1)	MBC1	D7	68n	MBC1 EBUS REG 27 H
R180(1)	MBC1	D7	68n	MBC1 EBUS REG 28 H
R151(1)	MBC1	D7	68n	MBC1 EBUS REG 29 H
R154(1)	MBC1	D7	68n	MBC1 EBUS REG 30 H
R160(1)	MBC1	D6	68n	MBC1 EBUS REG 31 H
R195(1)	MBC1	D6	68n	MBC1 EBUS REG 32 H
R182(1)	MBC1	D6	68n	MBC1 EBUS REG 33 H
R140(1)	MBC1	D2	68n	MBC1 MATCH HOLD 1 H
R142(1)	MBC1	D2	68n	MBC1 MATCH HOLD 2 H
R100(1)	MBC1	C7	68n	MBC1 PMA 34 A H
R150(1)	MBC1	C7	68n	-MBC1 PMA 34 A H
R146(1)	MBC1	C7	68n	MBC1 PMA 35 A H
R145(1)	MBC1	C7	68n	-MBC1 PMA 35 A H
R210(1)	MBC1	D3	68n	MBC1 PMA HOLD 26 H
R101(1)	MBC1	D4	68n	MBC1 PMA HOLD 27 H
R97(1)	MBC1	D4	68n	MBC1 PMA HOLD 28 H
R95(1)	MBC1	D4	68n	MBC1 PMA HOLD 29 H
R96(1)	MBC1	D4	68n	MBC1 PMA HOLD 30 H
R139(1)	MBC1	D3	68n	MBC1 PMA HOLD 31 H
R136(1)	MBC1	D3	68n	MBC1 PMA HOLD 32 H
R137(1)	MBC1	D3	68n	MBC1 PMA HOLD 33 H
R106(1)	MBC1	C2	68n	MBC1 PMA HOLD 34 H
R102(1)	MBC1	C2	68n	MBC1 PMA HOLD 35 H
R79(1)	MBC1	B1	68n	MBC1 RESET H
R83(1)	MBC1	B1	68n	-MBC1 RESET H
R117(1)	MBC1	B1	68n	-MBC1 WRITE OK H
R22(1)	MBC1	B2	68n	MBC1 WRITE OK IN A H
R23(1)	MBC1	B2	68n	MBC1 WRITE OK IN B H
R159(1)	MBC2	C1	68n	MBC2 CACHE WR 00 A H
R179(1)	MBC2	C1	68n	MBC2 CACHE WR 09 A H
R152(1)	MBC2	C1	68n	MBC2 CACHE WR 18 A H

RESISTOR LOC(PIN)	SHOWN ON DRAW#	ON REF	VALUE	TERMINATES SIGNAL
R155(1)	MBC2	C1	68n	MBC2 CACHE WR 27 A H
R123(1)	MBC2	D2	68n	MBC2 CSH ADR WR PLS FF H
R198(1)	MBC2	D1	68n	MBC2 CSH ADR WR PULSE H
R207(1)	MBC2	D2	68n	MBC2 CSH VAL WR PLS FF H
R114(1)	MBC2	D1	68n	MBC2 CSH VAL WR PULSE H
R149(1)	MBC2	C2	68n	MBC2 CSH WR DATA FF H
R204(1)	MBC2	D2	68n	MBC2 CSH WR WR PLS FF H
R196(1)	MBC2	D1	68n	MBC2 CSH WR WR PULSE H
R170(1)	MBC2	B6	68n	MBC2 FORCE BAD ADR PAR H
R40(1)	MBC2	C7	68n	MBC2 RQ HOLD H
R108(1)	MBC2	C6	68n	-MBC2 RQ HOLD FF H
R62(1)	MBC3	D7	68n	-MBC3 A CHANGE COMING H
R63(1)	MBC3	C7	68n	MBC3 A CHANGE COMING A H
R44(1)	MBC3	D7	68n	-MBC3 A CHANGE COMING B H
R87(1)	MBC3	D2	68n	MBC3 A PHASE H
R181(1)	MBC3	A1	68n	-MBC3 ANY SBUS RQ IN H
R24(1)	MBC3	C7	68n	MBC3 B CHANGE COMING H
R158(1)	MBC3	C7	68n	-MBC3 B CHANGE COMING H
R40(1)	MBC3	B7	68n	-MBC3 B CHANGE COMING A H
R4(1)	MBC3	D3	68n	MBC3 CLK A PHASE COMING H
R191(1)	MBC3	A3	68n	MBC3 CORE BUSY H
R105(1)	MBC3	C2	68n	-MBC3 FIRST WD ADR SEL H
R115(1)	MBC3	C2	68n	-MBC3 INH 1ST MB REQ H
R73(1)	MBC3	C4	68n	MBC3 MEM START CLR H
R74(1)	MBC3	B6	68n	-MBC3 MEM START SET H
R20(1)	MBC3	D1	68n	MBC3 PHASE CHANGE COMING H
R55(1)	MBC3	A5	68n	MBC3 RD PAUSE 2ND HALF H
R46(1)	MBC4	A1	68n	MBC4 ADR 34 H
R168(1)	MBC4	A1	68n	MBC4 ADR 35 H
R132(1)	MBC4	C4	68n	MBC4 ANY REQUEST; WIRE OR H
R90(1)	MBC4	C3	68n	-MBC4 ANY RQS LEFT H
R153(1)	MBC4	B6	68n	-MBC4 CORE DATA VALID -2 H
R85(1)	MBC4	A6	68n	-MBC4 CORE DATA VALID A H
R135(1)	MBC4	A6	68n	-MBC4 CORE RD IN PROG H
R192(1)	MBC4	A6	68n	MBC4 CORE RD IN PROG A H
R29(1)	MBC4	B3	68n	MBC4 INIT COMP H
R82(1)	MBC4	B2	68n	-MBC4 INIT COMP H
R25(1)	MBC4	C7	68n	MBC4 LAST ACKN H
R72(1)	MBC4	C7	68n	-MBC4 LAST ACKN H
R78(1)	MBC4	C6	68n	MBC4 LAST ACKN SEEN H

NOTE:
 1. ALL TERMINATORS HAVE PIN TWO CONNECTED TO -2.0V AND ARE 5% 1/4WATT UNLESS OTHERWISE SPECIFIED
 2. ENTRIES ARE SORTED BY SIGNAL NAME
 3. % INDICATES OUTPUT OF DIP LOC AND
 () INDICATES PIN NUMBER

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REVISIONS
CHK CHANGE NO. REV

digital	DRAWN: J. J. J. CHECKED: D. DELLORCO	DATE: 06-AUG-84	ENG: C. A. JENS	DATE: 06-AUG-84	TITLE: MBOX CONTROL TERMINATORS
		DATE: 06-AUG-84	BOARD LOCATION: 4AF22	SHEET: 2	OF: 2
XTRA1(MCA25.MB531)MB571.DRW 123-JUL-84 14:33		NEXT HIGHER ASSEMBLY:		SIZE: D	CODE: CS
FIRST USED ON OPTION/MODEL: MCA25		D-DD-MB57-0		NUMBER: M857-0-RES	REV: A

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REV.

NUMBER

533-0-RES

CS

D

SIZE

1

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R26(1)	MBC4	C6	68Ω	-MBC4 LAST ACKN SEEN H
R12(1)	MBC4	D6	68Ω	MBC4 MEM START H
R71(1)	MBC4	D6	68Ω	-MBC4 MEM START A H
R70(1)	MBC4	D6	68Ω	-MBC4 MEM START B H
R130(1)	MBC4	B3	68Ω	MBC4 MEM START RD H
R94(1)	MBC4	B3	68Ω	-MBC4 MEM START RD H
R169(1)	MBC4	D1	68Ω	MBC4 PMA ADR PAR HOLD H
R91(1)	MBC4	D3	68Ω	MBC4 RQ 0A H
R86(1)	MBC4	C4	68Ω	MBC4 RQ 0B H
R172(1)	MBC4	D3	68Ω	MBC4 RQ 1A H
R175(1)	MBC4	D3	68Ω	MBC4 RQ 2A H
R173(1)	MBC4	D3	68Ω	MBC4 RQ 3A H
R183(1)	MBC5	C7	68Ω	MBC5 CORE BUSY B H
R189(1)	MBC5	C7	68Ω	MBC5 DIAG 04 H
R190(1)	MBC5	C7	68Ω	MBC5 DIAG 05 H
R197(1)	MBC5	C7	68Ω	MBC5 DIAG 06 H
R194(1)	MBC5	B7	68Ω	-MBC5 DIAG READ FUNC 16X H
R141(1)	MBC5	A5	68Ω	MBC5 HOLD MATCH H
R217(1)	MBC1	B4	68Ω	MBX CSH ADR 26 H
R147(1)	MBC5	A2	68Ω	-MBX1 CCA ALL PAGES CYC H
R176(1)	MBC2	D4	68Ω	MBX1 CCA INVAL T4 A H
R111(1)	MBC5	A3	68Ω	-MBX1 FORCE MATCH EN H
R103(1)	MBC1	B4	68Ω	MBX1 REFILL ADR EN H
R17(1)	MBC2	C7	68Ω	-MBX2 CHAN WR CYC H
R53(1)	MBC1	C2	68Ω	MBX3 REFILL HOLD H
R81(1)	MBC3	B5	68Ω	-MBX4 CACHE TO MB T2 H
R184(1)	MBC3	B6	68Ω	-MBX4 CACHE TO MB T4 A H
R209(1)	MBC2	D4	68Ω	MBX4 WRITEBACK T2 H
R199(1)	MBC4	D2	68Ω	MBX5 MEM RD RQ IN H
R116(1)	MBC3	A2	68Ω	-MBX5 MEM TO C EN H
R203(1)	MBC4	D2	68Ω	MBX5 MEM WR RQ IN H
R124(1)	MBC4	D5	68Ω	MBX5 RQ 0 IN H
R125(1)	MBC4	D5	68Ω	MBX5 RQ 1 IN H
R127(1)	MBC4	D5	68Ω	MBX5 RQ 2 IN H
R134(1)	MBC4	D5	68Ω	MBX5 RQ 3 IN H
R163(1)	MBC4	C8	68Ω	MEM ACKN A H
R162(1)	MBC4	C8	68Ω	MEM ACKN B H
R45(1)	MBC4	B7	68Ω	-MEM DATA VALID A H
R43(1)	MBC4	B7	68Ω	-MEM DATA VALID B H
R42(1)	MBC6	A1	68Ω	NC

NOTE:

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2. ENTRIES ARE SORTED BY SIGNAL NAME
3. % INDICATES OUTPUT OF DIP LOC AND
() INDICATES PIN NUMBER

RESISTOR LOC(PIN)	SHOWN DRW#	ON REF	VALUE	TERMINATES SIGNAL
R126(1)	MBC6	A1	68Ω	NC
R193(1)	MBC4	A7	68Ω	NC
R77(1)	MBC4	C8	68Ω	NXM ACKN H
R41(1)	MBC4	B7	68Ω	-NXM DATA VAL H
R216(1)	MBC2	D6	68Ω	PAG1 PT 26 B H
R211(1)	MBC1	D3	68Ω	PMA3 PA 26 H
R52(1)	MBC1	B2	68Ω	PMA3 PA 27 H
R51(1)	MBC1	B2	68Ω	PMA3 PA 28 H
R58(1)	MBC1	B2	68Ω	PMA3 PA 29 H
R54(1)	MBC1	B2	68Ω	PMA3 PA 30 H
R1(1)	MBC1	A2	68Ω	PMA3 PA 31 H
R104(1)	MBC1	C8	68Ω	PMA4 34 B H
R144(1)	MBC1	C8	68Ω	PMA4 35 B H
R202(1)	MBC4	D2	68Ω	PMA4 ADR PAR H
R2(1)	MBC1	A2	68Ω	PMA4 PA 32 H
R5(1)	MBC1	A2	68Ω	PMA4 PA 33 H
R212(1)	MBC2	D7	68Ω	-PMA5 SEL 7 H

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REVISIONS

CHK CHANGE NO. REV

digital

DRW. J. J. J.
CHK'D. D. DELLORCO

DATE

ENG. C. A. JENS

DATE

06-AUG-84

TITLE:

MBOX CONTROL
TERMINATORS

XTRA:MCA25.M8531.M8578.DRW 23-JUL-84 14:33

NEXT HIGHER ASSEMBLY:

SIZE CODE

D CS

NUMBER

M857-0-RES

REV.

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MRO 1