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	H742Ø CUSTOMER PRINT SET

UNIT VARIATIONS COVERED BY THIS PRINT SET

AN1Ø-AA
AN1Ø-AB
AN1Ø-BA
AN1Ø-BB

AN1Ø
Field Maintenance
Print Set

Digital Equipment
Corporation

PRINT SET ORDER NO.
MPØØ326

REVISIONS	REV.		USED ON OPTION/MODEL	DRN.	DATE	TITLE: ARPANET INTERFACE												
	CHG. NO.		AN1Ø	M. NIKMAN	12 APR 77													
	DATE			CHK'D	P. Long	29 JUN 77	SIZE				CODE		NUMBER		REV.			
				PROJ. ENG.	D. Anderson	29 JUN 77	B		TC		AN1Ø-Ø-1							
				FIELD SERV.			DIST.											
			SHEET I OF 2															

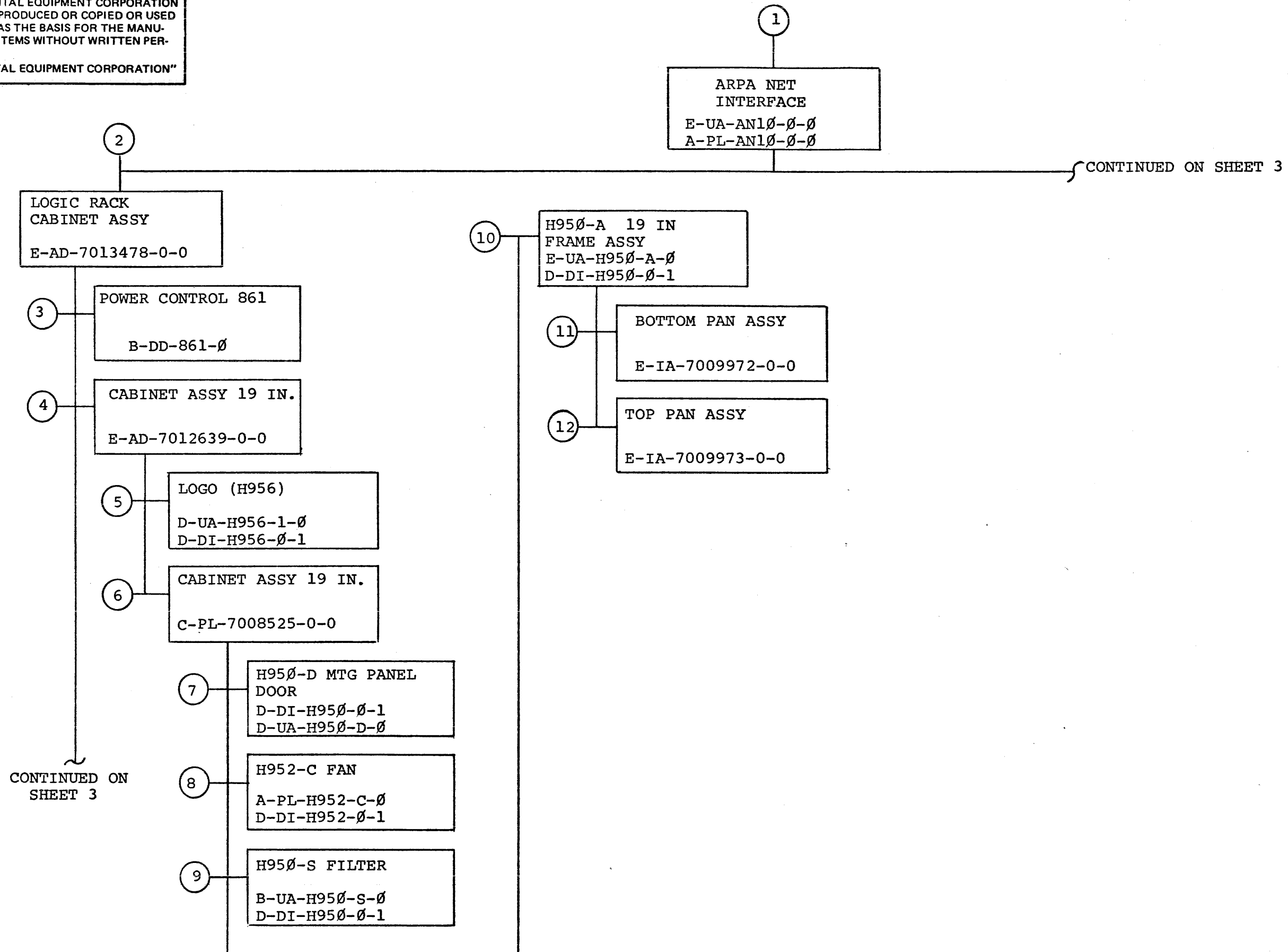
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D-IA-7010805-0-0
A-PL-AN1Ø-Ø-SL
A-SP-AN1Ø-Ø-AP
D-MU-AN1Ø-Ø-MU
D-IC-AN1Ø-Ø-PW
D-BS-AN1Ø-Ø-LCC

AC WIRING HARNESS ASSY
SHIP LIST, AN1Ø
ACCEPTANCE PROCEDURE, AN1Ø/AN2Ø
MODULE UTILIZATION, AN1Ø
WIRING DIAGRAM, AN1Ø
AN1Ø LOCAL CABLE, CONNECTIONS

TITLE		SIZE	CODE	NUMBER	REV
ARPANET INTERFACE	SHEET2 OF2	B	TC	AN1Ø-Ø-1	

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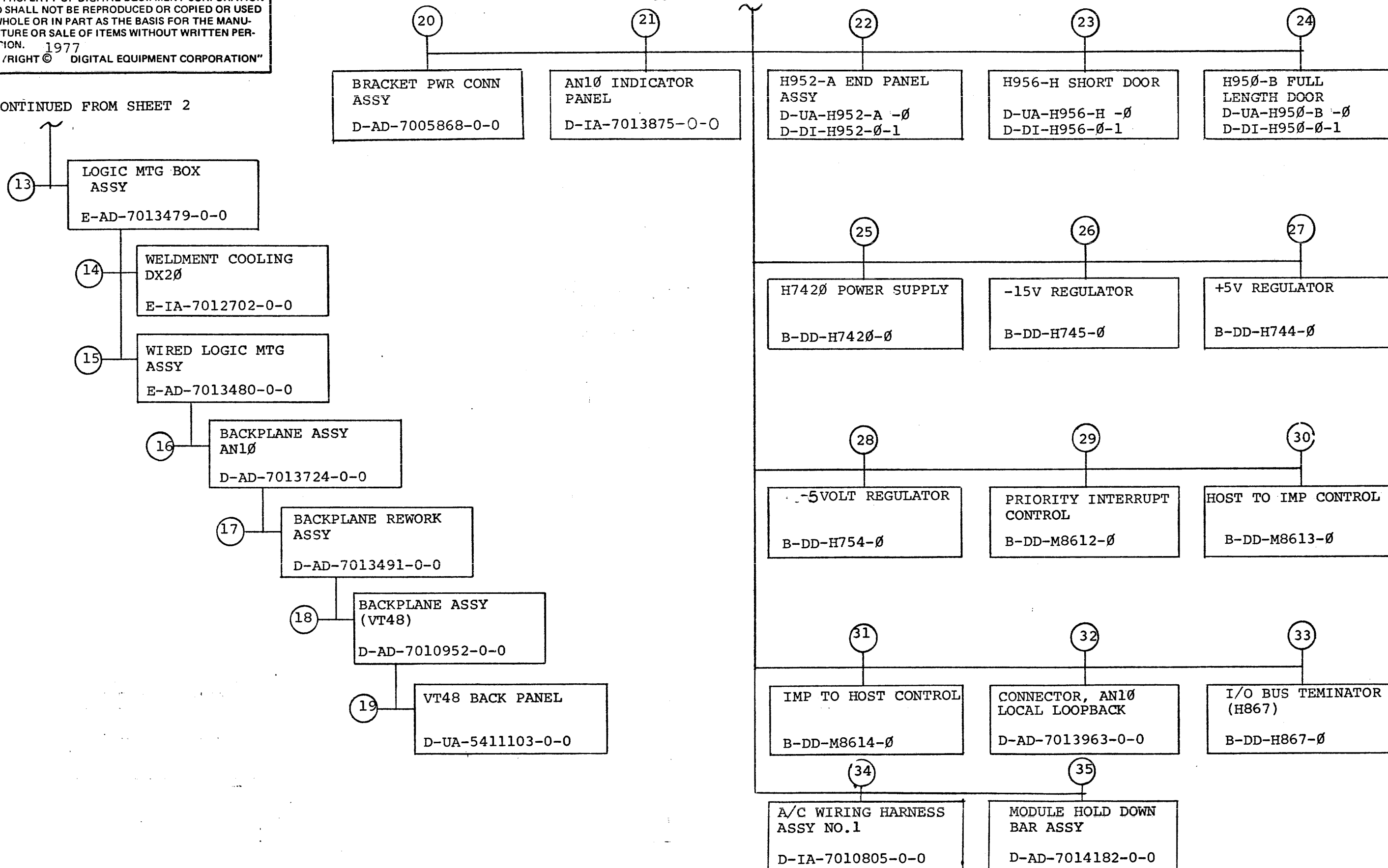


TITLE	SIZE	CODE	NUMBER	REV
ARPANET INTERFACE	B	DD	AN10-0	
SHEET 2 OF 7				

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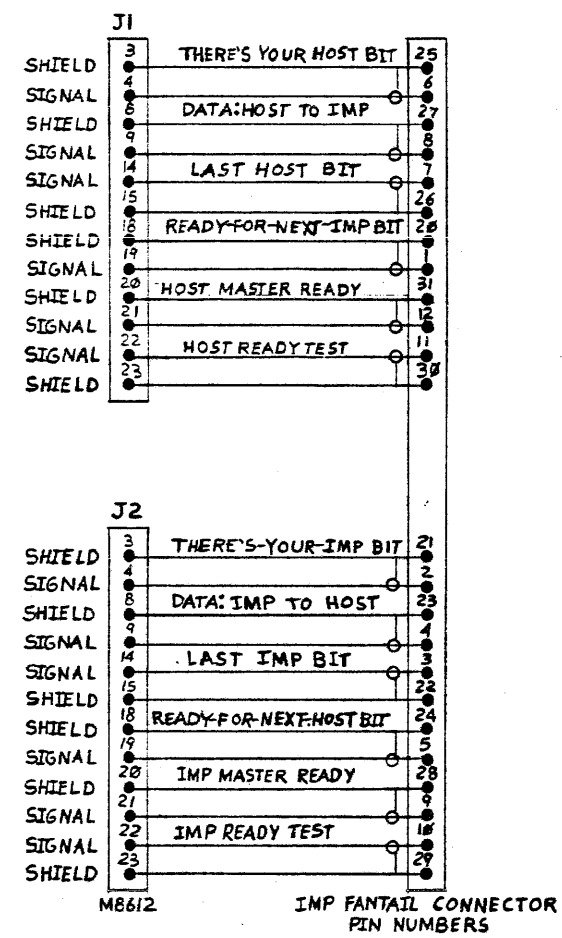
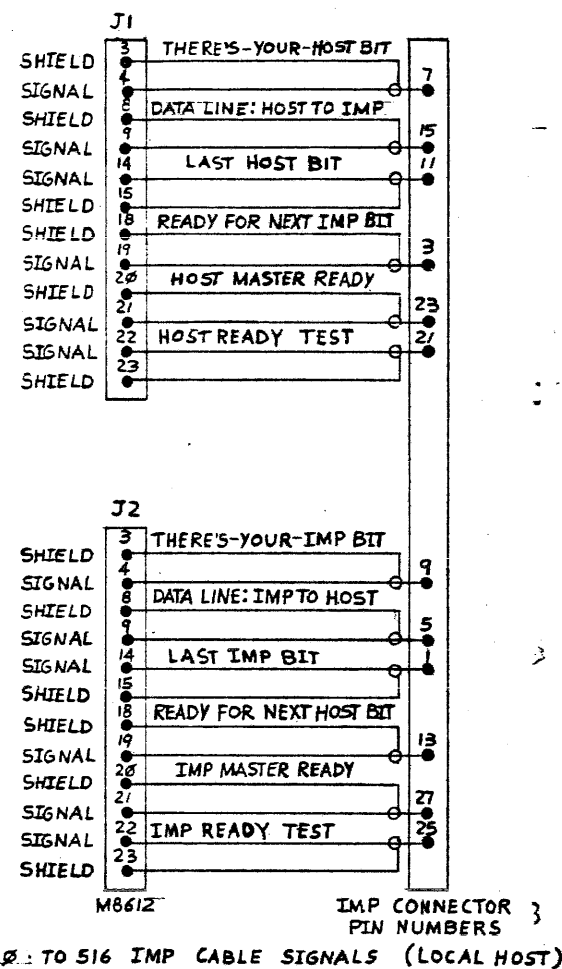
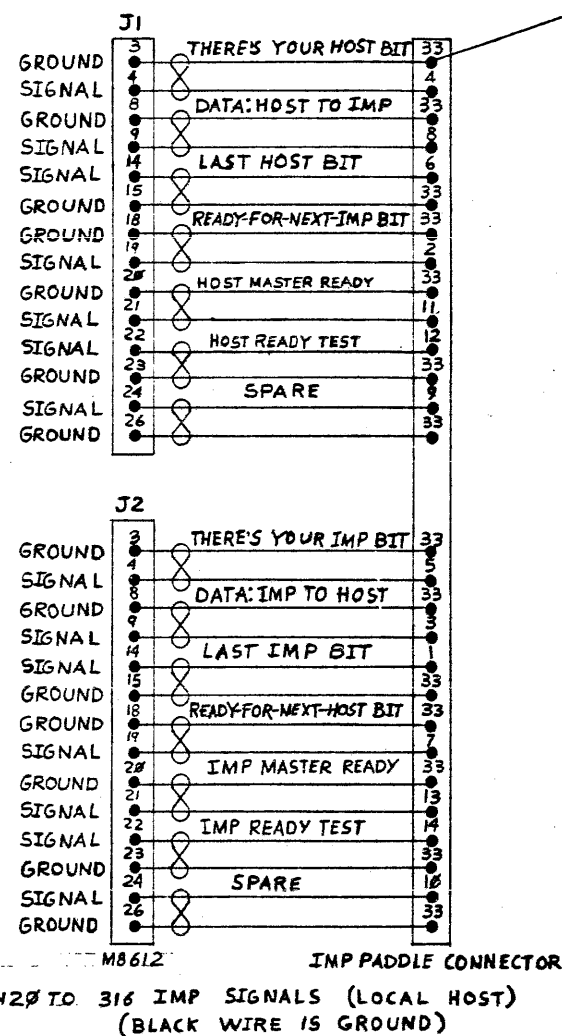
CONTINUED FROM SHEET 2

CONTINUED FROM SHEET 2



TITLE	SIZE	CODE	NUMBER	REV
ARPANET INTERFACE	B	DD	AN10-0-0	
SHEET 3 OF 7				

NOTES:
1. ALL PINS NUMBERED 33 ARE IMP GND



REV.	CHG.	NO.
1		
2		
3		
4		
5		
6		
7		
8		

DRN. <i>Edo. Hill</i>	26 Apr 77	FIRST USED ON	AN10		digital
CHK'D					
ENG. <i>J. J. Anderson</i>	23 Jun 77	TITLE			
PROJ. ENG. <i>J. J. Anderson</i>	6 Jul 77	AN10 LOCAL CABLE CONNECTIONS			
PROD. <i>J. J. Anderson</i>	6 Jul 77				
NEXT HIGHER ASSY.					
B-DD-AN10-0		SIZE	CODE	NUMBER	REV.
SCALE NONE		D	BS	AN10-0-LCC	
SHEET	1	OF	1	DIST.	

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	6	7	8	9	10	11	12	13	14	15	16	17	18
A										G89I	M564	M564	WIØ6
										CROBAR CROBAR L	CONI DATA I DATAO SET, CLR CCNO SET, CLR RESET PI SYNC	IØS 3-9	PI GRANT
B										M664	M564	M664	M564
										IØB 3-6	IØB 3-6	IØB 0-2 7-11	IØB 0-2 7-11
C										M664	M564	M664	M564
										IØB 12-19	IØB 12-19	IØB 20-27	IØB 20-27
D				M8614		M8613		M8612		M664		M664	M564
										PI REQ I-7		IØB 28-35	IØB 28-35
E													
F													
												M957	
												INDICATOR CABLE	

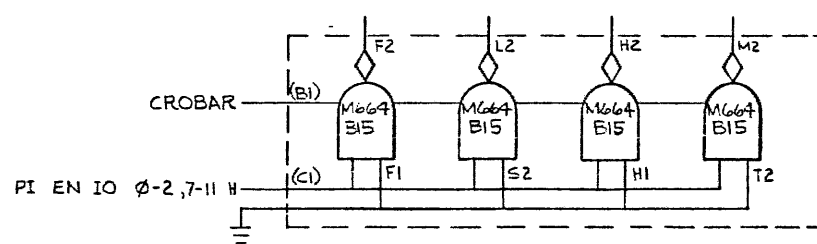
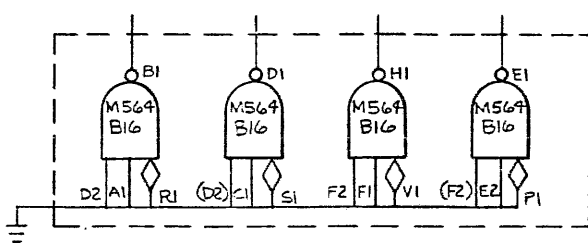
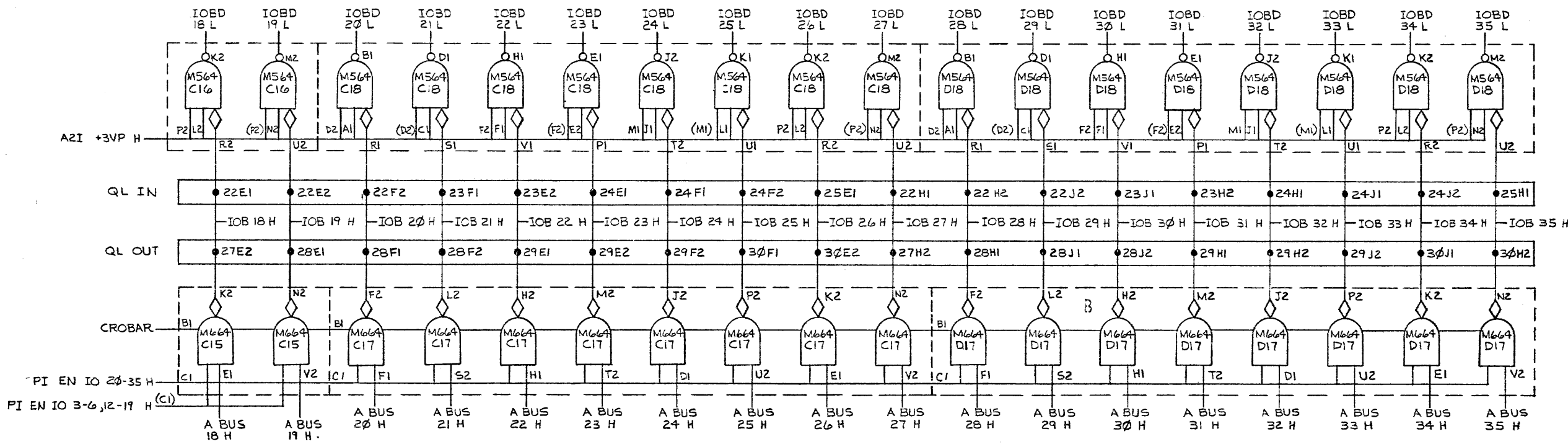
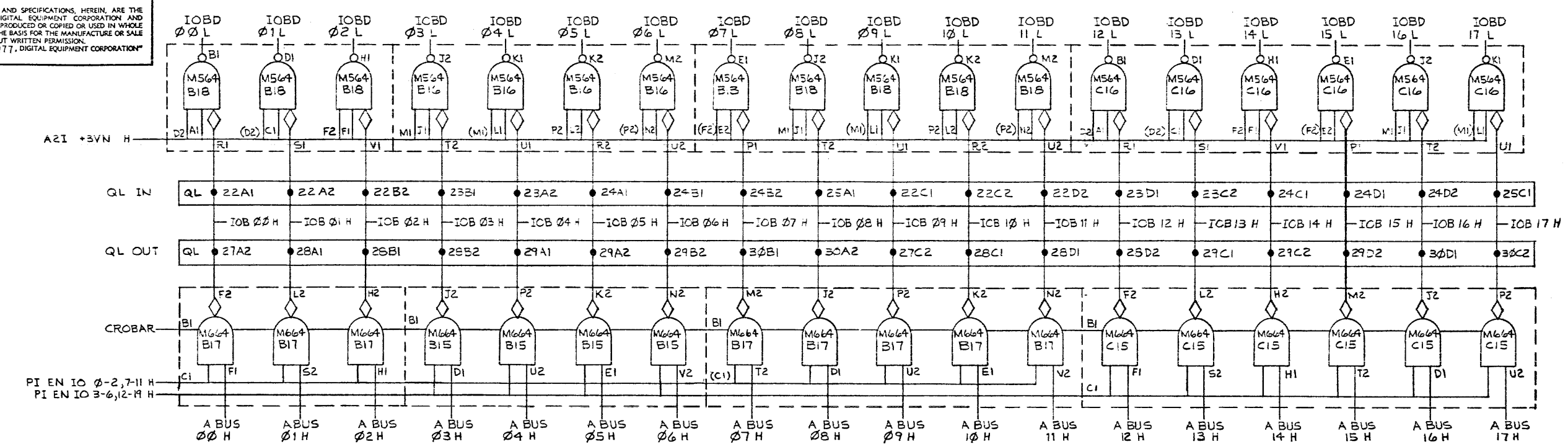
VIEW FROM PIN SIDE

REV.	
CHANGE NO.	
CHK	

DESIGNED	21 Jan 77	FIRST USED ON	ANIO	digital
CHK'D	22 Jan 77	TITLE	MODULE UTILIZATION, ANIO	
ENG.	23 Jan 77	SIZE	CODE	NUMBER
PROJ. ENG.	24 Jan 77	D	MU	ANIO-Ø-MU
PROD.	25 Jan 77	SHEET	1	OF 1
NEXT HIGHER ASSY.		SCALE	1/1	DIST.

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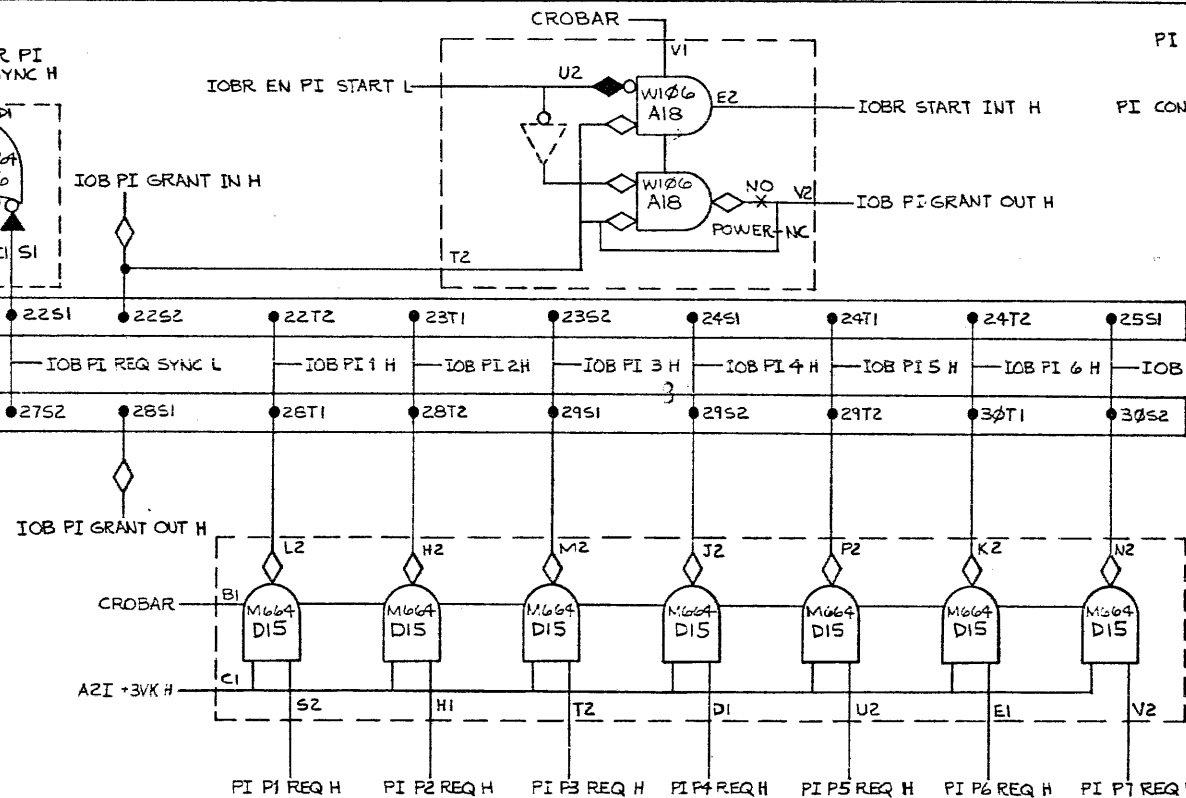
NOTES:
1. PINS C2, T1, K1 AND M1 ARE GROUND-ED ON MODULES B15, B17, C15, C17 AND D17.
2. PINS C2, T1, H2 AND N1 ARE GROUND-ED ON MODULES B16, B18, C16, C18 AND D18.



REV. 1
CHANGE NO. 1
DATE 1/1/77
BY [Signature]

DRN: 47AN77	FIRST USED ON: AN10	digital
CHK'D: R. W. Carter	TITLE: IBUS DATA (IOBD)	
ENG: [Signature]	PROJ. ENG: [Signature]	
PROD. [Signature]	NEXT HIGHER ASSY:	
E-00-AN10-0		
SCALE: 1	SIZE: D	CODE: BS
SHEET 1 OF 1		NUMBER: AN10-0-IOBD
		REV. PC2

IOBR
IOS 06 (0) H



A15
G891
POWER
FAIL
CROBAR

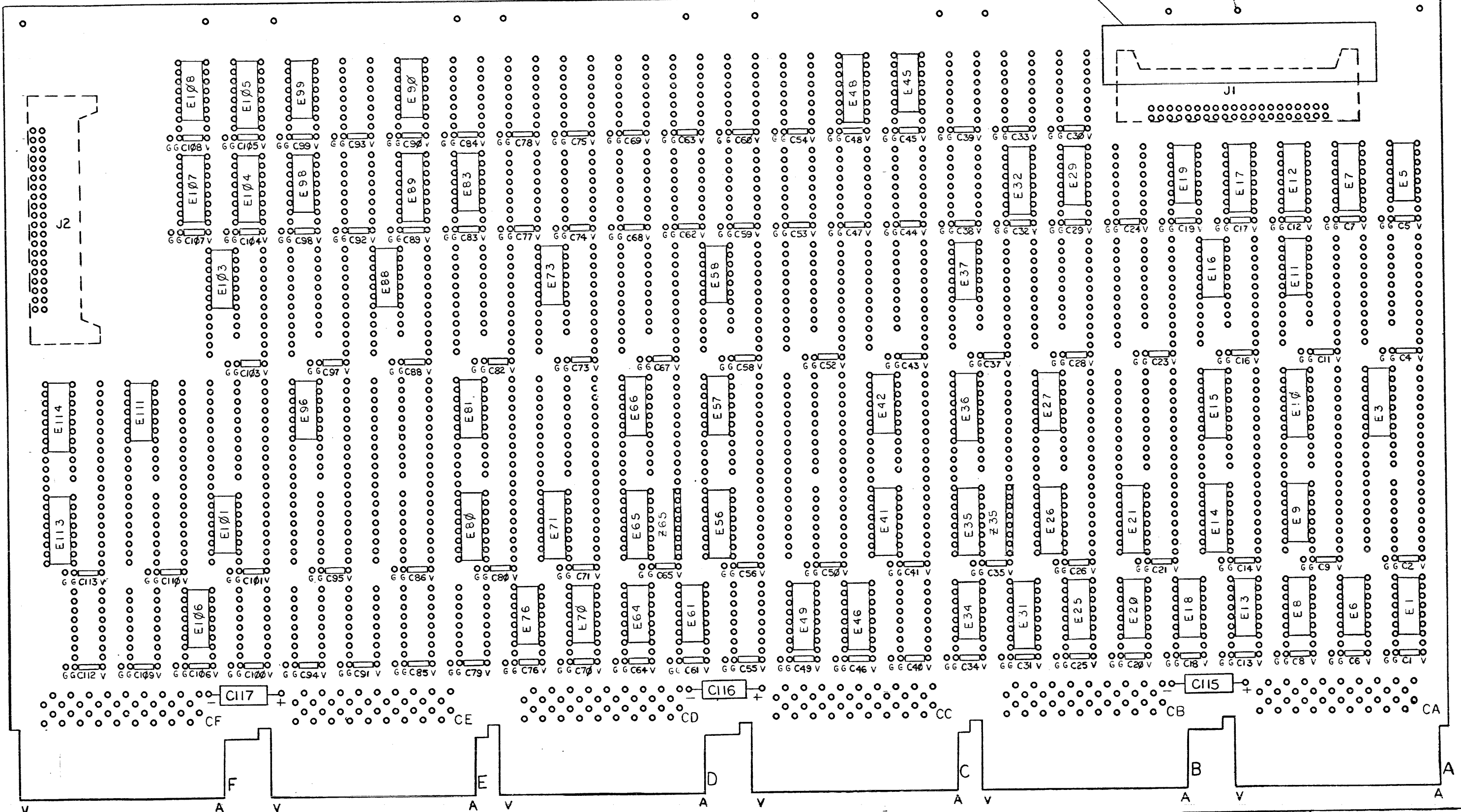
M2 — CROBAR

V2 — CROBAR

SEE NOTE 4,5

DRN. <i>R. Hancock</i>	<i>7-10-77</i>	FIRST USED ON	<i>ANIØ</i>				digital
CHK'D <i>R. Hancock</i>	<i>7-10-77</i>	TITLE	IBUS CONTROL (IOBR)				
ENG. <i>R. Hancock</i>	<i>7-10-77</i>						
PROJ. ENG. <i>R. Hancock</i>	<i>7-10-77</i>						
PROD. <i>R. Hancock</i>	<i>7-10-77</i>						
NEXT HIGHER ASSY.							
B-DD-ANIØ-Ø		SIZE	CODE	NUMBER		REV.	
SCALE <i>1</i>		D	BS	ANIØ-Ø-IOBR		PØ2	
SHEET <i>1</i> OF <i>1</i>		DIST.					

DRAWING NO.		NO. OF SHTS	PART NO.	DESCRIPTION	REVISIONS																																			
D-UA-M8612-0-0		2		WIRE WRAP MODULE	-																																			
B-PL-M8612-0-0		2		WIRE WRAP MODULE	-																																			
D-CS-M8612-Ø-I2A		1		IMP TO AN1Ø INTERFACE	-																																			
D-CS-M8612-Ø-A2I		1		AN1Ø TO IMP INTERFACE	-																																			
D-CS-M8612-Ø-PI		2		PRIORITY INTERRUPT	-																																			
D-CS-M8612-Ø-SP		1		SPARE IC's	-																																			
K-WL-M8612-0-WL		1		WIRE LIST M8612																																				
B-DD-W93Ø2-Ø		1		WIRE WRAP MODULE (HEX)	REF																																			
NOTES:					REVISIONS	DATE	CHG	NO.	REV.	ORIG																														
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						AN1Ø		CHK'D	25 FEB 77																															
								ENG.	6 JUL 77																															
								PROD.	6 JUL 77																															
SIZE		CODE	NUMBER		REV.																																			
B DD		M8612-Ø																																						
SHEET 1 OF 1																																								



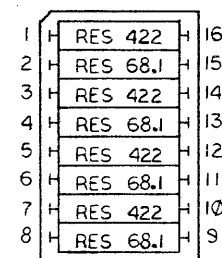
NOTES:

This image shows a single sheet of white paper with horizontal blue or grey ruling lines. The lines are evenly spaced and run across the width of the page. On the left side, there is a dark, irregular shape, possibly a shadow or a piece of tape. The paper appears to be part of a notebook or a set of loose-leaf papers.

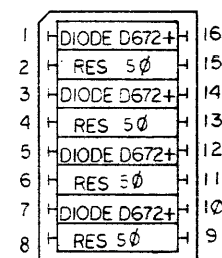
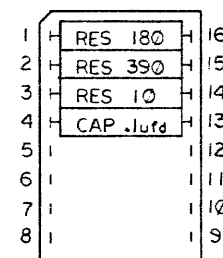
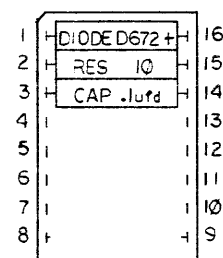
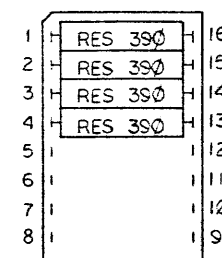
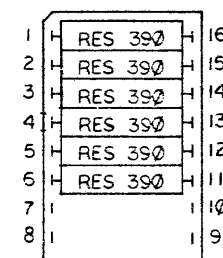
CHK	CHANGE	NO	REV

SIGNATURES		DATE	digital
DRN. <i>J. Wilson</i>		23 JAN 77	
CHK. <i>D. P. W. Austin</i>		19 JAN 77	
ENG. <i>B. J. Anderson</i>		6 JUL 77	
PROJ. <i>B. J. Anderson</i>		6 JUL 77	
PRCD. <i>F. J. Anderson</i>		16 JUL 77	
SCALE 2/1		SIZE CODE	NUMBER
SHT. 1 OF 2		D	UA M8612-0-0
ETCH REVA FIRST USED ON B-DD-M8612-0			

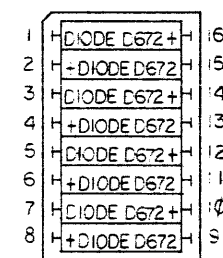
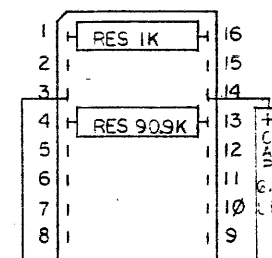
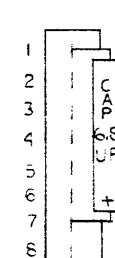
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E7&E17

E12E31E32E48

E89

E107E114

Z35, Z65

REVISIONS		
CHK	CHANGE NO.	REV.

DEC FORM NO.
DWD 138

8

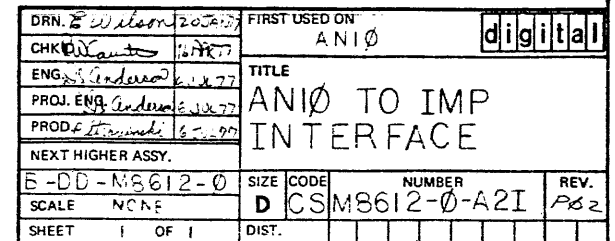
TITLE WIRE WRAP MODULE		SIZE D	CODE UA	NUMBER M8612-0-0	REV. A
SCALE 1" = 1"	SHEET 2 OF 2	DIST.			

	2	1
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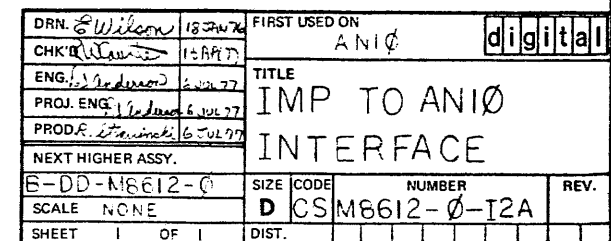
DIGITAL EQUIPMENT CORPORATION PARTS LIST				QUANTITY / VARIATION												NOTES: 1. FOR LOCATIONS OF ITEMS 2 THRU 12 SEE SHT 2 OF UA			
DESIGNED BY <i>E. Wilson</i> DATE <i>28 FEB 77</i>		CHECKED <i>RW Caunter</i> DATE <i>25 Feb 77</i>		SECTION 1															
ENGINEERED BY <i>D Anderson</i> DATE <i>6 JUL 77</i>		PROD <i>R Stroninski</i> DATE <i>6 JUL 77</i>		ISSUED SECTION 1															
ITEM NO.	DRAWING NO.	PART NO.	DESCRIPTION														REF DESIGNATION		
1	D-UA-W9302-0-0	W9302	WIRE WRAP MODULE (HEX)	1															
2		1005306	CAP 6.8MF 10%	3													SEE NOTE #1		
3		1302862	RES 50, 1%, 1/4W	4															
4		1010978-36	CAP .1MF 10%	2															
5		1105275	DIODE D672	13															
6		1301322	RES 180 5% 1/4W	1															
7		1300309	RES 390 5% 1/4W	11															
8		1300365	RES 1K 1% 1/4W	1															
9		1303067	RES 442 1% 1/4W	8															
10		1303226	RES 68.1 1% 1/4W	8															
11		1311320	RES 90.9K 1% 1/4W	1															
12		1301317	RES 10 5% 1/4W	2															
13		1311003-01	RESISTOR PACK	1													E36		
14		1211323	SOCKET, COMPONENT ADAPTOR	10													E7, E12, E17, E31, E32, E48, E89, E107, E114		
15		1912824	IC DEC 74LS74	2													E1, E106		
16		1912828	IC DEC 74LS85	6													E3, E10, E35, E41, E46, E49		
17		1912742	IC DEC 75112	2													E5, E19		
18		1912746	IC DEC 74S37	3													E6, E25, E37		
19		1912799	IC DEC 74LS00	7													E8, E13, E88, E98, E99, E101, E16		
20		1912816	IC DEC 74LS32	3													E9, E27, E111		
21		1912068	IC DEC 74128	1													E11,		
NO.																			
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								SHEET 1 OF 2				INSERTION PARTS LIST DATA BASE REV							

DIGITAL EQUIPMENT CORPORATION PARTS LIST				QUANTITY / VARIATION												NOTES:			
DRAWN BY <i>E. Wilson</i> DATE <i>28 FEB 77</i>		CHECKED <i>R W Caunter</i> DATE <i>25 Feb 77</i>		SECTION 1															
ENG <i>D J Anderson</i> DATE <i>6 JUL 77</i>		PROD <i>R Straninski</i> DATE <i>6 JUL 77</i>		ISSUED SECTION 1															
ITEM NO.	DRAWING NO.	PART NO.	DESCRIPTION													REF DESIGNATION			
22		1911676	IC DEC 74S139	1												E14			
23		1910544	IC DEC 74S74	3												E18, E57, E81			
24		1912389	IC DEC 74SØ8	1												E2Ø			
25		1912796	IC DEC 74148	1												E21			
26		1912853	IC DEC 74LS175	1												E26			
27		1912803	IC DEC 74LSØ4	2												E34, E42			
28		1912818	IC DEC 74LS38	3												E45, E83, E9Ø			
29		1912842	IC DEC 74LS138	4												E56, E65, E71, E8Ø			
30		19128Ø8	IC DEC 74LS11	2												E58, E73			
31		191281Ø	IC DEC 74LS2Ø	4												E61, E64, E7Ø, E76			
32		19128Ø5	IC DEC 74LSØ8	2												E66, E96			
33		191Ø268	IC DEC 751Ø7B	2												E1Ø3, E1Ø8			
34		191Ø528	IC DEC 8T14	2												E1Ø4, E1Ø5			
35		1912858	IC DEC 74LS221	1												E113			
36			RELAY MAGNECRAFT W171 DIP-2	1												E29			
37		1211164-04	SWITCH PACK	1												E15			
38	D-MD-7418759-O-O	7418759-7	HANDLE REWORK	1															
39		9006732	EYELET	12															
40		9009255-01	LABEL, REVISION	1															
EQ NO.																			
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								SHEET 2 OF 2				INSERTION PARTS LIST DATA BASE REV							

REVISIONS		REV.
CHK	CHANGE	7
	By [Signature]	7



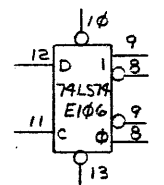
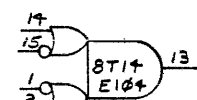
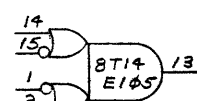
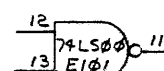
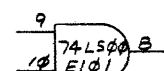
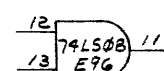
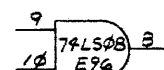
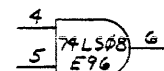
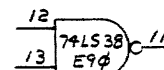
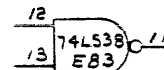
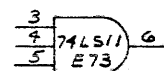
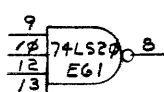
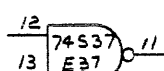
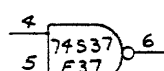
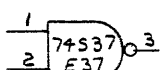
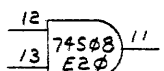
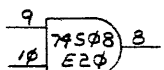
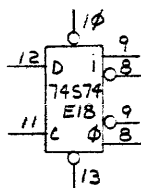
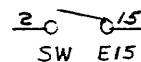
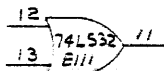
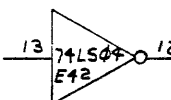
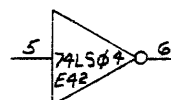
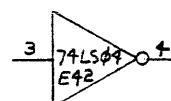
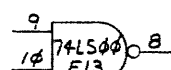
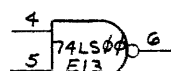
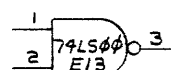
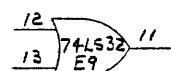
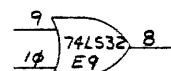
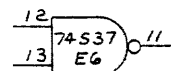
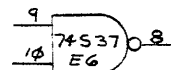
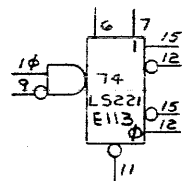
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DNR. <i>B. Wierzbicki</i>	13 JAN 77	FIRST USED ON		<i>AN10</i>		digital	
CHK'D <i>R. Wierzbicki</i>	14 JAN 77	TITLE PRIORITY INTERRUPT					
ENG. <i>G. Anderson</i>	6 JUL 77						
PROJ. ENG. <i>G. Anderson</i>	6 JUL 77						
PROD. <i>R. Starnowski</i>	6 JUL 77						
NEXT HIGHER ASSY.							
<i>B-DD-M8612-0</i>				SIZE		CODE	
SCALE <i>NONE</i>				D		CS	
SHEET <i>1</i> OF <i>2</i>				NUMBER		REV.	
DIST.				<i>M8612-0-PI</i>			

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REV.	CHANGE NO.	CHK.

DEC FORM 100, 1969-9

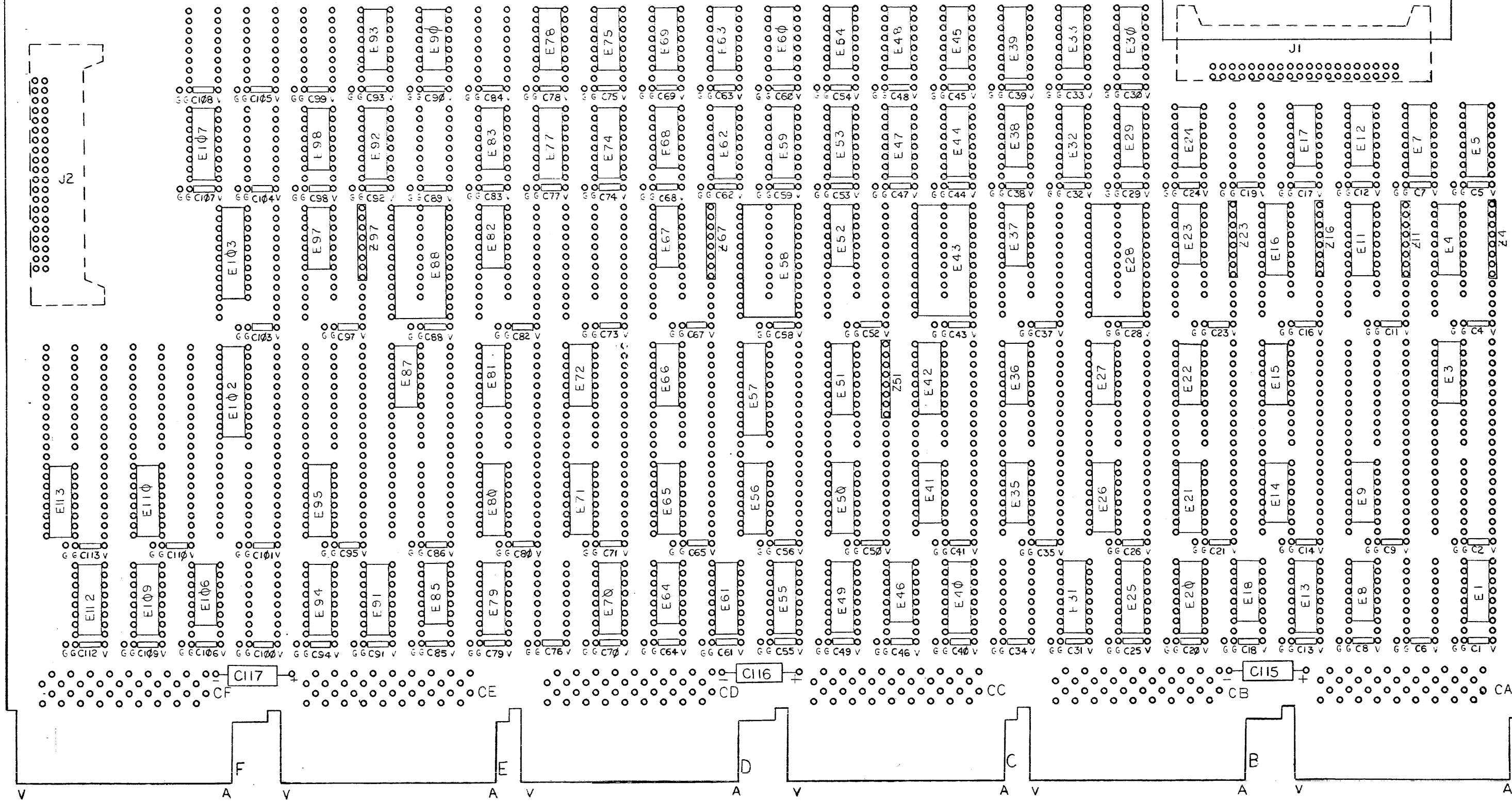
DRN. E. Wilson	28F677	FIRST USED ON	ANIØ	digital
CHK'D RW. Carter	28F677	TITLE	SPARE I.C.'S	
ENG. J. J. Jones	28F677	PROJ. ENG. J. J. Jones	28F677	PROD. J. J. Jones
NEXT HIGHER ASSY.				
B-DD-M8612-0		SIZE CODE	D	CSM8612-0-SP
SCALE NONE		DIST.		
SHEET 1 OF 1				

REV. NUMBER DCSM8612-0-SP

B

A

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NOTES: 1. REMOVE CONNECTORS J1 AND J2 AND RETURN TO STOCK.

CHK	CHANGE NO	REV

SIGNATURES		DATE
DRN. <i>[Signature]</i>	11/1/76	
CHK. <i>[Signature]</i>	11/1/76	
ENG. <i>[Signature]</i>	11/1/76	
PROJ. ENG. <i>[Signature]</i>	11/1/76	
PROD. <i>[Signature]</i>	11/1/76	
SCALE 2/1	SIZE CODE	NUMBER
SHT. 1 OF 2	D UA	M8613-0-0
ETCH REV. A	FIRST USED ON	

digital

WIRE WRAP
MODULE

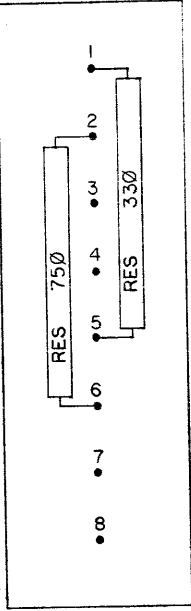
SIZE CODE
D UA

NUMBER
M8613-0-0

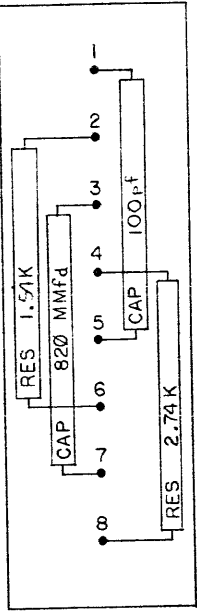
REV. 1
DUA M8613-0-0

A

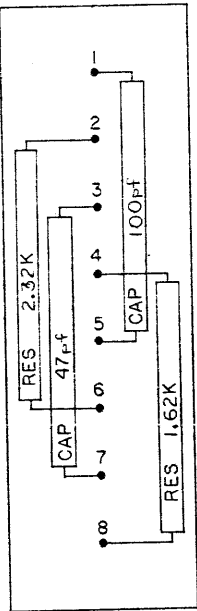
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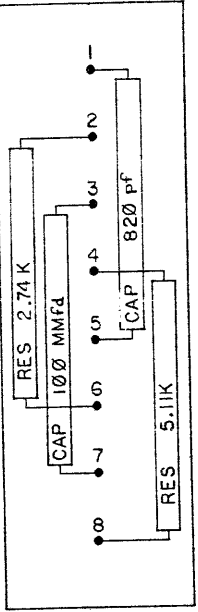
Z23, Z67, Z97



Z4

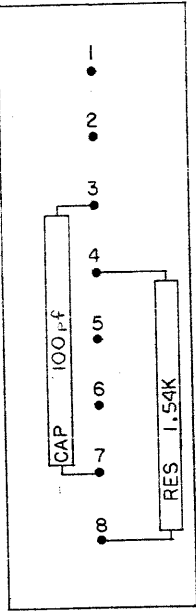


Z11



Z16

3



Z51

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		SIZE	CODE	NUMBER		REV.
WIRE WRAP MODULE		D	UA	M8613 - 0 - 0		
SCALE		SHEET 2 OF 2		DIST.		

WORKSPACE P-1555

DEC FORM NO. 139

8

7

6

5

4

3

2

1

REV.

B

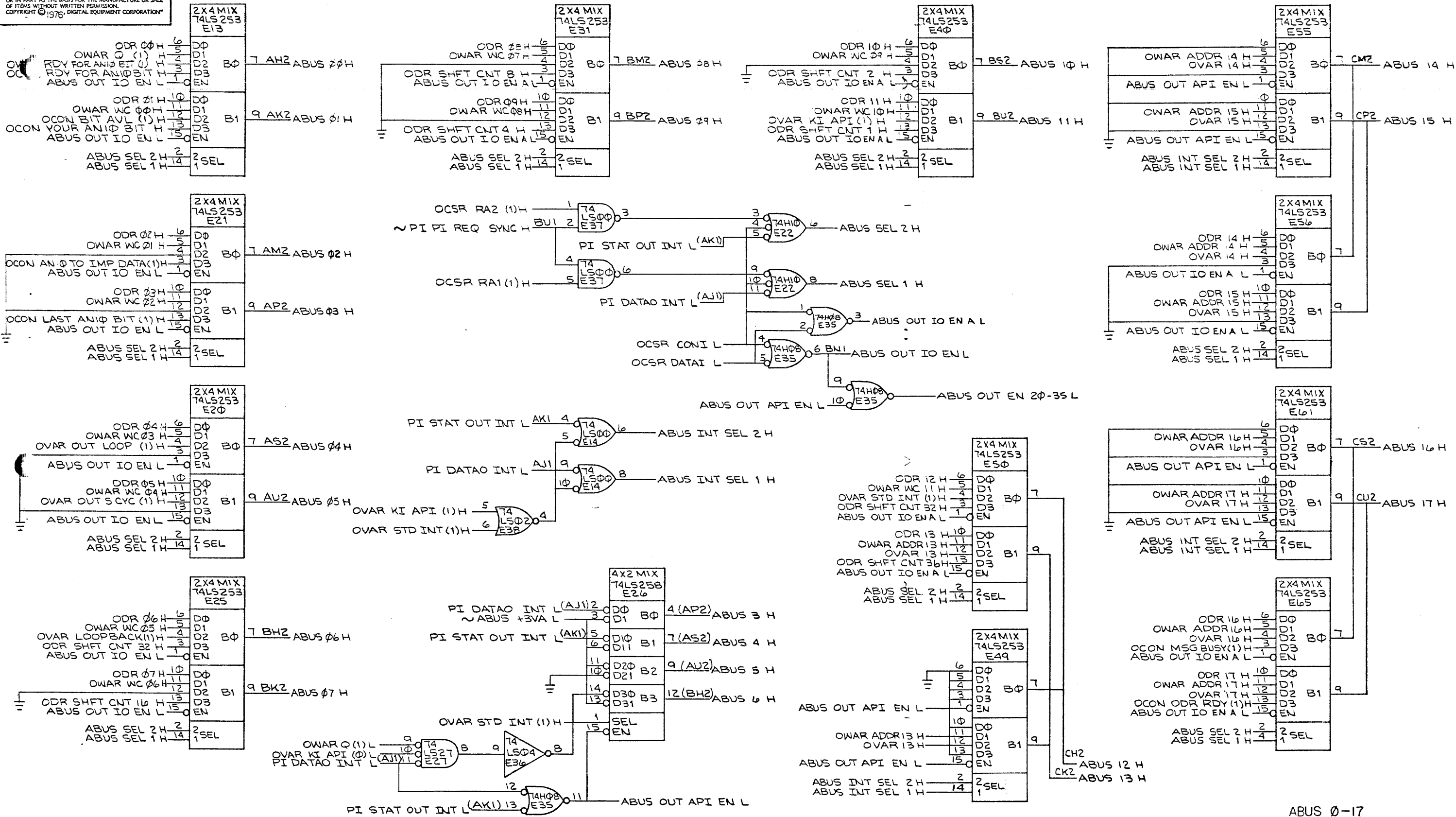
C

D

DIGITAL EQUIPMENT CORPORATION PARTS LIST				QUANTITY / VARIATION												NOTES: 1. FOR LOCATIONS OF ITEMS 2 THRU 12 SEE SHT 2 OF UA			
M E BY <i>F. Wilson</i> DATE <i>28 FEB 76</i>		CHECKED <i>RW Countee</i> DATE <i>28 Feb 77</i>		SECTION 1															
ENG <i>D J Anderson</i> DATE <i>6 JUL 77</i>		PROD <i>R. Stravinski</i> DATE <i>6 JUL 77</i>		ISSUED SECTION 1															
ITEM NO.	DRAWING NO.	PART NO.	DESCRIPTION															REF DESIGNATION	
1	D-UA-W93Ø2-Ø-Ø	W93Ø2	WIRE WRAP MODULE (HEX)	1													SEE NOTE #1		
2		1000011	CAP 47pF 5%	1															
3		1000016	CAP 100PF 5%	4															
4		1000027	CAP 820PF 5%	2															
5		1312480	RES 1.54K 1% ¼W	2															
6		1304854	RES 5.11K 1% ¼W	1															
7		1304868	RES 2.74K 1% ¼W	2															
8		1312545	RES 2.32K 1% ¼W	1															
9		1304862	RES 1.62K 1% ¼W	1															
10		1300295	RES 330 5% ¼W	3															
11		1301401	RES 750 5% ¼W	3															
12		1211323	SOCKET COMPONENT ADAPTOR	4															
13		19128Ø3	IC DEC 74LSØ4	8													E1, E18, E33, E36, E46, E64, E85, E1Ø6		
14		1912811	IC DEC 74LS21	1													E3		
15		1912858	IC DEC 74LS221	4													E4, E11, E16, E51		
16		1912854	IC DEC 74LS193	11													E5, E7, E32, E44, E47, E53, E59, E62, E74, E77, E92		
17		19124ØØ	IC DEC 74HO8	2													E8, E35		
18		19Ø9Ø56	IC DEC 74HØØ	1													E9		
19		1912799	IC DEC 74LSØØ	8													E12, E14, E24, E37, E69, E9Ø, E97, E98		
20		1912646	IC DEC 74LS253	22													E13, E2Ø, E21, E25, E31, E4Ø, E49, E5Ø, E55, E56, E61, E65, E7Ø, E71, E79, E8Ø, E91, E94, E1Ø9, E11Ø, E95, E112		
NO																			
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						SHEET 1 OF 2		INSERTION PARTS LIST DATA BASE REV											

DIGITAL EQUIPMENT CORPORATION PARTS LIST				QUANTITY / VARIATION												NOTES:			
DRAWN BY <i>F. Wilson</i> DATE <i>28 FEB 77</i>		CHECKED <i>RW Counter</i> DATE <i>28 Feb 77</i>		SECTION 1															
ENG <i>Dj Anderson</i> DATE <i>6 JUL 77</i>		PROD <i>R Stravinski</i> DATE <i>6 JUL 77</i>		ISSUED SECTION 1															
ITEM NO.	DRAWING NO.	PART NO.	DESCRIPTION														REF DESIGNATION		
21		1912824	IC DEC 74LS74	11													E15, E23, E29, E48, E52, E60, E66, E67, E78, E81, E82		
22		1912805	IC DEC 74LS08	5													E17, E30, E68, E87, E93		
23		1909057	IC DEC 74H10	1													E22		
24		1912859	IC DEC 74LS258	1													E26		
25		1912813	IC DEC 74LS27	1													E27		
26		1910842	IC DEC 74199	4													E28, E43, E58, E88		
27		1912801	IC DEC 74LS02	2													E38, E75		
28		1912853	IC DEC 74LS175	1													E39		
29		1912746	IC DEC 74S37	1													E41		
30		1912844	IC DEC 74LS151	1													E42		
31		1912808	IC DEC 74LS11	3													E45, E63, E72		
32		1912807	IC DEC 74LS10	2													E54, E83		
33		1912863	IC DEC 74LS273	3													E57, E102, E103		
34		1910623	IC DEC 74194	1													E107		
35		1912697	IC DEC 74LS174	1													E113		
36	D-MD-7418759-O - O	7418759-8	HANDLE REWORK	1															
37		9006732	EYELET	12															
38		9009255-01	LABEL, REVISION	1															
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								SHEET 2 OF 2				INSERTION PARTS LIST DATA BASE REV							

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REV.	CHG.	NO.
1		

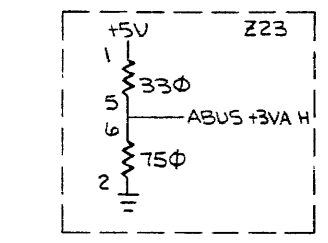
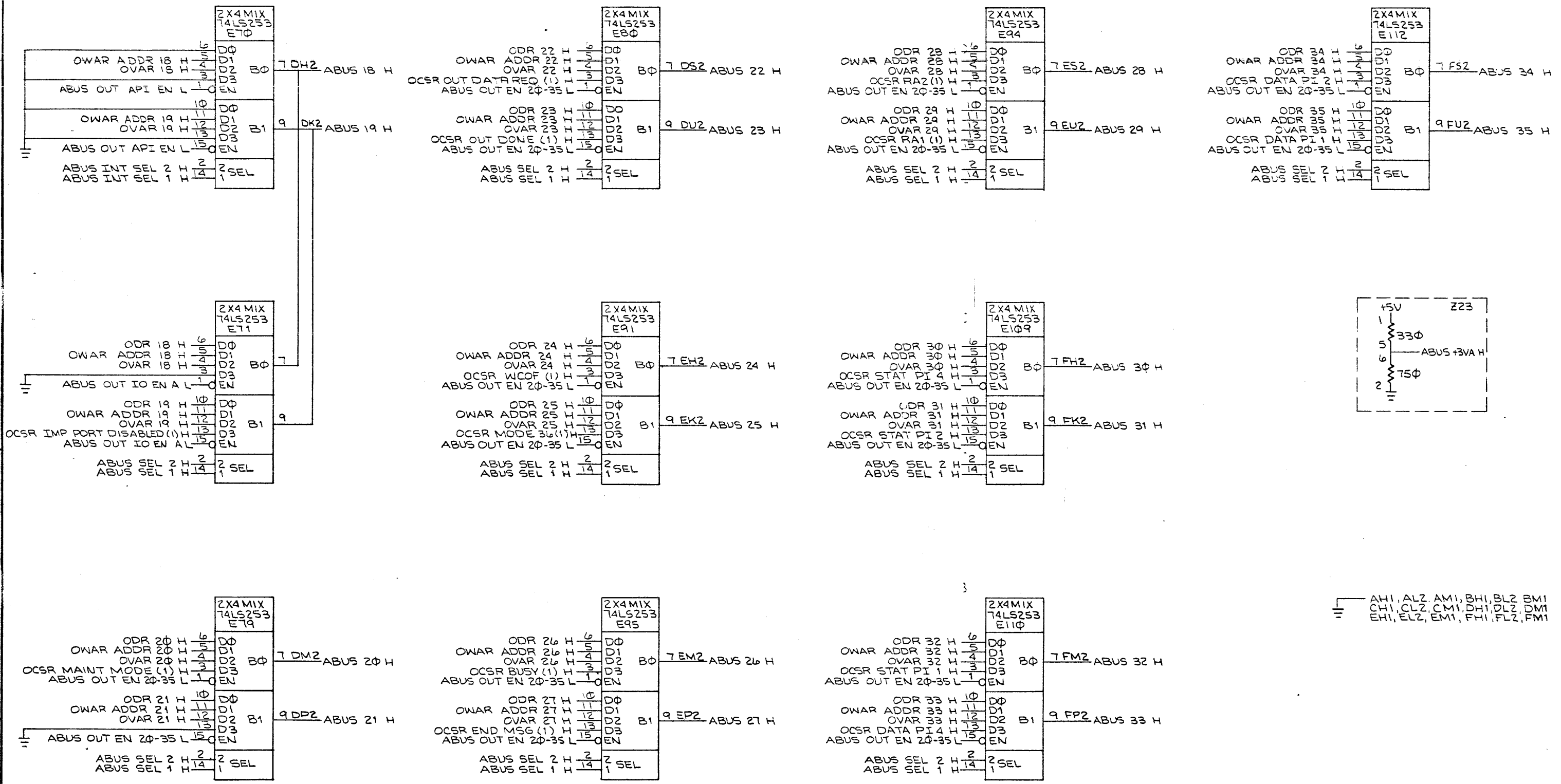
DRN. R.W. Counter	PROJ. 16-1000	FIRST USED ON	AN10	digital
CHK'D R.W. Counter	PROJ. 16-1000	TITLE	ABUS 0-35	
ENG. 16-1000	PROJ. 16-1000	SCALE	1 OF 2	
PROJ. 16-1000	PROJ. 16-1000	SHEET	1 OF 2	
NEXT HIGHER ASSY.				
B-DD-M8613-0				
SCALE				
SHEET 1 OF 2				
DIST.				

REV. 1
NUMBER 1
SIZE CODE D
CS M8613-0-ABUS

B

A

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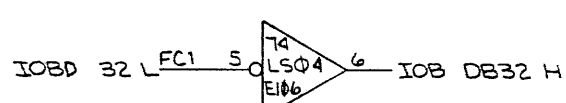
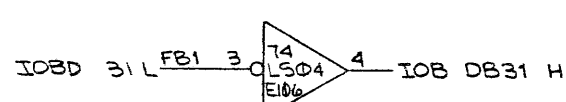
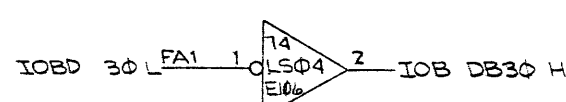
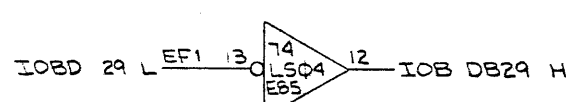
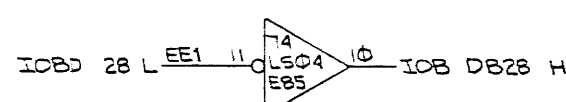
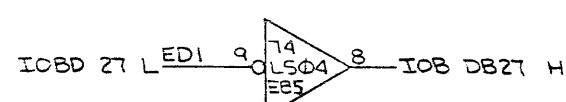
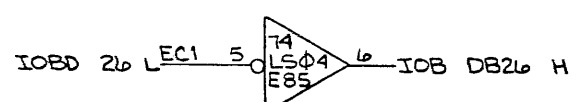
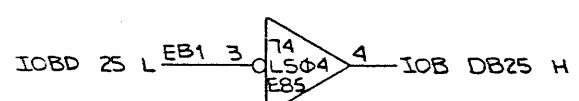
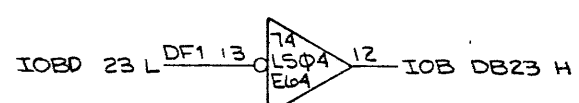
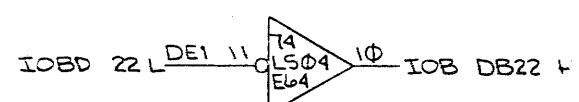
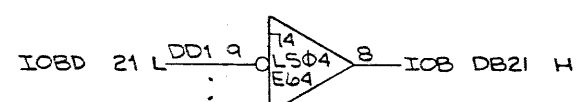
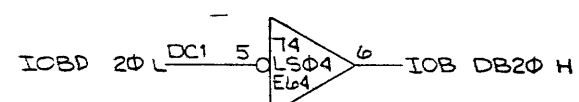
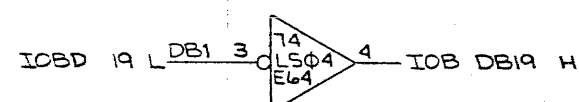
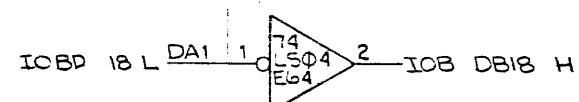
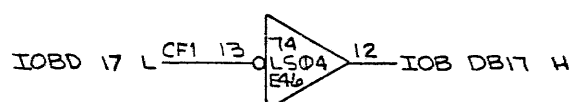
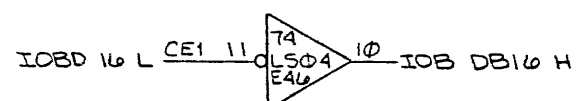
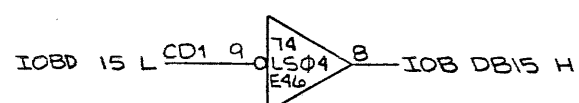
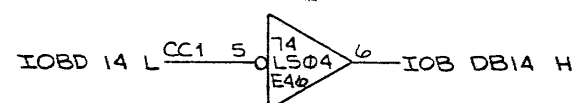
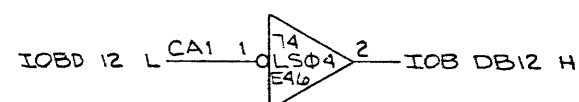
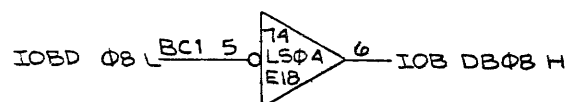
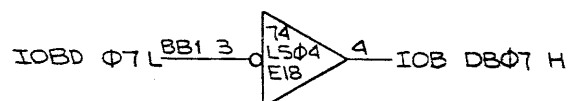
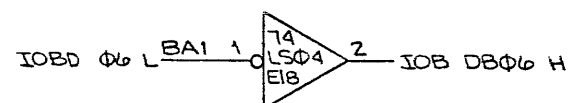
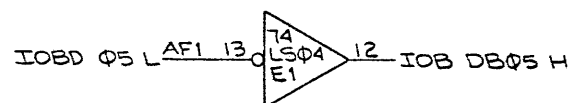
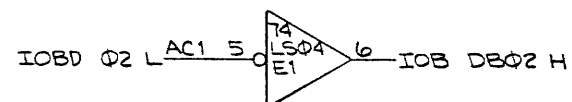
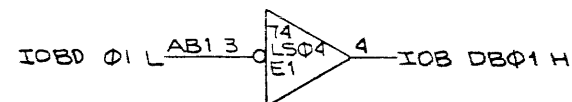
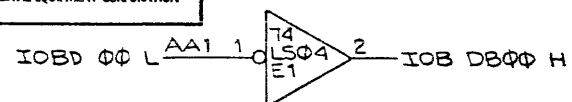
AH1, AL2, AM1, BH1, BL2, BM1
CH1, CL2, CM1, DH1, DL2, DM1
EH1, EL2, EM1, FH1, FL2, FM1

REVISIONS		
CHK	CHANGE NO.	REV.

										TITLE		SIZE CODE		NUMBER		REV.	
										ABUS Ø-35		D CS		M8613-Ø-ABUS			
SCALE										/ /		SHEET 2 OF 2		DIST.			

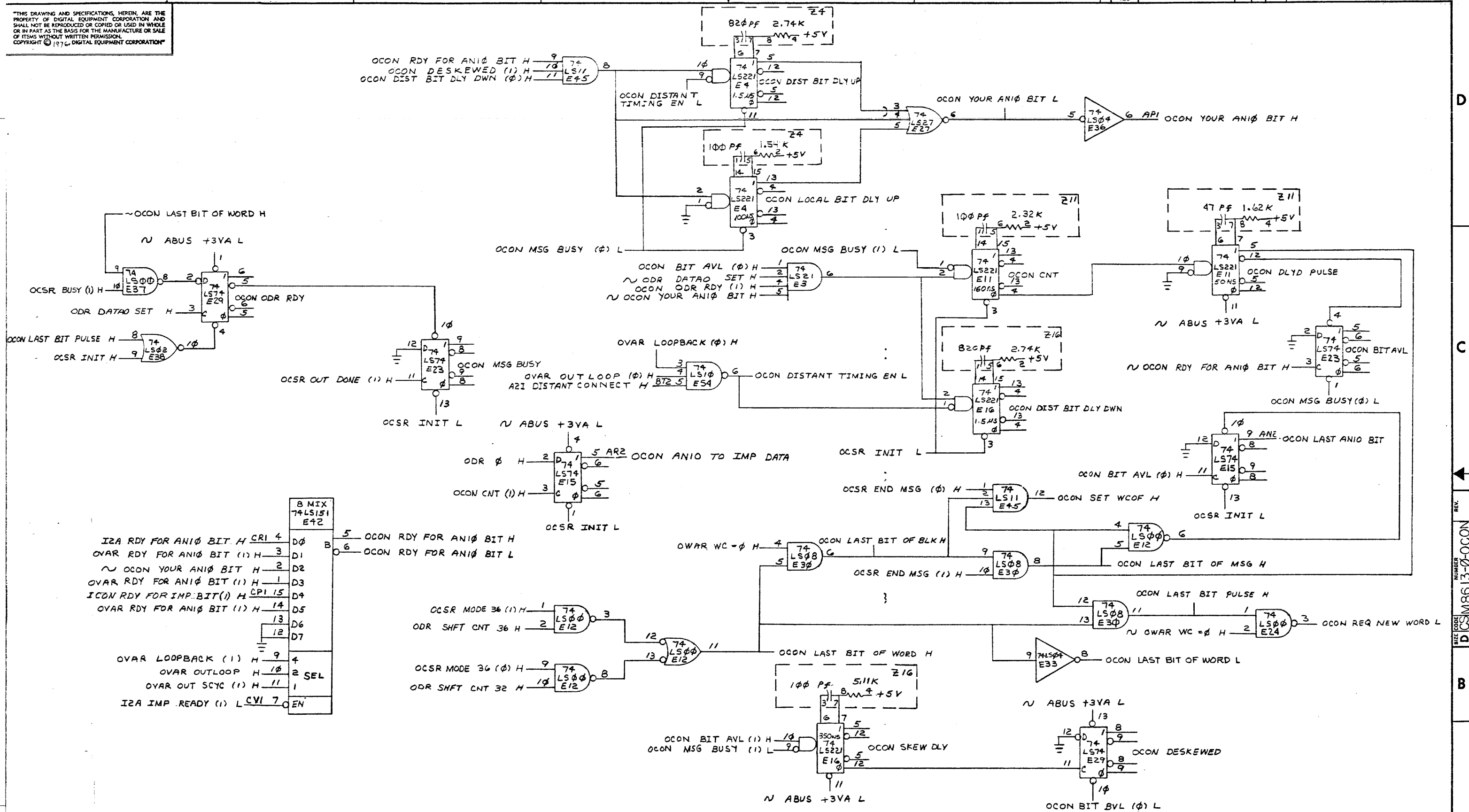
ABUS 18-35

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REV.	CHANGE NO.	DATE
CHK		

DRN: R. C. Carter	20 DEC 76	FIRST USED ON	AN10	digital		
CHK: D. W. Carter	5 JUL 77	TITLE	IO BUS DATA BUFFERED			
ENG: J. Anderson	5 JUL 77	SIZE	CODE		NUMBER	REV.
PROJ. ENG: J. Anderson	5 JUL 77	D	CS		M8613-0-IOB	
PROD. J. Anderson	5 JUL 77	SHEET	OF		DIST.	
NEXT HIGHER ASSY.						
B-DD-M8613-0						

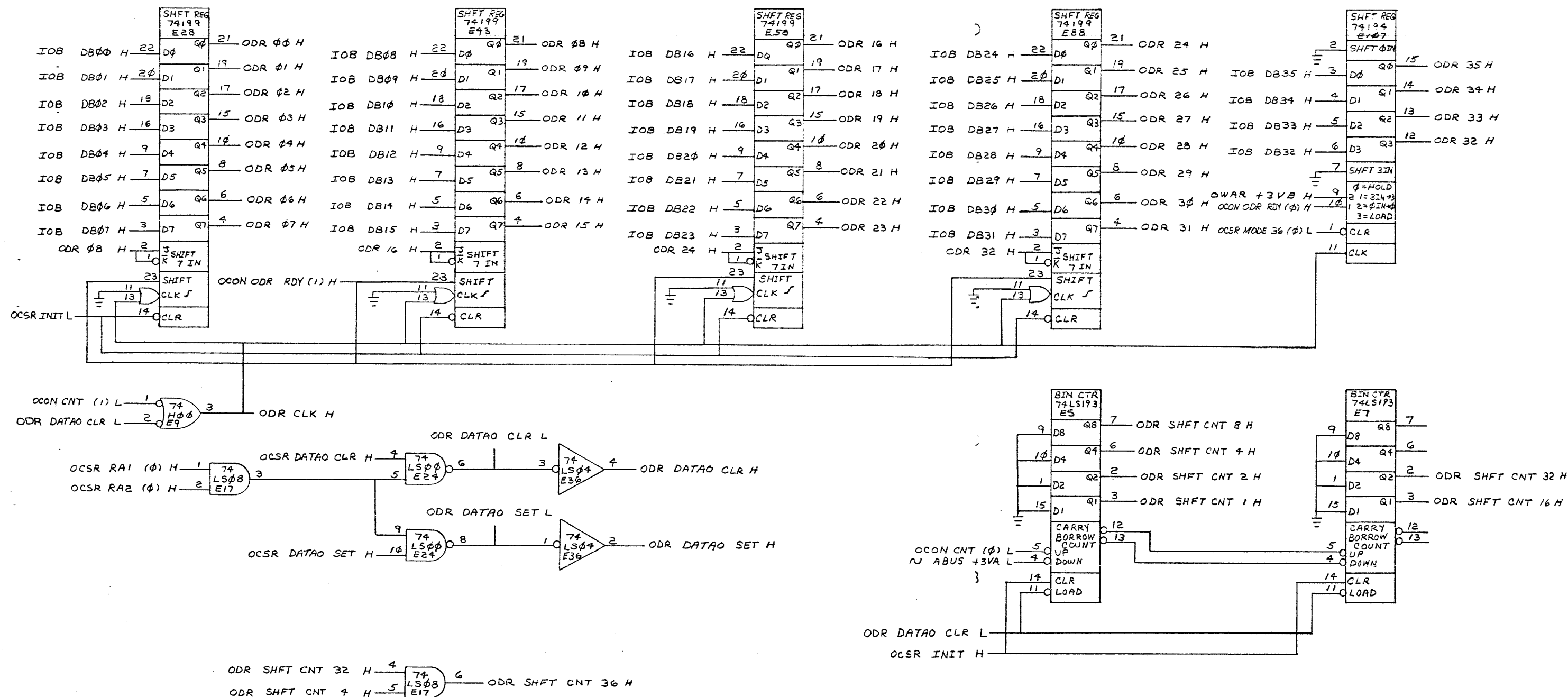


REV.	CHANGE NO.	CHK

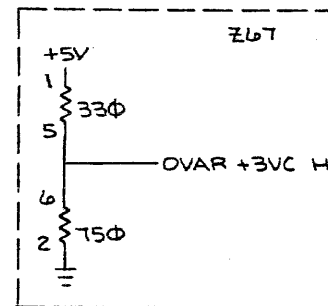
DRN. E. Wilson	8/1/76	FIRST USED ON	ANIØ
CHK'D R. G. Carter	8/1/76	TITLE	OUTPUT CONTROL
ENG. S. J. Anderson	8/1/76	SCALE	NONE
PROJ. ENG. J. Anderson	8/1/76	SHEET	1 OF 1
PROD. J. Anderson	8/1/76	DIST.	
NEXT HIGHER ASSY.			
E-00-M8613-0			

digital

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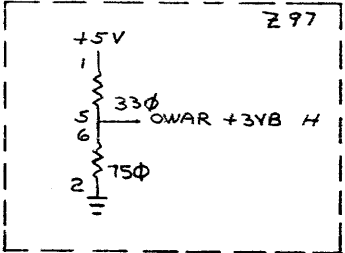


DRN. <i>Wilson</i>	9/15/76	FIRST USED ON	AN10	digital
CHK'D <i>R. Wilson</i>	9/15/76	TITLE	OUTPUT DATA REGISTER	
ENG. <i>R. Wilson</i>	9/15/76	PROJ. ENG. <i>R. Wilson</i>	6/15/77	
PROD. <i>R. Wilson</i>	6/15/77	NEXT HIGHER ASSY.		
B-DD-M8613-0	SCALE NONE	SHEET 1 OF 1	DIST.	



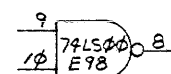
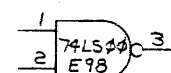
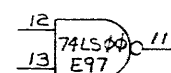
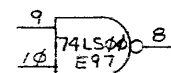
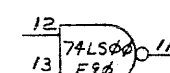
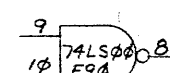
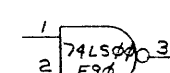
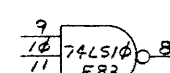
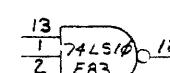
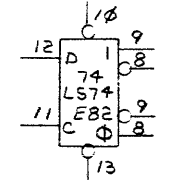
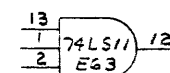
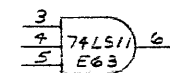
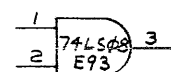
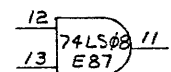
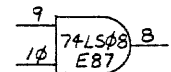
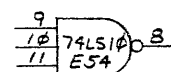
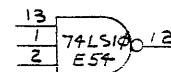
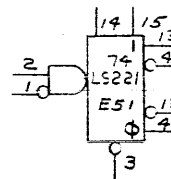
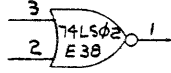
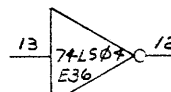
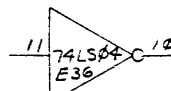
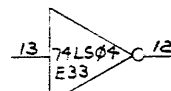
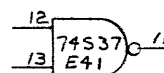
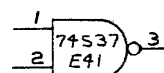
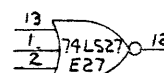
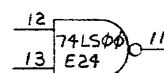
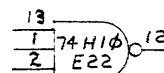
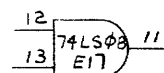
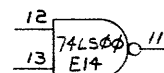
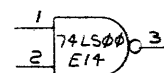
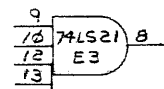
DRN. <i>S. Wilson</i>	3 Nov 76	FIRST USED ON		AN 10		digit	
CHK'D R/L Counter	<i>Wilson</i>	TITLE		OUTPUT VECTOR			
ENG. <i>V. Anderson</i>	6 JUL 77	PROJECT		ADDRESS REGISTER			
PROJ. ENG. <i>V. Anderson</i>	6 JUL 77	NEXT HIGHER ASSY.		B-DD-M8613-0			
PROD. <i>V. Anderson</i>	6 JUL 77	SIZE CODE		NUMBER		REV	
		D		CS M8613-0-OVAR			
SCALE NONE		DIST.					
SHEET 1 OF 1							

REVISIONS		REV.
CHK	CHANGE	



DRN. <i>Wilson</i>	3 NOV 76	FIRST USED ON	ANI		digital
CHK'D BY <i>Carver</i>	6 JUL 77	TITLE	OUTPUT WORDCOUNT		
ENG. <i>G. Andrews</i>	6 JUL 77	ADDRESS REGISTER			
PROJ. ENG. <i>G. Andrews</i>	6 JUL 77				
PROD. <i>G. Andrews</i>	6 JUL 77				
NEXT HIGHER ASSY.					
B-DD-M8613-0		SIZE	CODE	NUMBER	REV.
SCALE NONE		D	CS	M8613-0-OWAR	
SHEET 1 OF 1		DIST.			

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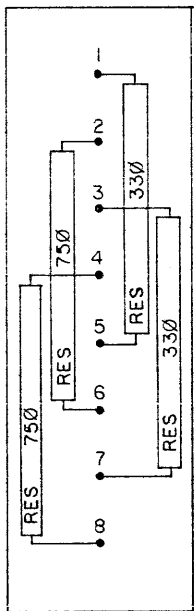


REV. 1
CHANGE NO. 1
CHK

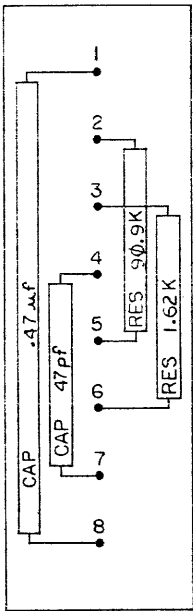
DEC FORM NO. 100 100-1

DRN. 20100000	20 Feb 77	FIRST USED ON	AN10	digital
CHK'D RWC	2 MAR 77	TITLE	SPARE I.C.'S	
ENG. J. J. J.	6 JUL 77			
PROJ. ENGR. J. J. J.	6 JUL 77			
PROD. J. J. J.	6 JUL 77			
NEXT HIGHER ASSY.				
B-00-M8613-0				
SCALE NONE	SIZE D	CODE CS	NUMBER M8613-0-SP	REV.
SHEET 1	OF 1	DIST.		

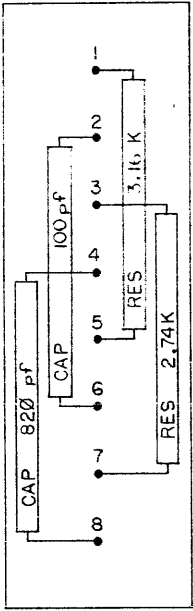
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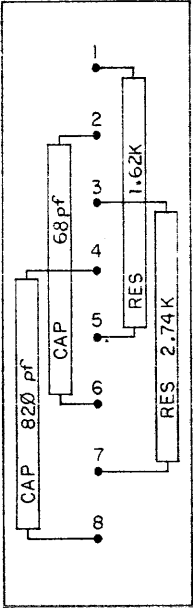
Z27, Z87



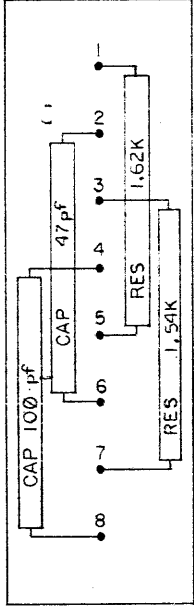
Z37



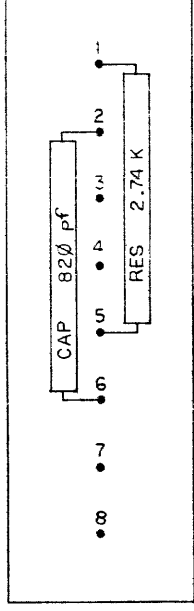
Z4



Z11



Z16, Z28



Z72

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		SIZE	CODE	NUMBER		REV.
WIRE WRAP MODULE		D	UA	M8614 - 0 - 0		
SCALE		SHEET 2 OF 2		DIST.		

DIGITAL EQUIPMENT CORPORATION PARTS LIST				QUANTITY / VARIATION												NOTES: 1. FOR LOCATIONS OF ITEMS 2 THRU 14 SEE SHT 2 OF UA			
DRAWN BY <i>E. Wilson</i> DATE <i>28 FEB 77</i>		CHECKED <i>RW Caunter</i> DATE <i>28 Feb 77</i>		SECTION 1															
ENG <i>D. Anderson</i> DATE <i>6 JUL 77</i>		PROD <i>R. Stroninski</i> DATE <i>6 JUL 77</i>		ISSUED SECTION 1														REF DESIGNATION	
ITEM NO.	DRAWING NO.	PART NO.	DESCRIPTION																
1	D-UA-W9302-0-0	W9302	WIRE WRAP MODULE (HEX)		1														
2		1000011	CAP 47PF 5%		3												SEE NOTE 1		
3		1000016	CAP 100PF 5%		3														
4		1000027	CAP 820PF 5%		3														
5		1010279	CAP .47MF 10%		1														
6		1300295	RES 330 5% 1/4W		4														
7		1301401	RES 750 5% 1/4W		4														
8		1304862	RES 1.62K 1% 1/4W		4														
9		1312480	RES 1.54K 1% 1/4W		2														
10		1303045	RES 3.16K 1% 1/4W		1														
11		1311320	RES 90.9K 1% 1/4W		1														
12		1000014	CAP 68pf 5%		1														
13		1304868	RES 2.74K 1% 1/4W		3														
14		1211323	SOCKET COMPONENT ADAPTOR		4														
15		1912803	IC DEC 74LS04		9											E1, E20, E40, E44, E45, E61, E79, E92, E100			
16		1912859	IC DEC 74LS258		1											E2			
17		1909057	IC DEC 74H10		1											E3			
18		1912858	IC DEC 74LS221		6											E4, E11, E16, E28, E37, E72			
19		1912824	IC DEC 74LS74		14											E5, E10, E15, E27, E35, E38, E47, E62, E73, E74, E77, E83, E97, E99			

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TITLE
WIRE WRAP MODULE

ASSY NO.
D-UA-M8614-0-0

SIZE
B

CODE
PL

NUMBER
M8614-0-0

REV.

SHEET 1 OF 3

INSERTION PARTS LIST DATA BASE REV

DIGITAL EQUIPMENT CORPORATION PARTS LIST				QUANTITY / VARIATION												NOTES:			
MAILED BY <i>F. Wilson</i> DATE <i>28 FEB 77</i>		CHECKED <i>RW Counter</i> DATE <i>28 Feb 77</i>		SECTION 1															
ENG <i>D. J. Anderson</i> DATE <i>6 JUL 77</i>		PROD <i>R. Stravinski</i> DATE <i>6 JUL 77</i>		ISSUED SECTION 1														REF DESIGNATION	
ITEM NO.	DRAWING NO.	PART NO.	DESCRIPTION																
20		1912646	IC DEC 74LS253	22													E6, E8, E13, E25, E31, E34, E36, E42, E46, E49, E51, E55, E64, E70, E71, E76, E85, E91, E94, E106, E109, E112		
21		1912801	IC DEC 74LS02	3													E7, E60, E98		
22		1912854	IC DEC 74LS193	11													E9, E14, E21, E50, E56, E65, E95, E101, E105, E108, E110		
23		1912799	IC DEC 74LS00	9													E12, E29, E53, E59, E80, E86, E96, E104, E107		
24		1912400	IC DEC 74H08	3													E18, E26, E30		
25		1912807	IC DEC 74LS10	2													E19, E90		
26		1912853	IC DEC 74LS175	1													E22		
27		1910842	IC DEC 74199	4													E23, E43, E67, E88		
28		1912808	IC DEC 74LS11	4													E24, E82, E84, E93		
29		1912805	IC DEC 74LS08	5													E32, E52, E68, E87, E89		
30		1912813	IC DEC 74LS27	1													E33		
31		1912746	IC DEC 74S37	1													E41		
32		1912863	IC DEC 74LS273	3													E57, E81, E102		
33		1912844	IC DEC 74LS151	1													E58		
34		1912845	IC DEC 74LS153	1													E66		
35		1910623	IC DEC 74194	1													E103		
36		1912697	IC DEC 74LS174	1													E113		
37	D-MD-7418759 - 0 - 0	7418759 - 9	HANDLE REWORK	1															
E.C. NO.																			
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								D-UA-M8614-0-0				B	PL	M8614-0-0					
								SHEET 2 OF 3				INSERTION PARTS LIST DATA BASE REV							

DIGITAL EQUIPMENT CORPORATION PARTS LIST

QUANTITY / VARIATION	
----------------------	--

NOTES:

MAILED BY *P. Wilson*
DATE *28 FEB 77*

CHECKED RW Counter
DATE 28 Feb 77

SECTION 1

ENG *D. J. Anderson*
DATE *6 JUL 77*

PROD	R Stasinski
DATE	6 JUL 77

ISSUED SECTION I

DRAWING NO.	PART NO.	DESCRIPTION
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DRAWING NO.

PART NO.

DESCRIPTION

REF DESIGNATION

	9006732	EYELET
--	---------	--------

9006732

EYELET

[illegible]

	9009255-01	LABEL REVISION
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9009255-01

LABEL REVISION

[illegible]

TITLE	WIRE WRAP MODULE
-------	------------------

TITLE	WIRE WRAP MODULE
-------	------------------

ASSY NO.	
D - UA - M8614 - Ø - Ø	
SHEET	3 OF 3

ASSY NO.	
D - UA - M8614 - Ø - Ø	
SHEET	3 OF 3

ASSY NO.	
D - UA - M8614 - Ø - Ø	
SHEET	3 OF 3

SIZE B	CODE PL
INSERTION PART	

SIZE B	CODE PL
INSERTION PART	

NUMBER
M8614-Ø-Ø

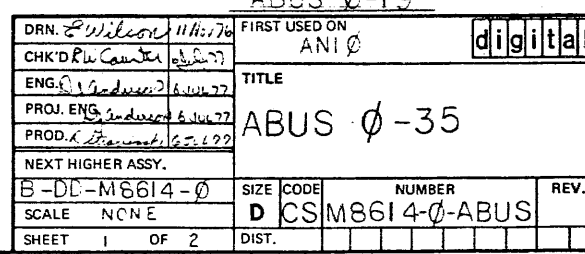
TS LIST DATA BASE REV

NUMBER
M8614-Ø-Ø

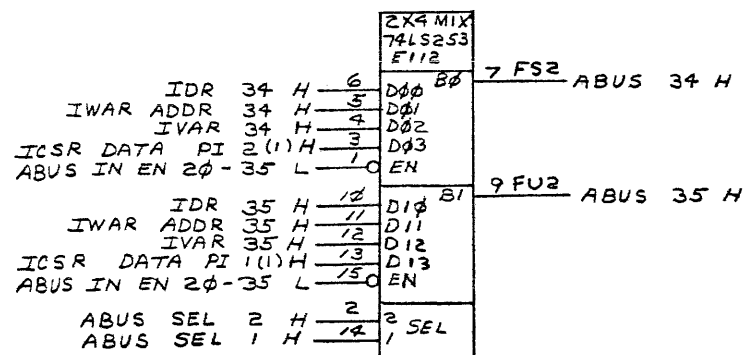
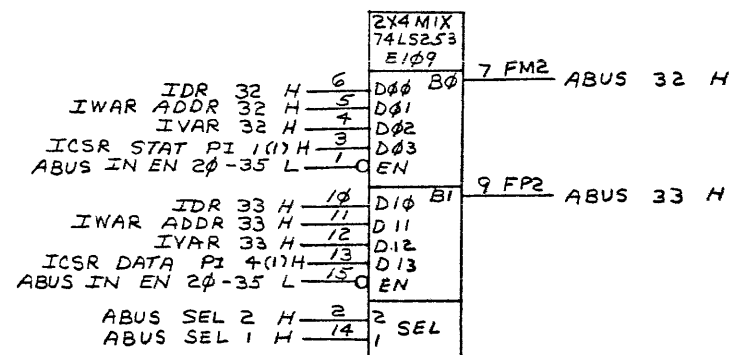
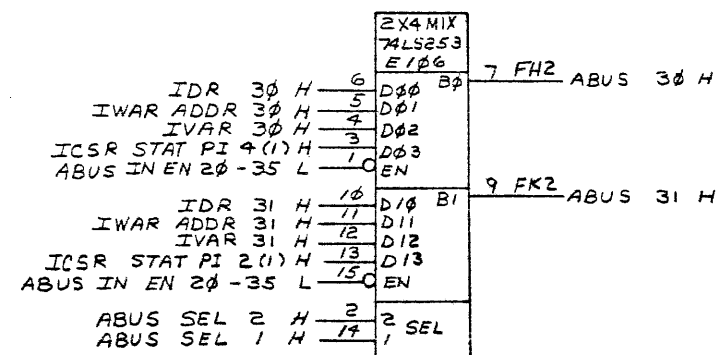
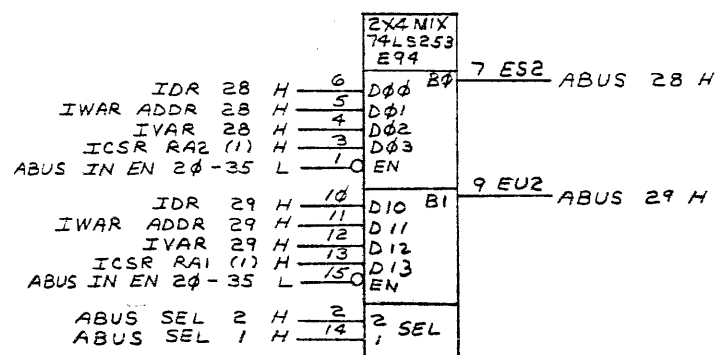
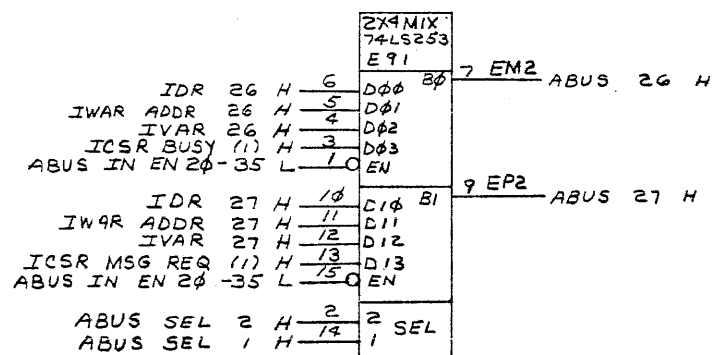
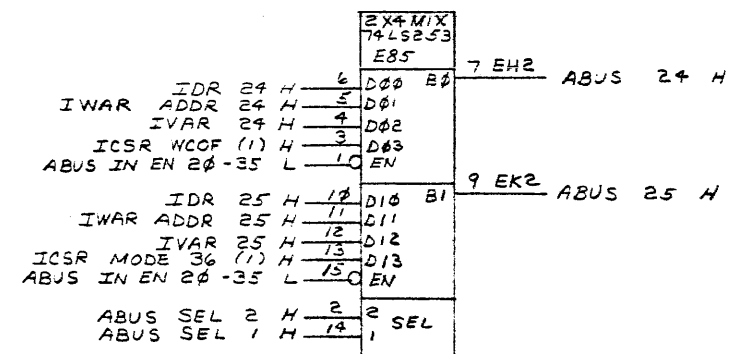
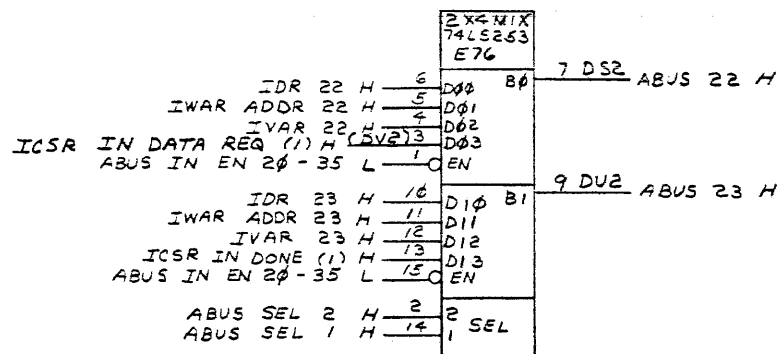
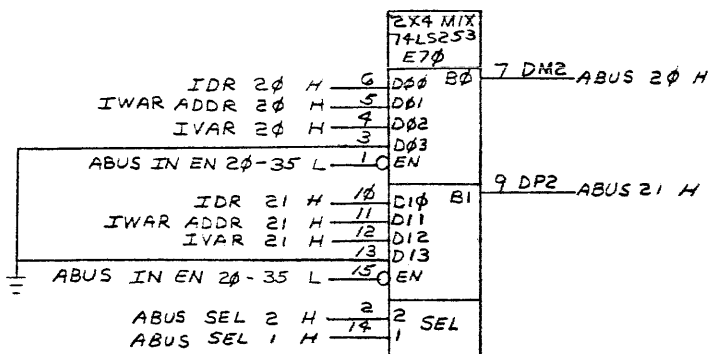
TS LIST DATA BASE REV

REV.	

SIZE B	CODE PL	NUMBER M8614-Ø-Ø
INSERTION PARTS LIST DATA BASE REV		



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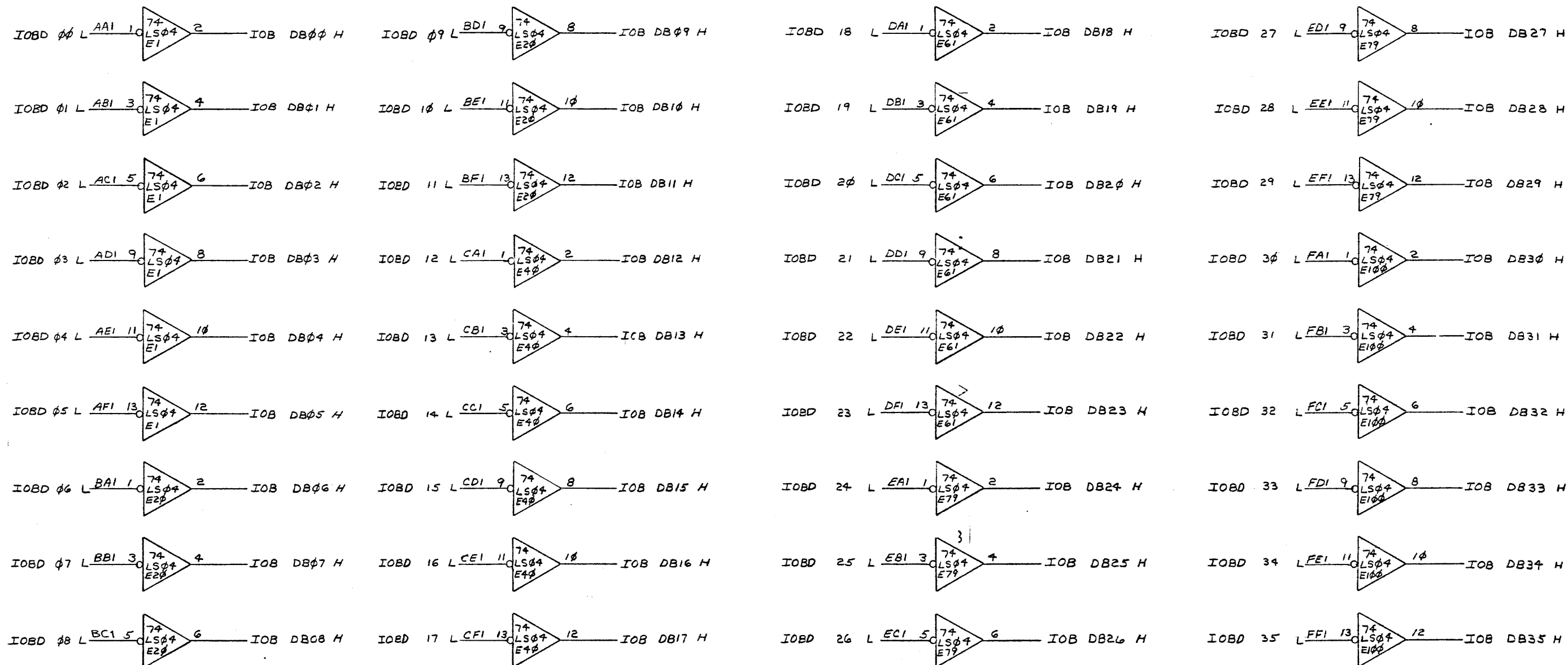


REVISIONS		
CHK	CHANGE NO.	REV.

ABUS 20-35

TITLE		SIZE	CODE	NUMBER		REV.
ABUS 0-35		D	CSM8614-0-ABUS			
SCALE NONE		SHEET 2 OF 2		DIST.		

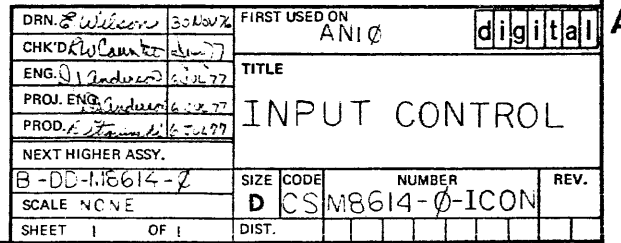
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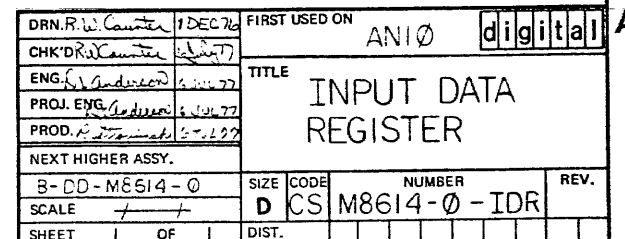
REVISIONS
CHK CHANGE NO. REV.

REC FORM NO. 102-0

DRN. <i>Wiberson</i>	12/10/76	FIRST USED ON	ANID	digital
CHK'D <i>R. J. Carter</i>	12/10/76	TITLE	IO BUS DATA BUFFERED	
ENG. <i>W. J. Anderson</i>	6/20/77	PROJ. ENG. <i>W. J. Anderson</i>	6/20/77	PROD. <i>W. J. Anderson</i>
NEXT HIGHER ASSY.				
E-DD-M8614-0		SIZE	CODE	NUMBER
SCALE NONE		D	CS	M8614-0-IOB
SHEET 1 OF 1		REV.		

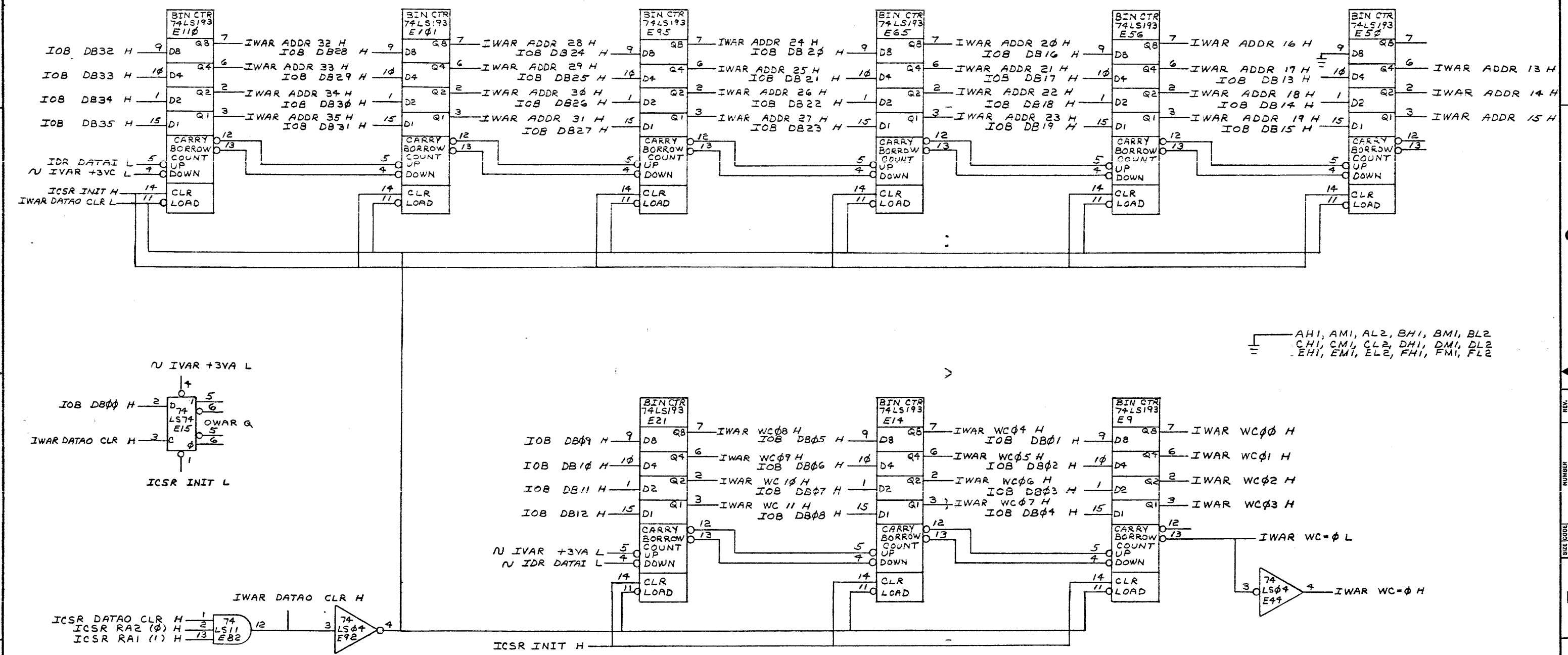


REVISIONS		
CHK	CHANGE NO.	REV.
	1	Pg 2



BA

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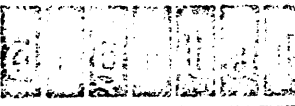


REV.	CHG.	NO.	DATE
1			

DRN. <i>E. Wilson</i>	<i>16107</i>	FIRST USED ON	<i>ANI 0</i>				digital
CHK'D BY <i>Wilson</i>	<i>6-10-77</i>	TITLE	INPUT WORDCOUNT ADDRESS REGISTER				
ENG. <i>E. Anderson</i>	<i>6-10-77</i>						
PROJ. ENG. <i>E. Anderson</i>	<i>6-10-77</i>						
PROD. <i>R. Thompson</i>	<i>6-10-77</i>						
NEXT HIGHER ASSY.							
<i>5-DD-ME614-0</i>		SIZE	CODE	NUMBER			REV.
SCALE NONE		D	CS	<i>M8614-0-IWAR</i>			
SHEET	I	OF	I	DIST.			

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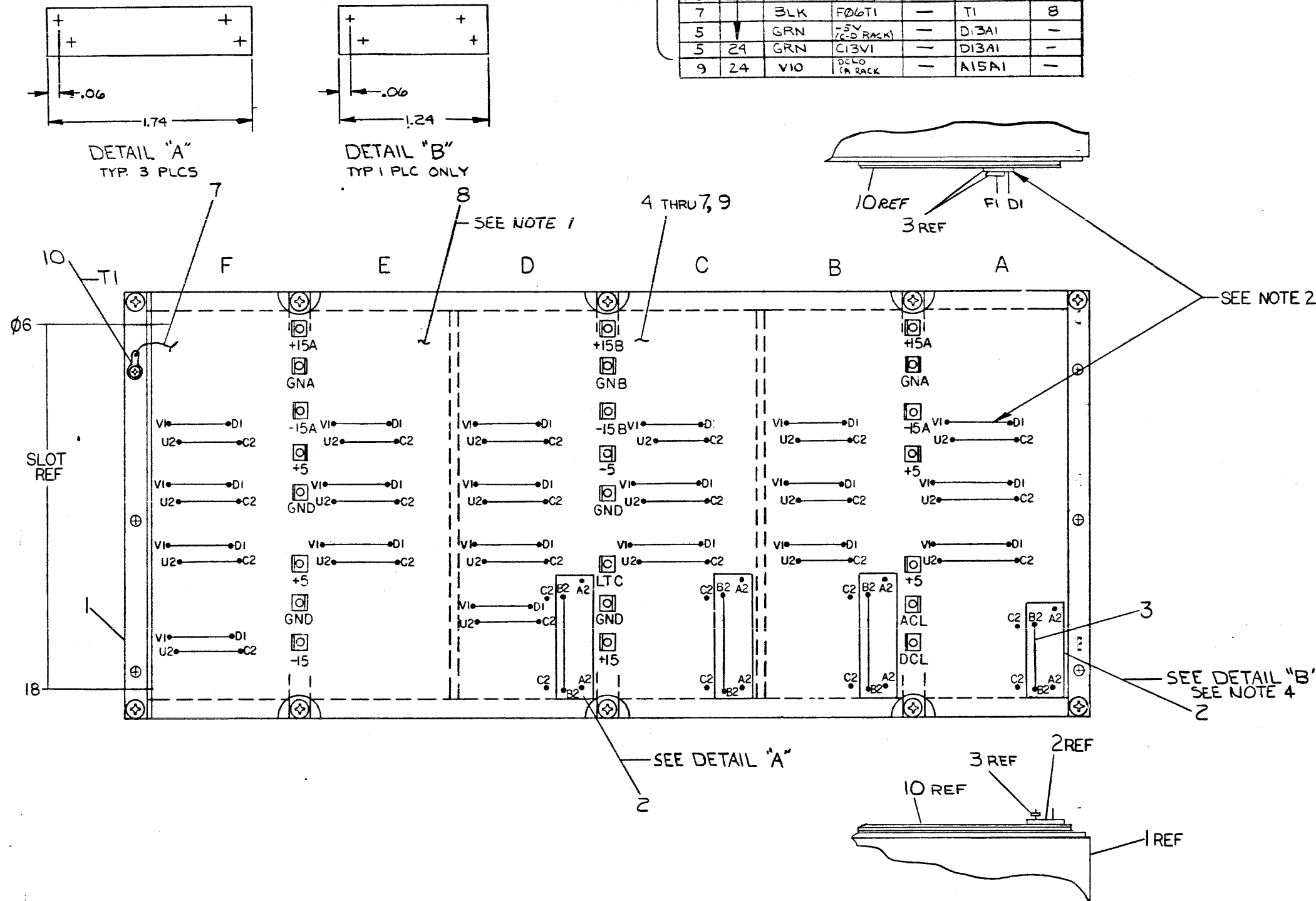
FIRST USED ON OPTION MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.										
ANIØ		PARTS LIST												
DRN. <i>M. Norman</i>	DATE 3 NOV 76	 EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS TITLE WIRE LIST ANIØ												
CHK'D. <i>J. Bissel</i>	DATE 10 NOV 76													
ENG. <i>D. Anderson</i>	DATE 29 JUN 77													
PROJ. ENG. <i>D. Anderson</i>	DATE 6 JUL 77													
PROD.	DATE 6 JUL 77													
NEXT HIGHER ASSEMBLY D-AD-7013480-0-0		SIZE CODE	NUMBER	REV.										
SCALE <i>1/1</i>		K WL	ANIØ - Ø - WL											
SHEET 1 OF 1		DIST. <table border="1"><tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr></table>												

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WIRE TABLE						
ITEM NO.	AWG	COLOR	FROM	WITH	TO	WITH
4	24	BLU	418B2	—	B18B2	—
4	24	BLU	C18D2	—	D18B2	—
4	24	BLU	D15B2	—	F15B2	—
4	24	BLU	B15B2	—	C15B2	—
6	24	YEL	ACLO (A RACK)	—	C13E1	—
7	24	BLK	F06T1	—	T1	8
5	24	GRN	-5V (C-D RACK)	—	D13A1	—
5	24	GRN	C13V1	—	D13A1	—
9	24	VIO	DCLO (A RACK)	—	A15A1	—

NOTES:

1. INSTALL ACETATE GROUND PLANE PROTECTION SHEET.
2. VERTICAL BUS STRIPS ARE TO BE PLACED FIRMLY AGAINST THE GROUND PLANE AND EVERY SECOND PIN SOLDERED BEGINNING WITH C2 (OR D1). THE SECOND STRIP IS TO BE PLACED FIRMLY AGAINST THE FIRST AND EVERY SECOND PIN SOLDERED BEGINNING WITH E2 (OR F1). BOTH LAYERS OF STRIPS SHOULD BE SOLDERED TOGETHER TO COMPLETE GROUND BUS.
3. WIRES IN WIRE TABLE TO BE ADDED AFTER MACHINE WIREWRAPPING IS COMPLETE.
4. INSULATOR STRIP GOES OVER PINS "A" AND "B" FOUR PLCS AS SHOWN. CUT STRIPS TO REQUIRED LENGTH PER DETAILS "A" AND "B".



ITEM NO.	DESCRIPTION	QTY	UNIT
1	TERMINAL SOLDER	9006773	10
A/R	24 AWG SOLID INS (VIO)	9107688-77	9
1	GND PLANE PROT SHT	D-1A-5513110-0-0	8
A/R	24 AWG SOLID INS (BLK)	9107688-00	7
A/R	24 AWG SOLID INS (YEL)	9107688-44	6
A/R	24 AWG SOLID INS (GRN)	9107688-55	5
A/R	24 AWG SOLID INS (BLU)	9107688-66	4
A/R	BUS STRIP	9008428	3
A/R	STRIP INSULATOR	7408967-1	2
1	BACKPLANE REWORK ASSY	D-AD-7013491-0-0	1

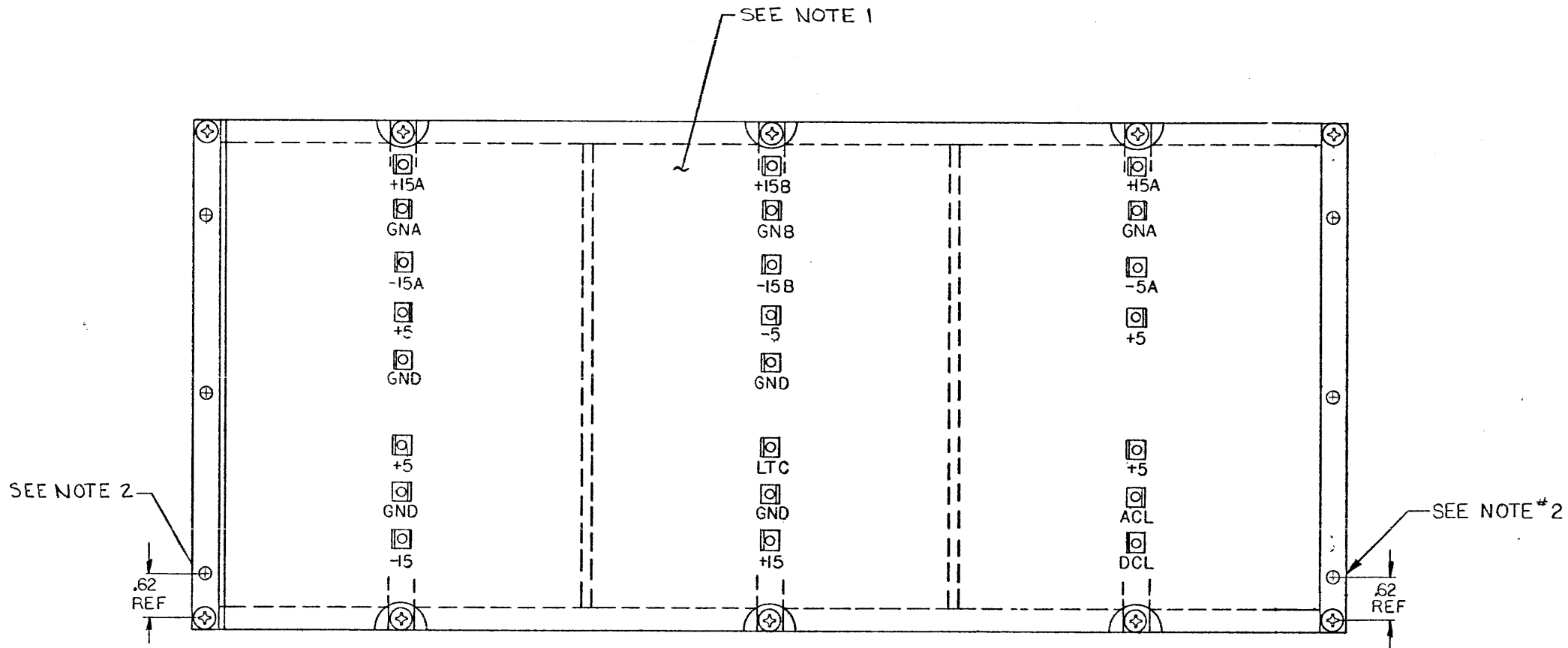
QUANTITY & VARIATION	DESCRIPTION	DWG. PART NO.	ITEM NO.
ANGLES: 0° 30'	CLASS OF ACCURACY	NOMINAL DIMENSION RANGE INCHES	
SURFACE QUALITY IN	CHECK ONE	OVER 0.2 TO 0.4	OVER 0.4 TO 0.6
MICROINCHES	PREFERRED	±.012	±.016

THIRD ANGLE PROJECTION	DRN: M. Hammond	DATE: 12/2/76	FIRST USED ON: ANIØ
REMOVE BURRS AND BREAK SHARP CORNERS	CHK'D: [Signature]	DATE: 12/2/76	TITLE: BACKPLANE ASSY ANIØ
DO NOT SCALE DWG	ENG: [Signature]	DATE: 12/2/76	PROD. [Signature]
MATERIAL: [Blank]	NEXT HIGHER ASSY:	SIZE: D	CODE: AD
FINISH: [Blank]	SCALE: 1/1	SHEET: 1	OF: 2

REVISIONS
CHANGE NO.
CHK
REV.

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NOTES:
1. MAKE FROM D-AD-7010952-0-0.
2. REVERSE BARS AS SHOWN.

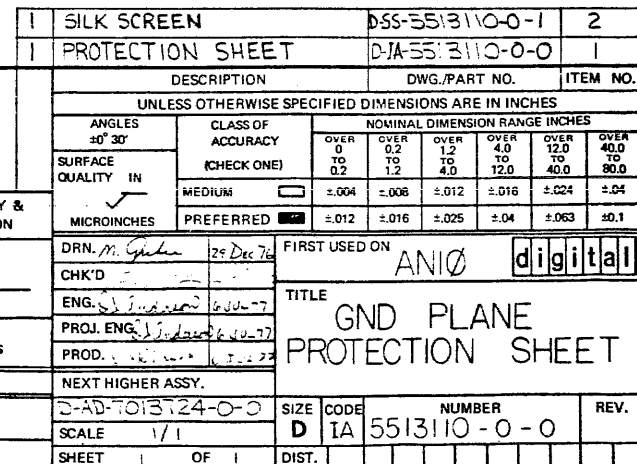


REVISIONS
CHANGE NO.
CHK

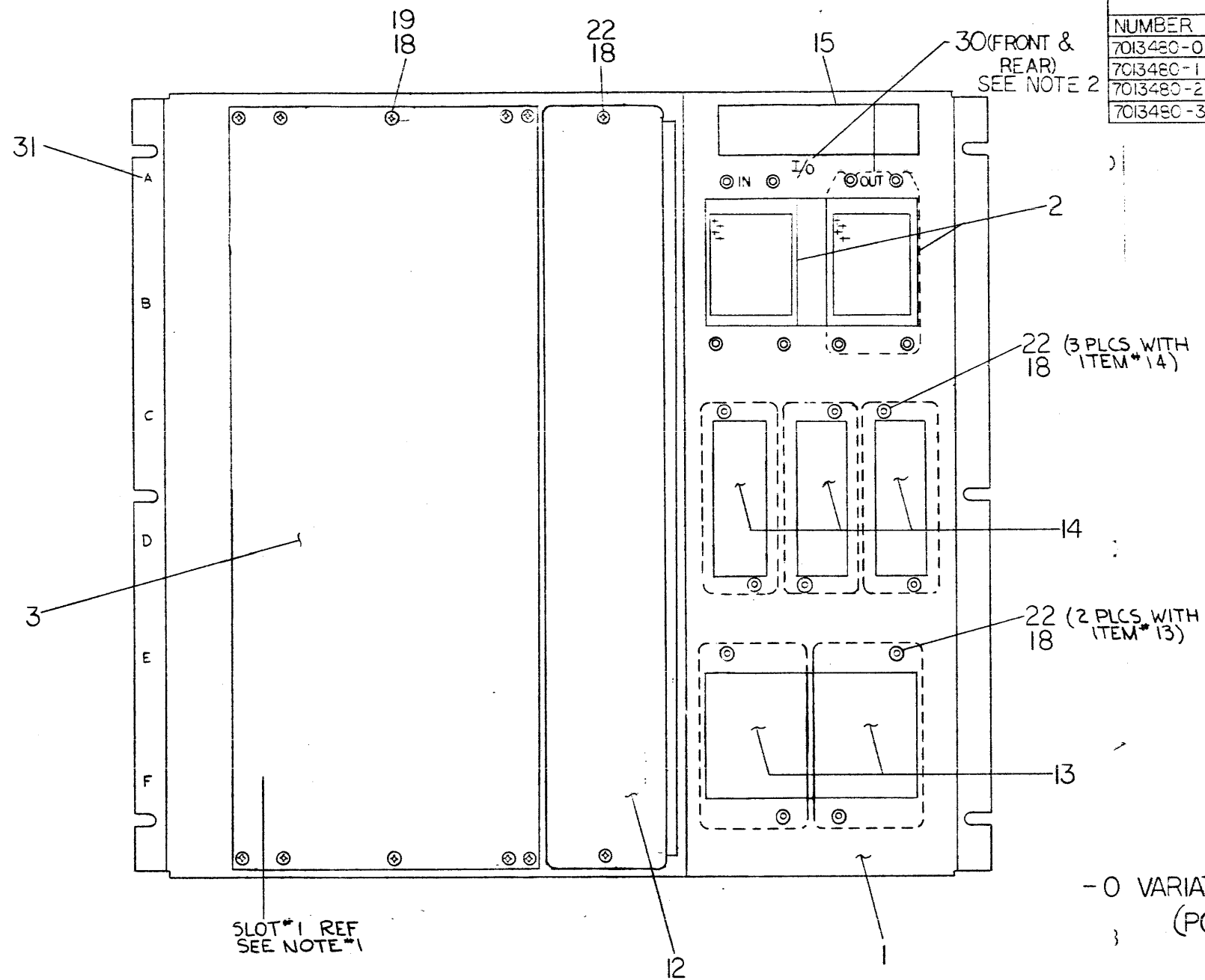
DEC FORM NO. 100-C

DESCRIPTION		DWG./PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES			
ANGLES ±0° 30'	CLASS OF ACCURACY	NOMINAL DIMENSION RANGE INCHES	
SURFACE QUALITY IN	(CHECK ONE)	OVER 0 TO 0.2	OVER 0.2 TO 1.2
MEDIUM	☒	OVER 1.2 TO 4.0	OVER 4.0 TO 12.0
MICROINCHES	PREFERRED ☐	OVER 12.0 TO 40.0	OVER 40.0 TO 80.0
THIRD ANGLE PROJECTION	DRN <i>M. Normand</i> 11/10/76	FIRST USED ON	
REMOVE BURRS AND BREAK SHARP CORNERS	CHK'D <i>B. Amos</i> 11/10/76	PCIØ <i>digital</i>	
DO NOT SCALE DWG	ENG. <i>S. J. Anderson</i> 11/10/76	TITLE	
MATERIAL	PROJ. ENG. <i>S. J. Anderson</i> 11/10/76	BACKPLANE REWORK ASSY (VT48/PCIØ)	
FINISH	PROD. <i>S. J. Anderson</i> 11/10/76	NEXT HIGHER ASSY.	
D-AD-7013918-0-0		SIZE	CODE
SCALE 1/1		D	AD
SHEET 1 OF 1		NUMBER	REV.
DIST.		AD 7013491 - 0 - 0	

NOTES
1. SILK SCREEN INK TO BE TRICLORETHYLENE
RESISTANT. DO NOT BAKE.



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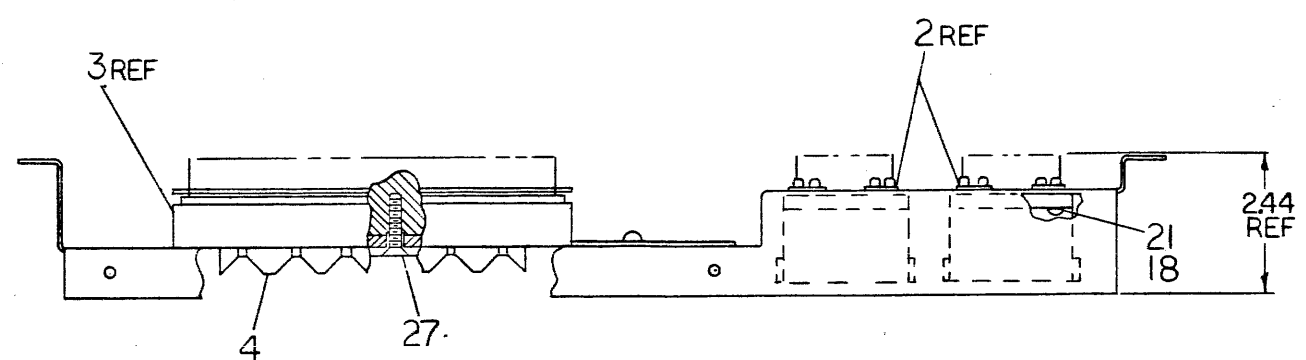
LEGEND		
NUMBER	VARIATION	SHEET #
7013480-0	PCIØ	1
7013480-1	DX2Ø	2
7013480-2	ANIØ	3
7013480-3	BACKPLANE ONLY	NOT SHOWN

HAND WIRE LIST				
ITEM NO	AWG	COLOR	FROM	TO
29	24	BLU	-15A TOP	BØ3B2
			AØ6B2	BØ6B2
			-15B CTR	DØ3B2
			CØ6B2	DØ6B2
			-15A BOT	FØ3B2
29	24	BLU	EØ6B2	FØ6B2

- NOTES
1. FOR SLOT LOCATIONS SEE SHEET #4.
 2. INSTALL DECALS ITEMS 30 AND 31 AS SHOWN. LABEL BOTH THE FRONT AND THE REAR OF THE LOGIC MOUNTING PLATE (ITEM 1) WHERE INDICATED.

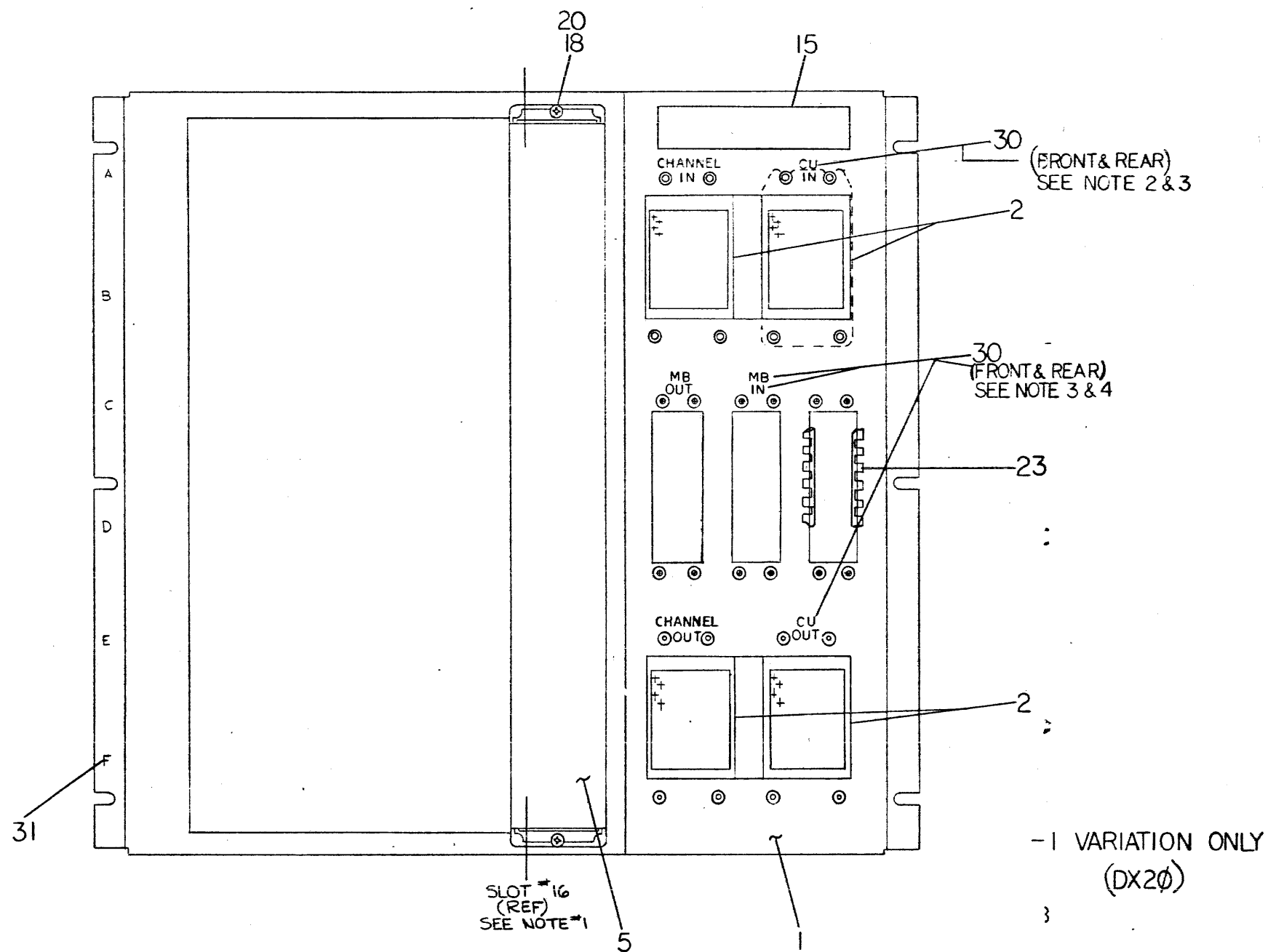
REV.	CHANGE NO.	REVISIONS
1		

CAUTION: OFF SHEETS PARTS LIST EXISTS
SEE A-PL-7013480-0-0



THIRD ANGLE PROJECTION		QUANTITY & VARIATION	
REMOVE BURRS AND BREAK SHARP CORNERS		DO NOT SCALE DWG	
MATERIAL		FINISH	
DRN. <i>[Signature]</i>		CHK'D <i>[Signature]</i>	
ENG. <i>[Signature]</i>		PROJ. ENG. <i>[Signature]</i>	
PRD. <i>[Signature]</i>		NEXT HIGHER ASSY.	
D-AD-7013479-0-0		SCALE	
SHEET 1 OF 5		DIST.	
FIRST USED ON		TITLE	
PCIØ		WIRED LOGIC MTG ASSY	
SIZE		NUMBER	
D AD		7013480-0-0	
REV.		REV.	

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NOTES:

1. FOR SLOT LOCATIONS SEE SHEET #4.
2. INSTALL DECALS ITEMS 30 AND 31 AS SHOWN. LABEL BOTH THE FRONT AND THE REAR OF THE LOGIC MTG PLATE (ITEM 1) WHERE INDICATED.
3. PREPARE "CU" AND "MB" DECALS USING TRANS-ARTYPE NO. T1191 FUTURA BOLD 12-PT RUB ON LETTERING OR EQUIVALENT.

HAND WIRE LIST

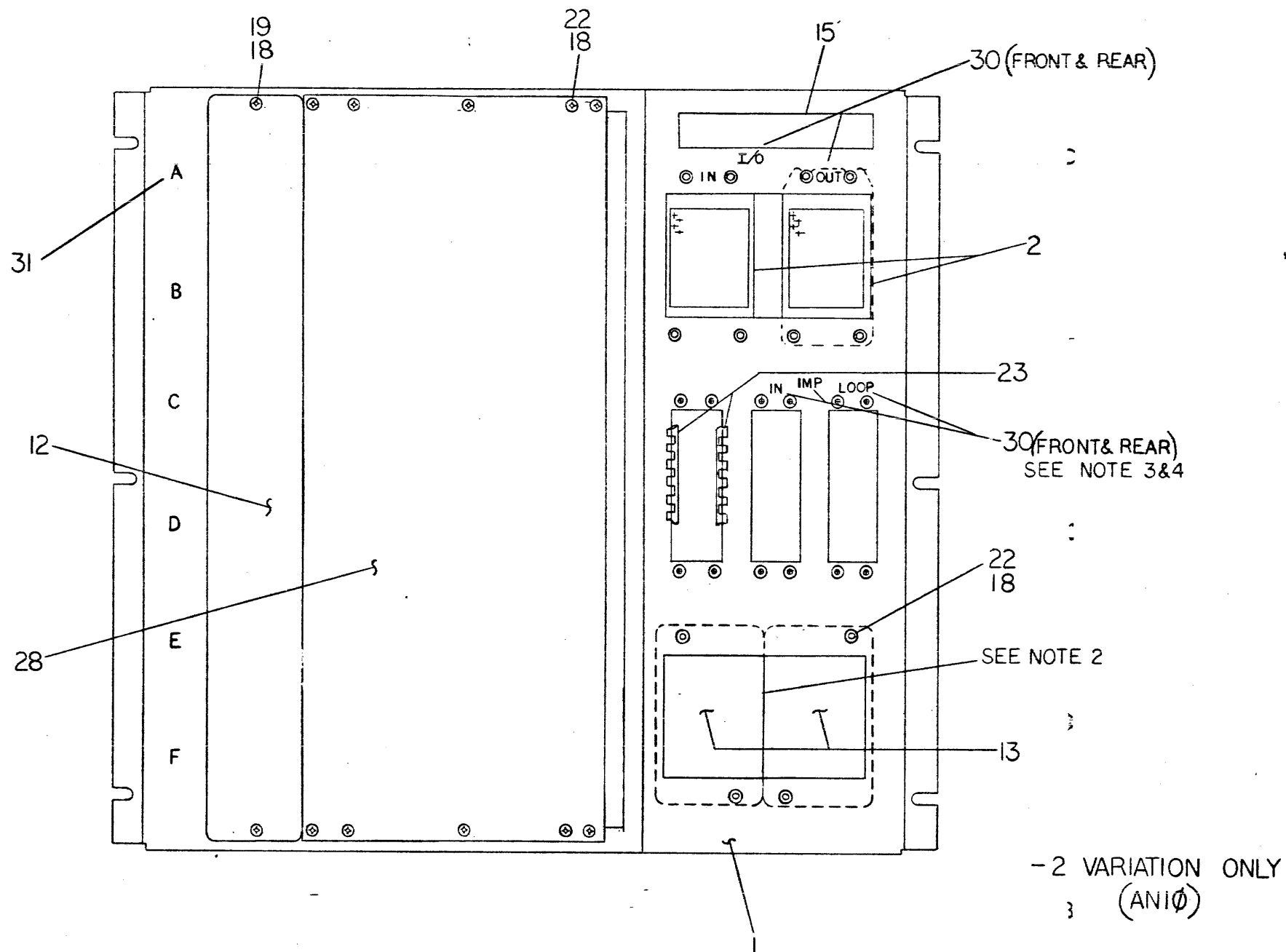
ITEM NO.	AWG	COLOR	FROM	TO
29	24	BLUE	-15 BOT	E17B2
33	24	VIO	DCLORIGHT	C16A1
32	24	YEL	AC60 RIGHT	F16B1

CAUTION OFF SHEETS PARTS LISTS EXIST
SEE A-PL-7013480-0-0

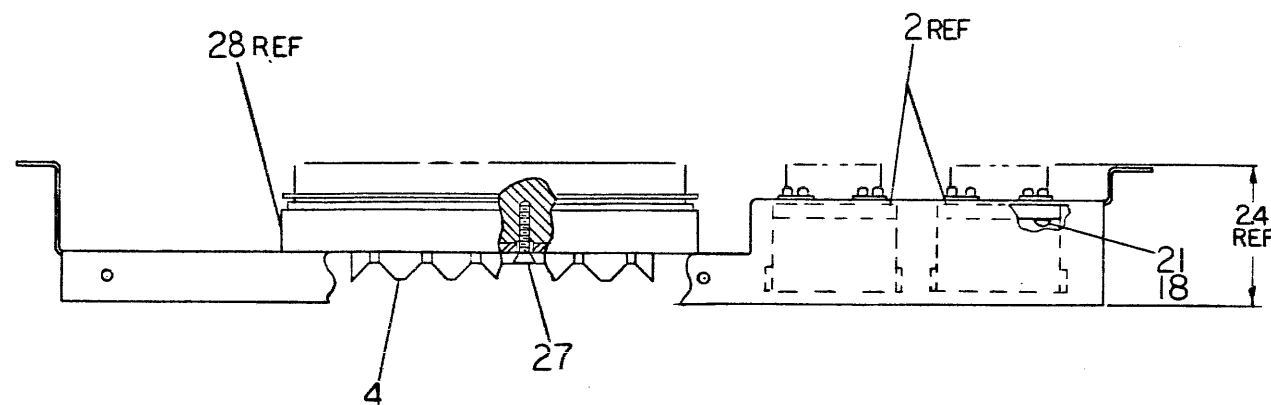
THIRD ANGLE PROJECTION 	QUANTITY & VARIATION	DESCRIPTION	DWG./PART NO.	ITEM NO.				
		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES						
		ANGLES ±0° 30'	CLASS OF ACCURACY (CHECK ONE)	NOMINAL DIMENSION RANGE INCHES				
		SURFACE QUALITY IN ✓ MICROINCHES	MEDIUM PREFERRED	OVER 0 TO 0.2 ±.004 ±.012	OVER 0.2 TO 1.2 ±.008 ±.016	OVER 1.2 TO 4.0 ±.012 ±.025	OVER 4.0 TO 12.0 ±.016 ±.04	OVER 12.0 TO 40.0 ±.024 ±.063
REMOVE BURRS AND BREAK SHARP CORNERS	DO NOT SCALE DWG	NEXT HIGHER ASSY.	FIRST USED ON DX2Ø			digital		
MATERIAL FINISH	SCALE	SHEET 2 OF 5	TITLE WIRED LOGIC MTG ASSY			REV.		
D-AD-7013479-0-0			SIZE CODE D AD			NUMBER 7013480-0-0		
DIST.			SHEET 2 OF 5			REV.		

REVISIONS
CHANGE NO.
CHK
REV.

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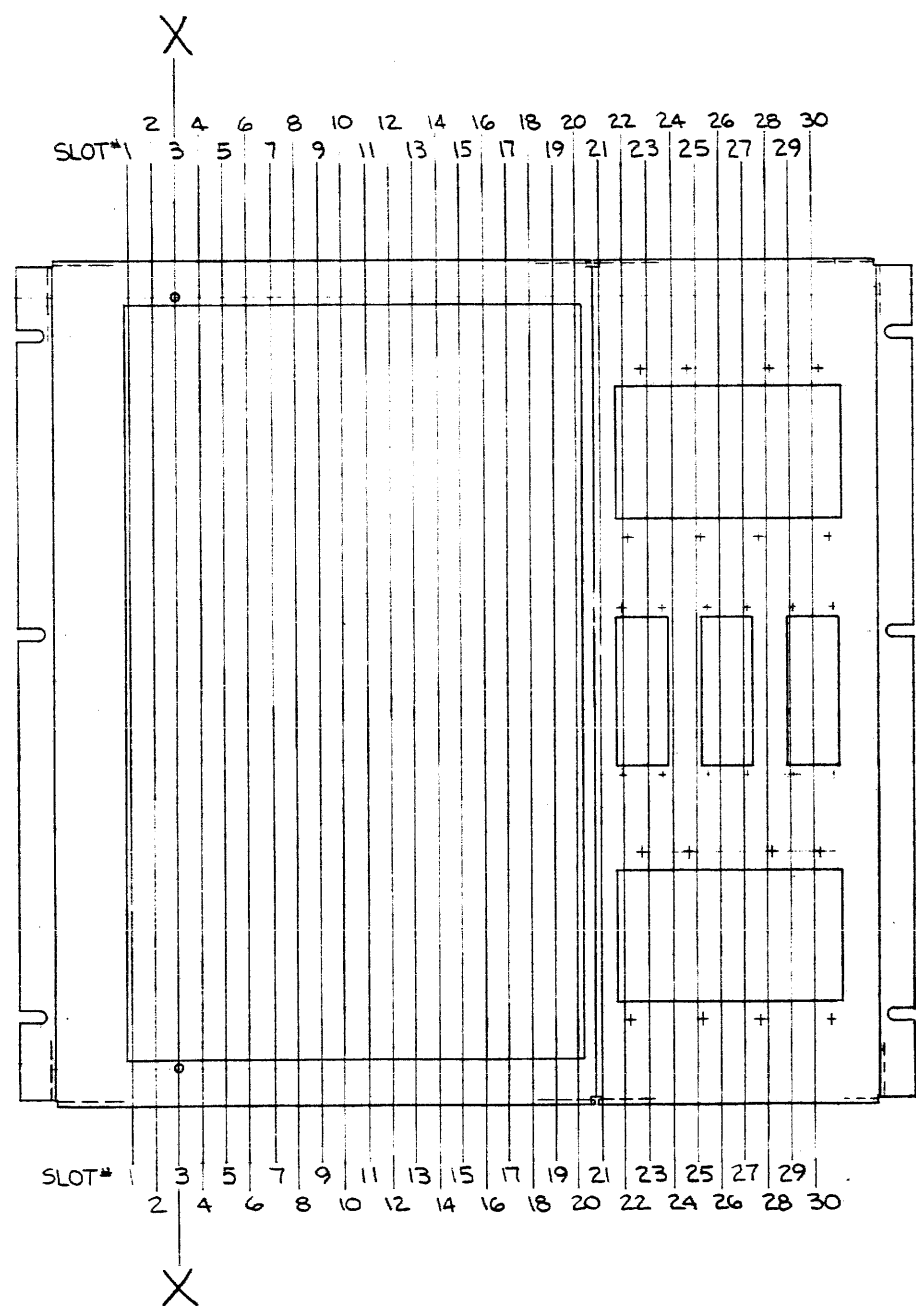
- NOTES
1. FOR SLOT LOCATIONS SEE SHEET 4.
 2. INSTALL ITEMS 13 AS SHOWN. POSITION SO THAT THE TWO ADJACENT PLATES TOUCH TO SEAL THE LOGIC MOUNTING PLATE CUT OUT.
 3. INSTALL DECALS ITEMS 30 AND 31 AS SHOWN. LABEL BOTH THE FRONT AND THE REAR OF THE LOGIC MTG. PLATE (ITEM 1) WHERE INDICATED.
 4. PREPARE 'IMP' AND 'LOOP' DECALS USING TRANS-ARTYPE NO. T11191 FUTURA BOLD 12-PT. RUB ON LETTERING OR EQUIVALENT.



CAUTION OFF SHEETS PARTS LIST EXIST
SEE A-PL-7013480-0-0

THIRD ANGLE PROJECTION		QUANTITY & VARIATION		DESCRIPTION		DWG./PART NO.		ITEM NO.	
REMOVE BURRS AND BREAK SHARP CORNERS		DO NOT SCALE DWG		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		CLASS OF ACCURACY (CHECK ONE)		NOMINAL DIMENSION RANGE INCHES	
MATERIAL		FINISH		ANGLER		SURFACE QUALITY		MICROINCHES	
SCALE		SHEET		OF		MEDIUM		PREFERRED	
DIST.		REV.		DRN		CHK'D		ENG.	
PROJ. ENG.		PROD.		NEXT HIGHER ASSY.		FIRST USED ON		TITLE	
D-AD-7013479-0-0		D-AD-7013480-0-0		WIRED LOGIC MTG ASSY		ANIO		digital	
D-AD-7013480-0-0		D-AD-7013480-0-0		D-AD-7013480-0-0		D-AD-7013480-0-0		D-AD-7013480-0-0	
D-AD-7013480-0-0		D-AD-7013480-0-0		D-AD-7013480-0-0		D-AD-7013480-0-0		D-AD-7013480-0-0	

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SLOT LOCATION		REMARKS		
SLOT #	X	PCIØ	DX2Ø	AN1Ø
1	-1.00	*	—	—
2	-1.50	*	*	—
3	0	*	*	—
4	+1.50	*	*	—
5	+1.00	*	*	—
6	+1.50	*	*	*
7	+2.00	*	*	*
8	+2.50	*	*	*
9	+3.00	*	*	*
10	+3.50	*	*	*
11	+4.00	*	*	*
12	+4.50	*	*	*
13	+5.00	*	*	*
14	+5.50	—	*	*
15	+6.00	—	—	*
16	+6.50	—	*	*
17	+7.00	—	*	*
18	+7.50	—	*	*
19	+8.00	—	*	—
20	+8.50	—	—	—
21	+9.00	—	—	—
22	+9.50	*	*	*
23	+10.00	*	*	*
24	+10.50	*	*	*
25	+11.00	*	*	*
26	+11.50	*	*	*
27	+12.00	*	*	*
28	+12.50	*	*	*
29	+13.00	*	*	*
30	+13.50	*	*	*

* INDICATES SLOT IS USED BY THE OPTION
— INDICATES SLOT IS NOT USED BY THE OPTION

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(SEE SHEET 4) SLOT REF 22 23 24 25 27 28 29 30

ROW
A
REF

ROW
B
REF

ROW
C
REF

ROW
D
REF

ROW
E
REF

ROW
F
REF

BACKPLANE PIN REF

BACKPLANE PIN REF

SEE DETAIL "A"

QL1

QL2

2REF

QL3

QL4

2REF

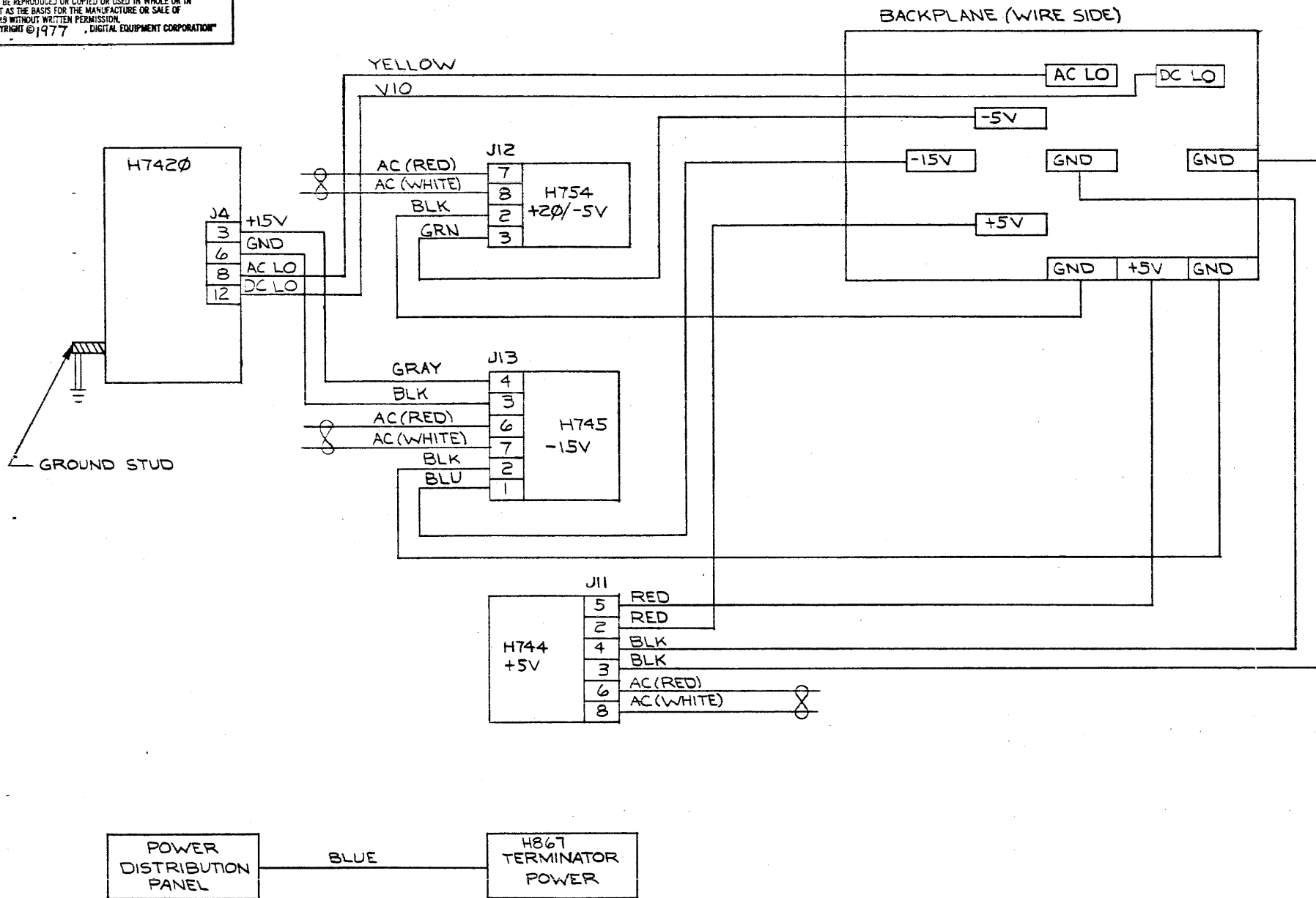
U	A1	B1	A2	B2
V	C	D	C	D
W	E	F	E	F
X	G	H	G	H
Y	I	J	I	J
Z	K	L	K	L
A	M	N	M	N
B	O	P	O	P
C	Q	R	Q	R
D	S	T	S	T
E	U	V	U	V
F	W	X	W	X
G	Y	Z	Y	Z
H	A	B	A	B
I	C	D	C	D
J	E	F	E	F
K	G	H	G	H
L	I	J	I	J

DETAIL "A"
SCALE: NONE
TYP 16 PLCS

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE WIRED LOGIC MTG ASSY		SIZE CODE D AD		NUMBER 7013480-0-0		REV.
SCALE	SHEET 5 OF 5	DIST.				

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THIRD ANGLE PROJECTION		DESCRIPTION		DWG./PART NO.		ITEM NO.	
QUANTITY & VARIATION		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		CLASS OF ACCURACY		NOMINAL DIMENSION RANGE INCHES	
ANGLES 30° 30'		CLASS OF ACCURACY (CHECK ONE)		OVER 0.2		OVER 0.2	
SURFACE QUALITY IN		MEDIUM		OVER 0.2		OVER 0.2	
MICROINCHES		PREFERRED		OVER 0.2		OVER 0.2	
DO NOT SCALE DWG		NEXT HIGHER ASSY.		OVER 0.2		OVER 0.2	
MATERIAL		E-UT-AN10-0-0		OVER 0.2		OVER 0.2	
FINISH		SCALE NONE		OVER 0.2		OVER 0.2	
SHEET		OF 1		OVER 0.2		OVER 0.2	
DIST.		OF 1		OVER 0.2		OVER 0.2	

DRN: M. R. R. 10/77
CHK'D: S. J. J. 10/77
ENG: S. J. J. 10/77
PROJ. ENG: S. J. J. 10/77
PROD. S. J. J. 10/77

FIRST USED ON
AN10

TITLE
WIRING DIAGRAM, AN10

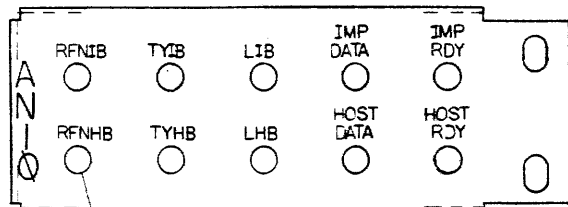
SIZE CODE
D IC AN10-0-PW

NUMBER
AN10-0-PW

REV.

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DETAIL "A"
SCALE: NONE



4 TYP 10 PLCS

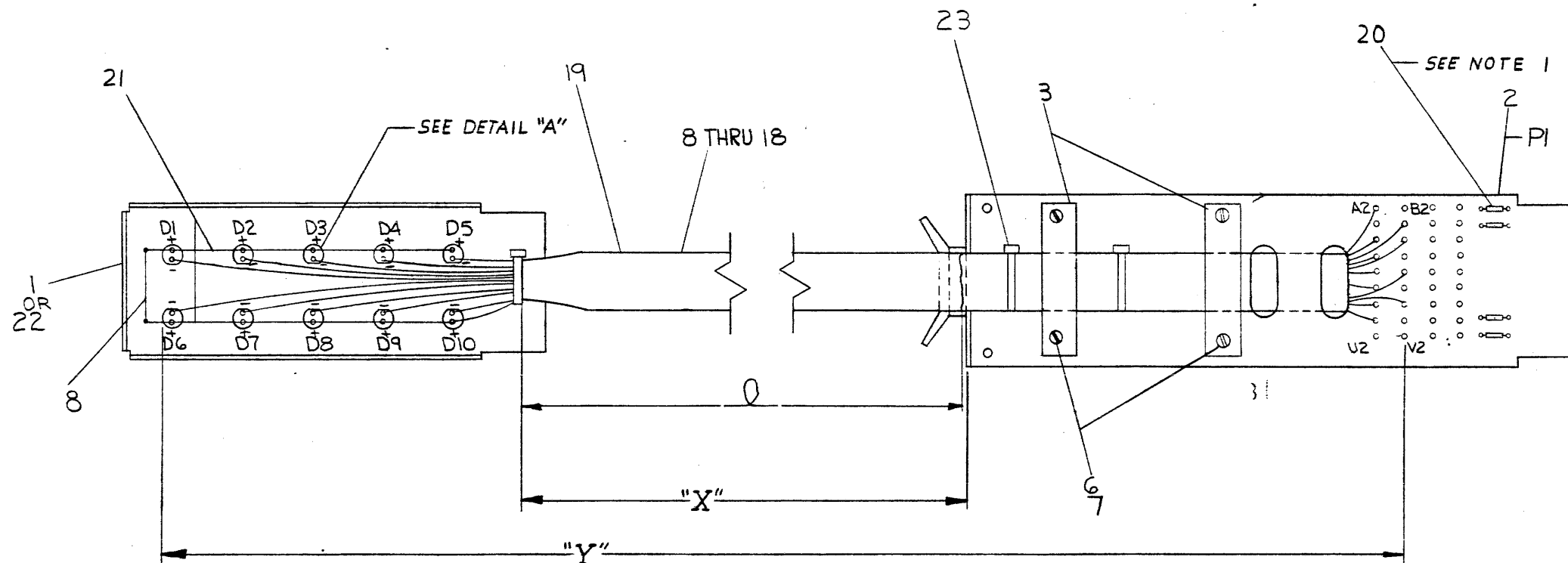
WIRE TABLE						
ITEM NO.	AWG	COLOR	FROM		TO	
			CONN	WITH	CONN	WITH
8	18	RED	D1-POS	SOLD	D6-POS	SOLD
9		GRY	D6-NEG		P1-D2	
16		WHT/BRN	D2-NEG		P1-E2	
17		WHT/BLU	D3-NEG		P1-F2	
10		VIOLET	D4-NEG		P1-H2	
11		WHT	D7-NEG		P1-K2	
12		YEL	D9-NEG		P1-L2	
13		BRN	D8-NEG		P1-M2	
18		WHT/YEL	D1-NEG		P1-P2	
14		ORN	D10-NEG		P1-R2	
15		BLU	D5-NEG		P1-S2	
8	18	RED	D10-POS	SOLD	P1-A2	SOLD

LEGEND

NUMBER	DIM "X"	DIM "Y" PRECUT
7013875-4D	4 FT 4 IN	5 FT 6 IN

NOTES:

1. REPLACE 10 Ω RESISTOR WITH INSULATED JUMPER ITEM #20.



3	3	CABLE TIES	9007880	23
1	-	BRACKET, MTG LED	D-IA-7418222-1-0	22
A/R	A/R	BUS STRIP	9008428	21
1	1	JUMPER, INSULATED	9009185	20
A/R	A/R	WRAP, SPIRAL .25	9107240	19
A/R	A/R	WIRE, #18AWG (WHT/YEL)	9107360-94	18
A/R	A/R	WIRE, #18AWG (WHT/BLU)	9107360-96	17
A/R	A/R	WIRE, #18AWG (WHT/BRN)	9107360-91	16
A/R	A/R	WIRE, STRD #18AWG (BLU)	9107360-66	15
A/R	A/R	WIRE, STRD #18AWG (ORN)	9107360-33	14
A/R	A/R	WIRE, STRD #18AWG (BRN)	9107360-11	13
A/R	A/R	WIRE, STRD #18AWG (YEL)	9107360-44	12
A/R	A/R	WIRE, STRD #18AWG (WHT)	9107360-99	11
A/R	A/R	WIRE, STRD #18AWG (VIOLET)	9107360-77	10
A/R	A/R	WIRE, STRD #18AWG (GRAY)	9107360-88	9
A/R	A/R	WIRE, STRD #18AWG (RED)	9107360-22	8
4	4	NUT, HEX #4 NYLON	9007992	7
4	4	SCR. RD. SLOTTED #4-40X.250	9006407-5	6
10	10	LED HOLDER	1210791	5
10	10	LED	1110324	4
2	2	CABLE, CLAMP	1202704	3
1	1	CABLE, CONN M957	D-CS-M957-0-1	2
-	1	BRACKET, MTG LED	D-MD-7418222-0-0	1

QUANTITY & VARIATION	DESCRIPTION	DWG./PART NO.	ITEM NO.
7013875-1	INDICATOR PANEL ASSY	D-IA-7418222-1-0	1
7013875-0	INDICATOR PANEL ASSY	D-IA-7418222-0-0	0
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES			
ANGLES: 90° 30'			
CLASS OF ACCURACY: 12.0 TO 40.0			
NOMINAL DIMENSION RANGE INCHES: 0.004 TO 0.008			
SURFACE QUALITY: IN			
MICROINCHES: 0.004 TO 0.008			
PREFERRED: 0.004 TO 0.008			
THIRD ANGLE PROJECTION			
REMOVE BURRS AND BREAK SHARP CORNERS			
DO NOT SCALE DWG			
MATERIAL: SEE PART LIST			
FINISH: 1			
FIRST USED ON: ANI0			
TITLE: INDICATOR PANEL ASSY			
SIZE: D			
CODE: IA			
NUMBER: 7013875-0-0			
REV.:			

REVISIONS
CHANGE NO.
CHK

8

7

6

5

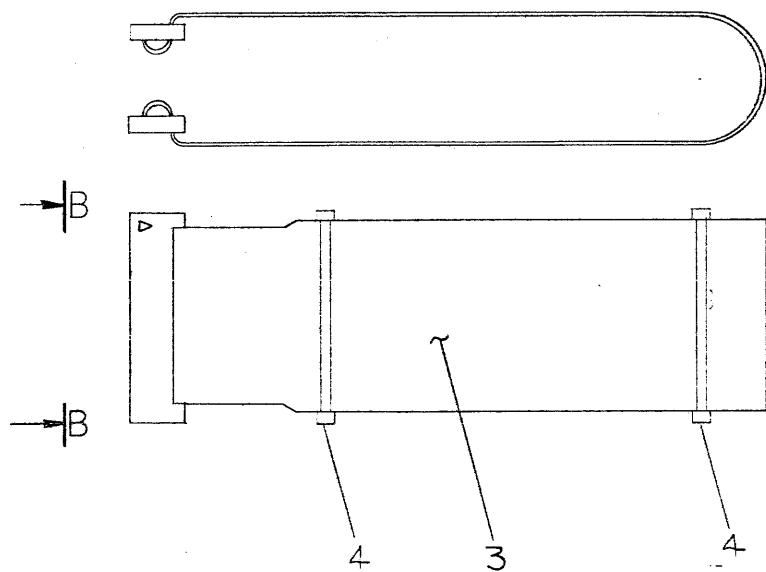
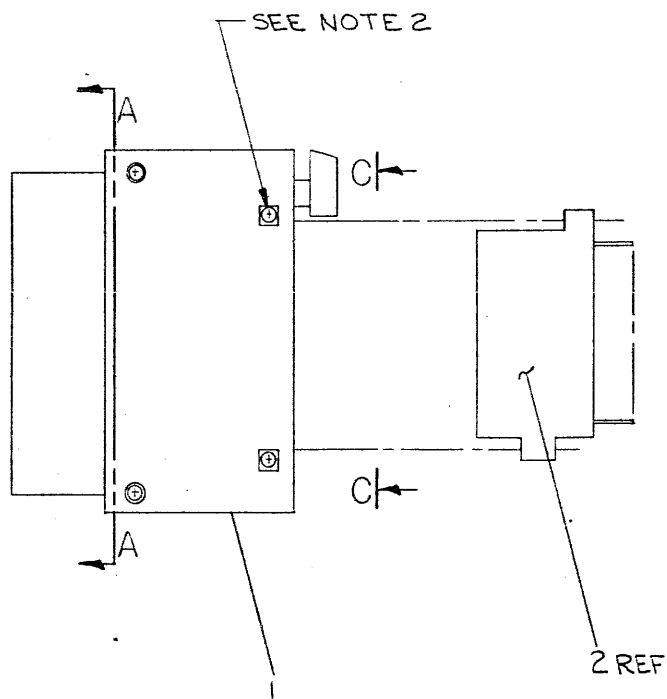
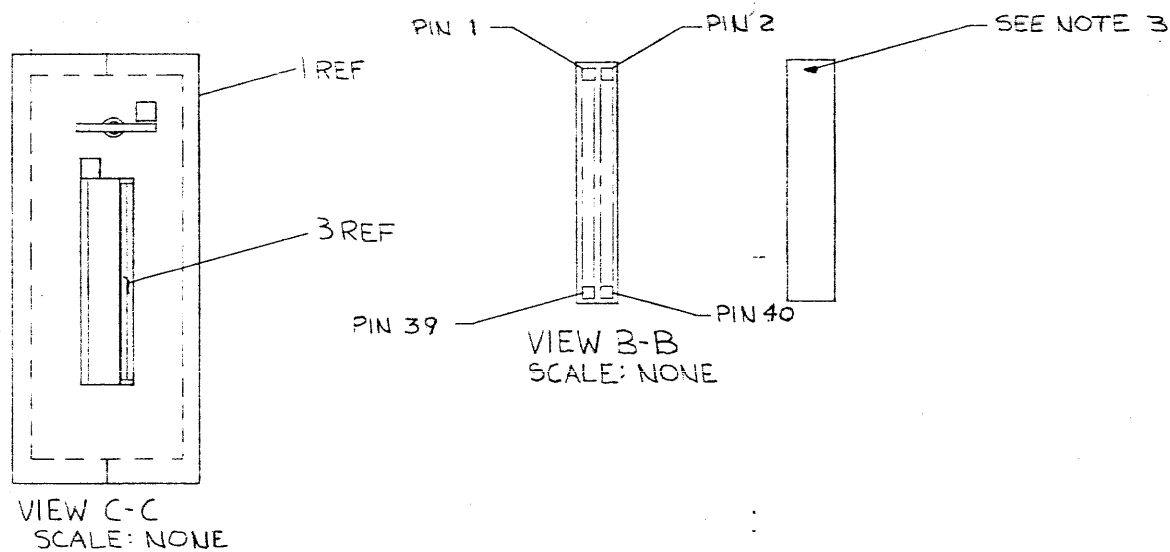
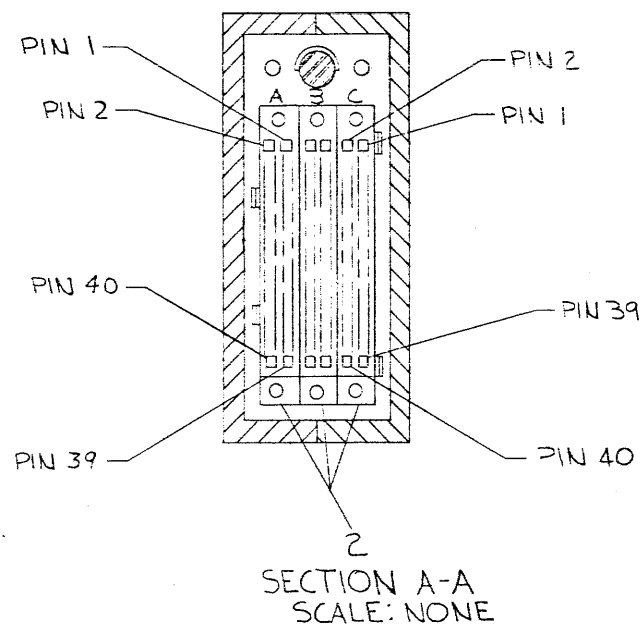
4

3

2

1

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NOTES:

1. IF ITEM #3 HAS "BERG" TYPE CONNECTORS CUT BACK PLASTIC PIECES SHOWN IN FIG 1.
2. CUT .25" FROM THE TWO #4-40X1.0 LG. SELF-TAPPING SCREW (PART OF ITEM #1).
3. ARROW ON BC06R CONNECTOR GO TO PIN1 AND PIN2 ON TERMINATOR PLUG ASSY.
4. COMPLETE ASSY OF ITEM #1 (INSERTS PROVIDED IN ITEM #1 ARE NOT TO BE USED)

2	CABLE TIES	9007880	4
1	BC06R I/O CABLE (1FT)	D-UA-BC06R-1-0	3
3	TERMINATOR PLUG ASSY	1211591-31	2
1	PLUG KIT, FLAT CABLE	1211591-40	1

			DESCRIPTION		DWG./PART NO.		ITEM NO.		
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES									
ANGLES ±0° 30'			CLASS OF ACCURACY (CHECK ONE)	NOMINAL DIMENSION RANGE INCHES					
				OVER 0 TO 0.2	OVER 0.2 TO 1.2	OVER 1.2 TO 4.0	OVER 4.0 TO 12.0	OVER 12.0 TO 40.0	OVER 40.0 TO 80.0
SURFACE QUALITY IN			MEDIUM ☐	±.004	±.008	±.012	±.016	±.024	±.04
QUANTITY & VARIATION			PREFERRED ☐	±.012	±.016	±.025	±.04	±.063	±.1
			MICROINCHES						

THIRD ANGLE PROJECTION	DR: <i>[Signature]</i>	FIRST USED ON	ANIO
CHK'D	ENG: <i>[Signature]</i>	TITLE	CONN, ANIO LOCAL LOOPBACK
REMOVE BURRS AND BREAK SHARP CORNERS	PROJ. ENG: <i>[Signature]</i>		
DO NOT SCALE DWG	PROD: <i>[Signature]</i>		
MATERIAL	E-UA-ANIO-0-0	SIZE	D
FINISH	SEE PARTS LIST	CODE	AD
		NUMBER	7013963-0-0
		REV.	
		SHEET	1 OF 2
		DIST.	

REVISIONS
CHANGE NO.
CHK

DEC FORM NO.
DMD 100-6

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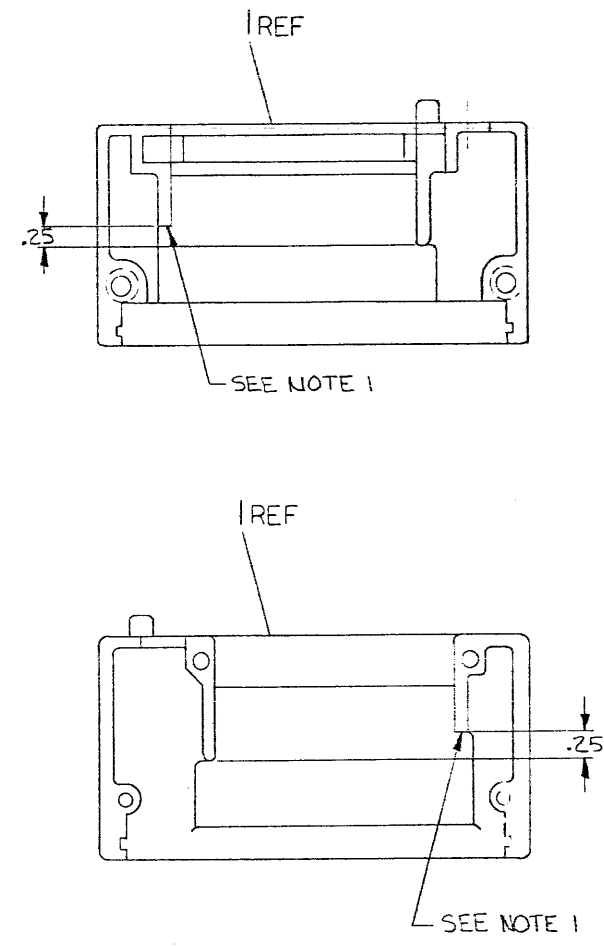


FIG. 1

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE CONNECTOR, ANIØ		SIZE CODE D AD	NUMBER 7013963 - 0 - 0	REV.
SCALE 1	SHEET 2	OF 2	DIST.	

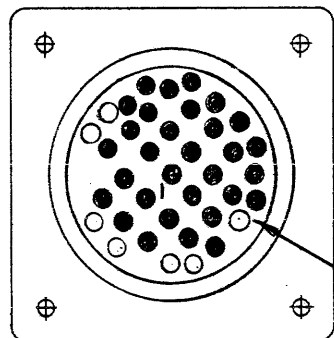
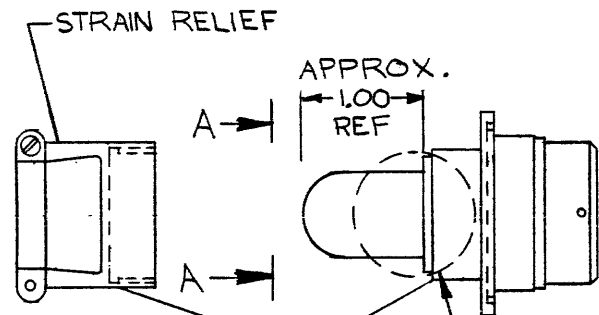
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WIRE TABLE

ITEM NO	DESCRIPTION		FROM		TO		REMARKS
	AWG	COLOR	CONN	WITH	CONN	WITH	
1	22	BLK	PI-1	ITEM#2	PI-21	ITEM#2	
↑	↑	↑	PI-2	↑	PI-22	↑	
			PI-3		PI-23		
			PI-4		PI-24		
			PI-5		PI-19		
			PI-6		PI-20		
			PI-7		PI-17		
			PI-8		PI-18		
			PI-9		PI-15		
			PI-10		PI-16		
↓	↓	↓	PI-11	↓	PI-13	↓	
1	22	BLK	PI-12	ITEM#2	PI-14	ITEM#2	

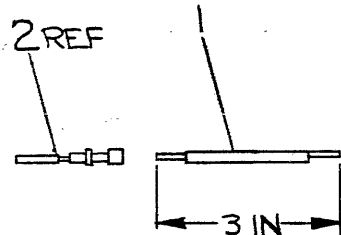
NOTES:

1. SEE FACE OF ACTUAL CONN FOR NUMBERING & EXACT LOCATION OF CONNECTOR HOLES.



VIEW A-A
SCALE: 2/1

SEE DETAIL A



DETAIL "A"
SCALE: NONE

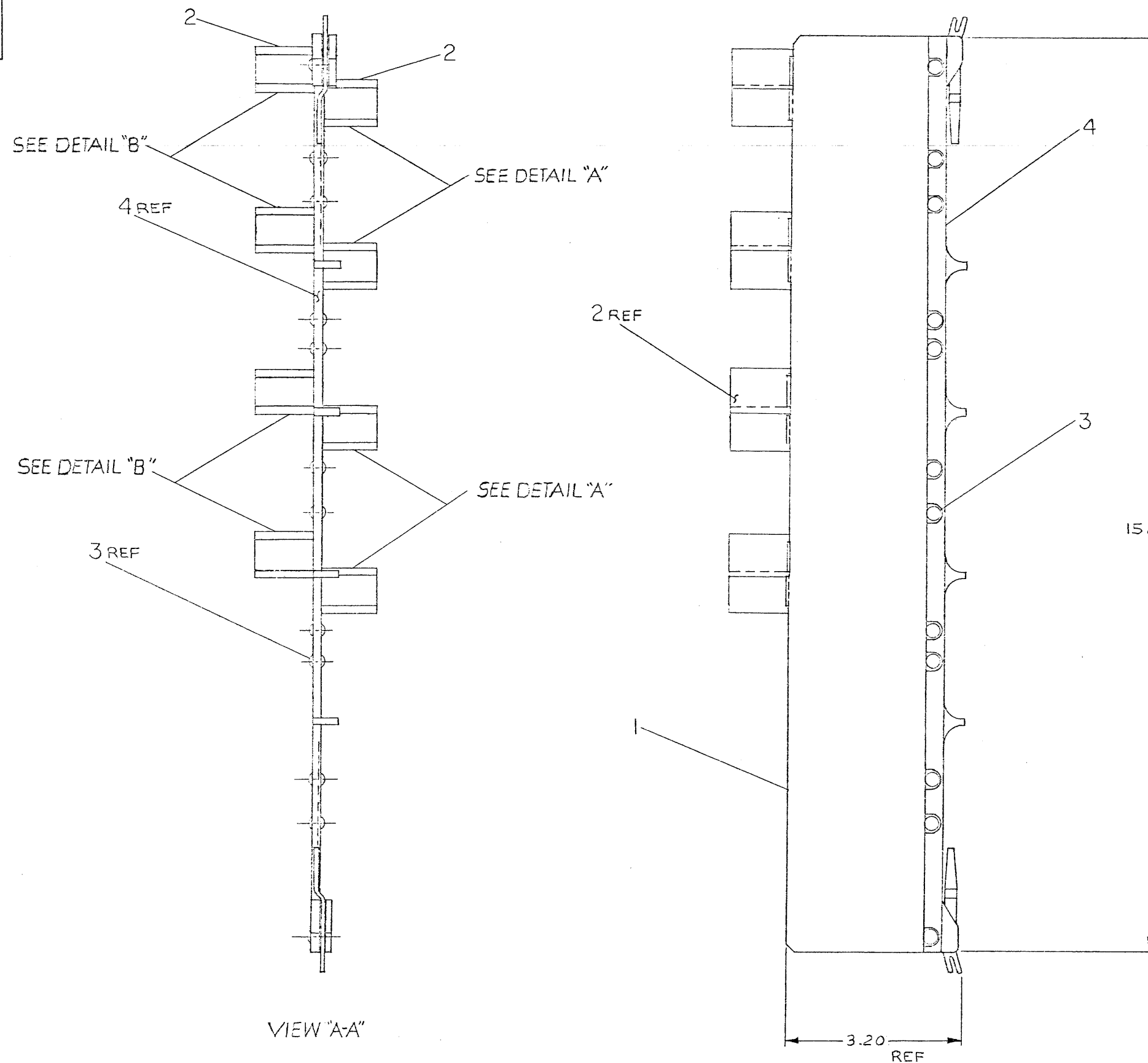
1	CONNECTOR, 31 SKT FOR 20AWG	1214480	2
A/R	WIRE, 20AWG BLK STRD	9107460-00	1

			DESCRIPTION			DWG./PART NO.		ITEM NO.		
			UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES							
			ANGLES ±0° 30'	CLASS OF ACCURACY (CHECK ONE)	NOMINAL DIMENSION RANGE INCHES					
					OVER 0 TO 0.2	OVER 0.2 TO 1.2	OVER 1.2 TO 4.0	OVER 4.0 TO 12.0	OVER 12.0 TO 40.0	OVER 40.0 TO 80.0
SURFACE QUALITY IN	MEDIUM ☐	±.004	±.008	±.012	±.016	±.024	±.04			
	MICROINCHES ☒	PREFERRED ☐	±.012	±.016	±.025	±.04	±.06	±0.1		
QUANTITY & VARIATION										

FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.								
ANIØ													
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES <table><tr><th>DECIMALS</th><th>ANGLES</th></tr><tr><td>.xxx = .005</td><td>10° 30'</td></tr><tr><td>.xx = .02</td><td></td></tr><tr><td>.x = .1</td><td></td></tr></table> REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY Y		DECIMALS	ANGLES	.xxx = .005	10° 30'	.xx = .02		.x = .1		PARTS LIST			
		DECIMALS	ANGLES										
		.xxx = .005	10° 30'										
		.xx = .02											
		.x = .1											
DRN. <i>M. Normand</i>	DATE <i>2 APRIL 77</i>	digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS											
CHK'D. <i>Bill - J</i>	DATE <i>2-2-77</i>												
ENG. <i>D. Anderson</i>	DATE <i>23 JUL 77</i>												
PROJ. ENG. <i>D. Anderson</i>	DATE <i>6 JUL 77</i>												
PROD. <i>D. Karmack</i>	DATE <i>6 JUL 77</i>												
MATERIAL		NEXT HIGHER ASSY.		TITLE CABLE CONN, ANIØ DISTANT (LOOPBACK)									
SEE PARTS LIST		B-DD-ANIØ-Ø											
FINISH <i>1</i>		SCALE <i>NONE</i>											
SHEET <i>1</i> OF <i>1</i>		DIST.											

REV.	NO.	CHK.

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SEE DETAIL "B"

SEE DETAIL "A"

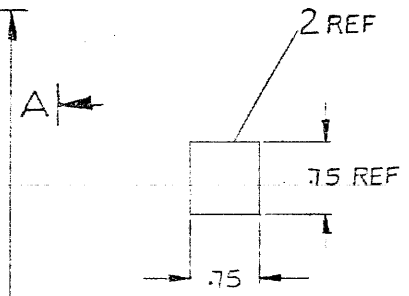
4 REF

SEE DETAIL "B"

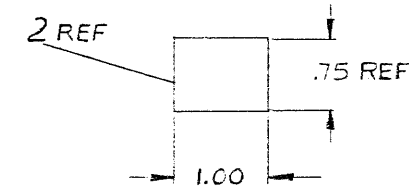
SEE DETAIL "A"

3 REF

2 REF



DETAIL "A"
TYP 4 PLACES



DETAIL "B"
TYP 4 PLACES

15.66 REF

3.20
REF

VIEW "A-A"

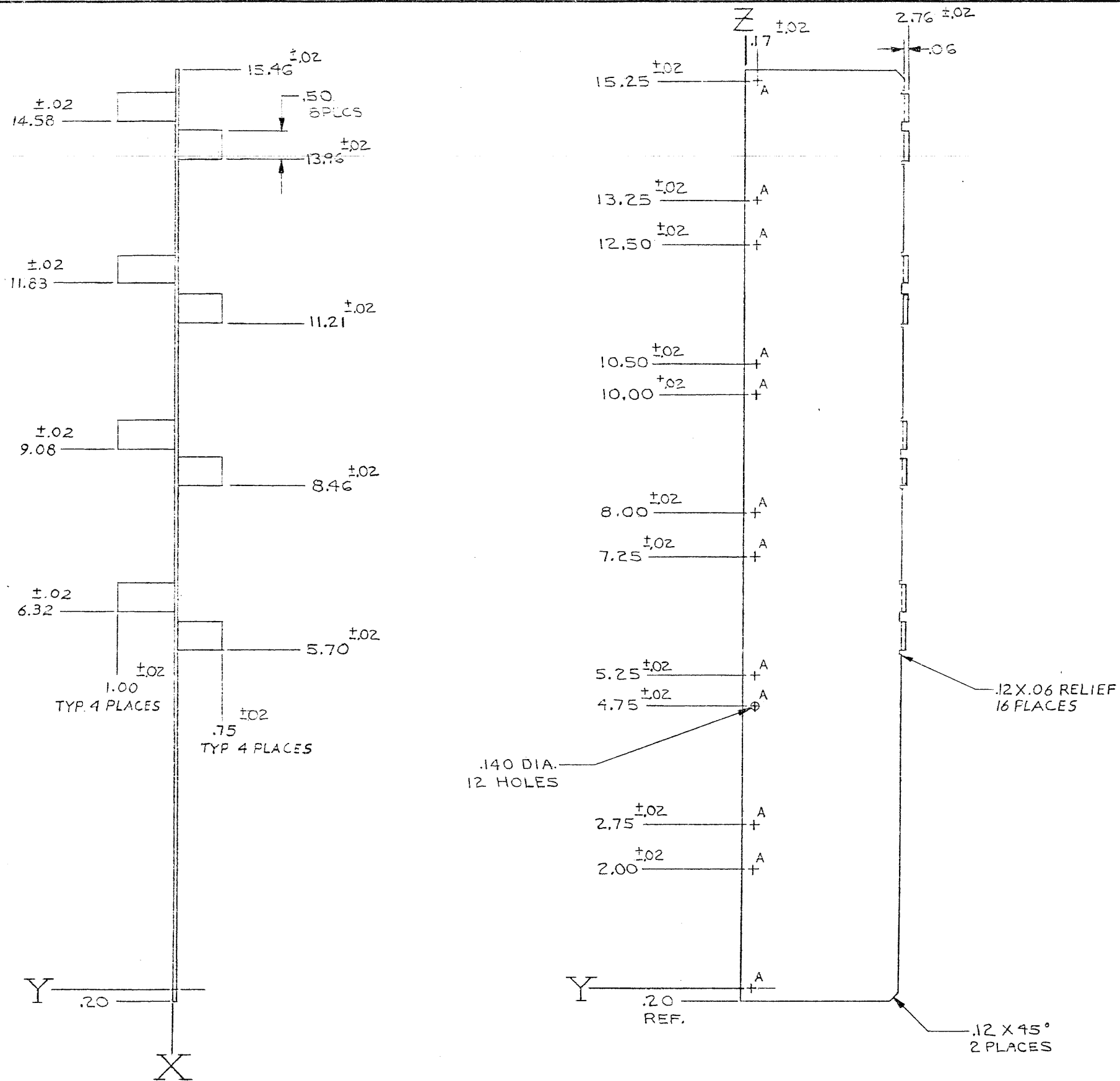
1	HANDLE MODULE HEX BOARD	E-FS-1210711-0-0	4
12	RIVETS	900 6458	3
1/2	FOAM, TAPE, ADHESIVE BACK	900 8214	2
1	HOLD DOWN BAR	D-MD-7418263-0-0	1

DESCRIPTION	DWG. PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		
ANGLES ±0° 30'	CLASS OF ACCURACY	NOMINAL DIMENSION RANGE INCHES
SURFACE QUALITY	(CHECK ONE)	OVER 0 TO 0.2
IN	MEDIUM	OVER 0.2 TO 1.2
MICROINCHES	PREFERRED	OVER 1.2 TO 4.0
		OVER 4.0 TO 12.0
		OVER 12.0 TO 40.0
		OVER 40.0 TO 80.0
		OVER 80.0 TO 160.0

THIRD ANGLE PROJECTION	DRN. <i>[Signature]</i>	12-8-77	FIRST USED ON	AN17	digital
REMOVE BURRS AND BREAK SHARP CORNERS	CHK'D		TITLE	MODULE HOLD DOWN BAR ASSY	
DO NOT SCALE DWG	PROJ. ENG. <i>[Signature]</i>		SCALE	1/1	
MATERIAL SEE PARTS LIST	PROD. <i>[Signature]</i>		SHEET	1	OF 1
FINISH			DIST.		

REVISIONS	REV.
CHANGE	
CHK	

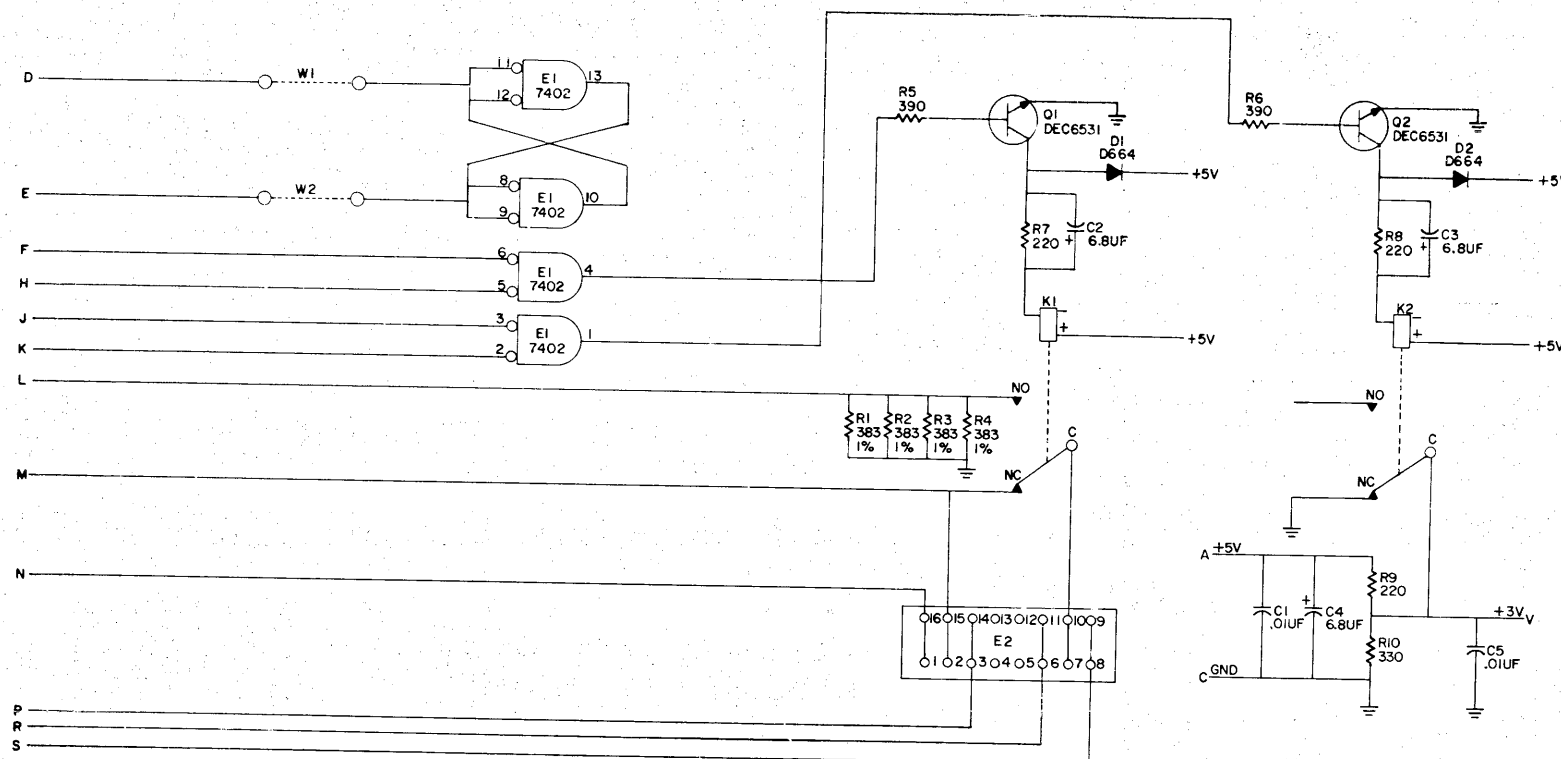
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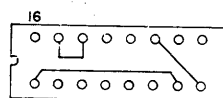
QUANTITY & VARIATION		DESCRIPTION		DWG. PART NO.		ITEM NO.	
THIRD ANGLE PROJECTION		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		D-AD-7014182-C-0		7418963-0-0	
REMOVE BURRS AND BREAK SHARP CORNERS		CLASS OF ACCURACY (CHECK ONE)		SIZE		CODE	
DO NOT SCALE DWG		MEDIUM		D		MD	
MATERIAL .06 THK AL 5052-H32		NOMINAL DIMENSION RANGE INCHES		NUMBER		REV.	
FINISH 9200200-C-0		OVER 0.2 TO 0.2		1/1		1	
		OVER 0.2 TO 1.2					
		OVER 1.2 TO 4.0					
		OVER 4.0 TO 12.0					
		OVER 12.0 TO 40.0					
		OVER 40.0 TO 80.0					
		PREFERRED					
		±.004					
		±.008					
		±.012					
		±.016					
		±.025					
		±.04					
		±.063					
		±.1					
		FIRST USED ON		ANIO		digital	
		TITLE		MODULE HOLD DOWN BAR			
		SHEET		OF			
		DIST.					

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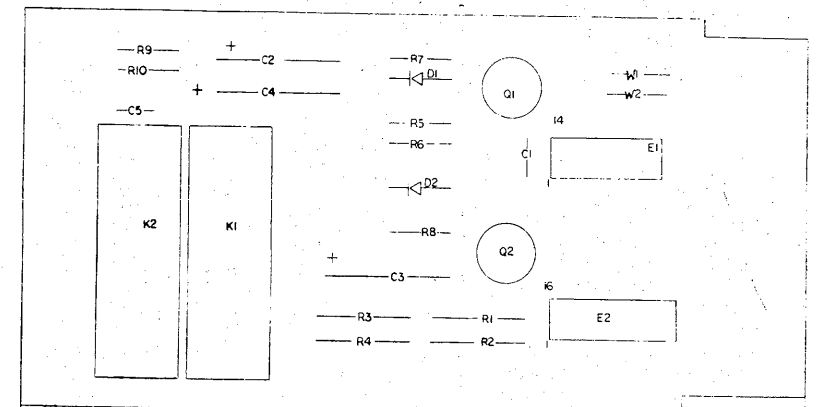
B 1-0-0689 53 0
A3H 4 REVISION 10001 1215



PIN 7 = GND ON E1
PIN 14 = +5V



1209837 - JUMPER PLUG FOR E2



A/R	DESCRIPTION	QTY.	REF. DESIGNATION
A/R	#24 on #20 AWG (FOR PLUG)	1	
A/R	TAPE, TRANSPARENT VINYL	1	
1	HANDLE, PLIP CHIP - GREEN	5000337-01	
2	EYELET #13L-7	5000337-01	
1	E1	1000337-01	
2	Q1, Q2	1000337-01	
4	R1, R2, R3, R4	1000337-01	
2	R5, R6	1000337-01	
1	R10	1000337-01	
3	R9, R7, R8	1000337-01	
1	E2	1000337-01	
1	PLUG 16 PHONG	1000337-01	
2	K1, K2	1000337-01	
2	D1, D2	1000337-01	
2	C1, C5	1000337-01	
3	C2, C3, C4	1000337-01	
1	ETCHED CIRCUIT BOARD	5000337-01	
	MODULE BUILT HISTORY		
	ASSY/DRILLING HOLE LAYOUT		
	X-Y COORDINATE HOLE LOCATION		
	DESCRIPTION		

DATE	1-2-72	DATE	1-2-72
BY	W. J. Dwyer	DATE	1-2-72
CHKD	W. J. Dwyer	DATE	1-2-72
DATE	1-2-72	DATE	1-2-72

TRANSISTOR & DIODE CONVERSION CHART	DATE	1-2-72	
DEC	EM	DEC	EM
DEC6531	MPS6531	DEC6531	MPS6531
D664	IN3806	D664	IN3806

TITLE		360 POWER FAIL AND SELECT-BY-PASS	
SIZE	CODE	NUMBER	REV
D	CS	G890-0-1	B

PRINTED CIRCUIT REV	B
---------------------	---

1976

DEC 16-(325)-1097-N174

PARTS LIST

QUANTITY VARIATION

SECTION

1

ISSUED SECT.

1

DESCRIPTION

1	-	1	-
-	1	-	1
2	2	2	2
1	1	1	1
1	1	1	1
1	-	1	-
-	1	-	1
1	1	1	1
1	1	1	1
1	1	1	1
1	1	1	1
1	1	1	1
1	1	1	1
1	1	1	1
1	1	1	1
1	1	1	1
6	6	6	6
7	7	7	7
1	1	1	1
1	1	1	1
1	1	1	1
28	28	28	28

ECO NO.

AN1.Ø-Ø-Ø

DIST

DIGITAL EQUIPMENT CORPORATION

MAYNARD, MASSACHUSETTS

PARTS LIST

MADE BY <i>M. Norman</i>	CHECKED <i>[Signature]</i>	SECTION
DATE <i>23 Jun 77</i>	DATE <i>22 Jun 77</i>	1
ENG <i>D. J. Anderson</i>	PROD <i>D. Halmack</i>	ISSUED SECT.
DATE <i>23-JUN-77</i>	DATE <i>6 JUL 77</i>	1

QUANTITY VARIATION

ITEM NO.	DWG NO./PART NO.	DESCRIPTION	AN10-AA	AN10-AB	AN10-BA	AN10-BB												
23	9007867	TIE, CABLE MOUNT	5	5	5	5												
24	9008264	TIE CABLE MOUNT ADHESIVE	14	14	14	14												
25	D-AD-7014182-0-0	MODULE HOLD DOWN BAR ASSY	1	1	1	1												
26	9006074-03	SCREW #10-32 x .62 LG	11	11	11	11												
27	9007651	WASHER #10-EXT TOOTH LK	14	14	14	14												
28	9007786	TINNERMAN NUT #10-32	6	6	6	6												
29	9107779-54	10 AWG WIRE GRN W/YEL TRACER	A/RA	RA/RA	RA/RA	A/R												
30	J-IA-7014056-0-0	HARNESS, DC (AN10)	1	1	1	1												
31	9007033	CABLE TIE & MOUNT	2	2	2	2												
32	D-UA-BC06R-1-0	BC06R CABLE (1 FT)	1	1	1	1												
33	D-UA-BC06R-4-0	BC06R CABLE (4 FT)	2	2	2	2												
34	9007959	EXT TOOTH LK WASHER 7/16"	2	2	2	2												
35	9006565	NUT, KEP 10-32	1	1	1	1												
36	9006724	WASHER, 1/4" EXT TOOTH LK	3	3	3	3												
37	9006568	NUT, KEP 5/16-18	1	1	1	1												
38	9007085	CLAMP, CABLE, 1/2"	2	2	2	2												
39	D-AD-7009861-0-0	RECEPTACLE HSG ASSY	2	2	2	2												
40	9006010-01	SCR. PHL HD 4-40 x .31 LG	8	8	8	8												
41	9006632	WASHER #4 EXT TOOTH LK	8	8	8	8												
42	E-UA-BC10T-0-0	CABLE, BC10T	-	-	1	1												
43	3613272	DECAL, GROUND	4	4	4	4												
44	D-UA-BC10K-25-0	CABLE ASSY (BC10K) 25 FT	1	1	1	1												

TITLE ARPA NET INTERFACE	ASSY NO. E-UA-AN10-0-0	SIZE A	CODE PL	NUMBER AN10-0-0	REV	ECO NO.
	SHEET 2 OF 3	DIST				

DIGITAL EQUIPMENT CORPORATION

MAYNARD, MASSACHUSETTS

PARTS LIST

MADE BY *M. Normand*CHECKED *[Signature]*

SECTION

DATE *23 Jun 77*DATE *22 Jun 77*

1

ENG *D. Anderson*PROD *D. Hadner*

ISSUED SECT.

DATE *23-JUN-77*DATE *6 JUL 77*

1

QUANTITY VARIATION

AN10-AA

AN10-AB

AN10-BA

AN10-BB

ITEM NO.	DWG NO./PART NO.	DESCRIPTION
45	D-UA-H867-0-0	I/O BUS TERMINATOR (H867)
46	D-AD-7013963-0-0	CONNECTOR, AN10 TEST
47	D-IA-7010805-0-0	HARNESS, H7420 AC WIRING
48	1209340-01	CONN, HOUSING, 8 PIN MALE
49	1209340	CONN, HOUSING, 8 PIN FEMALE
50	1211591-01	CONN, ZIF, PLUG KIT
51	1211591-07	CONN, ZIF, ACTIVE CONTACT ASSY
52	1211591-11	CONN, ZIF, CONTACT COVER
53	1211591-12	CONN, ZIF, STRAIN RELIEF KIT
54	1210776-04	CABLE BUSHING, RUBBER, SHELL #16
55	1210776-05	CABLE BUSHING, RUBBER, SHELL #18
56	1210776-06	CABLE BUSHING, RUBBER, SHELL #20
57	1210776-07	CABLE BUSHING, RUBBER, SHELL #24
58	9009696	RING TERMINAL, 1/4" ID, 10 AWG
59	9007926-01	RING TERMINAL, #10, 10 AWG
60	9008072	WASHER, #8 EXT TOOTH LK
61	9006242-09	SCR, HEX HD, 1/4-20 x .62 LG.
62	9006039-01	SCR, PHL PAN HD, 8-32 x .50 LG.
63	3612675-0-1	DECAL, CAUTION LEAKAGE
64	3613210	DECAL, SERIAL/MODEL
65	9007083	CLAMP, CABLE, 3/8"
66	D-IC-AN10-0-PW	WIRING DIAGRAM, AN10

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1	1	1	1																
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2	2	2	2																
1	1	-	-																
3	3	-	-																
6	6	-	-																
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3	3	3	3																
3	3	3	3																
7	7	7	7																
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1	1	1	1																
REF	REF	REF	REF																

TITLE

ARPA NET INTERFACE

ASSY NO.

E-UA-AN10-0-0

SIZE

A

CODE

PL

NUMBER

AN10-0-0

REV

ECO NO.

SHEET 3 OF 3

DIST

DIGITAL EQUIPMENT CORPORATION

MAYNARD, MASSACHUSETTS

PARTS LIST

MADE BY M. NORMAND	CHECKED <i>[Signature]</i>	SECTION 1
DATE 29 OCTOBER 1976	DATE 28 JUN 77	
ENG <i>[Signature]</i>	PROD D Walmark	ISSUED SECT. 1
DATE 23-JUN-77	DATE 6 JUL 77	

QUANTITY / VARIATION

ITEM NO.	DWG NO. / PART NO.	DESCRIPTION	-0 (	-1 (	-2 (	-3												
1	E-IA-7416546-0-0	LOGIC MTG PLATE	1	1	1	-												
2	C-AD-7008711-0-0	QUICK LATCH BLOCK	2	4	2	-												
33	D-AD-7013918-0-0	BACKPLANE ASSY PCIØ	1	-	-	-												
4	C-PS-1210698-0-0	GUIDE, CARD	-	3	9	-												
5	D-AD-7009152-0-0	4 SLOT BACKPLANE ASSY	-	1	-	-												
6	K-WL-PC1Ø-Ø-WL	PC1Ø WIRE LIST	REF	-	-	-												
7	A-WT-7013480-0-0	AWT REV SHEET (PC1Ø)	REF	-	-	-												
8	K-WL-DX2Ø-Ø-WL	DX2Ø WIRE LIST	-	REF	-	-												
9	A-WT-7013480-1-0	AWT REV SHEET (DX2Ø)	-	REF	-	-												
10	K-WL-AN1Ø-Ø-WL	AN1Ø WIRE LIST	-	-	REF	-												
11	A-WT-7013480-2-0	AWT REV SHEET (AN1Ø)	-	-	REF	-												
12	C-MD-7417523-0-0	PLATE #1	1	-	1	-												
13	C-MD-7417534-0-0	PLATE #2	2	-	2	-												
14	C-MD-7417536-0-0	PLATE #3	3	-	-	-												
15	A-DC-7411881-1-0	DECAL LOGIC REVISION	1	1	1	-												
16	9009100-3	SC #4-40 X .25 LG TR HD	6	-	-	-												
17	9006632	LOCKWASHER #4	6	-	-	-												
18	9006634	WSHR LOCK #8	18	22	20	-												
19	9006043-1	SCREW #8-32 X 1 LG	6	-	6	-												
20	9008005-1	SCREW #8-32 X 1.38 LG	-	2	-	-												
21	9006039-1	SCREW #8-32 X .50 LG	8	16	8	-												
22	9006037-3	SCREW TRUSS HD #8-.32 X .38 LG	4	4	6	-												

TITLE WIRED LOGIC MTG ASSY	ASSY NO. E-AD-7013480-0-0	SIZE A	CODE PL	NUMBER 7013480-0-0	REV.	ECO NO.
SHEET 1 OF 2		DIST.				

QUANTITY / VARIATION

PARTS LIST

SECTION
1

ISSUED SECT.	1
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-0 (PC10)
 -1 (DX20)
 -2 (AN10)
 -3

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100	101	102	103	104	105	106	107	108	109	110	111	112	113	114	115	116	117	118	119	120	121	122	123	124	125	126	127	128	129	130	131	132	133	134	135	136	137	138	139	140	141	142	143	144	145	146	147	148	149	150	151	152	153	154	155	156	157	158	159	160	161	162	163	164	165	166	167	168	169	170	171	172	173	174	175	176	177	178	179	180	181	182	183	184	185	186	187	188	189	190	191	192	193	194	195	196	197	198	199	200	201	202	203	204	205	206	207	208	209	210	211	212	213	214	215	216	217	218	219	220	221	222	223	224	225	226	227	228	229	230	231	232	233	234	235	236	237	238	239	240	241	242	243	244	245	246	247	248	249	250	251	252	253	254	255	256	257	258	259	260	261	262	263	264	265	266	267	268	269	270	271	272	273	274	275	276	277	278	279	280	281	282	283	284	285	286	287	288	289	290	291	292	293	294	295	296	297	298	299	300	301	302	303	304	305	306	307	308	309	310	311	312	313	314	315	316	317	318	319	320	321	322	323	324	325	326	327	328	329	330	331	332	333	334	335	336	337	338	339	340	341	342	343	344	345	346	347	348	349	350	351	352	353	354	355	356	357	358	359	360	361	362	363	364	365	366	367	368	369	370	371	372	373	374	375	376	377	378	379	380	381	382	383	384	385	386	387	388	389	390	391	392	393	394	395	396	397	398	399	400	401	402	403	404	405	406	407	408	409	410	411	412	413	414	415	416	417	418	419	420	421	422	423	424	425	426	427	428	429	430	431	432	433	434	435	436	437	438	439	440	441	442	443	444	445	446	447	448	449	450	451	452	453	454	455	456	457	458	459	460	461	462	463	464	465	466
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1	2	3	4
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A	RA	RA	R
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A	RA	RA	R
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18	6	18
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1	2	3	4
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$$\frac{A}{RA/R} -$$
$$A / RA / RA / R$$

A	/	RA	/	RA	/	R
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- A/R -

- A/R -

REV.	ECO NO.
247	

DIST.

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE 27 JUN 77

TITLE ANIO/AN20 ACCEPTANCE PROCEDURE

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE

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ENG D. Anderson 29 JUN 77	APPD R. Travinchi 6-JUL-77	SIZE A	CODE SP	NUMBER ANIO-0-AP	REV
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TITLE

ANIO/AN20 ACCEPTANCE PROCEDURE

1.0 GENERAL INTRODUCTION

The ANIO/AN20 are standard Federal Systems Group supported hardware options designed to provide a hardware/software package to users of the ARPANET (Advanced Research Projects Agency Network) for purposes of integrating the DECsystem-10 (1090T) and DECsystem-20 (2040T and 2050T) hosts to the ARPANET. The ANIO will electrically and logically interface between the KLIO IBUS, and the ARPANET IMP (Interface Message Processor). The AN20 will electrically and logically interface between the KLIO internal EBUS, and the ARPANET IMP. Both will support both local (up to 30 feet), and distant (up to 2000 feet) connections to the IMP. They both require two device codes which are functionally partitioned as an input device and an output device. Input is given priority over output to facilitate full duplex message transmissions between the host and the IMP. Bit to word packing and unpacking is handled by the ANIO/AN20.

2.0 OPTIONS

ANIO-AA AN20-AA	Basic KLIO to IMP interface for the ARPA net. Includes logical and electrical interface for both local and distant connections plus cable connector for terminating a 30 foot local IMP cable, 60 Hz, 120 VAC.
ANIO-AB AN20-AB	Same as ANIO-AA/AN20-AA, but for 50 Hz, 240 VAC. systems.
ANIO-BA AN20-BA	ANIO-AA/AN20-AA with the local cable connector replaced with 50 foot distant host cable assembly (BC10T-50).
ANIO-BB AN20-BB	Same as ANIO-BA/AN20-BA, but for 50 Hz, 240 VAC. systems.

3.0 SOFTWARE

3.1	MAINDEC-10-DDANA	EXEC MODE BASIC ANIO/AN20 Test
3.2	MAINDEC-10-DDANB	USER MODE ANIO/AN20 IMPST RELIABILITY

4.0 PRINT SETS AND MANUALS

4.1	ANIO or AN20 print set (MP00326 OR MP00399)
4.2	ANIO / AN20 manual (EK-ANIOS-MM)

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SPNUMBER
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TITLE ANI0/AN20 ACCEPTANCE PROCEDURE

5.0 ACCEPTANCE TEST PROCEDURES

Perform the following procedures to check out and accept the ANI0/AN20 Interface Option.

- 5.1 If the ANI0/AN20 is to be configured for a distant IMP interface, position switch EI5-1 on the M8612 module OFF to enable the local cable drivers and receivers. Otherwise, switch EI5-1 ON to enable the distant receivers and drivers.
- 5.2 Load MD-10-DDANA; consulting the listing for switch settings and operating instructions.
- 5.3 Start execution of MD-10-DDANA. Type a carriage return (CR) to the TTY prompt message to specify execution of all basic tests. In response to the TTY prompt regarding the type of loopback to be tested, type "I" to specify internal loopback. Execute 10 minutes, no errors allowed.
- 5.4 Disconnect the ANI0 to IMP cable (BCI0T or equivalent) from the ANI0/AN20 logic assembly ZIF connector labeled "IMP IN". Move the ANI0/AN20 loopback test connector 70-13963 from the ZIF connector labeled "IMP LOOP" to the "IMP IN" position. Repeat step 5.3 except type "E" to the appropriate TTY prompt to select external loopback.
- 5.5 If the IMP interface is configured for a local connection, plug the ANI0/AN20 end of the local cable into the ZIF connector labeled "IMP LOOP". Call the Network Control Center and arrange with them to run a cable continuity test from the IMP. This test is optional unless cable continuity problems are suspected.
- 5.6 If the ANI0/AN20 is to be configured for a distant IMP interface, position switch EI5-1 on the M8612 module ON to enable the distant cable drivers and receivers. Otherwise, switch EI5-1 OFF to enable the local receivers and drivers.
- 5.7 Repeat step 5.3, typing "I" to the loopback TTY prompt to select internal loopback.
- 5.8 Repeat step 5.3, typing "E" to the loopback TTY prompt to select external loopback.
- 5.9 Restore the ANI0/AN20 loopback test connector 70-13963 to its storage position on the ZIF connector labeled "IMP LOOP". Connect the ANI0/AN20 end of the BCI0T or equivalent cable to its operational position on the ZIF connector labeled "IMP IN".

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TITLE ANI0/AN20 ACCEPTANCE PROCEDURE

- 5.10 If the unit under test is an ANI0-B/AN20-B (supplied with a BCI0T distant cable), disconnect the BCI0T from the IMP and attach the BCI0T cable loopback test connector 70-13995 to the BCI0T cable. Repeat step 5.3, typing "E" to the loopback TTY prompt to select external loopback. After successful completion of this test, disconnect the 70-13995 test connector from the BCI0T cable and reattach the BCI0T cable in its normal operating position within the IMP.
- 5.11 Start execution of MD-I0-DDANA. Type "I" to the first TTY prompt to select the IMPTST (or reliability) portion of the diagnostic. Type "ESC" (escape or altmode) to the parameter select prompt to specify all pattern combinations and random message lengths. Execute this test 1 hour error free. Consult the listing for operational procedures. Note that while IMPTST is running the ANI0/AN20 is on the ARPANET, meaning that other connections may send messages to this ANI0/AN20. These messages will be printed on the TTY log and are not errors.
- 5.12 Bring up the TOPS20-AN Operating System on the system under test. Load MD-I0-DDANB, consulting the listing for switch settings and operating instructions. Execute for 45 minutes error free. The note for step 5.11 also applies in this test. This test is for customer acceptance only and is not performed as part of factory acceptance. This test is not valid on systems not supporting TOPS20-AN.
- 5.13 Successful completion of the above tests constitutes acceptance of the ANI0/AN20 ARPANET Interface Option.

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ACODE
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ANI0-0-AP

REV

DIGITAL EQUIPMENT CORPORATION

MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE 27 JUN 77

TITLE AN10/AN20 MANUFACTURING TEST PROCEDURE

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE

ENG D. Anderson 29 JUN 77	APPD R. Stravinski 6-JUL-77	SIZE A	CODE SP	NUMBER AN10-0-MTP	REV
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TITLE ANIO/AN20 MANUFACTURING TEST PROCEDURE

1.0 GENERAL INTRODUCTION

The ANIO/AN20 are standard Federal Systems Group supported hardware options designed to provide a hardware/software package to users of the ARPANET (Advanced Research Projects Agency Network) for purposes of integrating the DECsystem-10 (1090T) and DECsystem-20 (2040T and 2050T) hosts to the ARPANET. The ANIO will electrically and logically interface between the KLIO IBUS, and the ARPANET IMP (Interface Message Processor). The AN20 will electrically and logically interface between the KLIO internal EBUS, and the ARPANET IMP. Both will support both local (up to 30 feet), and distant (up to 2000 feet) connections to the IMP. They both require two device codes which are functionally partitioned as an input device and an output device. Input is given priority over output to facilitate full duplex message transmissions between the host and the IMP. Bit to word data packing and unpacking is handled by the ANIO/AN20.

2.0 OPTIONS

ANIO-AA AN20-AA	Basic KLIO to IMP interface for the ARPA net. Includes Logical and electrical interface for both local and distant connections plus cable connector for terminating a 30 foot local IMP cable, 60 Hz, 120 VAC.
ANIO-AB AN20-AB	Same as ANIO-AA/AN20-AA, but for 50 Hz, 240 VAC systems.
ANIO-BA AN20-BA	ANIO-AA/AN20-AA with the local cable connector replaced with 50 foot distant host cable assembly (BCIOT-50).
ANIO-BB AN20-BB	Same as ANIO-BA/AN20-BA, but for 50 Hz, 240 VAC systems.

3.0 TEST EQUIPMENT

- 3.1 Tektronix 454 oscilloscope or equivalent
- 3.2 Three voltage probes
- 3.3 Triplet model 630 VOM or equivalent
- 3.4 Digital voltmeter

4.0 SOFTWARE

- 4.1 MAINDEC-10-DDANA,
EXEC MODE BASIC ANIO/AN20 DIAGNOSTIC

5.0 PRINT SETS AND MANUALS

- 5.1 ANIO or AN20 print set
- 5.2 ANIO or AN20 manual (EK-ANIOS-MM)

SIZE	CODE	NUMBER	REV
A	SP	ANIO-0-MTP	

ENGINEERING SPECIFICATION

original

CONTINUATION SHEET

TITLE AN10/AN20 MANUFACTURING TEST PROCEDURE

6.0 MODULE ASSEMBLY

6.1 M8612

- 6.1.1 Verify that M8612 module is up to the latest revision.
- 6.1.2 Airblast module, remove all debris.
- 6.1.3 Verify that no shorts exist between the power and ground planes of the module.
- 6.1.4 The component sockets are now to be assembled. Consult print UA-M8612.
- 6.1.5 Insert the component sockets and dips in their respective positions. Consult print UA-M8612.
- 6.1.6 Set the switch PKG switches to device code 52X. Set switches 3,5,7 to the one state, all other switches off. Switch 1 determines whether the local or distant receiver/drivers are enabled. Set switch 1 to off, which enables the local receiver/drivers and disables the distant receiver/drivers.

6.2 M8613

- 6.2.1 Verify that the M8613 module is up to the latest revision.
- 6.2.2 Airblast module, remove all debris.
- 6.2.3 Verify that no shorts exist between the power and ground planes of the module.
- 6.2.4 The SIP strips are now to be assembled. Consult print UA-M8613.
- 6.2.5 Insert the SIP strips and dips in their respective positions. Consult print UA-M8613.

6.3 M8614

- 6.3.1 Verify that the M8614 module is up to the latest revision.
- 6.3.2 Airblast module, remove all debris.
- 6.3.3 Verify that no shorts exist between the power and ground planes of the module.
- 6.3.4 The SIP strips are now to be assembled. Consult print UA-M8614.

TITLE AN10/AN20 MANUFACTURING TEST PROCEDURE

6.3.5 Insert the SIP strips in their respective positions.
consult print UA-M8614.

7.0 Preliminary checks

- 7.1 Verify that the backplane assembly is up to the latest revision.
- 7.2 Airblast logic assembly, remove all debris.
- 7.3 Verify that no shorts between +5 volts, -5 volts, -15 volts and ground exist on the backplane.
- 7.4 Power up the AN10/AN20 and verify that DC voltages are reasonably close to their respective voltages.
- 7.5 Airblast all HEX modules, verify that no shorts exist on the HEX modules between +5 volts, -5 volts and ground.
- 7.6 Insert all modules as listed on module utilization.
- 7.7 Power up the cab and adjust all DC voltages to their respective voltages.
- 7.8 Verify that the terminator in the IO bus out quick latch has the required -15V power applied.

8.0 Online Check Out

- 8.1 Load MAINDEC-10-DDANA, consult listing for switch settings and operating instructions.
- 8.2 Run internal loop 15 minutes, no errors allowed.
- 8.3 Insert 70-13963 AN10 test connector, run external loop 15 minutes, no errors allowed.
- 8.4 Set switch #1 on the M8612 to the on position, this enables the distant receivers/drivers and disables the local receiver/drivers.
- 8.5 Run internal loop 15 minutes, no errors allowed.
- 8.6 Run external loop 15 minutes, no errors allowed.
- 8.7 Delay Check
 - 8.7.1. Put the M8614 module on a extender card and verify that the following pulse widths are within the following specifications, loop on test 74, external loop, consult listing for operating procedures.

SIGNAL NAME	PIN NUMBER	NORMINAL VALUE	MINIMUM VALUE	MAXIMUM VALUE
ICON DESKEW DELAY	E01613	50NS	40NS	90NS
ICON DLY CLK	E01605	100NS	80NS	160NS
ICON LOCAL BIT DLY UP	E01113	75NS	60NS	130NS
ICON DIST BIT DLY UP	E01105	1.5US	1.1US	2.0US
ICON RDY FOR I BIT CLK	E03705	50NS	40NS	90NS
ICON RDY DWN TIME DLY	E00413	200NS	160NS	275NS
ICON DIST BIT DLY DWN	E00405	1.5US	1.1US	2.0US

ENGINEERING SPECIFICATION

digital

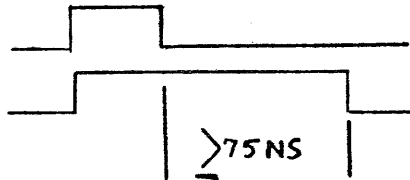
CONTINUATION SHEET

TITLE AN10/AN20 MANUFACTURING TEST PROCEDURE

8.7.2. Verify that the following pulse widths are within the following specifications; loop on test 70.

SIGNAL NAME	PIN NUMBER	NORMINAL VALUE	MINIMUM VALUE	MAXIMUM VALUE
ICON DATA PAD DLY	E02813	50NS	40NS	90NS
ICON DATA PAD CLK	E02805	100NS	80NS	160NS
ICSR RESET	E07213	1.5US	1.1US	2.0US

8.7.3. Verify that the time from the negation of ICON SHFT DATA H (E05903) to the negation of ICON RDY DWN TIME DLY (1)H (E00413) is ≥ 75 NS; loop on test 74.



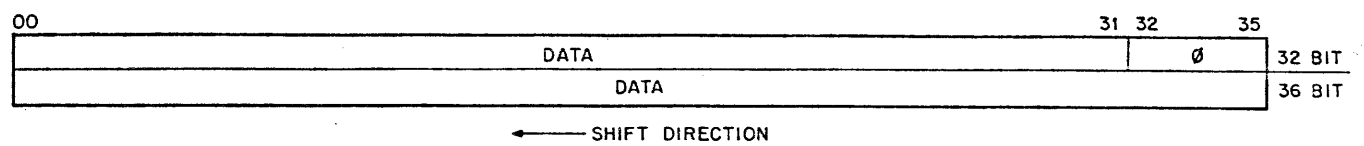
8.7.4. Put the M8613 module on an extender card and verify that the pulse widths are within the following specifications. Loop on test 74, external loop, consult listing for operating procedures.

SIGNAL NAME	PIN NUMBER	NORMINAL VALUE	MINIMUM VALUE	MAXIMUM VALUE
OCON DIST BIT DLY UP	E00405	1.5US	1.1US	2.0US
OCON LOCAL BIT DLY UP	E00413	100NS	80NS	160NS
OCON CNT	E01113	160NS	125NS	220NS
OCON DIST BIT DLY DWN	E01613	1.5US	1.1US	2.0US
OCON DLYD PULSE	E01105	50NS	40NS	90NS
OCON SKEW DLY	E01605	350NS	270NS	450NS
OCSR CONO CLR DLY	E05105	100NS	80NS	160NS

- 8.8 Set SW #1 on the M8612 to the configuration to be shipped.
- 8.9 If an AN10-BA, AN10-BB, AN20-BA, or AN20-BB is to be shipped insert the BC10T cable. Insert 7013995 connector at end of cable.
- 8.10 If an AN10-AA, AN10-AB, AN20-AA, or AN20-AB is to be shipped insert the AN10 test connector.
- 8.11 Run external loop, vibrate all modules, cables, harnesses, and subassemblies, no errors allowed.
- 8.12 Run external loop 1 hour, no errors allowed.
- 8.13 If an IMP is available run "I" test 1 hour error free, consult listing for operating procedures.

SIZE A	CODE SP	NUMBER AN10-Ø-MTP	REV
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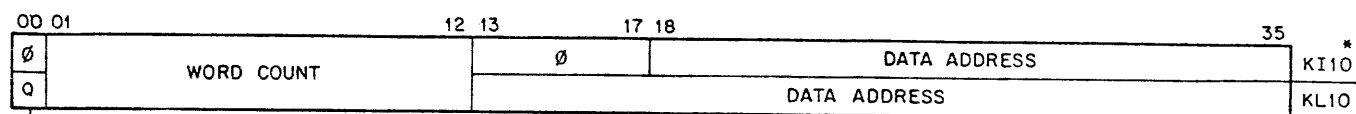
DATAO/DATAI ODEV
SELECT 00



10-2429

Figure 2 Output Data Register (ODR)

DATAO/DATAI ODEV
 SELECT 01



QUALIFIER 0 NO PROTECT OR RELOCATE
 1 APPLY PROTECT AND RELOCATE

*Bit positions illustrated by "0" may actually be loaded and read by DATAO/DATAI.
 They will not, however, be gated to the API function word.

10-2421

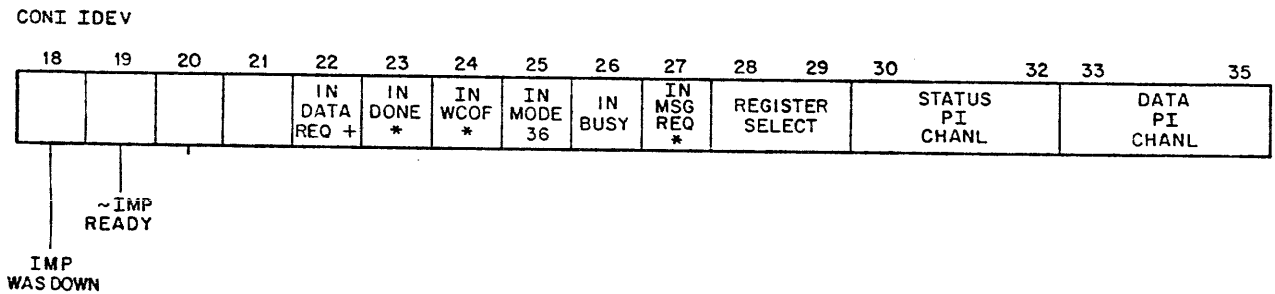
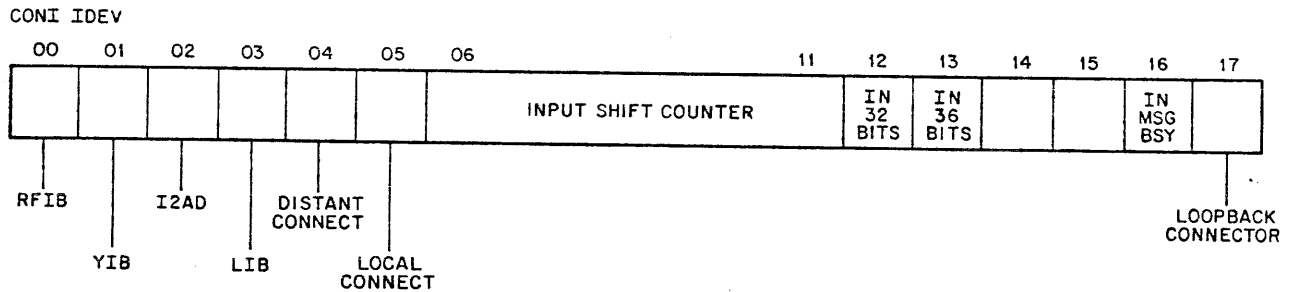
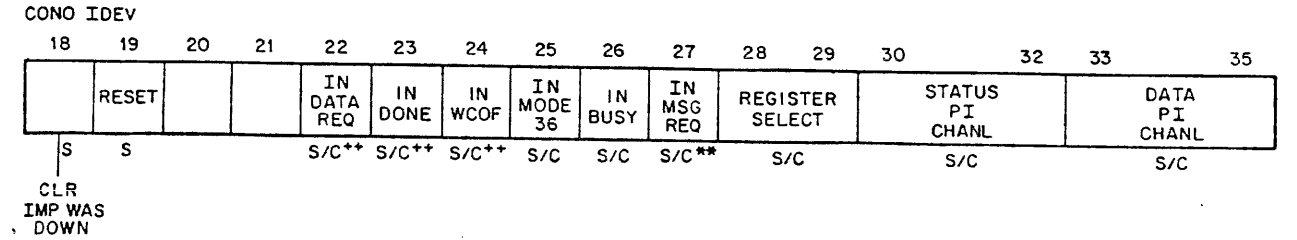
Figure 3 Output Word Count/Address Register (OWAR)

[illegible]

**Conditional write, writable only in MAINT MODE and if IMP
PORT DISABLED is asserted.

+Bit positions illustrated by "0" may actually be loaded and read by DATA0/DATA1. They will not, however, be gated to the API function word.

Figure 4 Output Vector Address Register (OVAR)



+Data interrupt condition.

*Status interrupt condition.

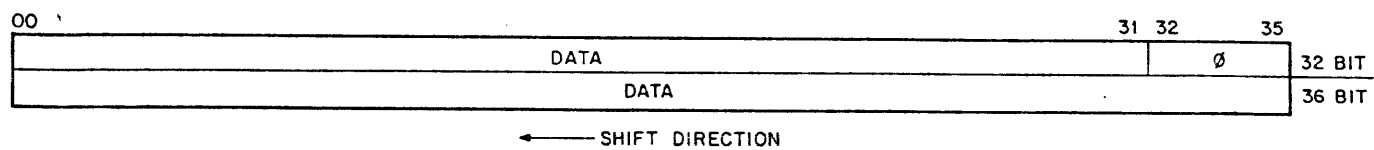
+Conditional set, settable only in MAINT MODE.

**Conditional write, writeable only in MAINT MODE.

10-2423

Figure 5 Input Control/Status Register (ICSR)

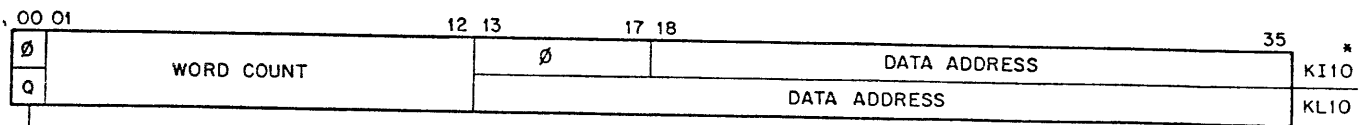
DATA0/DATA1 IDEV
SELECT 00



10-2424

Figure 6 Input Data Register (IDR)

DATAO/DATAI IDEV
SELECT 01

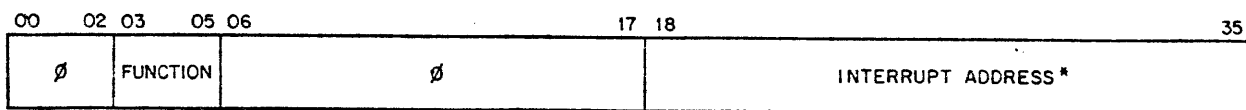


QUALIFIER 0 NO PROTECT OR RELOCATE
1 APPLY PROTECT AND RELOCATE

*Bit positions illustrated by "0" may actually be loaded and read by DATAO/DATAI.
They will not, however, be gated to the API function word.

10-2425

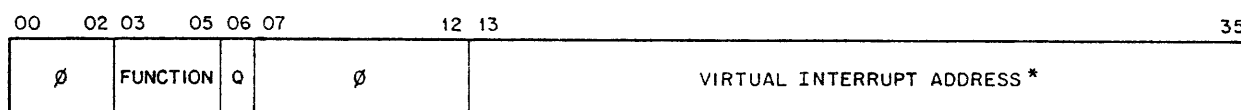
Figure 7 Input Word Count/Address Register (IWAR)



*Address is not supplied for API Function 1.

10-2431

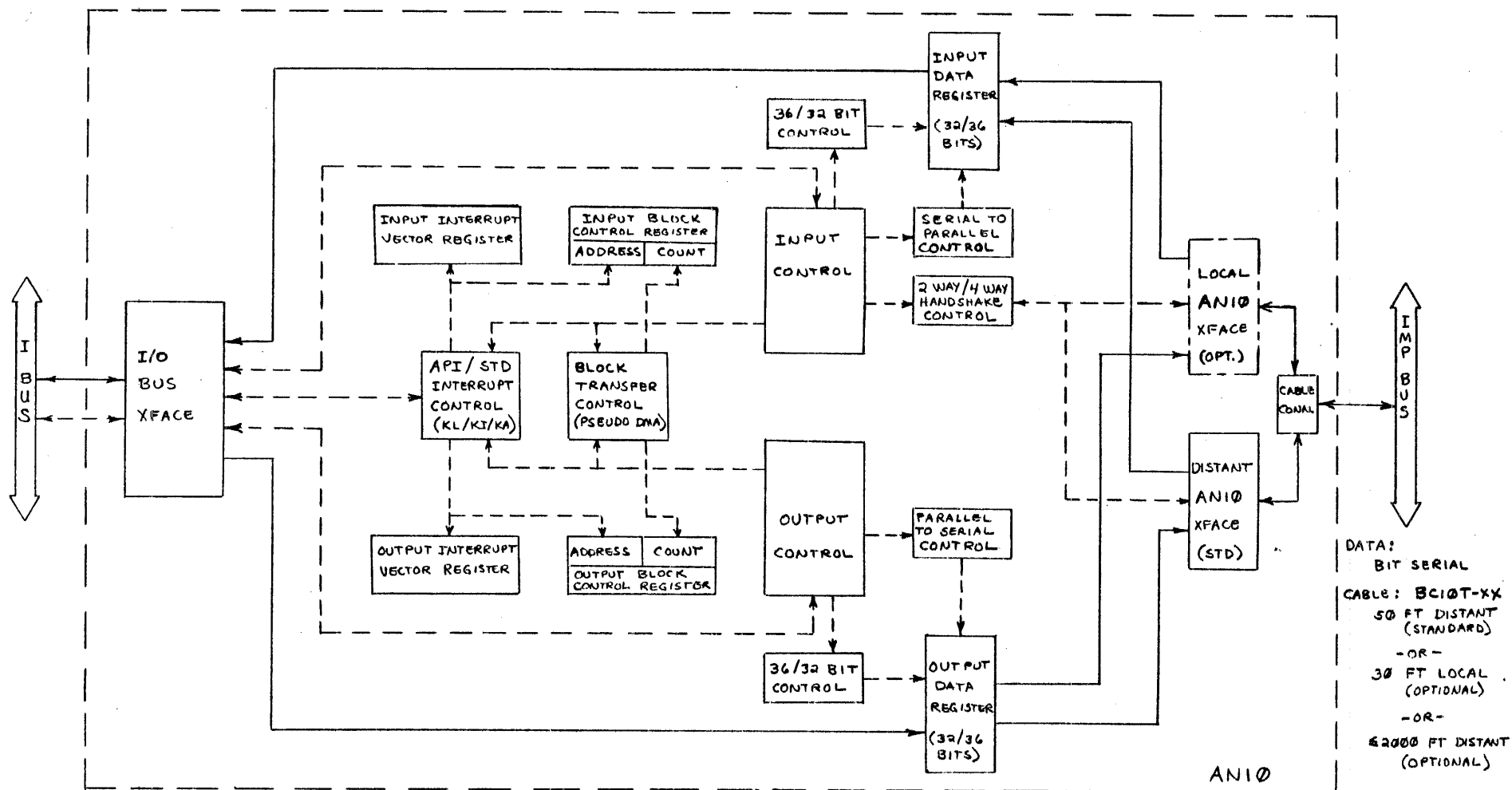
Figure 9 KI10 API Format



*Address is not supplied for API Function 1.

10-2432

Figure 10 KL10 API Format



LEGEND:

- > CONTROL PATH
- > DATA PATH

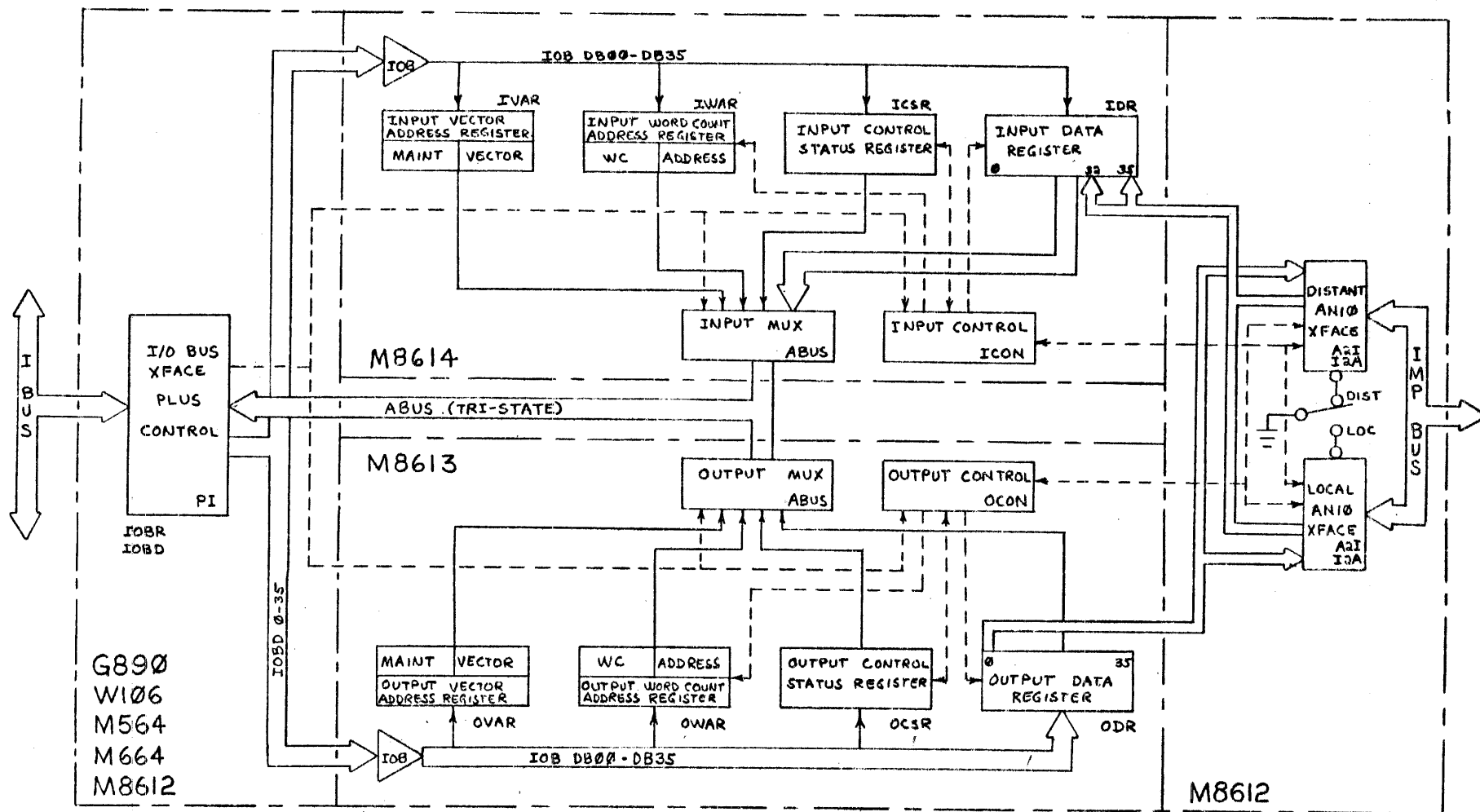
NOTE:

THE ANIO HAS BOTH LOCAL AND DISTANT CABLE DRIVERS/RECEIVERS. THE SET ACTIVATED IS DETERMINED BY SWITCH SETTING.

ANIO FUNCTIONAL BLOCK DIAGRAM

17-FEB-77

FIGURE 13.



ANIO BLOCK DIAGRAM

18-FEB-77

LEGEND:

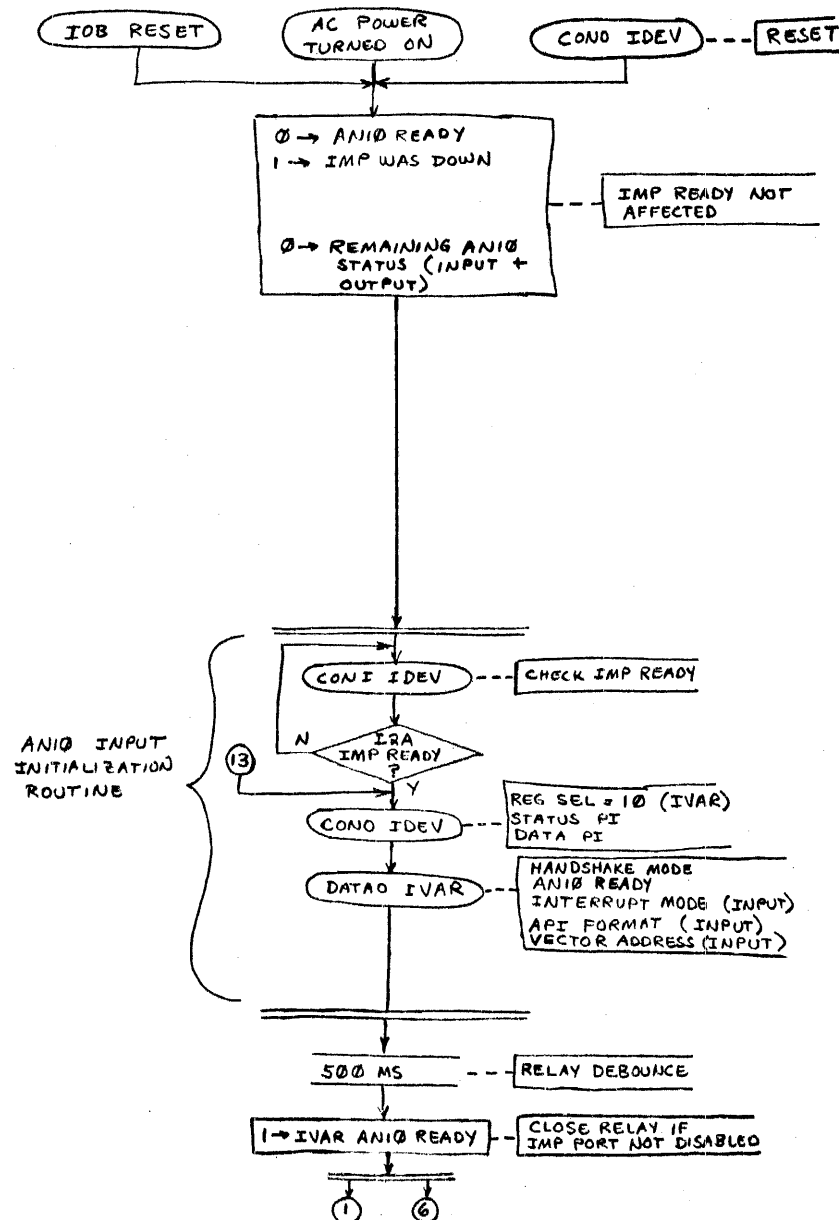
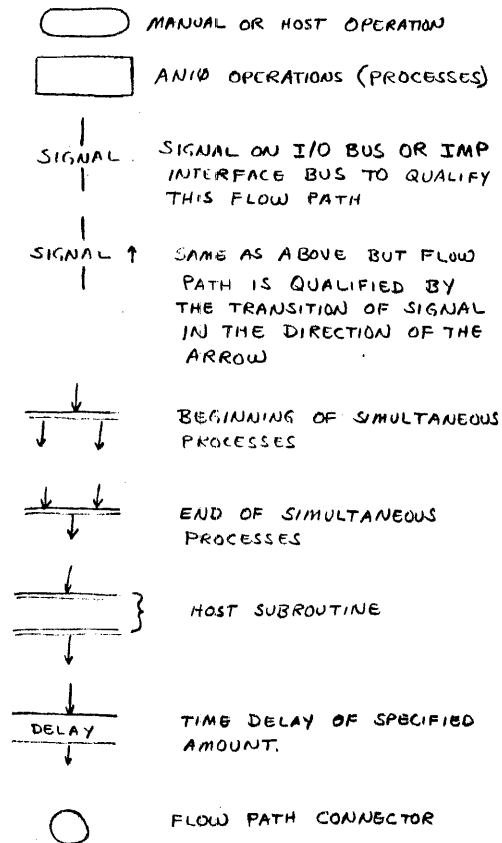
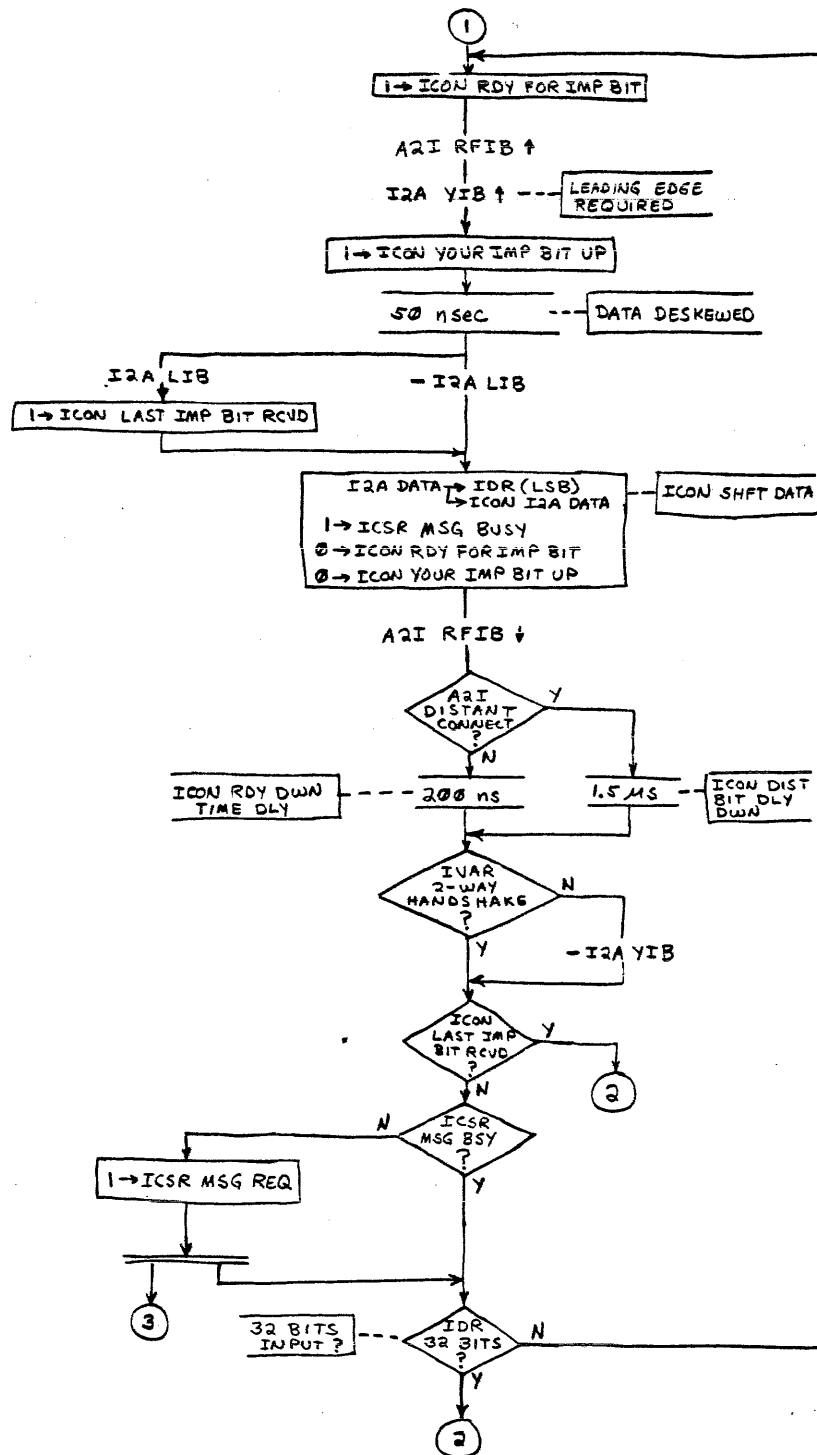
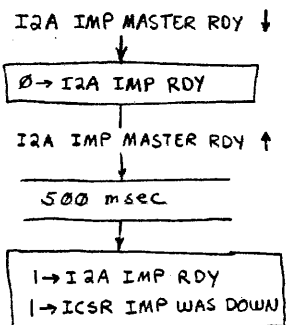


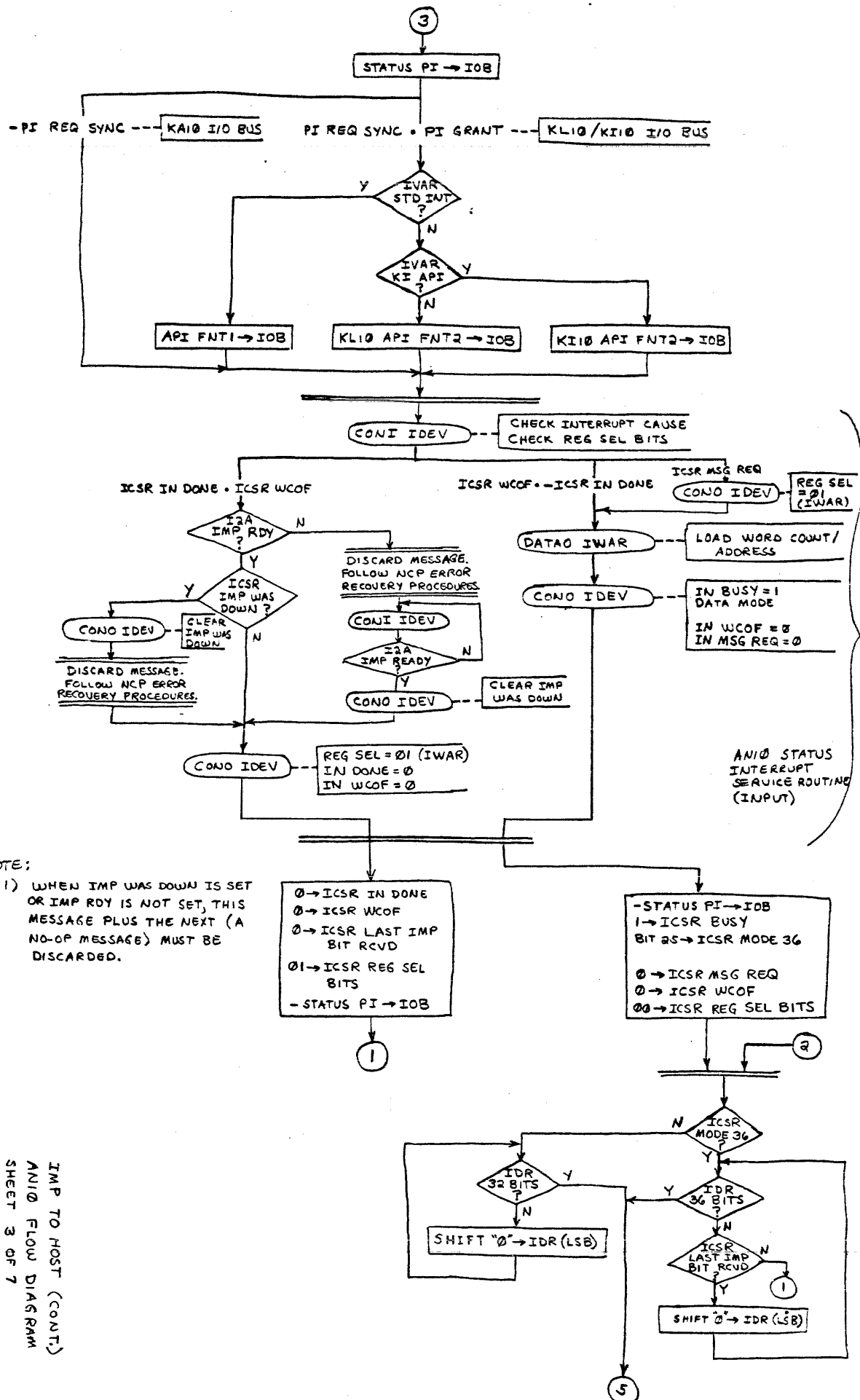
FIGURE 12.

IMP TO HOST
 ANIO FLOW DIAGRAM
 SHEET 1 OF 7
 REV F 17-FEB-77

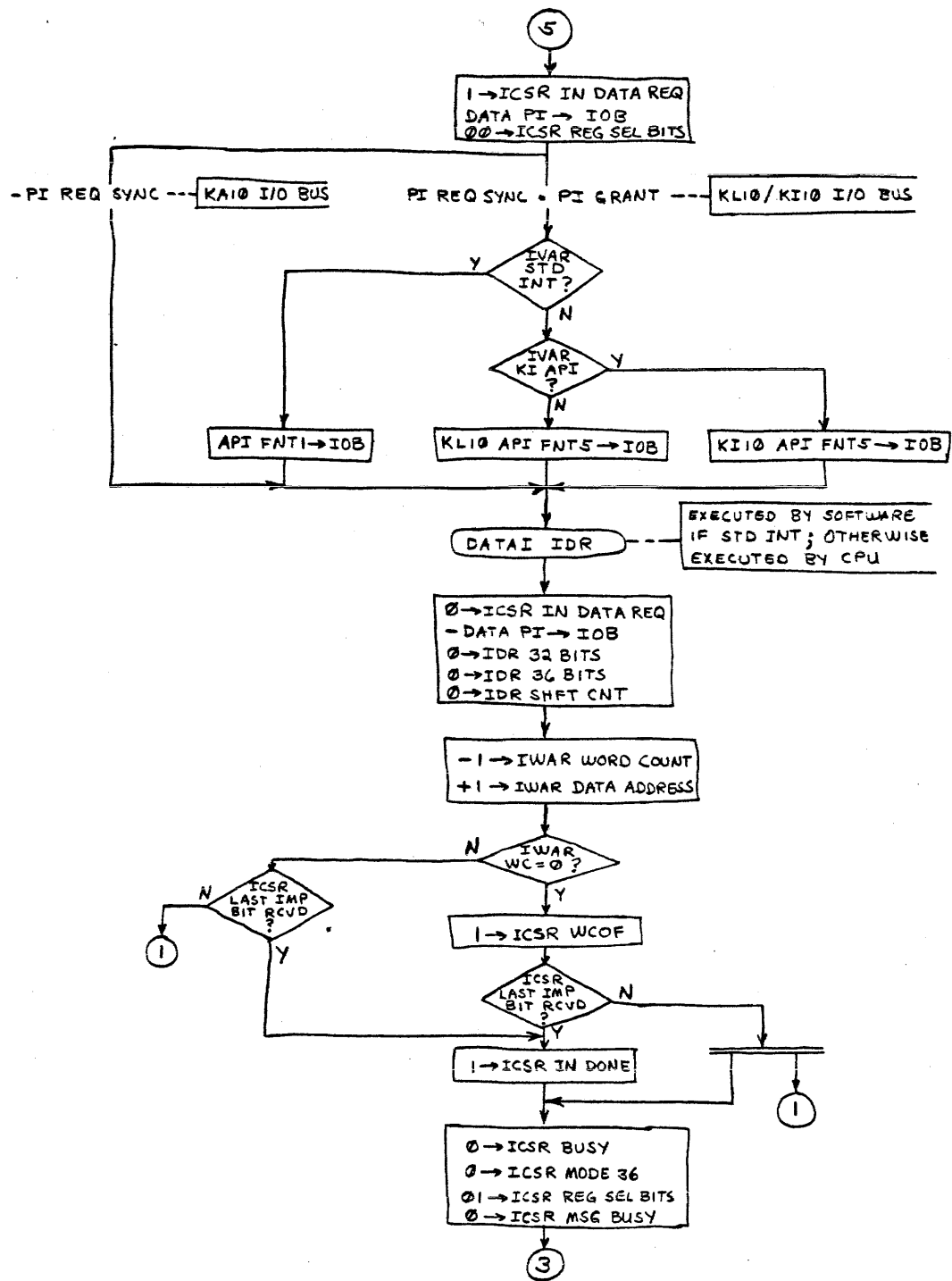


IMP TO HOST (CONT.)
 ANIO FLOW DIAGRAM
 SHEET 2 OF 7
 REV F 17-FEB-77

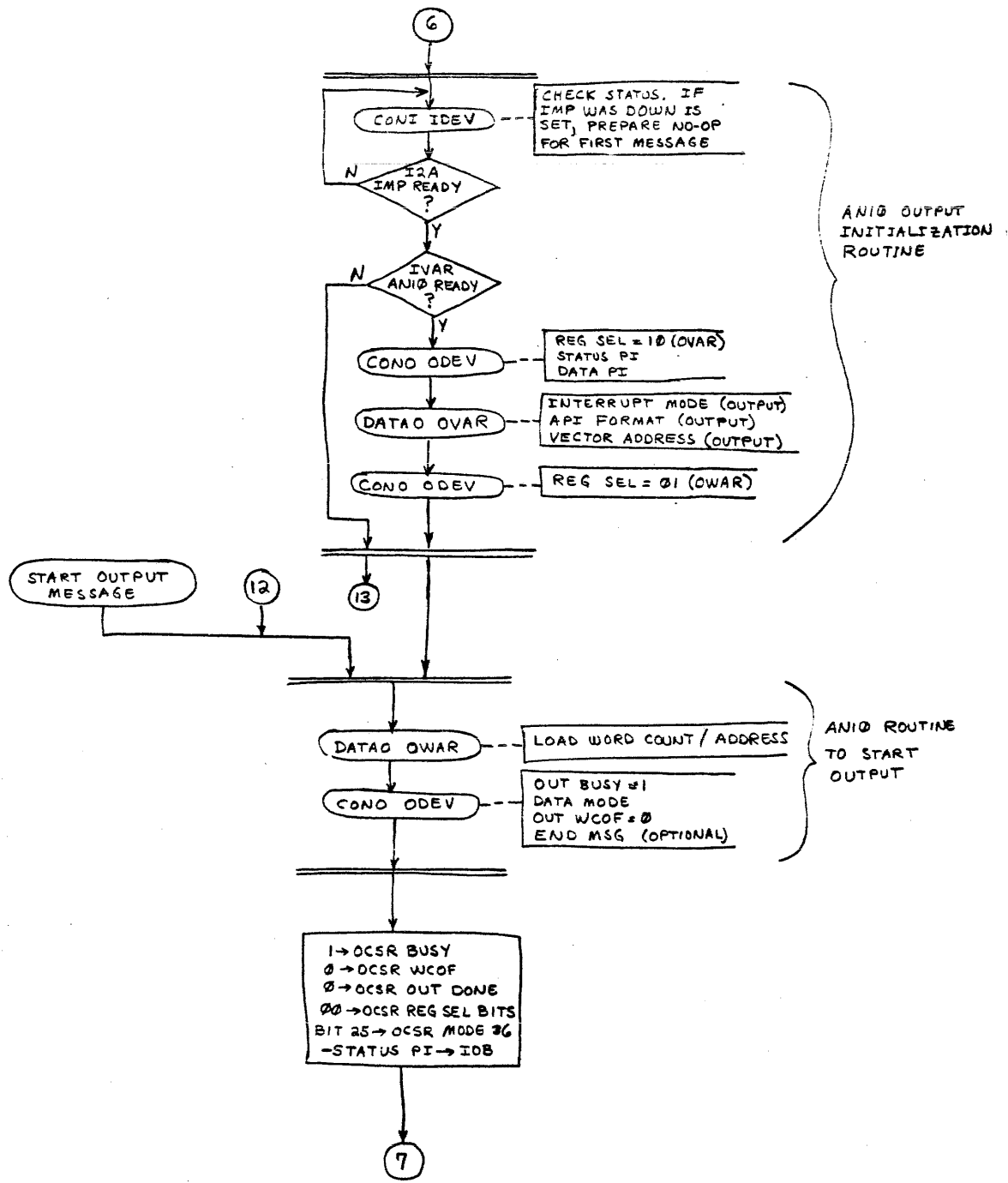


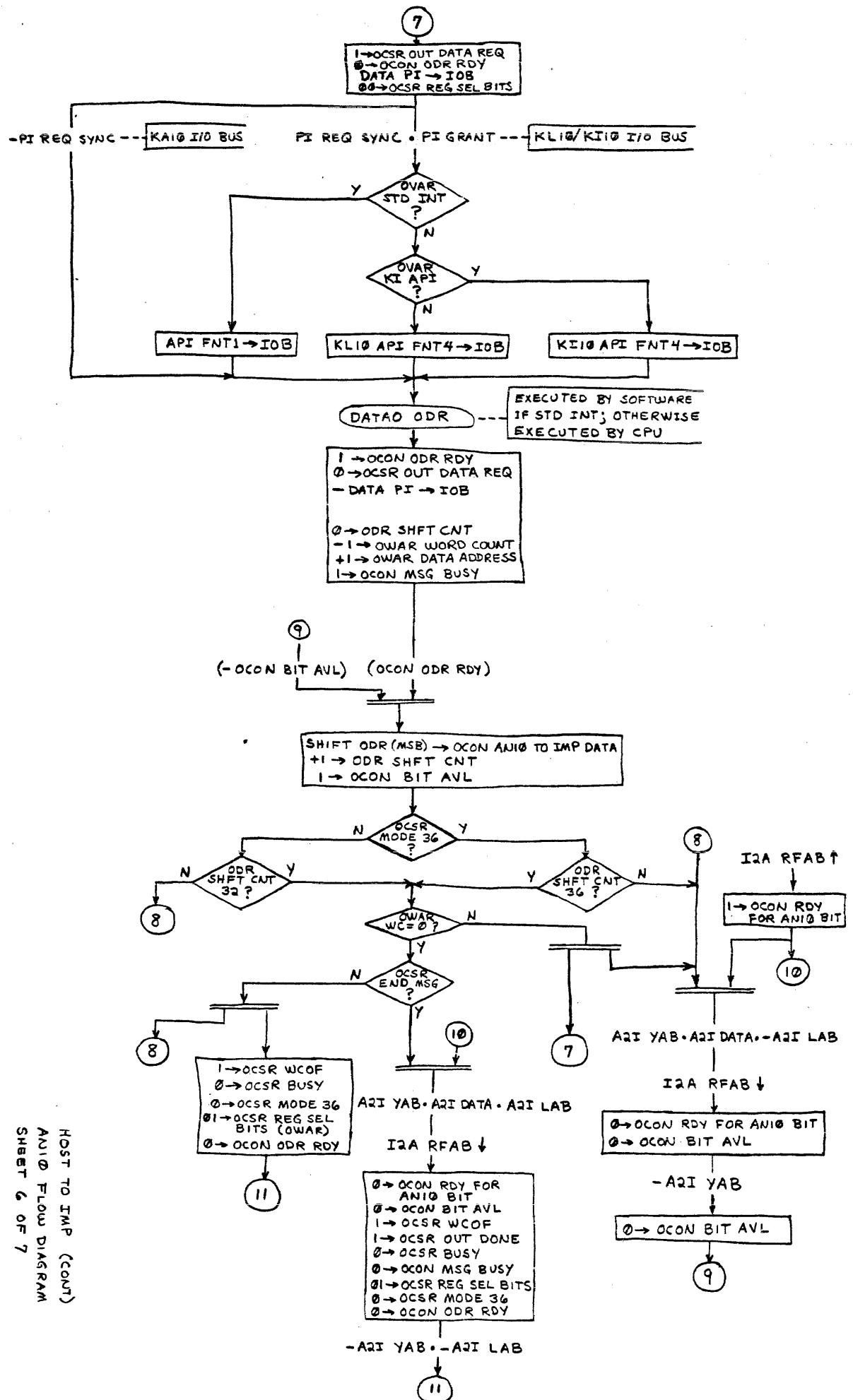


IMP TO HOST (CONT.)
AN10 FLOW DIAGRAM
SHEET 3 OF 7
REV F 17-FEB-77

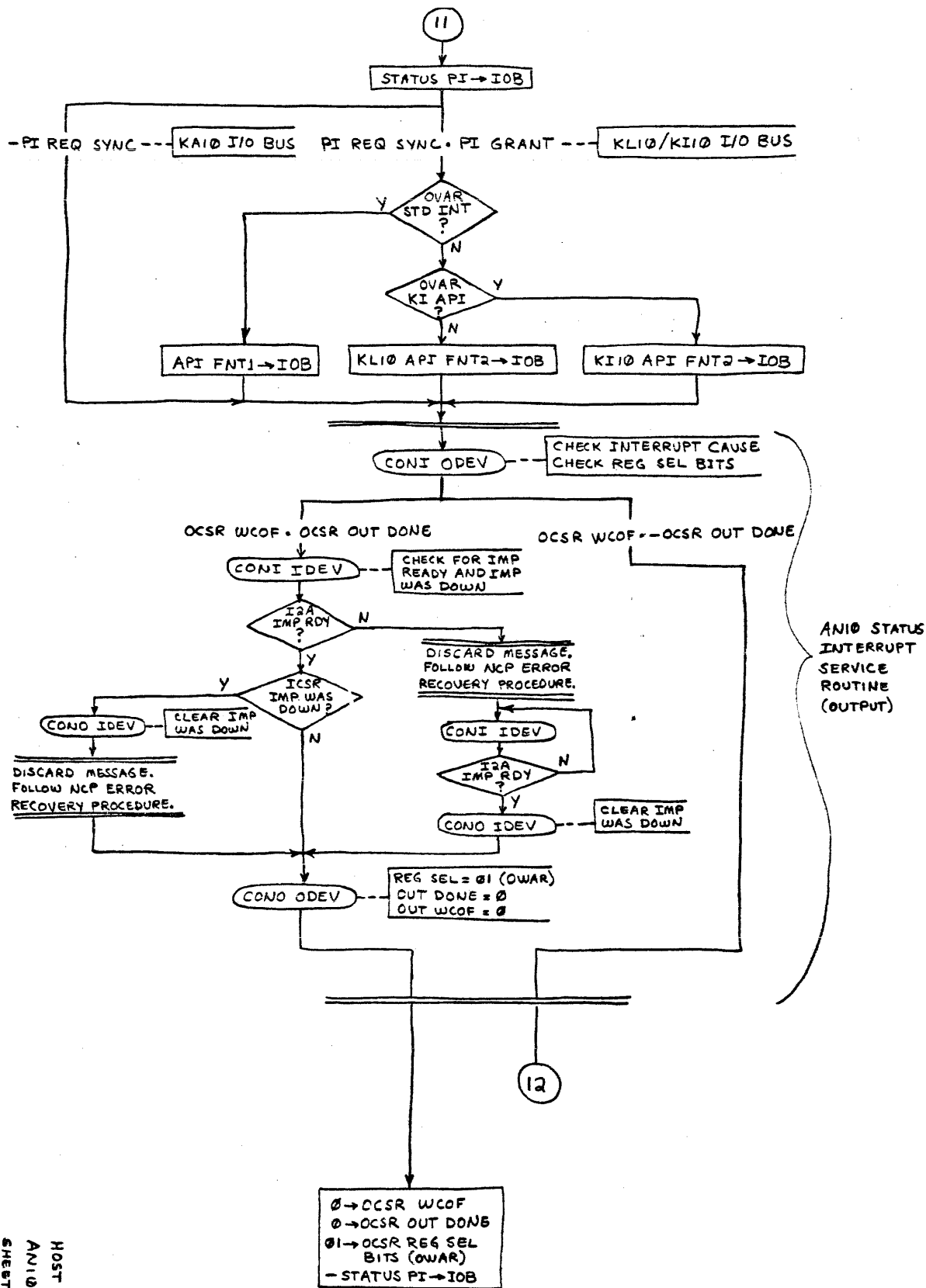


IMP TO HOST (CONT.)
 ANIO FLOW DIAGRAM
 SHEET 4 OF 7
 REV F 17-FEB-77





HOST TO IMP (CONT)
ANIO FLOW DIAGRAM
SHEET 6 OF 7
REV F 17-FEB-77



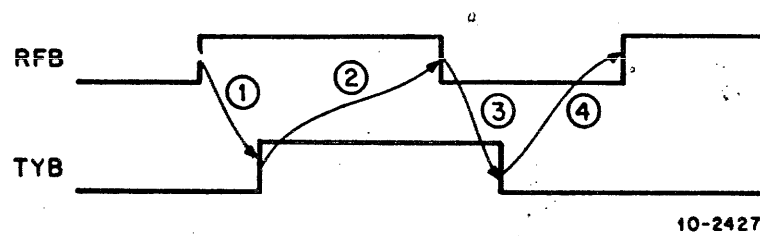
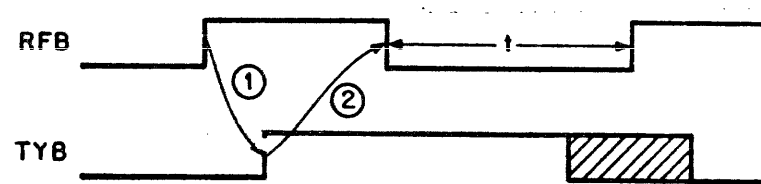


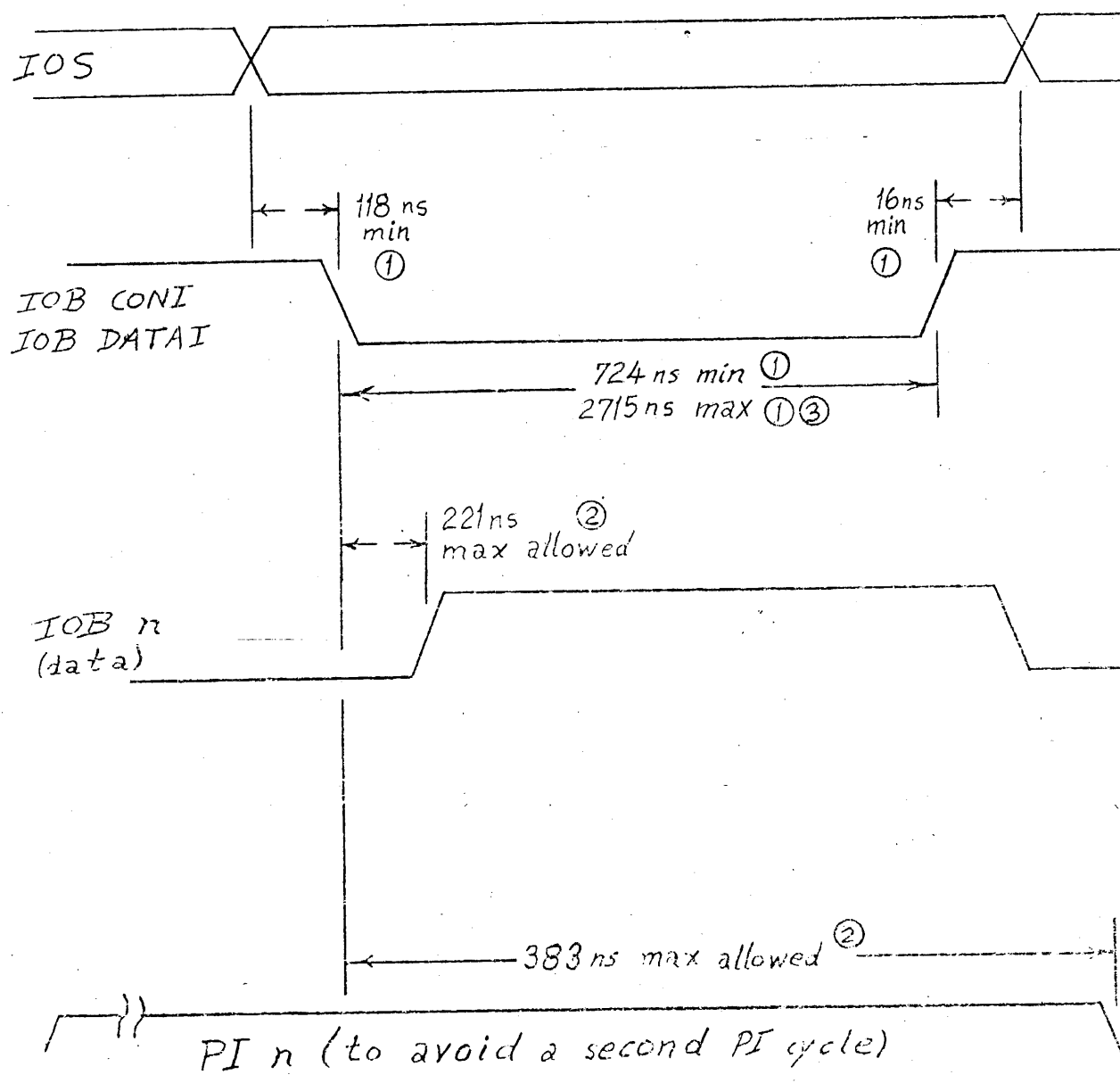
Figure 13 Four-Way Handshake



$t = 100\text{nsec local, } 1.5\mu\text{sec distant}$

10-2428

Figure 14 Two-Way Handshake



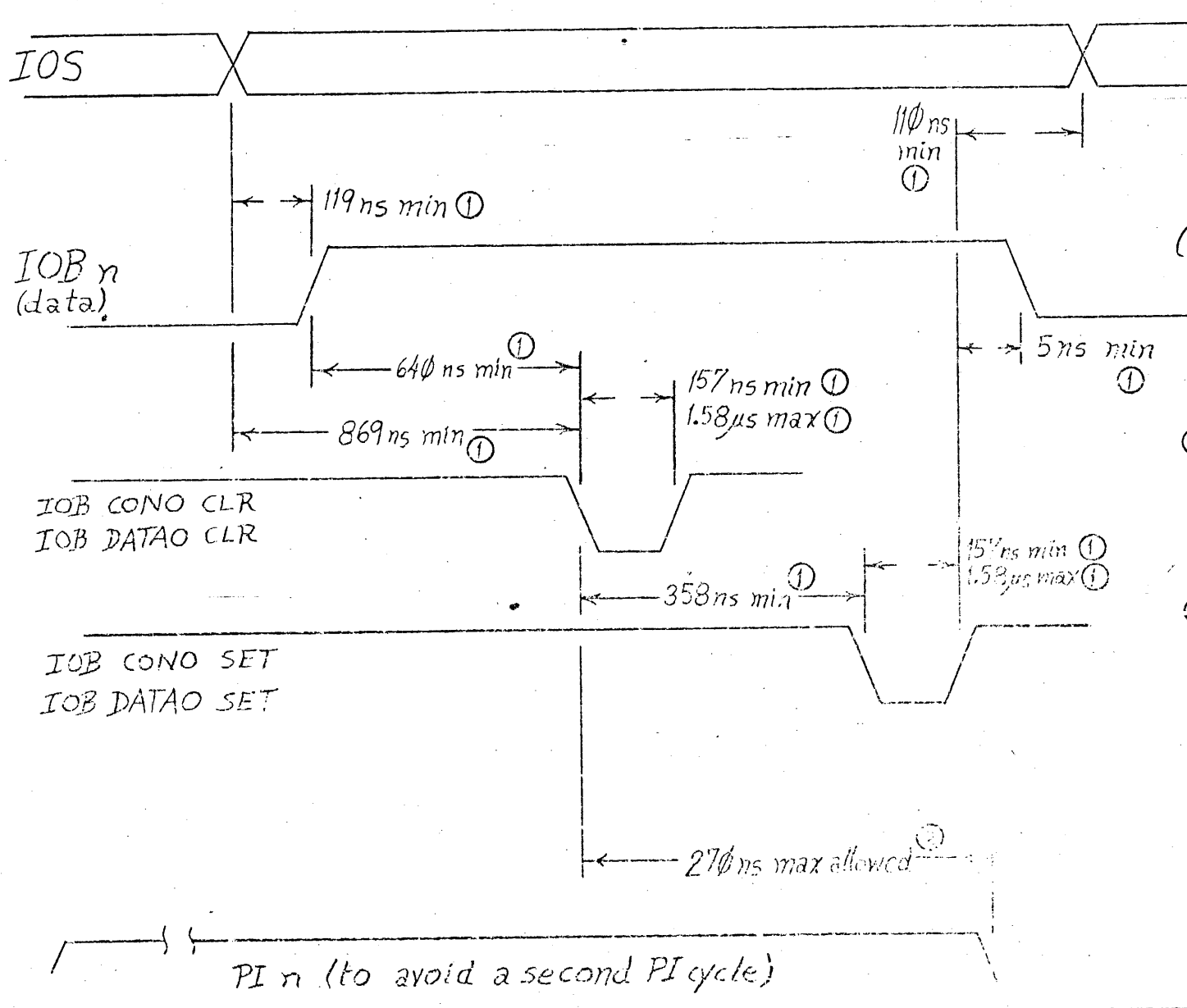
PDP-10 I/O Bus Device Requirements I/O Incoming (including KI10 Fast Bus)

All timing measured
at device on bus.

① Timing controlled by
processor and bus
design.

② Timing required of
device

③ No maximum when
KI10 is operated in
single-step mode.

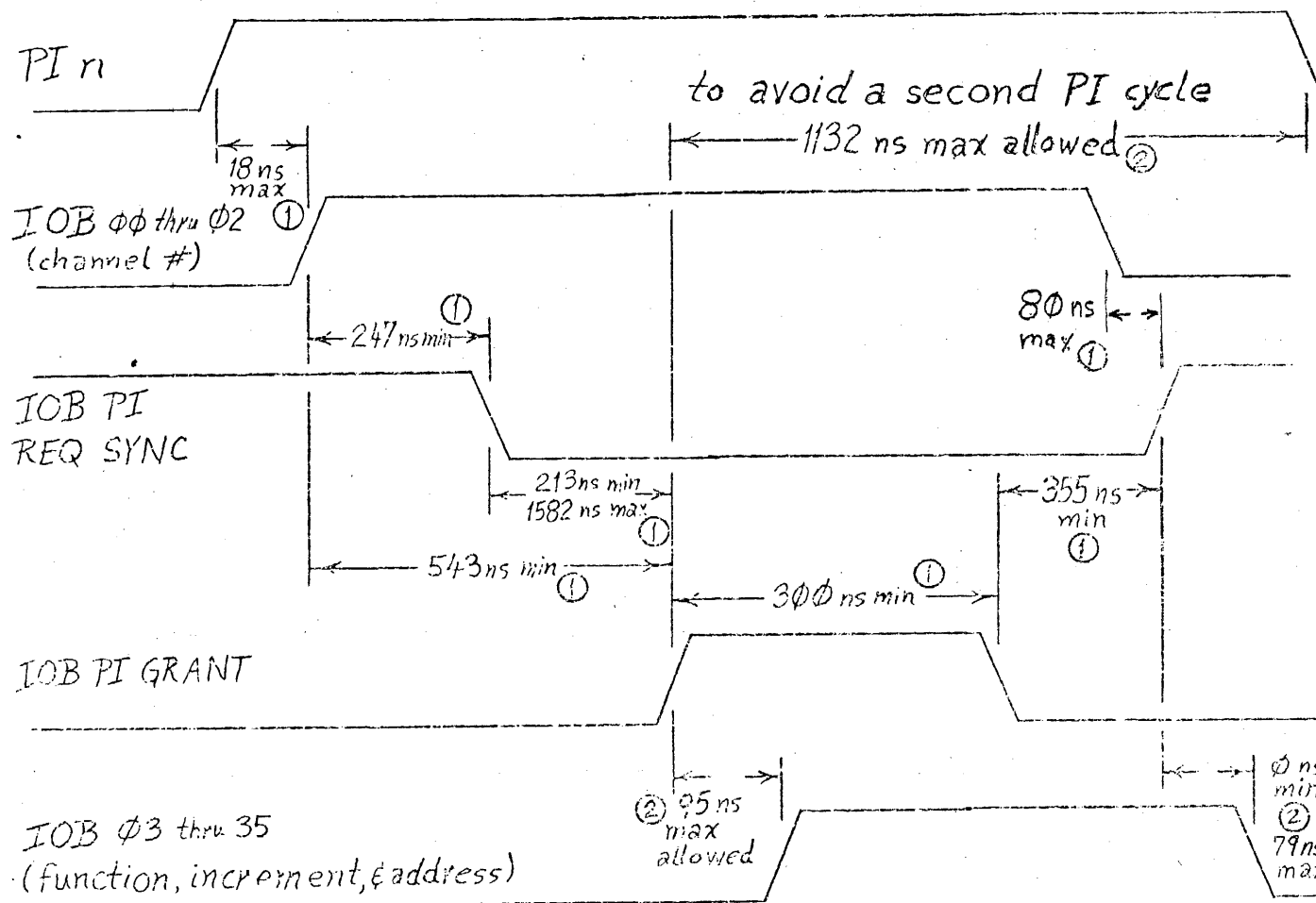


PDP-11
I/O Bus
Device Requirements
I/O Outgoing
(including K110 Fast Bus)

All timing measured
at device on bus.

① Timing controlled by
processor and bus
design.

② Timing required of
device.



PDP-10 I/O Bus

Device Requirements
Auto PI
(not available on
KA10 or 105 processors)

All timing measured
at device on bus.

① Timing controlled by
processor and bus
design.

② Timing required
of device.

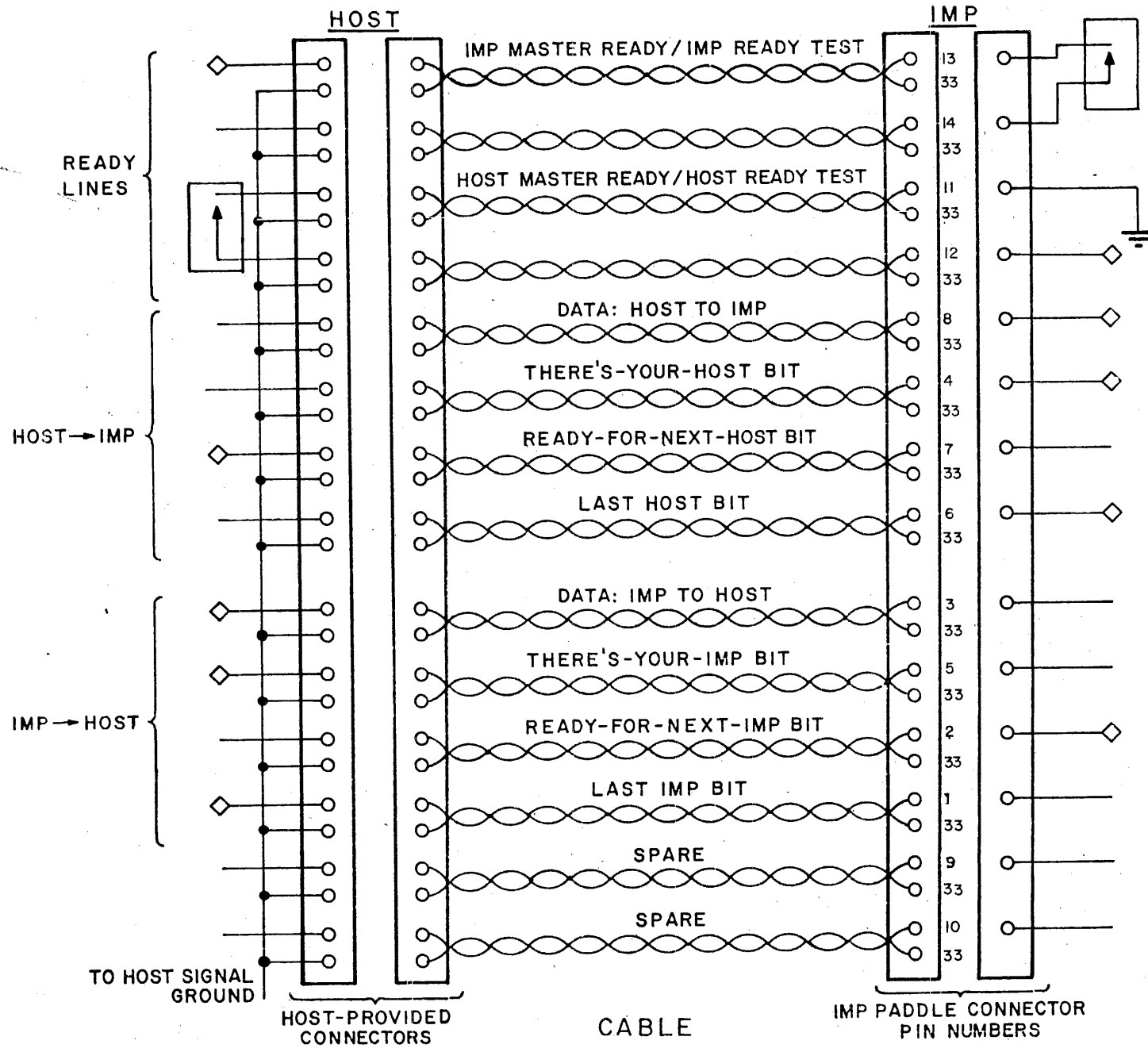


FIG. 4-5 316 HOST CABLE SIGNALS (LOCAL HOST)

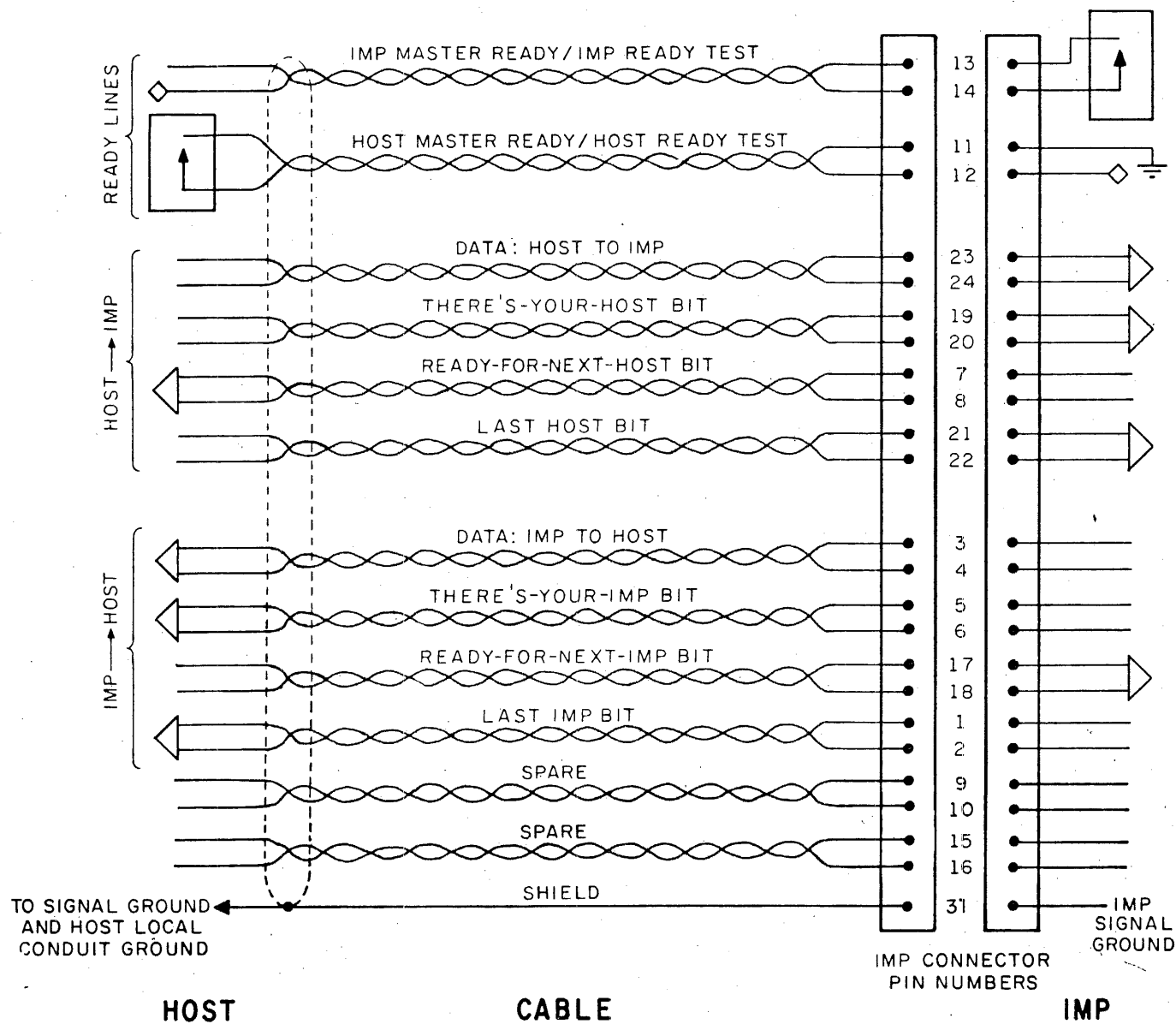


FIG. 4-7 HOST CABLE SIGNALS (DISTANT HOST)

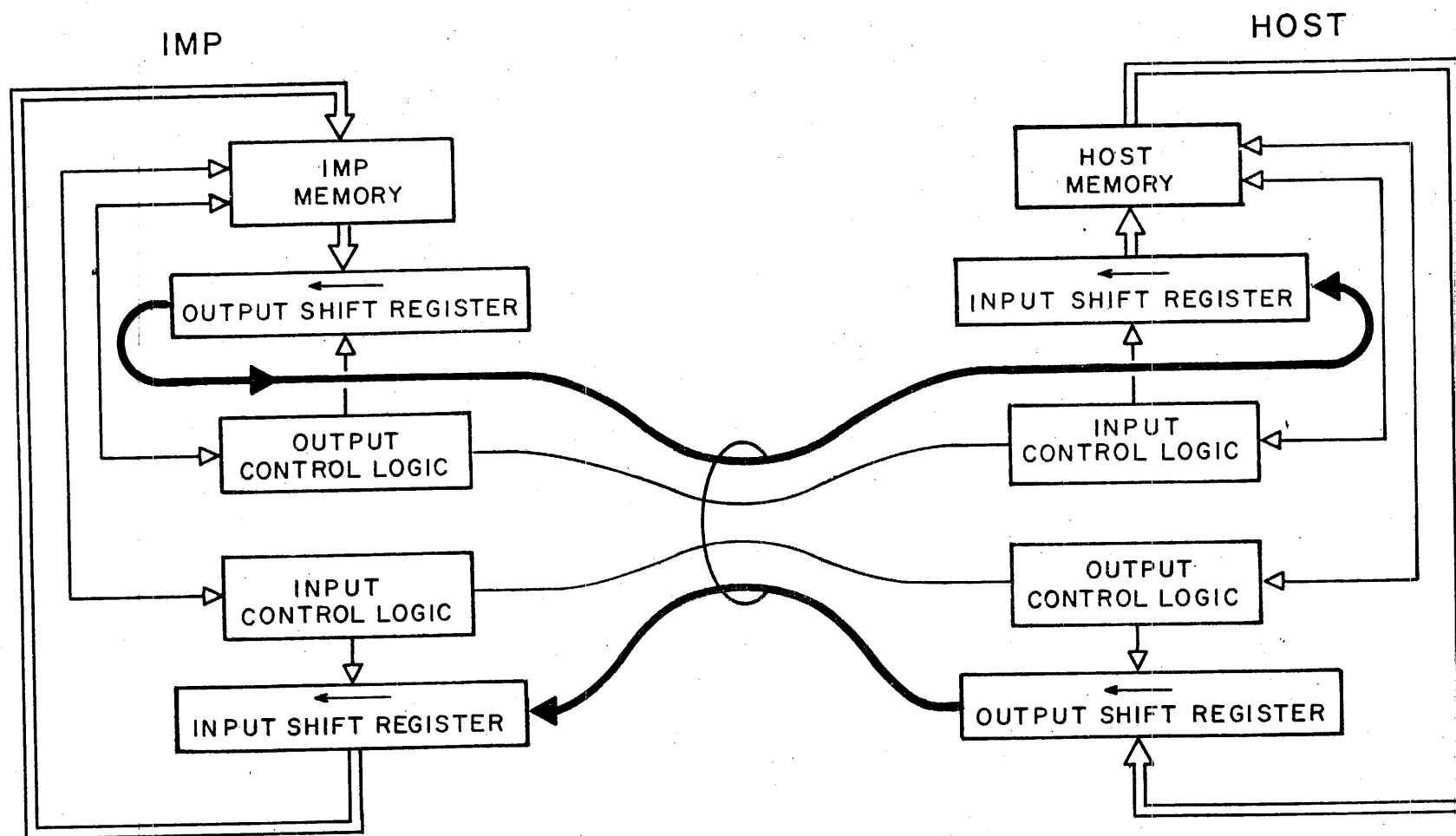


Figure 4-1. Simplified Illustration of Host/IMP Interface

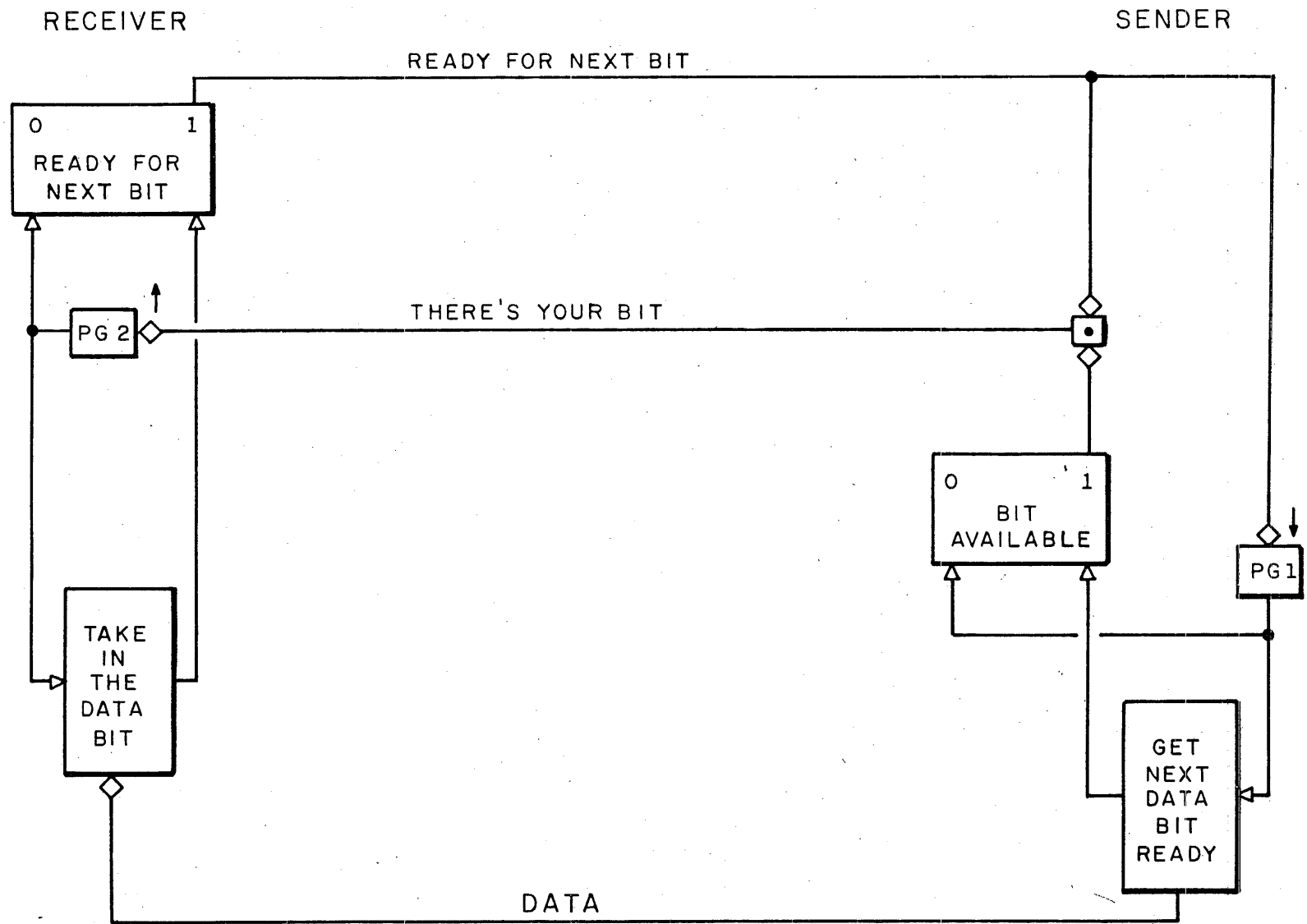


FIG.4-2 SIMPLIFIED CONTROL LOGIC FOR HOST/IMP HANDSHAKING

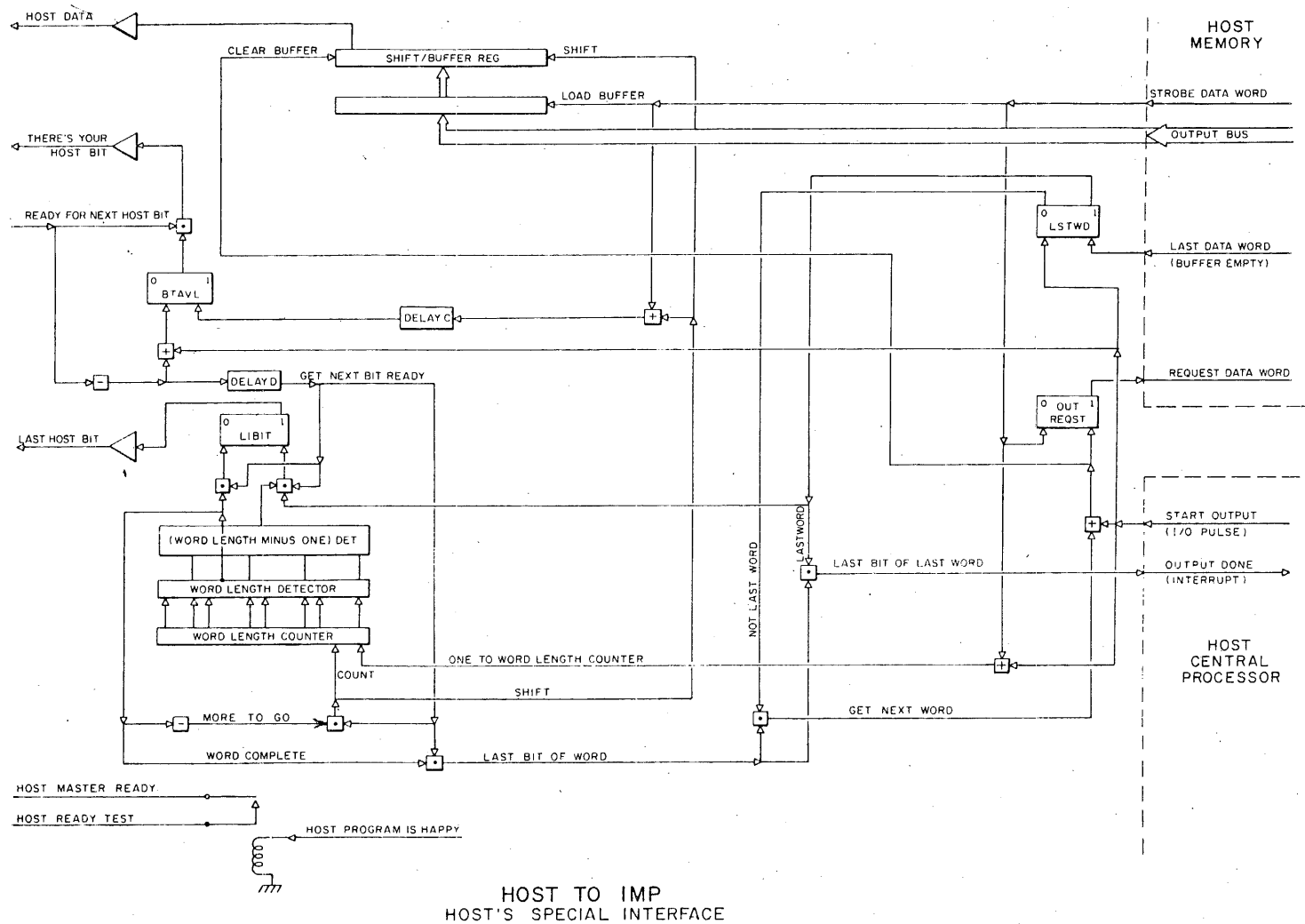


FIGURE B-1

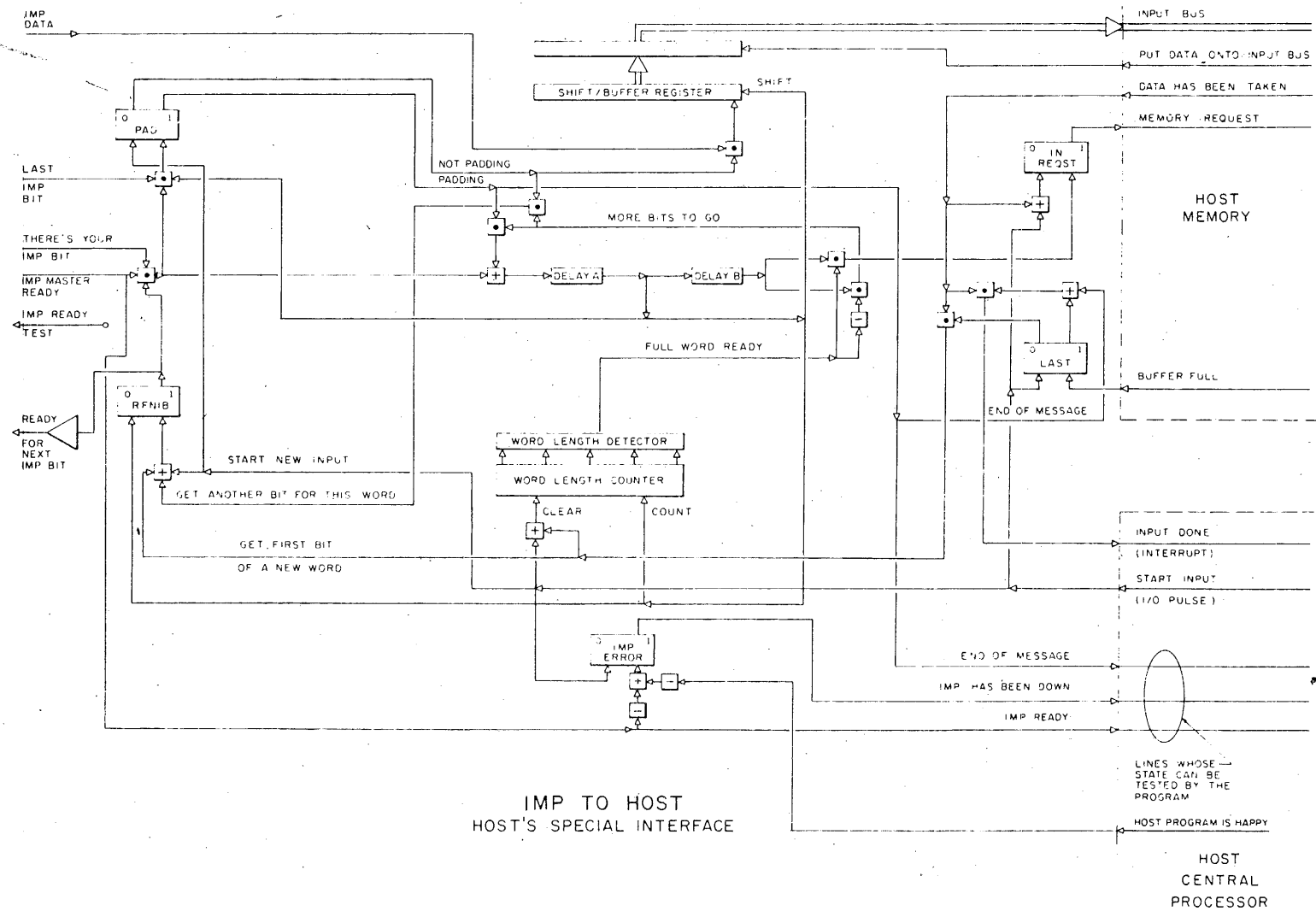


FIGURE B-2

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DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE 20-July-77

TITLE INSTALLATION PROCEDURE, AN10 INTERFACE OPTION.

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE

ENG	20-JUL-77	APPD	26-JUL-77	SIZE	CODE	NUMBER	REV
David Anderson		David Anderson		A	SP	AN10-0-IP	

EN-1079A-16 (R873-(392))
DRA 107A

SHEET 1 OF 5

TITLE INSTALLATION PROCEDURE, AN10 INTERFACE OPTION

1.0 SITE CONSIDERATIONS

All general requirements specified for the DECsystem-10 and the DECsystem-20 in the DECsystem-10/20 Packet Switching System Site Preparation Guide EK-PACSS-SP are applicable to the AN10.

The AN10 should be located so as to provide sufficient service clearance areas at the front and rear of the H956 cabinet in which it is mounted. A 1-phase standard power circuit should be provided for the AN10.

1.1 Local Connection to the IMP

When the AN10 is supporting a local connection to the IMP, the cable-run distance must be no more than nine meters (30 feet) from the IMP.

1.2 Distant Connection to the IMP

When the AN10 is supporting a distant connection to the IMP, the cable-run distance must be no more than 610 meters (2000 feet) from the IMP; a 15-meter (50 foot) BC10T cable is standard with the AN10-B.

2.0 INSPECTION

Upon receipt of the AN10, inspect the exterior and interior of the equipment for any visible signs of damage from shipping. Verify that all logic modules are secure in the correct slots. Any damage observed should be immediately reported to both the carrier and the manufacturer. Check the contents of the shipment with the shipping list (A-SP-AN10-SP) and immediately report any omissions to the manufacturer. Installation is not recommended until all materials are present.

3.0 INSTALLATION AND CABLING

The AN10 Interface Option logic and associated H7420A Power Supply and 861 Power Controller are mounted in an H956 cabinet. To install the AN10 Interface Option, perform the following installation and cabling procedures:

SIZE	CODE	NUMBER	REV
A	SP	AN10-0-IP	

TITLE INSTALLATION PROCEDURE, AN10 INTERFACE OPTION

3.0 (continued)

1. Remove the cabinet from the shipping skid and position it according to the applicable site installation floor plan.
2. Set the H7420A Power Supply circuit breaker to OFF. Set the 861 Power Controller circuit breaker to OFF.
3. Assure that logic modules are securely installed accordingly to drawing D-MU-AN10-0-MU.
4. Connect the AN10 cabinet ground stud to the adjacent cabinet (if any) with a 90-08887 or equivalent ground braid or else to an appropriate earth ground by #4AWG copper wire (7010789 or equivalent). Refer to E-UA-AN10-0-0.
5. Connect the BC10B margin check cable between the AN10's 70-05868 Margin Check Power Distribution Bar and the margin check control panel of the DECsystem-10. When the AN10 is the last device on the margin check bus, assure that margin check terminating plug 70-05486 is also installed in the 70-05868 Margin Check Power Distribution Bar. Proper installation of the margin check bus is necessary because the bus supplies -15V power for terminating the H867 I/O bus terminator.
6. Connect the AN10 861 Power Controller to the power control bus in the DECsystem-10 with the 70-08288 Remote Turn-on cable.
7. Assure that the H867 I/O Bus Terminator is securely installed in Quick-Latch connector QL2 labeled "I/O OUT" (on rear of AN10 Wired Logic Mounting Assembly -- refer to drawing D-AD-7013480-0-0).
8. Install the I/O Bus Cable (BC10J or K) to the AN10 cable connector QL1 labeled "I/O IN" (on rear of AN10 Wired Logic Mounting Assembly -- refer to drawing D-AD-701384-0-0) according to applicable site installation floor plan. Assure that the BC10J or K cable is properly connected to the Host computer.

	SIZE A	CODE SP	NUMBER AN10-0-IP	REV
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TITLE

INSTALLATION PROCEDURE, AN10 INTERFACE OPTION

9. Connect the AN10 IMP cable (BC10T-xx ((distant connection)) or customer supplied cable ((local connection)) to the mating ZIF connector labeled "IMP IN" (on rear of AN10 Wired Logic Mounting Assembly). Install the IMP end of the AN10 IMP cable at the IMP interface according to the appropriate site installation plan supplied with the IMP.
10. Verify that a BC06R-4 flat 40-conductor cable is connected into the left-most (facing front of AN10 cabinet) "IMP IN" connector. The other end of this cable feeds through the wired assembly cable service opening and installs into connector J1 (top connector) on the M8612 module in slots AF13. See print E-UA-AN10-0-0.
11. Verify that a BC06R-4 flat 40-conductor cable is connected into the right-most (facing front of AN10 cabinet) "IMP IN" connector. The other end of this cable feeds through the wired assembly cable service opening and installs into connector J2 (bottom connector) on the M8612 module in slots AF13. See print E-UA-AN10-0-0.
12. Verify that a BC06R-1 40-conductor flat cable is connected from the right-most "IMP LOOP" connector (facing front of AN10 cabinet) to the left-most "IMP LOOP" connector. The cable loop should be dressed through the wired logic assembly cable service opening. Refer to print E-UA-AN10-0-0.
13. Install the AN10 Loop Back Test connector assembly 70-13963 on the mating ZIF connector labeled "IMP LOOP" (on back of AN10 Wired Logic Mounting Assembly).
14. Verify that no shorts between +5V, -5V, -15V, and ground exist on the backplane assembly.
15. Connect the 861 Power Controller input power cable to the appropriate AC facility power. Refer to EK-PACSS-SP.
16. Set the 861 Power Controller REMOTE ON/OFF/LOCAL ON toggle switch to REMOTE ON. Set the circuit breaker to ON.
17. Set the H7420A Power Supply circuit breaker to ON.

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TITLE INSTALLATION PROCEDURE, AN10 INTERFACE OPTION

18. Power up the DECsystem and check the AN10 for proper fan rotation and for absence of smoke.
19. Adjust the H744, H745, and H754 power supply regulators for their respective voltage as necessary.
20. Verify that -15V power is properly applied to the I/O bus terminator at pin 30T2 of the quick-latch connector QL2 labeled "I/O OUT".
21. With the AN10 powered up, verify that the G891 power fail crobar has properly released. That is, A15M2 should no longer be at ground potential and A15V2 should be at +3V.
22. Perform the specified Acceptance Test Procedure. (A-SP-AN10-0-AP).

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