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PARTS REFERENCE

ITEM NO	DRAWING REFERENCE	DESCRIPTION	PART NUMBER	QUANTITY
1	C1, C2, C3, C4, C5, C6, C7, C8, C9, C10, C11, C12	.01 MFD, 100V, 20%	CAP 1001610	12
2	C13	6.8 MFD, 35V, 20%	CAP 1000067	1
3	C14	680 PF, 100V, 5%	CAP 1000026	1
4	C15	2200 PF, 250V, 10%	CAP 1000055	1
5	C16	1.0 MFD, 35V, 10%	CAP 1001776	1
6	C17	3.9 MFD, 10V, 10%	CAP 1000064	1
7	C18	.47 MFD, 35V, 10%	CAP 1005965	1
8	D1, D2, D3, D4, D5	6664	DIODE 1100114	5
9	E1, E5	DEC 7402N	I.C. 1909004	2
10	E2, E7	DEC 7474N	I.C. 1905547	2
11	E3	DEC 7450N	I.C. 1905580	1
12	E4, E8, E12	DEC 9601N	I.C. 1909373	3
13	E6, E10	DEC 7400N	I.C. 1905575	2
14	E9	DEC 7410N	I.C. 1905576	1
15	E11	DEC 8881	I.C. 1909705	1
16	R1, R3	470 OHM, 1/4W, 5%	RES. 1300316	2
17	R2, R4	220 OHM, 1/4W, 5%	RES. 1300271	2
18	R5	20K OHM, 1/4W, 5%	RES. 1302391	1
19	R6	22K OHM, 1/4W, 5%	RES. 1301808	1
20	R7	10K OHM, 1/4W, 5%	RES. 1300479	1

NOTES:

1. PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE PROCESSOR. MODULE REFERENCE ALONE IS OBTAINED BY DELETING THE NUMBER (SLOT LOCATION) AFTER THE FIRST LETTER, AND CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT RIGHT.

2. ALL SIGNALS THAT HAVE MODULE PINS ARE SO NOTED; MULTIPLE NOTATIONS OF THE SAME SIGNALS WITHIN A MODULE HAVE THE PIN NOTED ON EACH. AN INPUT SIGNAL IS NOTED ONLY ONCE PER SHEET UNLESS SEPERATE PINS ARE USED; MULTIPLE INPUTS ARE CONNECTED. MODULE OUTPUT SIGNALS ARE BROUGHT TO THE EXTREME RIGHT OF EACH SHEET.

3. PROCESSOR SIGNAL SOURCE NOTATION (KP-2, FOR EXAMPLE) IDENTIFIES THE SIGNAL SOURCE (PRINT AND MODULE). THE FIRST NUMBER AFTER THE K INDICATES THE MODULE PRINT SET, WHILE THE SECOND INDICATES THE SHEET WITHIN THE SET. IF ON A PRINT, THE FIRST NUMBER OF THE K PREFIXES COINCIDE FOR A SIGNAL NAME AND THE PRINT (SEE TITLE BLOCK). THE SIGNAL IS GENERATED ON THE MODULE. A DIFFERENCE IN THE FIRST NUMBER OF THE K PREFIXES INDICATES A SIGNAL GENERATED OFF THE MODULE. SIGNALS WITH A "BUS" PREFIX REPRESENT A "WIRED OR" SITUATION, AND MULTIPLE SOURCES FOR THE SIGNAL CAN EXIST.

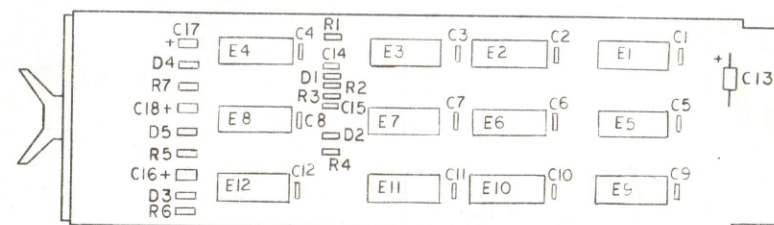
4. DETAILS ON COMPONENTS ARE NOTED IN THE PARTS REFERENCE. PLACEMENT IS NOTED IN THE COMPONENT PLACEMENT DIAGRAM.

5. GND AND +5V ARE USUALLY PIN 7 AND PIN 14, RESPECTIVELY. EXCEPTIONS ARE:

IC TYPE	GND	+5V
DEC 7481	PIN 10	PIN 4
DEC 7482	PIN 11	PIN 4
DEC 8251	PIN 8	PIN 16
DEC 8271	PIN 8	PIN 16
DEC 380	PIN 1	PIN 8
DEC 384	PIN 1	PIN 8

6. UNLESS OTHERWISE NOTED: RESISTANCE IS IN OHMS; CAPACITANCE IS IN PICO FARADS; CAPACITORS WITHOUT ANY NOTED VALUES ARE .01 MFD

COMPONENT PLACEMENT



PIN NOMENCLATURE

MODULE PROCESSOR

A

B

D

C

REV

NUMBER

DCS M7217-0-1

REV

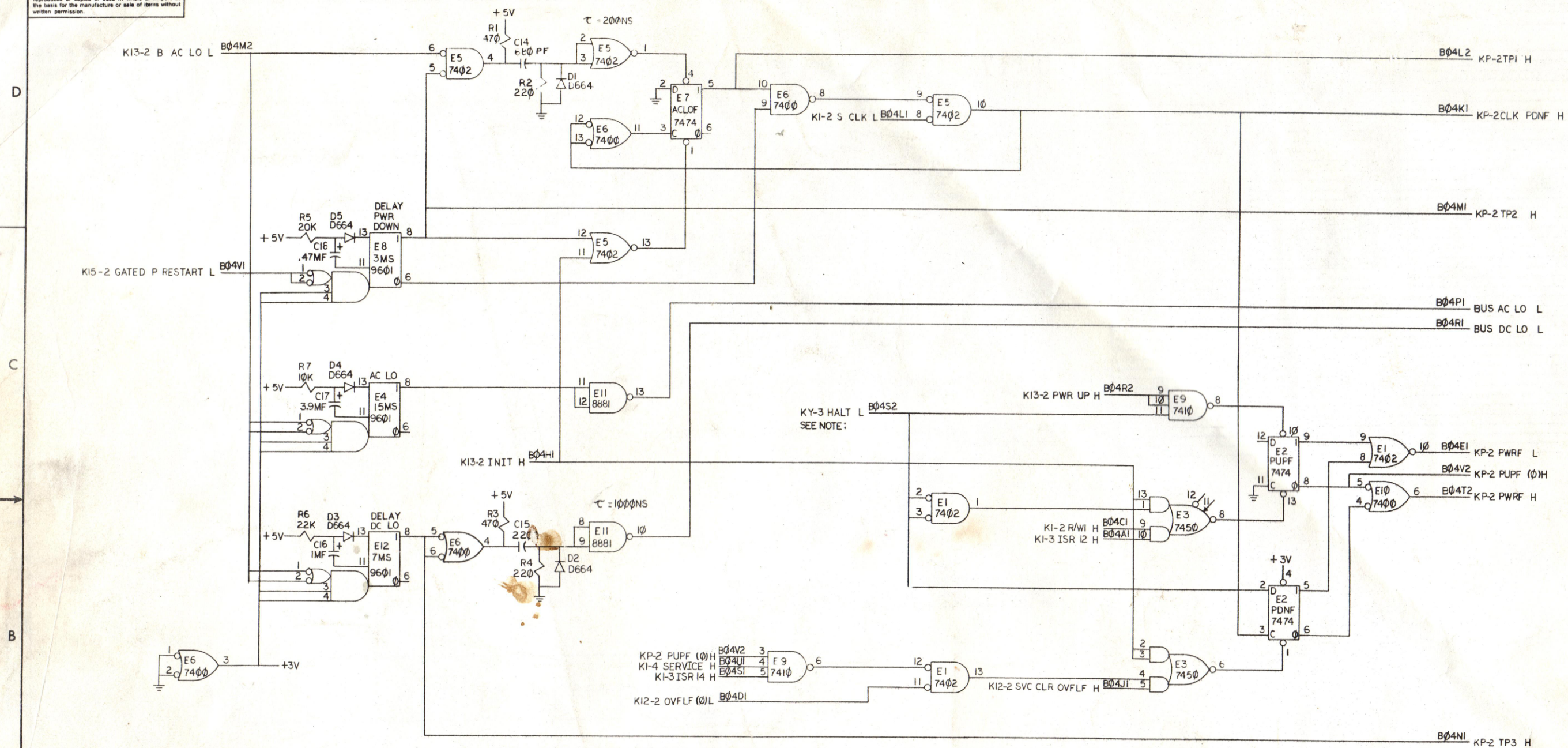
B

A

FIRST USED ON OPTION / MOD	QTY.	DESCRIPTION	PART NO.	ITEM NO.
KC11				
UNLESS OTHERWISE SPECIFIED	DRN. <i>Pay</i>	DATE <i>11-24-70</i>	PARTS LIST	
UNLESS OTHERWISE SPECIFIED	CHK'D. <i>Wall</i>	DATE <i>11/8/71</i>	digital EQUIPMENT CORPORATION	
DIMENSION IN INCHES	ENG. <i>Chen</i>	DATE <i>1-7-71</i>	TITLE	
TOLERANCES	PROJ. ENG. <i>Chen</i>	DATE <i>4-7-71</i>	PWR FAIL & RESTART	
DECIMALS FRACTIONS ANGLES	PROD. <i>Chen</i>	DATE	KP-2	
± .005 ± .164 ± 0°30'			NEXT HIGHER ASSY	
FINAL SURFACE QUALITY / REMOVE BURRS AND BREAK SHARP CORNERS			SIZE CODE	
MATERIAL			NUMBER	
FINISH			DCS M7217-0-1	
			REV. C	
			SHEET 1 OF 2	
			DIST. <i>344 1234 1235 2</i>	

5 PINK 1

MS 1



NOTE:
IF KY3 ECO #00003 (REV B WIRE LIST AND HIGHER)
AND ECO 5409235 #00003 (CS REV D AND HIGHER) ARE INSTALLED
THE SIGNAL AT B0452 IS KY-3 PHALT L AND NOT KY-3 HALT L.

FIRST USED ON OPTION / MOD PDP11		QTY.	DESCRIPTION		PART NO.		ITEM NO.	
UNLESS OTHERWISE SPECIFIED		DRN.	PARTS LIST					
UNLESS OTHERWISE SPECIFIED DIMENSIC IN INCHES		CHK'D.	DATE	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS				
TOLERANCES		ENG.	DATE	TITLE				
DECIMALS	FRACTIONS			PWR FAIL & RESTART				
± .005	± 1/64	PROJ.	DATE					
FINAL SURFACE QUALITY			DATE					
REMOVE BURRS AND BREAK SHARP CORNERS								
MATERIAL				M7217		KP-2		
				SIZE CODE	NUMBER	REV.		
				D	C	M7217-0-1	C	
				DIST.				

5 PINAL 1 MS 11