

FIELD MAINTENANCE PRINT SET

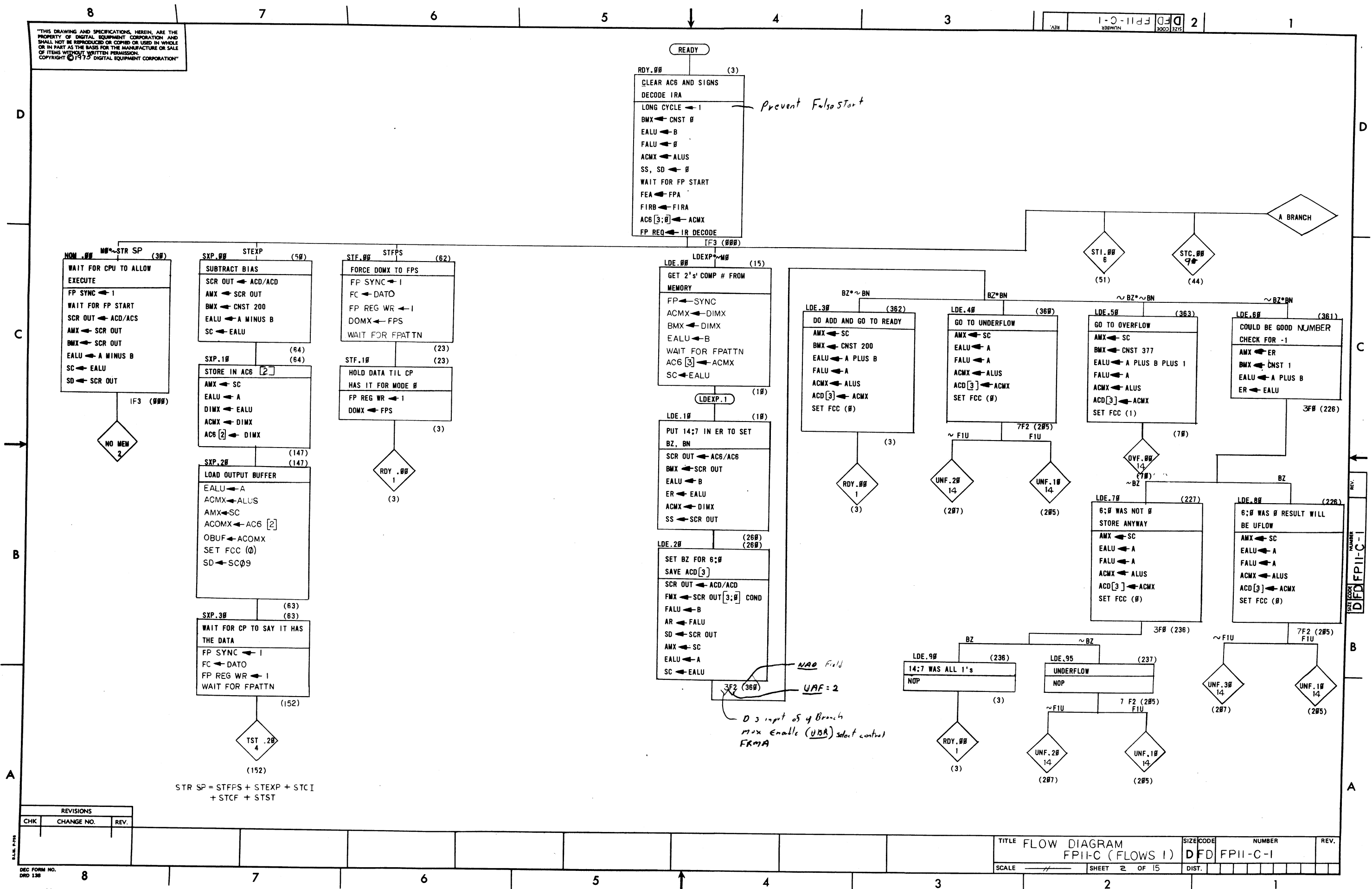
TABLE OF CONTENTS

[illegible]

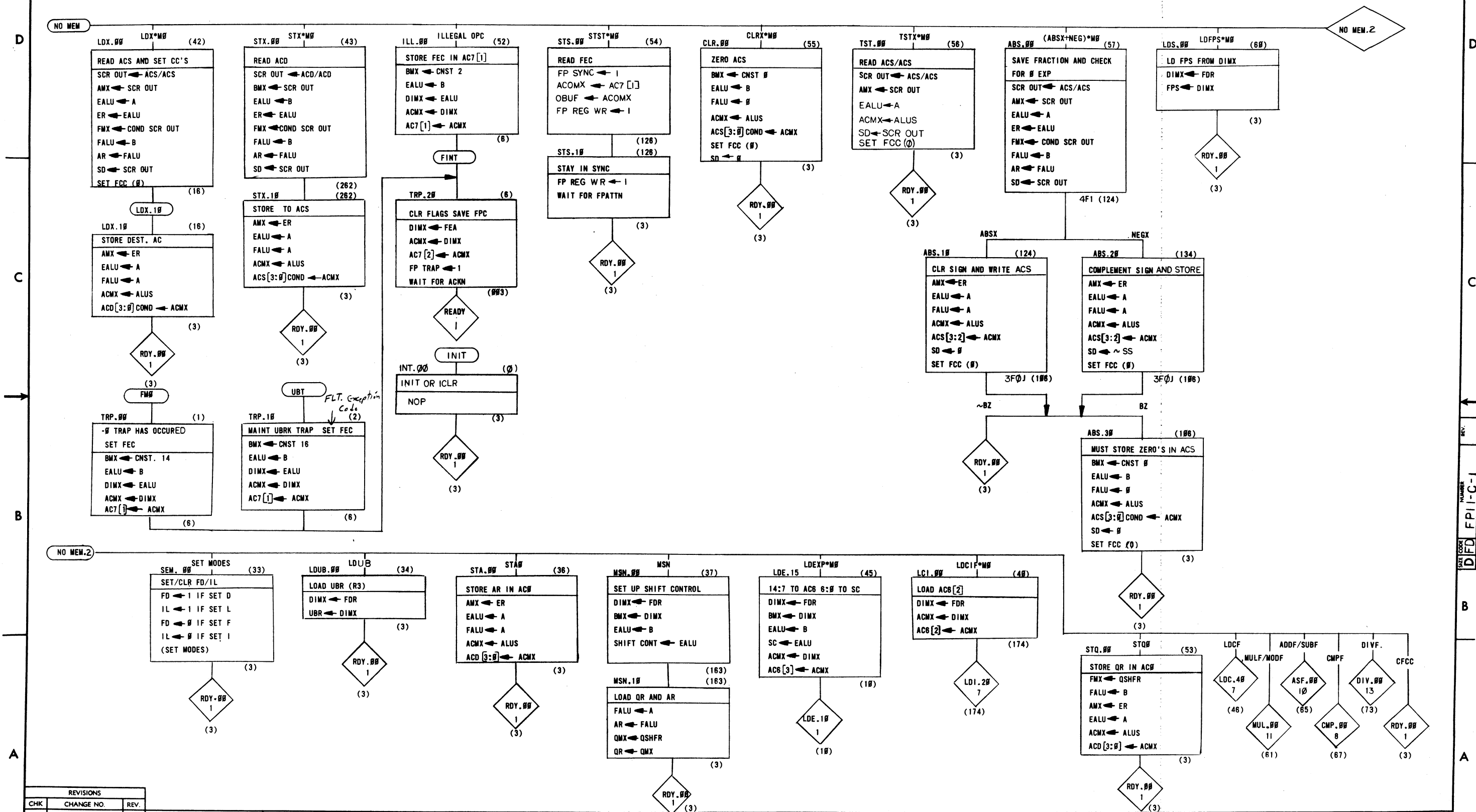
Digital Equipment Corporation

REVISIONS		USED ON OPTION/MODEL		DRN.	DATE	TITLE:											
DATE	CHG. NO.	REV.															
				11/72		D. HEALY	12/16/75	digit FLOATING POINT PROCESSOR FP11-C									
						CHK'D	DATE										
						D. HEALY	12/17/75										
						PROJ. ENG.	DATE										
						<i>Richard Old...</i>	12/22/75										
						FIELD SERV.	DATE	SIZE	CODE	NUMBER				REV.			
								B	TC	FP11-C-6							
								DIST.									

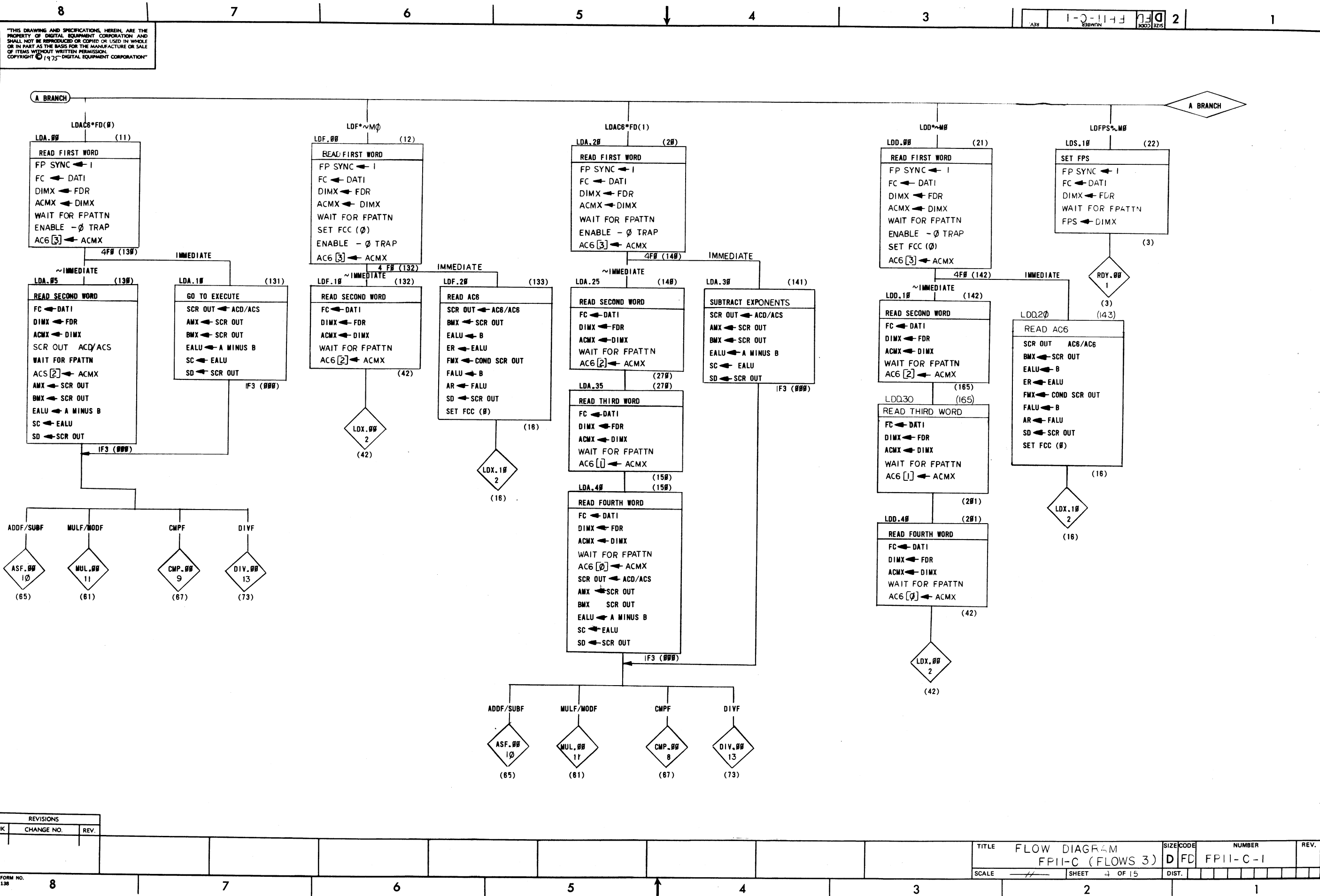
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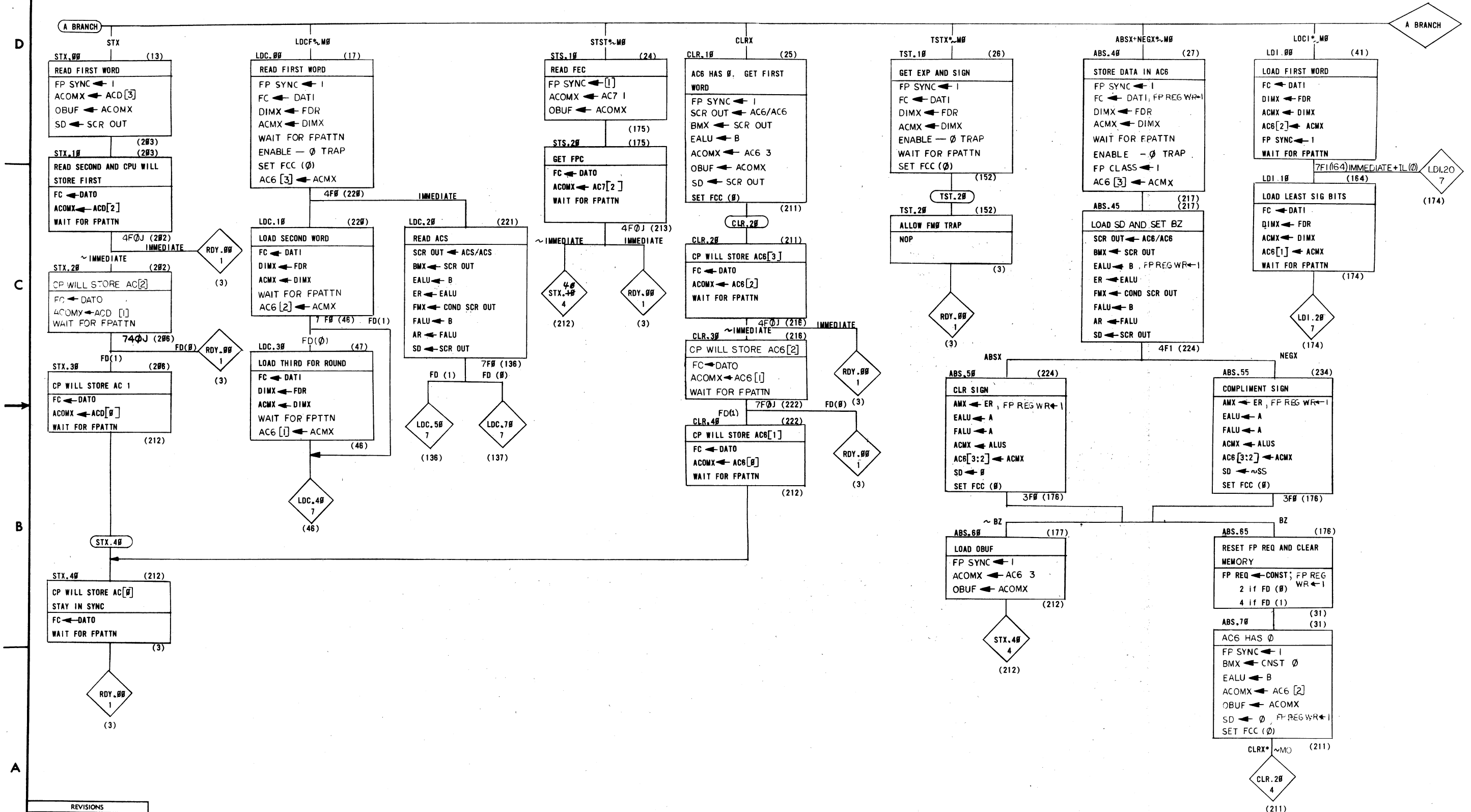
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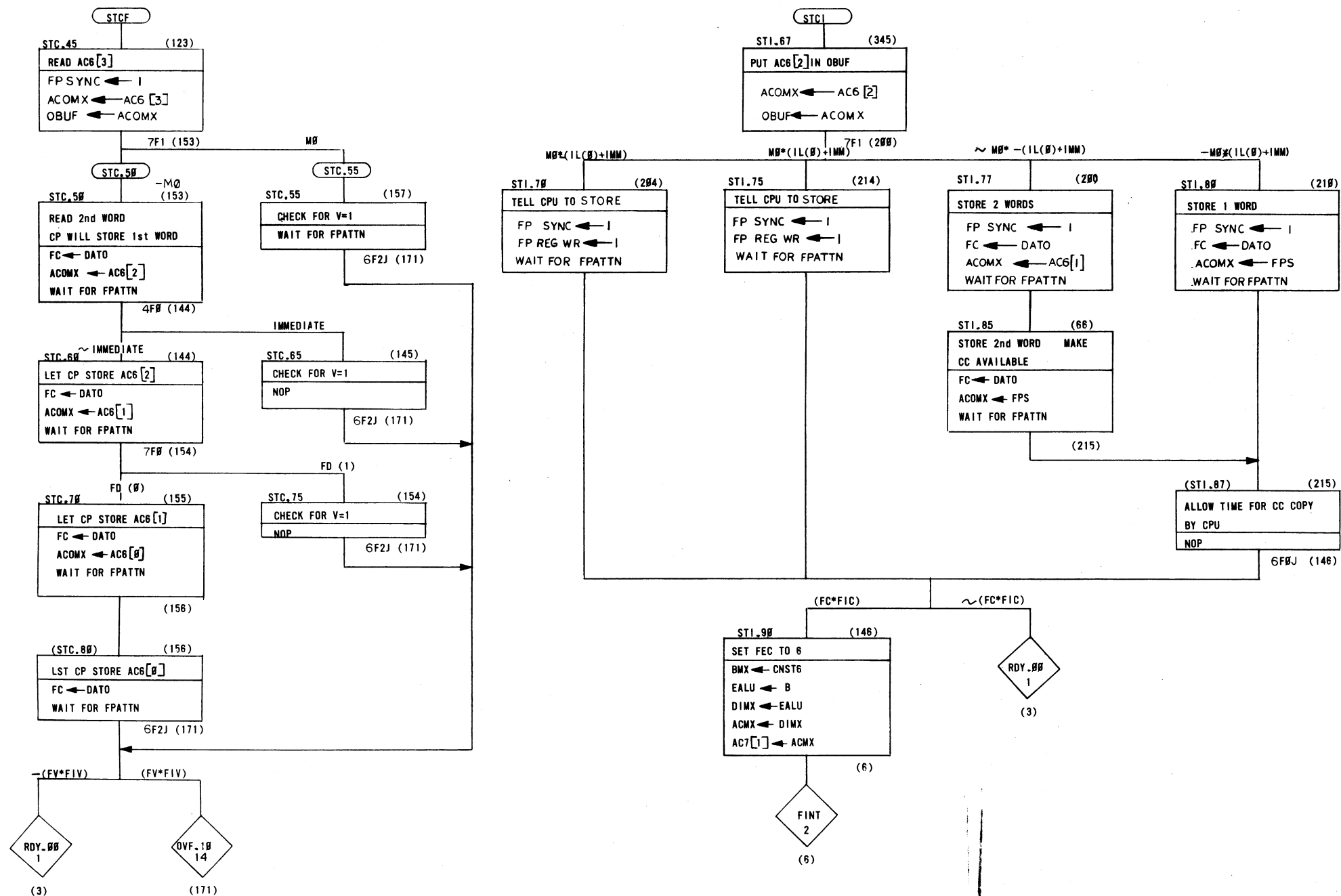
REVISIONS		
CHK	CHANGE NO.	REV.



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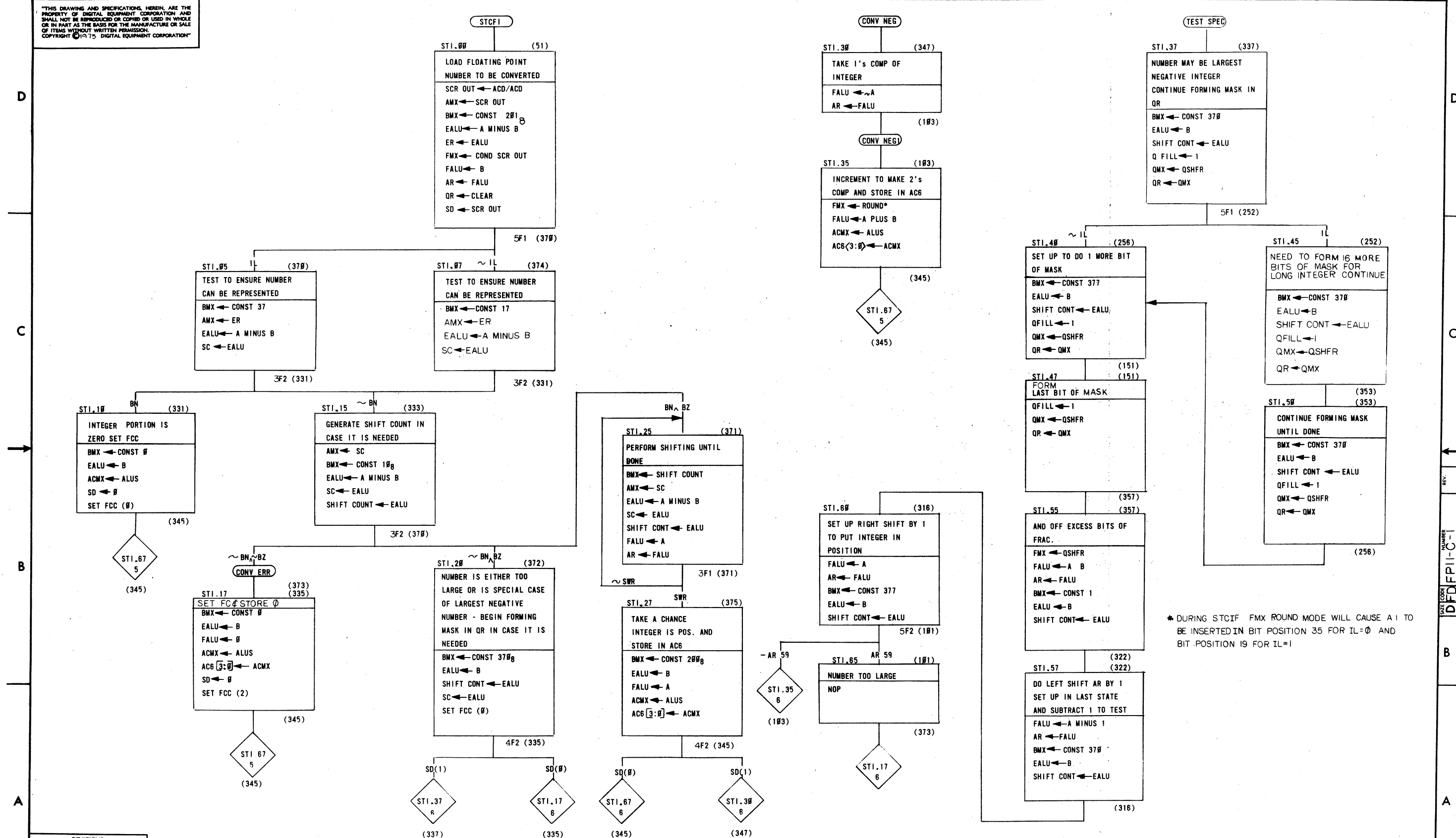


REVISIONS		
CHK	CHANGE NO.	REV.

DEC FORM NO. DRD 138

TITLE		FLOW DIAGRAM	SIZE CODE	NUMBER	REV.
FD		FD	FD	FD	FD
SCALE		1/1	SHEET	6 OF 15	DIST.

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REVISIONS		
CHK	CHANGE NO.	REV

8 7 6 5 4 3 2 1

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LDCIF (174)
LOAD INTEGER TO BE CONVERTED
SCR OUT ← ACS/ACS
FMX ← SCR OUT
FALU ← B
AR ← FALU
QR ← CLEAR
BMX ← CONST 7
EALU ← B
SHIFT CONT ← EALU
SD ← 0

LDC30 (213)
SHIFT INTEGER LEFT 7 PLACES
BMX ← CONST 2
EALU ← B
SHIFT CONT ← EALU
FALU ← A
AR ← FALU

LDC40 (310)
SHIFT INTEGER 2 PLACES AND CREATE EXPONENT
BMX ← CONST 2378
EALU ← B
ER ← EALU
FALU ← A
AR ← FALU

LDC50 (314)
SHIFT INTEGER 2 PLACES AND CREATE EXPONENT
BMX ← CONST 2178
EALU ← B
ER ← EALU
FALU ← A
AR ← FALU

LDC60 (312)
CHECK FOR NEGATIVE NUMBER OR ZERO
FALU ← A MINUS 1
AR ← FALU

LDC70 (251)
INTEGER WAS NEGATIVE
MAKE POS AND SET SIGN
FALU ← A
AR ← FALU
SD ← SS
SHIFT CONT ← NORM

LDC80 (253)
INTEGER WAS POSITIVE
RESTORE AND CHECK FOR ZERO
FMX ← QSHFR
FALU ← A PLUS B PLUS 1
AR ← FALU
SD ← 0
SHIFT CONT ← NORM

LDC50 (136)
ONLY WANT 1st HALF
LDCFD
LONG CYC ← 1
AR LO ← 0
FALU ← A
AMX ← ER
EALU ← A
ACMX ← ALUS
ACD[3:0] ← ACMX
SET FCC (0)

LDC40 (46)
READ ACS
SCR OUT ← ACS/ACS
BMX ← SCR OUT
EALU ← B
ER ← EALU
FMX ← SCR OUT
FALU ← B
AR ← FALU
SD ← SCR OUT

LDC70 (137)
LDC DF RND FRACTION
FMX ← RND
FALU ← A PLUS B
AR ← FALU
SHIFT CONT ← NORM

LDC80 (230)
EXP WAS ZERO CLR ACD
BMX ← CONST 0
EALU ← B
FALU ← 0
ACMX ← ALUS
ACD[3:0] COND ← ACMX
SD ← 0
SET FCC (0)

LDC90 (231)
TEST FOR FRACTION OVERFLOW
AMX ← ER
BMX ← SHIFT COUNT
EALU ← A MINUS B
ER ← EALU
FALU ← A
AR ← FALU
ACMX ← ALUS
ACD[3:0] COND ← ACMX
SET FCC (1)

RDY.00 1 (3)

LDC80 (276)
EXP WAS 0; CLR ACD
BMX ← CONST 0
EALU ← B
FALU ← 0
ACMX ← ALUS
ACD[3:0] ← ACMX
SD ← 0
SET FCC (0)

RDY.00 1 (3)

RDY.00 1 (3)

NORM.30 14 (163)

NORM.40 14 (161)

NORM.50 14 (162)

RDY.00 1 (3)

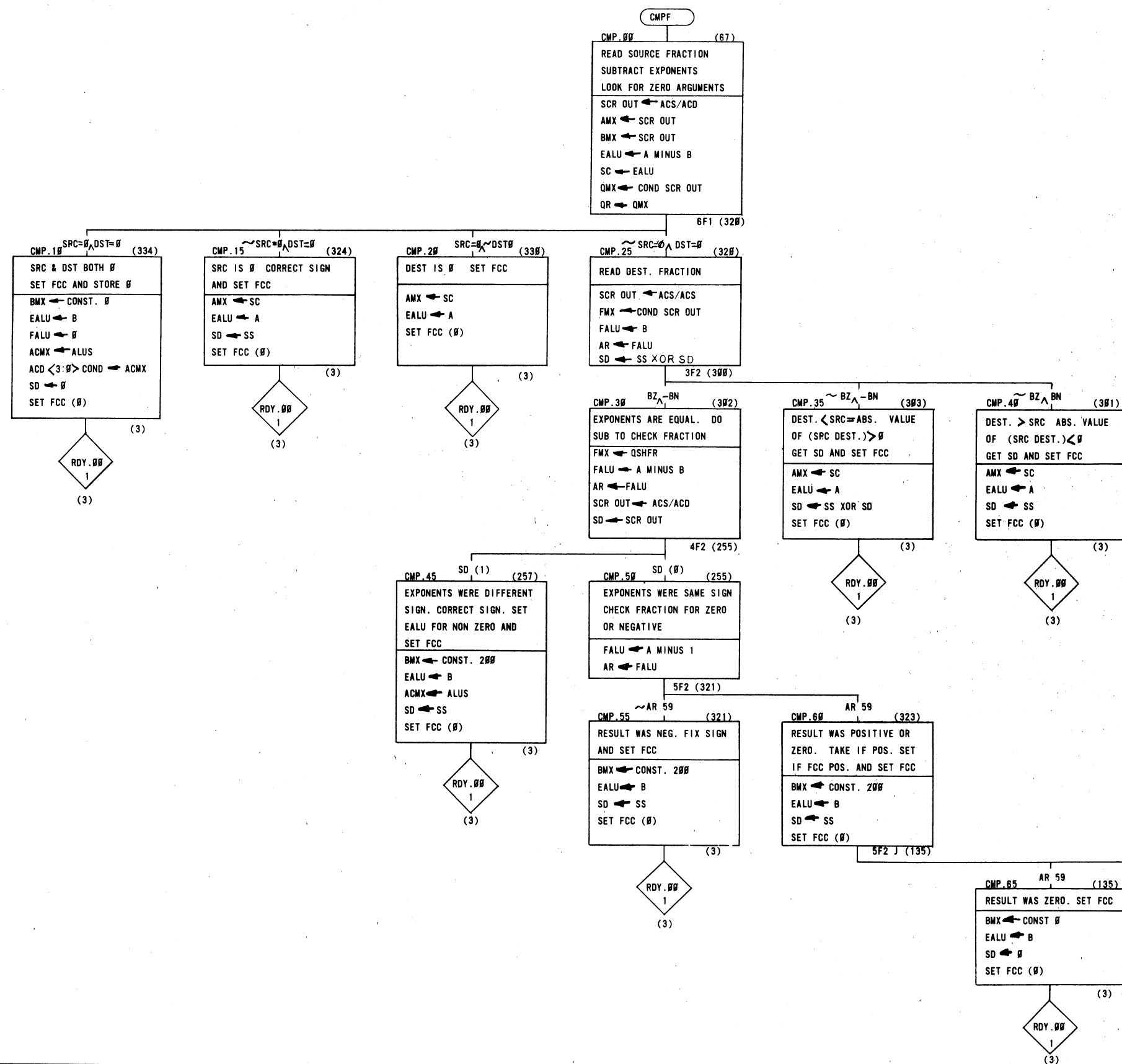
ALT clocked at TS3
Rom state chosen at TS2

REVISIONS		
CHK	CHANGE NO.	REV.

DEC FORM NO. 0WD 138

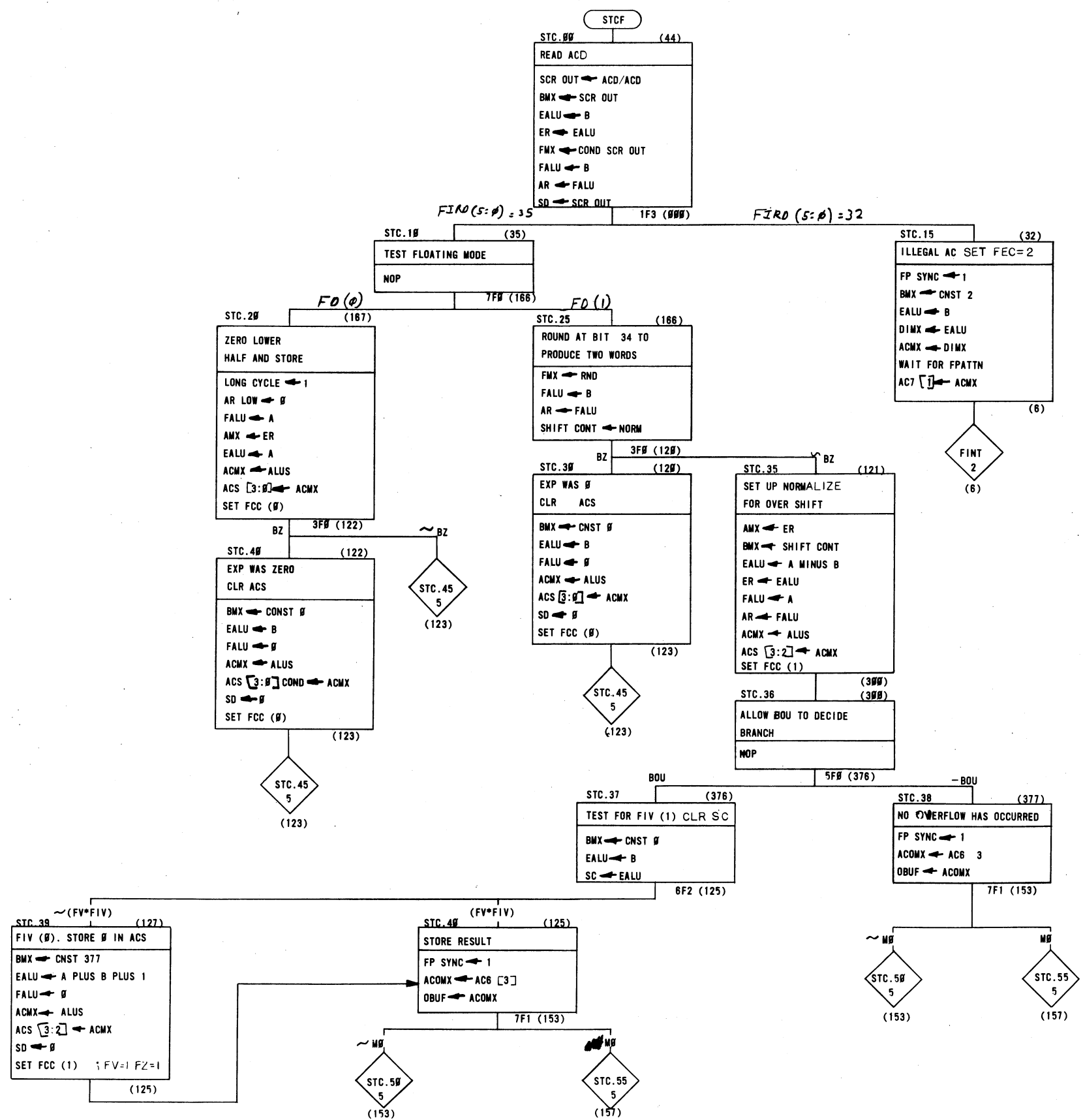
TITLE		FLOW DIAGRAM FPII-C (FLOWS 7)	SIZE CODE	NUMBER	REV.
SCALE		—	D	FD FPII-C-1	
SHEET		8	OF 15		DIST.

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REVISIONS		
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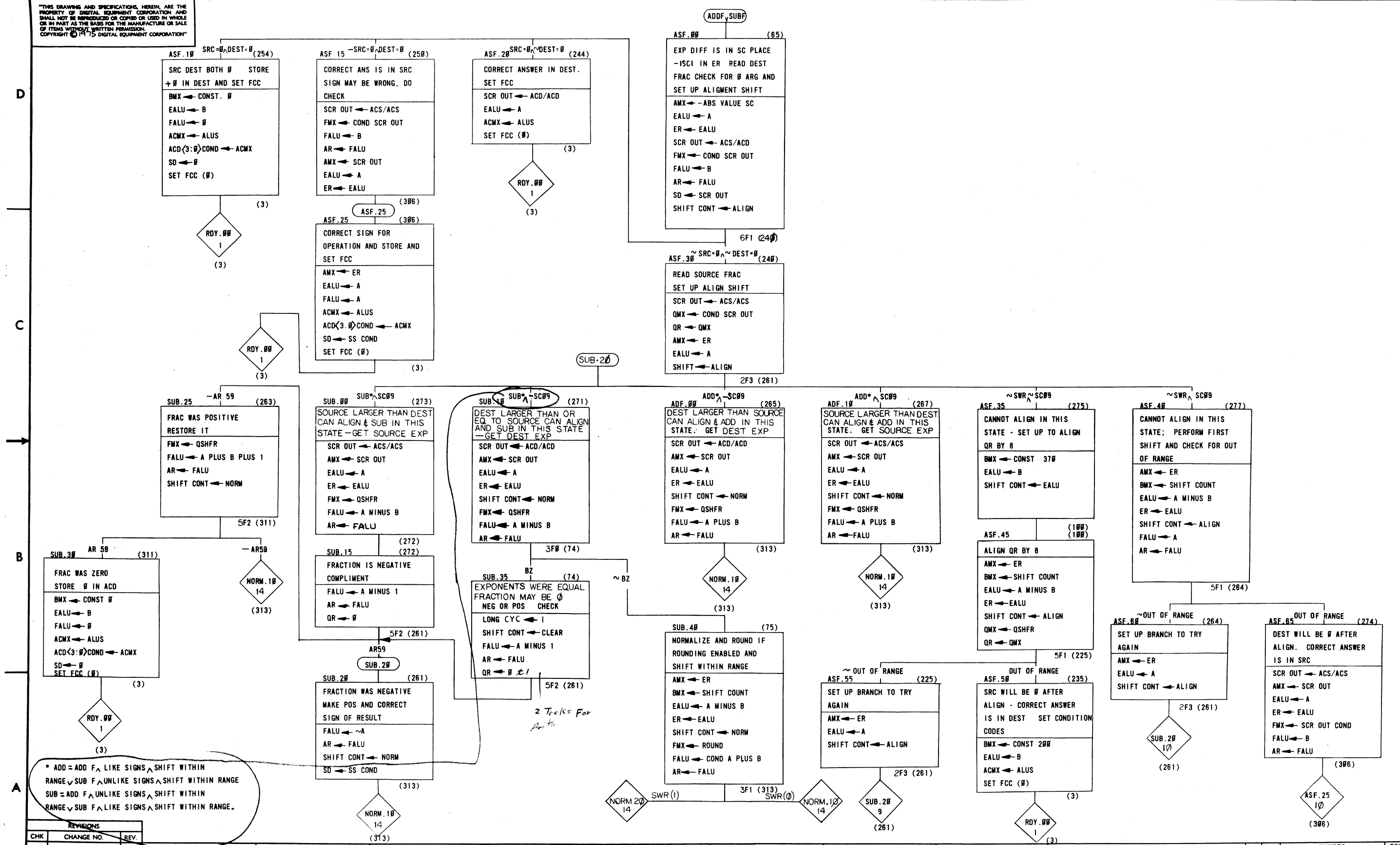


REVISIONS		
CHK	CHANGE NO.	REV.

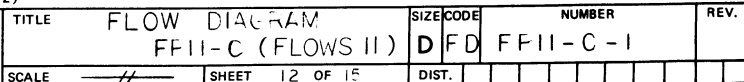
DEC FORM NO. DRD 138

TITLE		SIZE CODE	NUMBER	REV.
FLOW DIAGRAM		DFD	FP11-C-1	
SHEET 10 OF 15		DIST.		

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MUL 78	
MUL 78	(72)
NORMALIZE AND ROUND IF ENABLED — CALCULATE SIGN	
FMAX ← ROUND	
FALU ← COND A PLUS B	
AR ← FALU	
AMX ← ER	
BMX ← SHIFT COUNT	
EALU ← A MINUS B	
ER ← EALU	
SHIFT COUNT ← NORM	
SD ← SS XOR SD	



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D

C

B

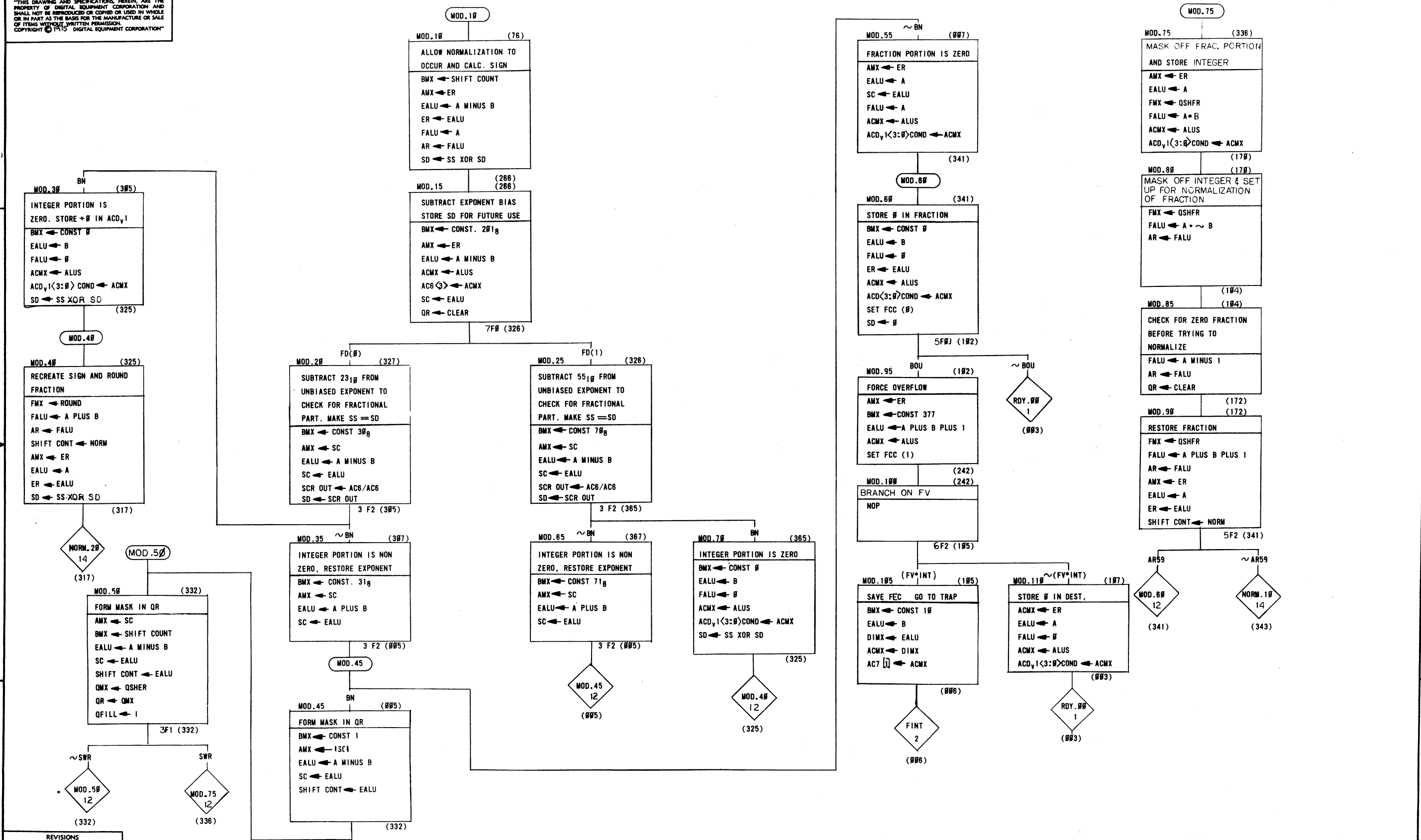
A

D

C

B

A



REVISIONS		
CHK	CHANGE NO.	REV.

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REV. 1
D FPII-C-1
SIZE CODE 300 3215

D

D

C

C

B

B

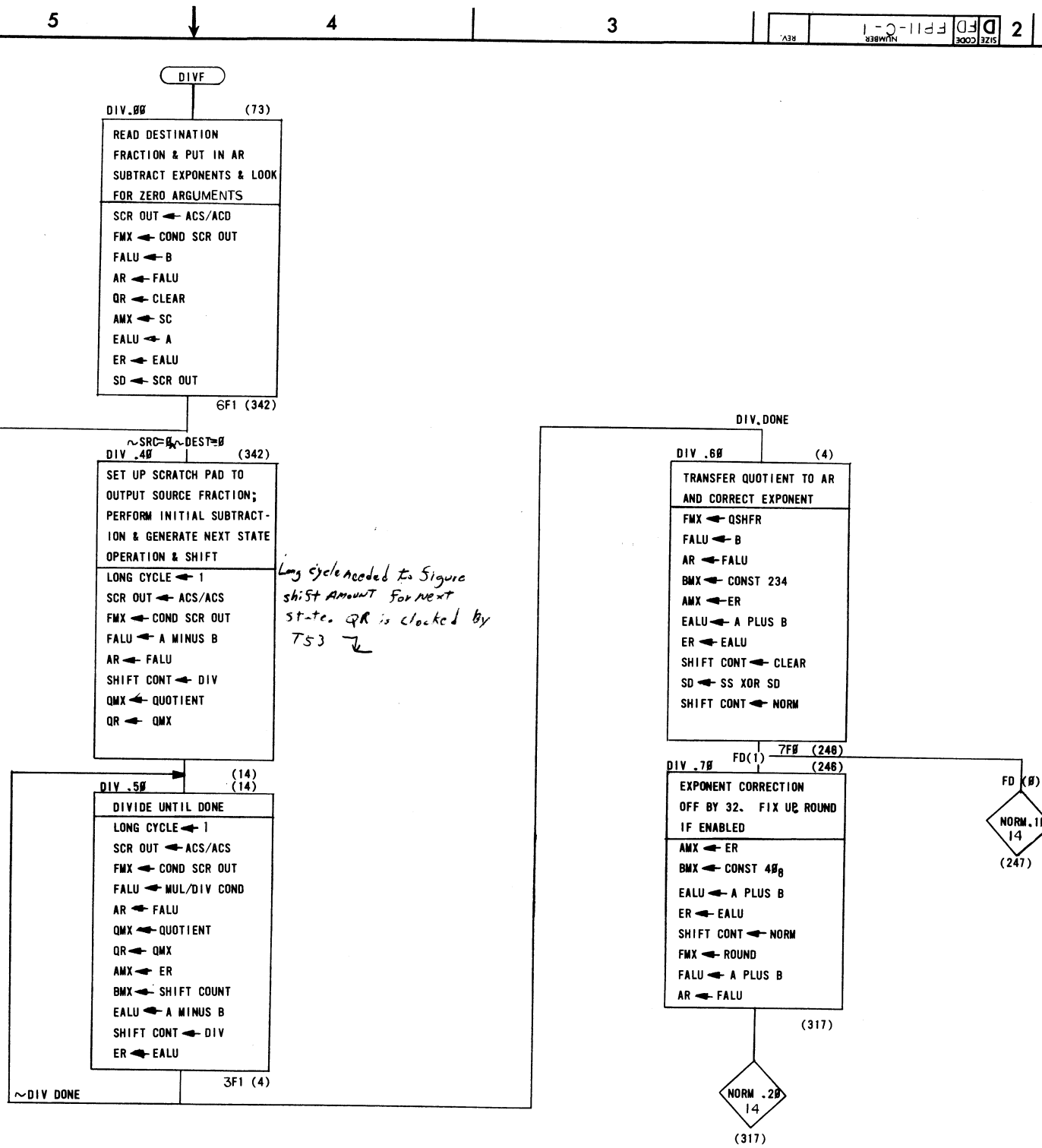
A

A

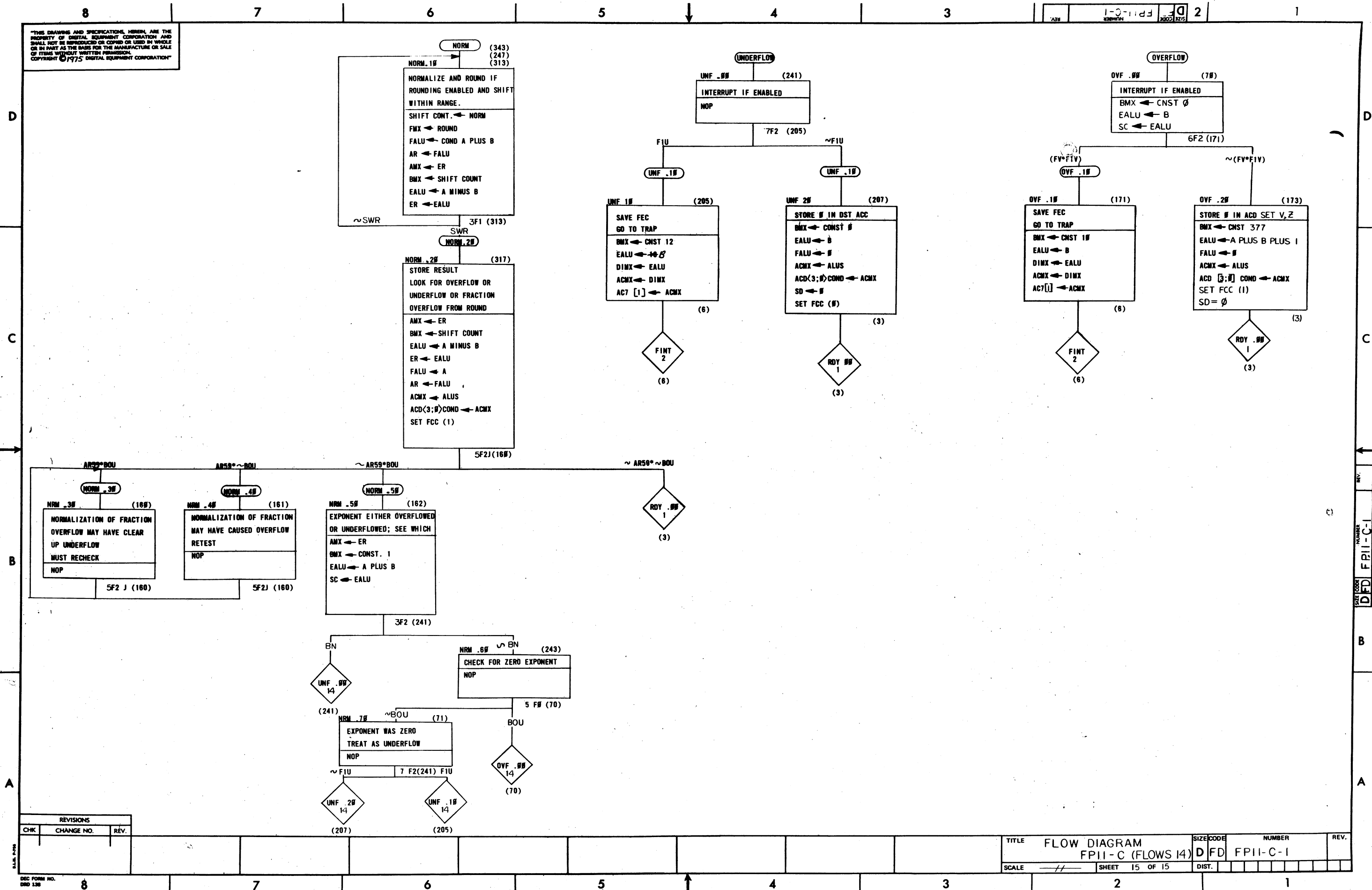
REVISIONS		
CHK	CHANGE NO.	REV.

DEC FORM NO.
DRD 138

TITLE		SIZE CODE	NUMBER	REV.
FLOW DIAGRAM FPII-C (FLOWS 13)		D FPII-C-1	FPII-C-1	
SCALE		SHEET	DIST.	
1/1		14 OF 15		

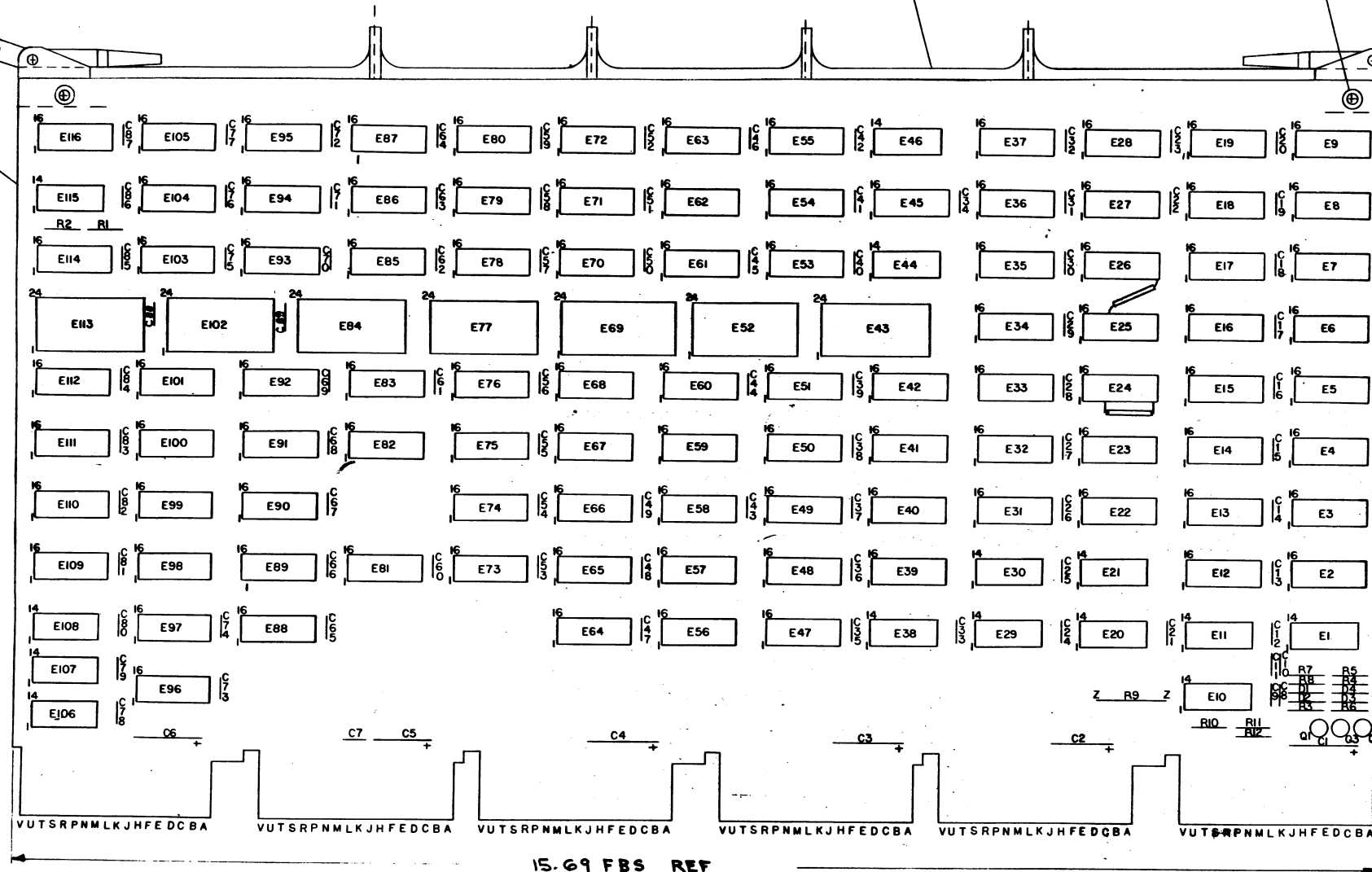


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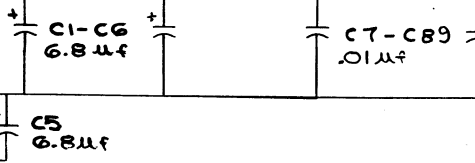
NOTES:



15.69 FBS REF

AA2, AV1, CA2, CV1
BA2, BV1, DA2, DV1 +5V
EA2, EV1, FA2, FV1

AC2, AN2, AH1, AT1
BC2, BN2, BH1, BT1
CC2, CN2, CH1, CT1
DC2, DN2, DH1, DT1
EC2, EN2, EH1, ET1
FC2, FN2, FH1, FT1
-15V EB2



IC TYPE	GND	+5V
IC 74S182	8	16
IC 74S85	8	16
IC 74S175	8	16
IC 74157	8	16
IC 74174	8	16
IC 74S174	8	16
IC 74S158	8	16
IC 74S157	8	16
IC 74S153	8	16
IC 74S181	12	24
IC TYPE	GND	+5V
GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY EXCEPTS ARE STATED ABOVE		
IC PIN LOCATIONS		

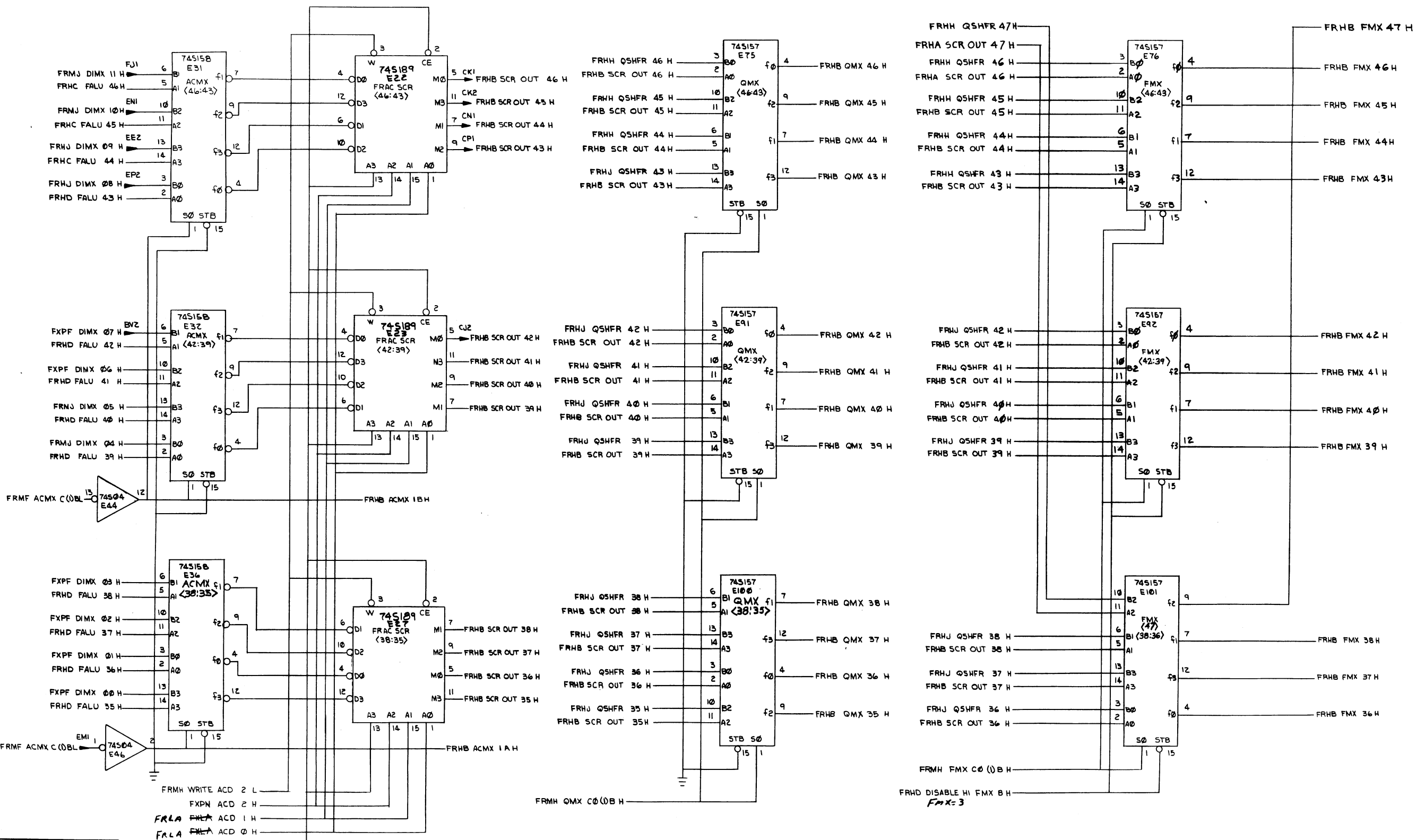
DEC FORM NO.
ORD 135.8

REF		X-Y COORDINATE HOLE LOCATION	K-CO-M8126-0-4	1
REF		ASSY/DRILLING HOLE LAYOUT	D-AH-M8126-0-5	2
REF		MODULE ECO HISTORY	B-MH-M8126-0-6	3
1		ETCHED CIRCUIT BOARD	5011870	4
3	C7 THRU C89	CAPACITOR, 0.1uf	1001610-00	5
6	C1 THRU C6	CAPACITOR, 6.8uf	1005306	6
4	D1 THRU D4	DIODE, D652	1100113	7
1		HANDLE HEX MODULE	1210711-2	8
3	R1,R7,R11	RESISTOR, 330 OHM, 1/4W, 5%	1300295	9
1	R3	RESISTOR, 470 OHM, 1/4W, 5%	1300316	10
3	R2,R8,R12	RESISTOR, 680 OHM, 1/4W, 5%	1301424	11
1	R6	RESISTOR, 82 OHM, 1/4W, 5%	1301477	12
2	R4,R5	RESISTOR, 22 OHM, 1/4W, 5%	1301969	13
1	R9	RESISTOR, 300 OHM, 1W, 5%	1300292	14
1	R10	RESISTOR, 100 OHM, 1/4W, 5%	1300294	15
2	Q1,Q2	TRANSISTOR, 3009B	1503100	16
1	Q3	TRANSISTOR, 4258	1505321	17
1	E115	I.C. DEC 8242	1909712	18
7	E43,E52,E69,E77,E84,E102,E113	I.C. DEC 74S181	1910531	19
3	E1,E11,E30	I.C. DEC 74S00	1910532	20
4	E29,E38,E44,E46	I.C. DEC 74S04	1910534	21
2	E20,E18	I.C. DEC 74S10	1910536	22
3	E21,E106,E107	I.C. DEC 74S11	1910537	23
1	E10	I.C. DEC 74S74	1910544	24
5	E13,E16,E112,E4,E15	I.C. DEC 74S153	1910547	25
17	E2,E7,E17,E50,E51,E59,E60,E67,E68,E75,E76,E82,E83,E91,E92,E100,E101	I.C. DEC 74S157	1910548	26
7	E31 THRU E37	I.C. DEC 74S158	1910549	27
12	E3,E4,E47,E56,E61,E64,E78,E85,E88,E97,E103,E114	I.C. DEC 74S174	1910550	28
1/2		#30 AWG. JUMPERS	9105740-55	29
3	E40,E41,E111	I.C. DEC 74157	1910655	30
3	E5,E6,E53	I.C. DEC 74S175	1910957	31
1	E108	I.C. DEC 74S51	1911712-00-S	32
1	E12	I.C. DEC 74S85	1912089-00-S	33
7	E22 THRU E28	I.C. DEC 74S189	1912661	34
2	E42,E45	I.C. DEC 74148	1912796	35
2	E70,E93	I.C. DEC 74S182	1912097	36
30	E48,E49,E54,E55,E57,E58,E62,E63,E65,E66,E71 THRU E74,E79 THRU E81,E86,E87,E89,E90,E94,E95,E98,E99,E104,E105,E109,E110,E116	I.C. DEC 25S10	1912693	37
1	E39	ROM	23D09A2	38
1	E96	ROM	23D06A2	39
12		EYELET	9006732	40

QTY	REF. DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
ETCH BOARD REV. C				
FIRST USED ON OPTION MODEL FPII-C				
DRN. DATE 11/25/75				
CHKD. DATE 11/25/75				
ENG. DATE 11/25/75				
PROG. ENG. DATE 11/25/75				
PROD. DATE 11/25/75				
NEXT HIGHER ASSY B-DD-FPII-C				
SCALE 1 OF 13				
SEMICONDUCTOR CONVERSION CHART				
DEC NO.	EIA NO.	DEC NO.	EIA NO.	
DIGITAL				
TITLE FRACTION PROC HIGH ORDER				
SIZE CODE NUMBER REV. DCS M8126-0-1 C				

T. NORTHUP	2 JAN 76	C	M8126-00002	T. NORTHUP	31 DEC 75	B	M8126-00001	REV.
CHK	CHANGE NO.	REV.	CHK	CHANGE NO.	REV.	CHK	CHANGE NO.	REV.

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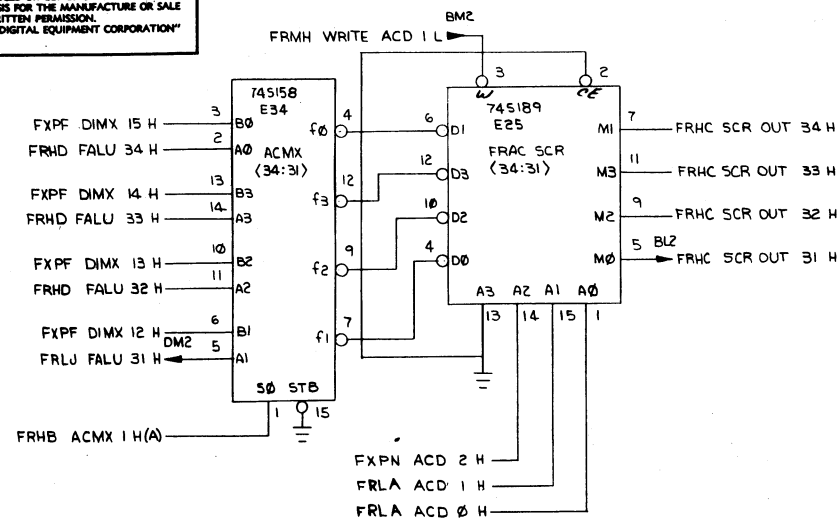


REVISIONS		
CHK	CHANGE NO.	REV.

DEC FORM NO. DND 138

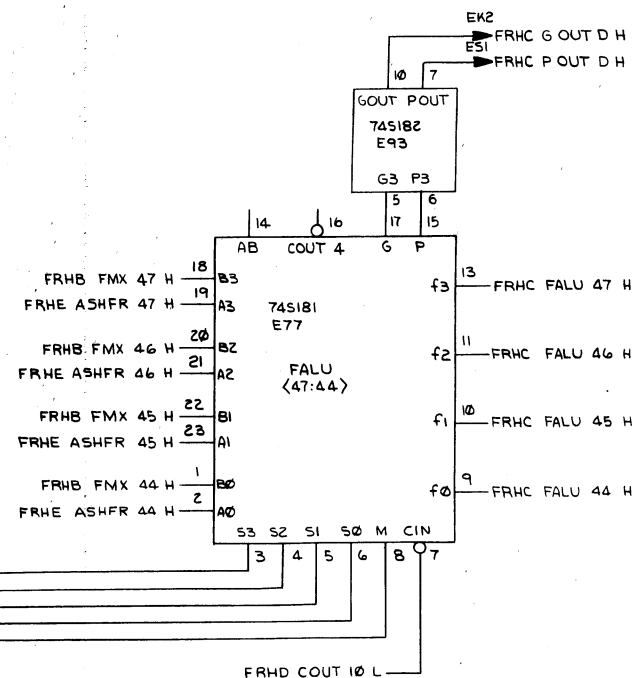
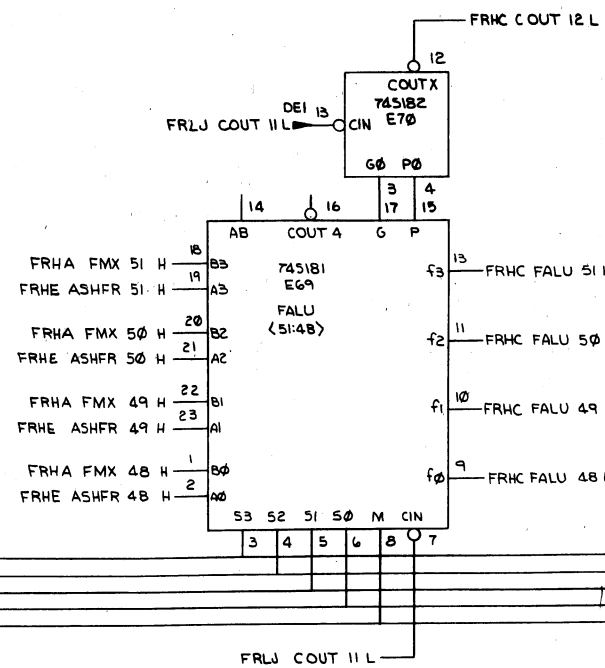
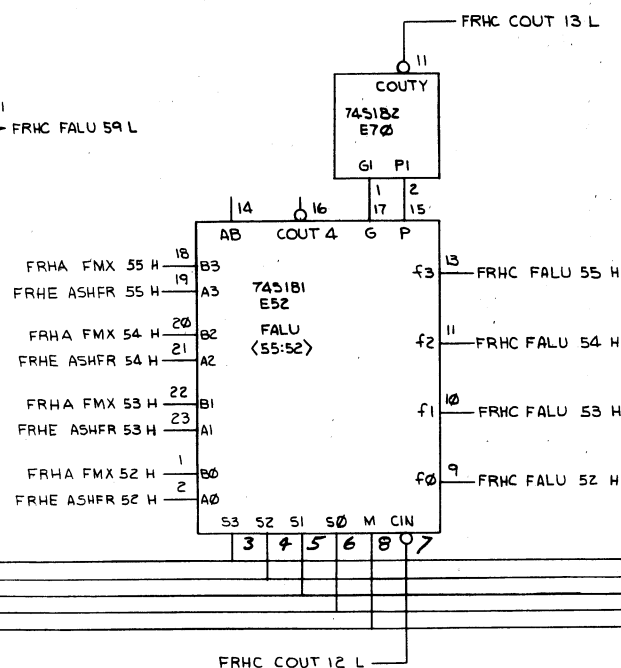
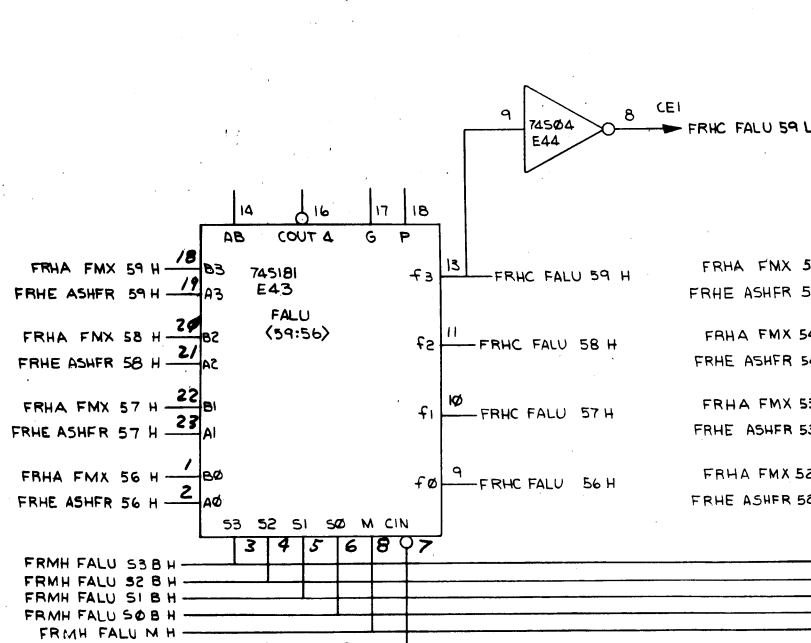
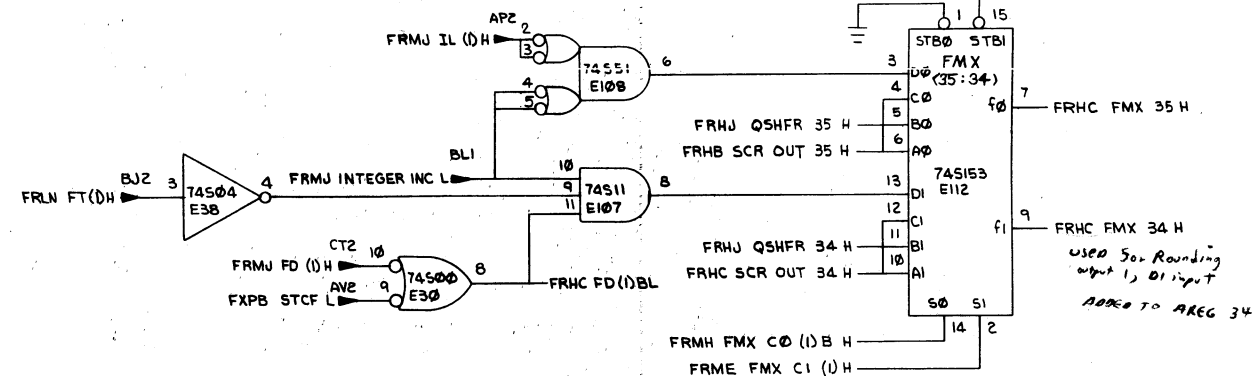
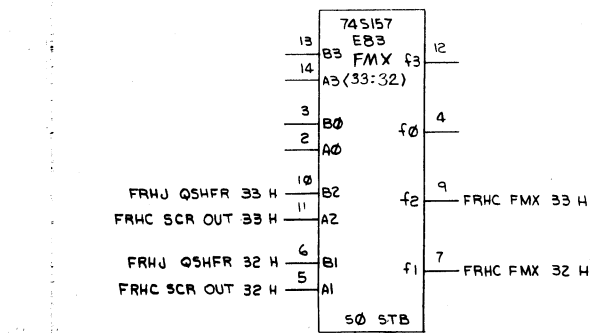
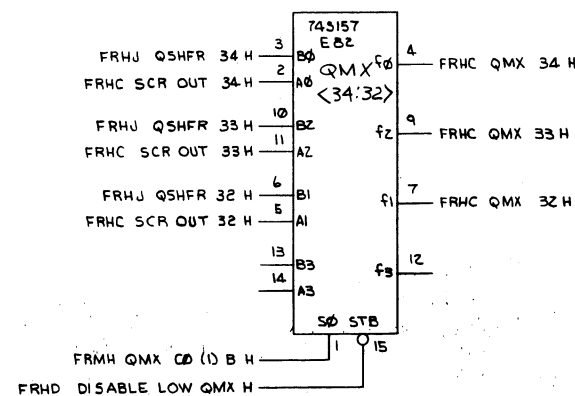
TITLE		FRACTION (FRHB) PROC. HIGH ORDER		SIZE CODE		NUMBER		REV.	
SCALE		SHEET 3 OF 13		D		CSM826-0-1		C	

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FMXC1	FMXC0	FMX FUNCTION
0	0	FMX (59:00) ← SCR OUT (59:00)
0	1	FMX (59:00) ← QSHFR (59:00)
1	0	FMX (59:35) ← SCR OUT (59:35) IF FD(1) FMX (34:00) ← SCR OUT (34:00) ELSE FMX (34:00) ← 0
1	1	FMX (59:36) ← 0; FMX (33:20) (18:0) ← 0; FMX (1:0) ← 0; FMX (34) ← FT(0) FD(0) INTEGER INC; FMX (02) ← FT(0) AFD(0); FMX (35) ← INTEGER INCAL (0); FMX (19) ← INTEGER INCAL (1)

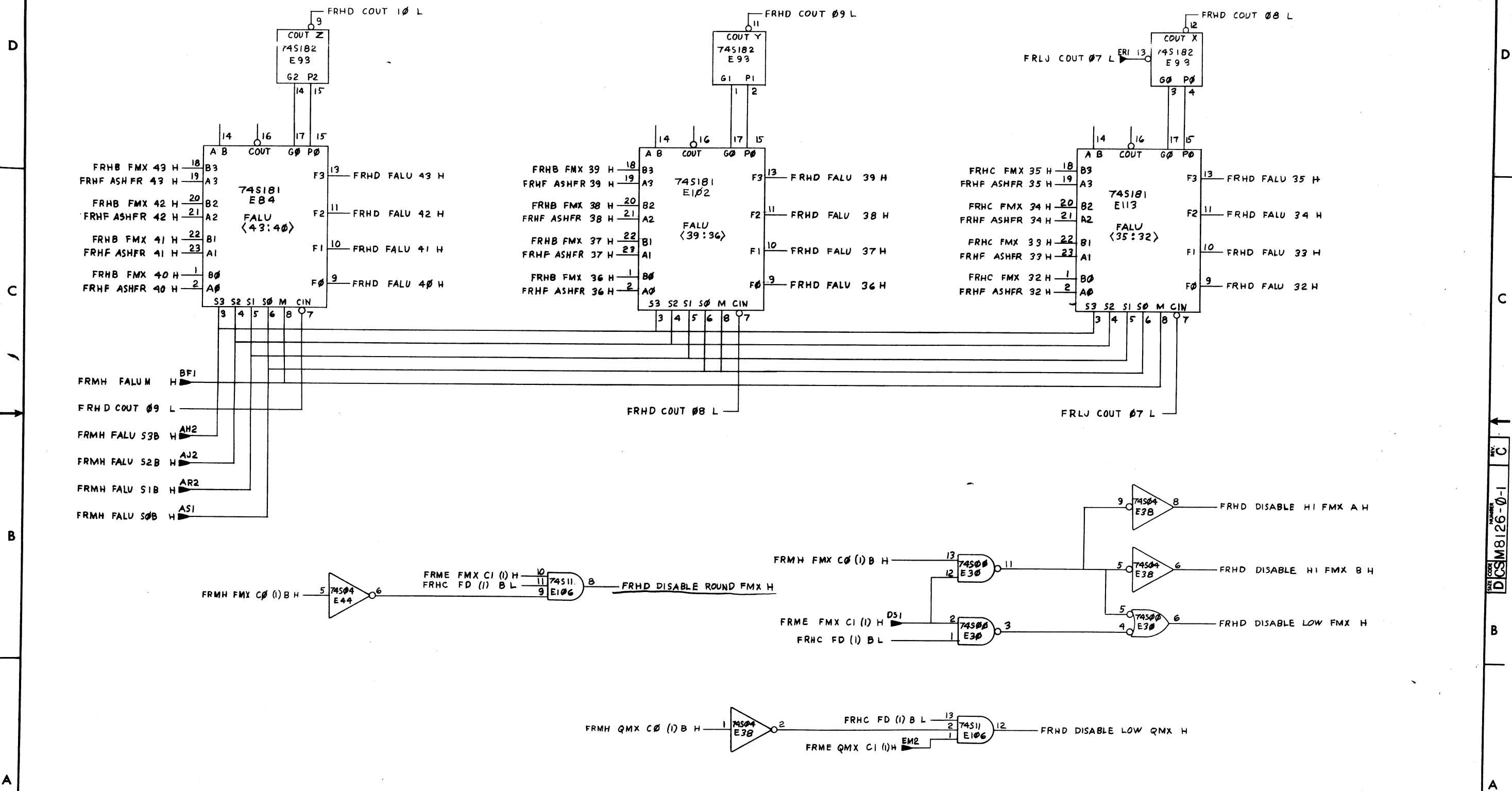
QMXC1	QMXC0	QMX FUNCTION
0	0	QMX (59:00) ← SCR OUT (59:00)
0	1	QMX (59:00) ← QSHFR (59:00)
1	0	QMX (59:35) ← SCR OUT (59:35) IF FD(1) QMX (34:00) ← SCR OUT (34:00) ELSE QMX (34:00) ← 0
1	1	QMX (59:00) ← QSHFR (59:00) IF FD(0) QSHFR (31:24) ← QUOT



REV	CHG	NO	REV

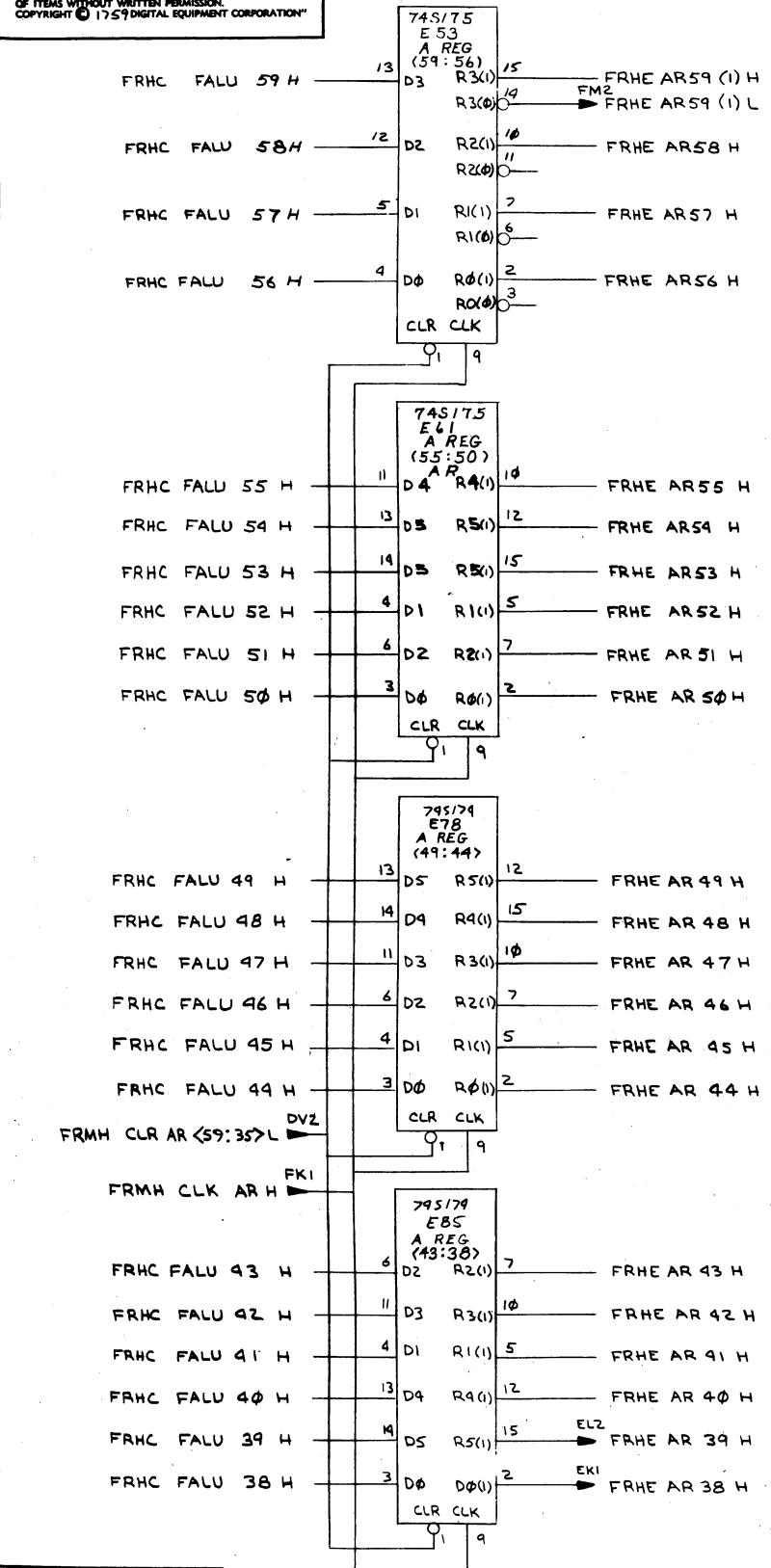
TITLE	FRACTION (FRHC)	SIZE CODE	NUMBER	REV.
PROC HIGH ORDER		D	CS M8126-0-1	C
SCALE		SHEET	4 OF 13	

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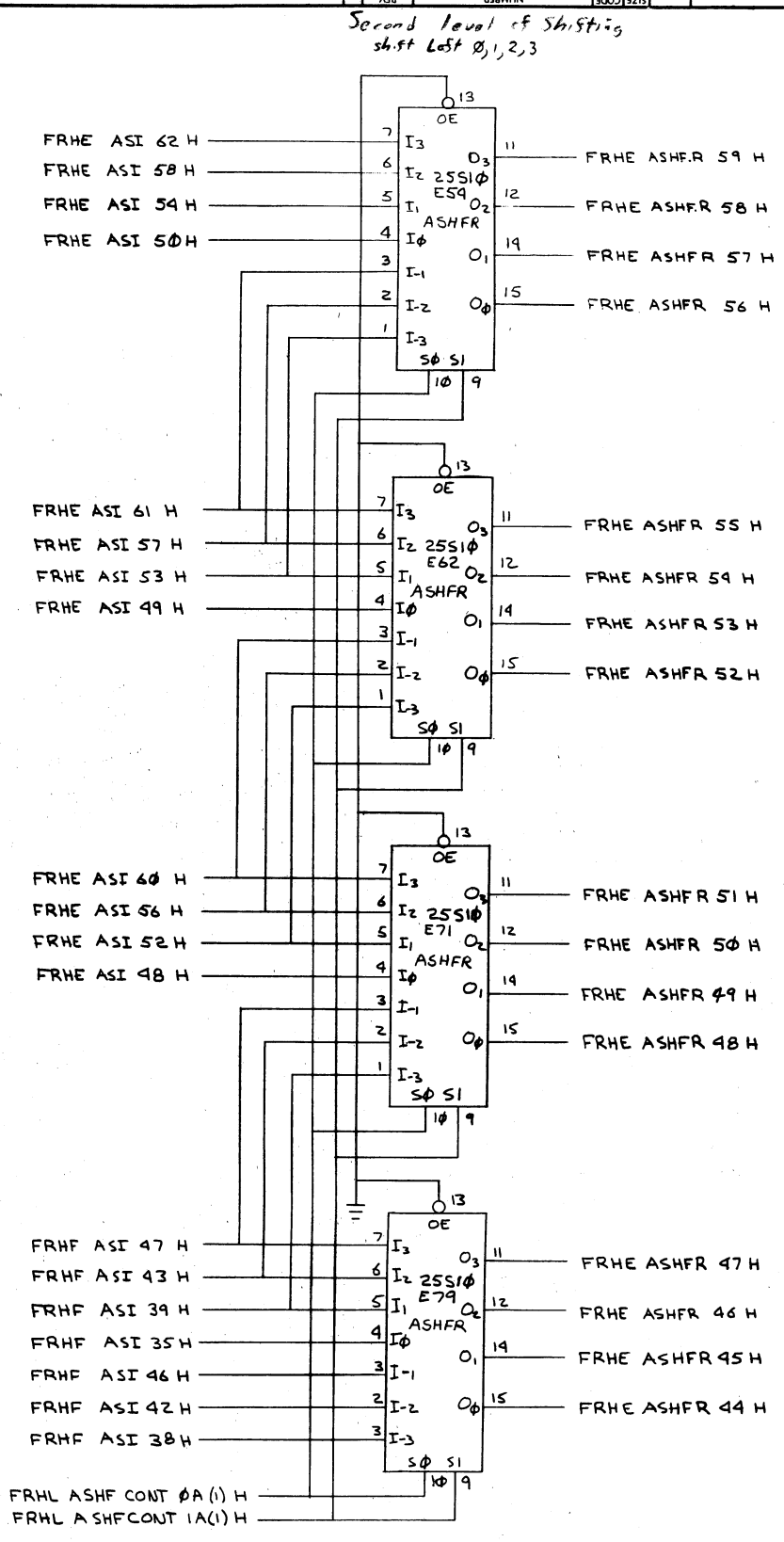
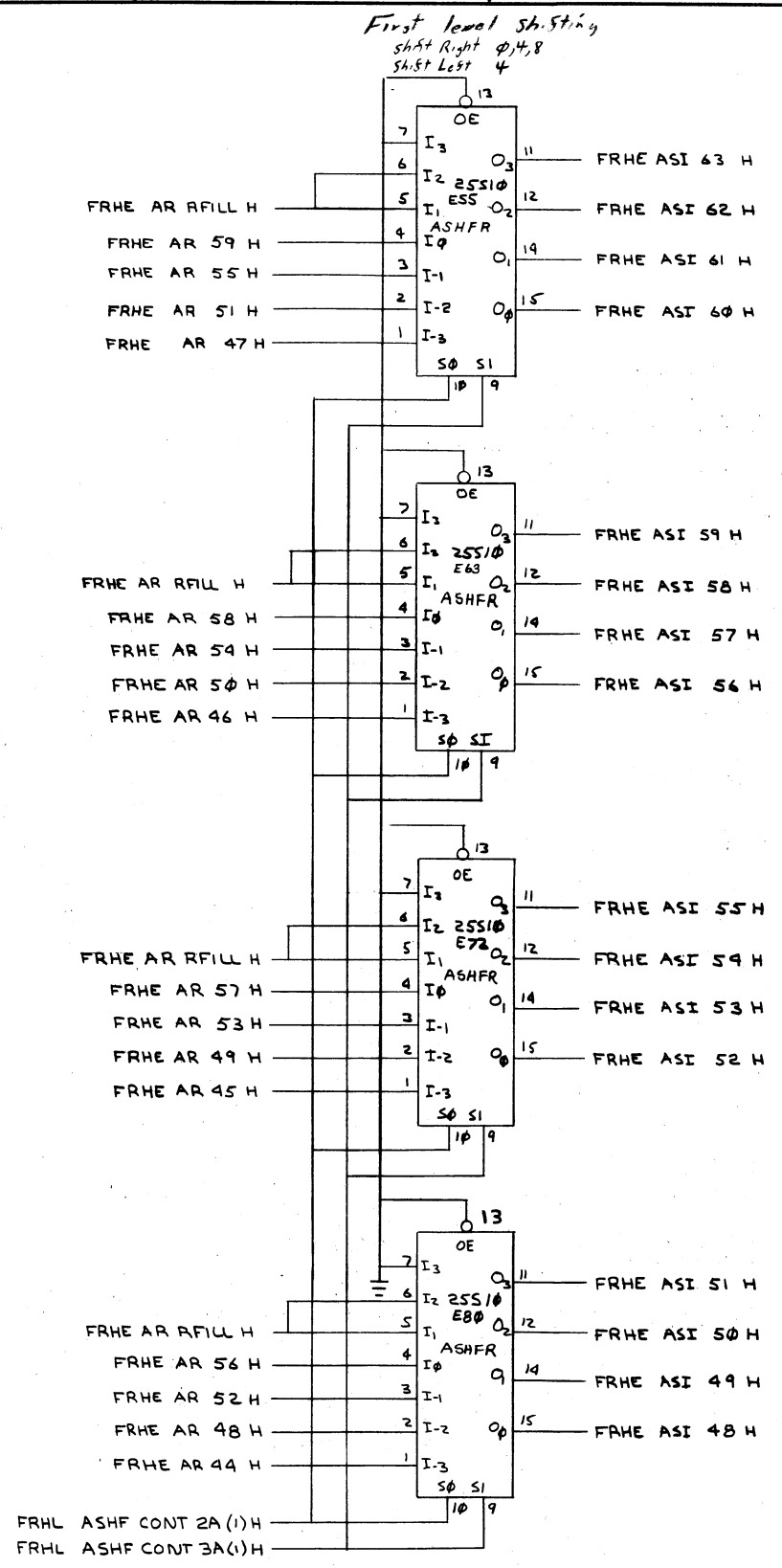


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D
C
B
A

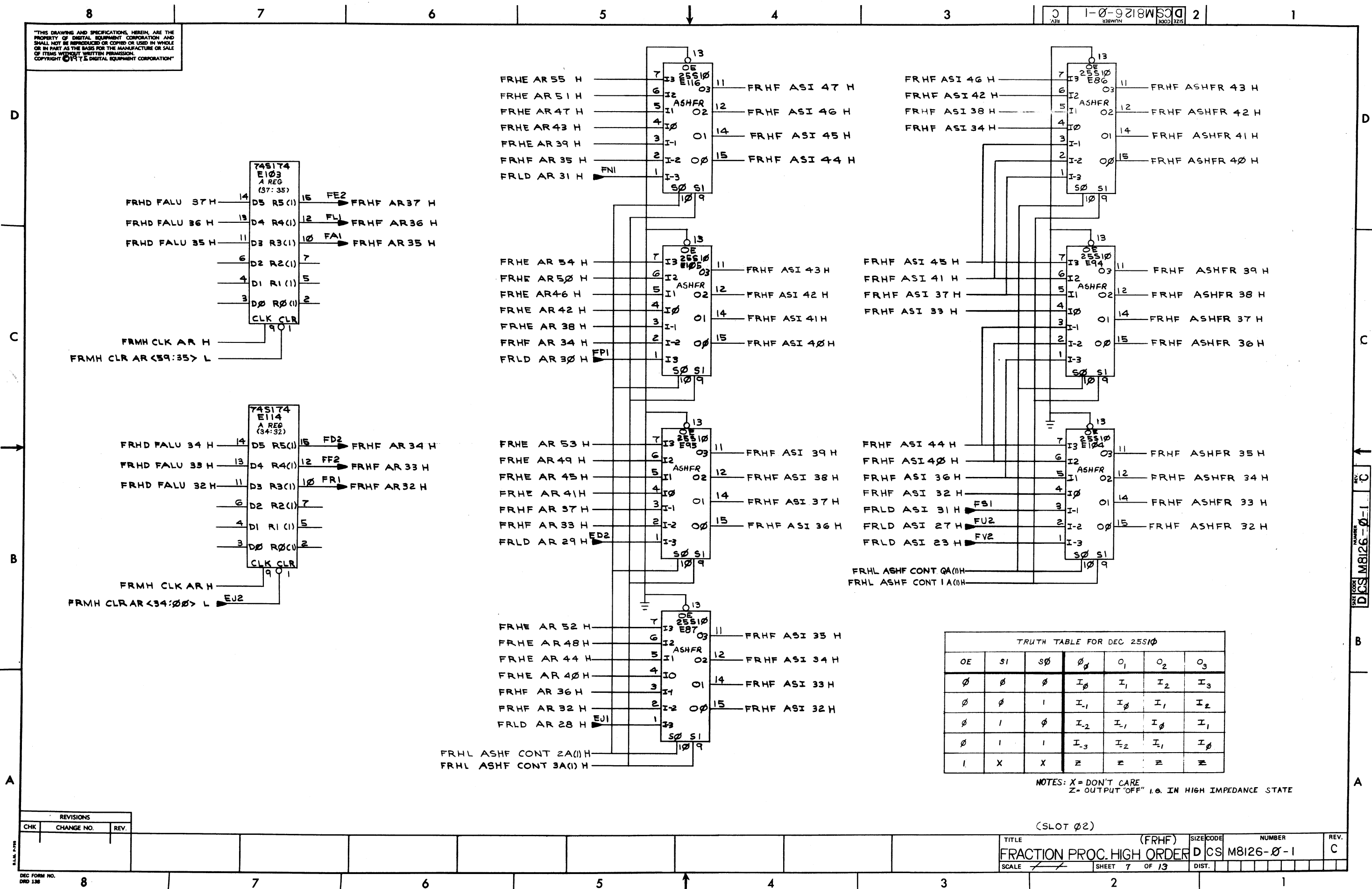


REVISIONS		
CHK	CHANGE NO.	REV.



D
C
B
A

TITLE FRACTION PROC HIGH ORDER (FRHE)		SIZE CODE DCSM8126-0-1	NUMBER	REV. C
SCALE	SHEET 6 OF 13	DIST.		



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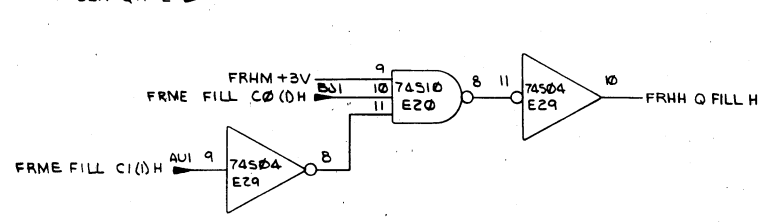
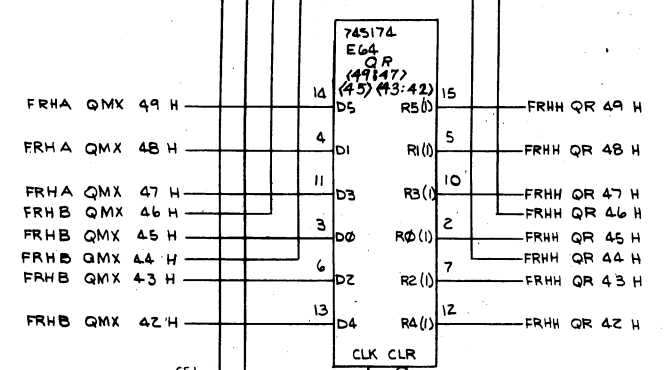
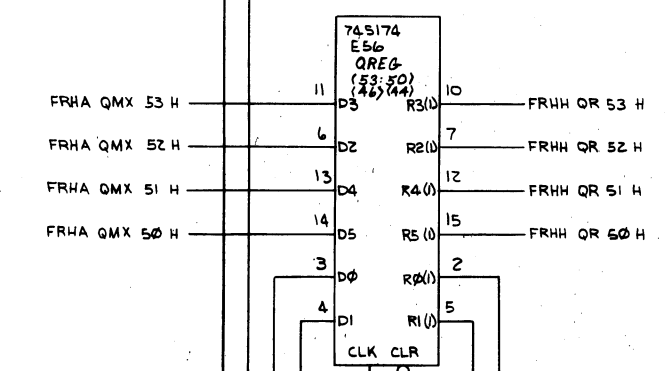
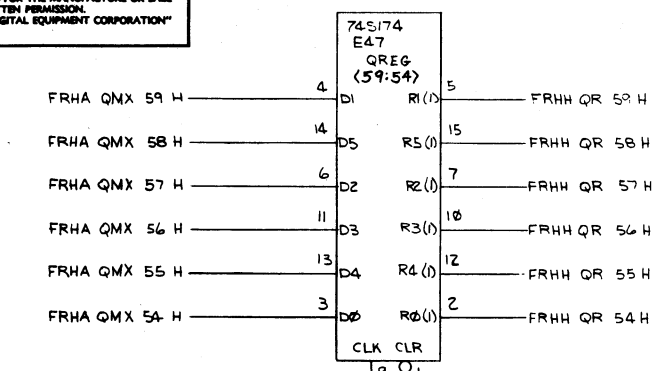
TRUTH TABLE FOR DEC 25S10						
OE	S1	S0	00	01	02	03
0	0	0	I0	I1	I2	I3
0	0	1	I-1	I0	I1	I2
0	1	0	I-2	I-1	I0	I1
0	1	1	I-3	I-2	I-1	I0
1	X	X	Z	Z	Z	Z

NOTES: X = DON'T CARE
Z = OUTPUT "OFF" I.E. IN HIGH IMPEDANCE STATE

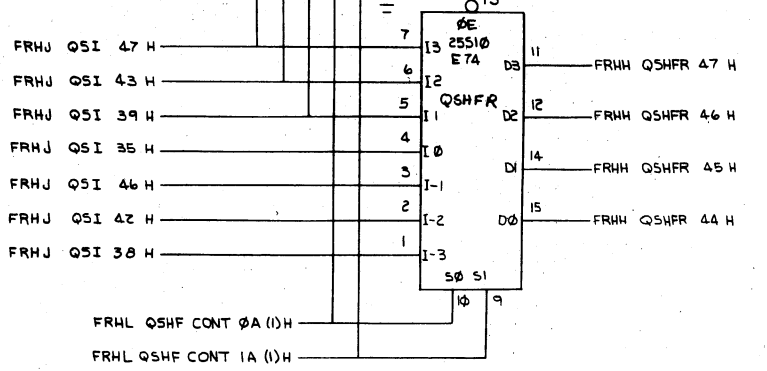
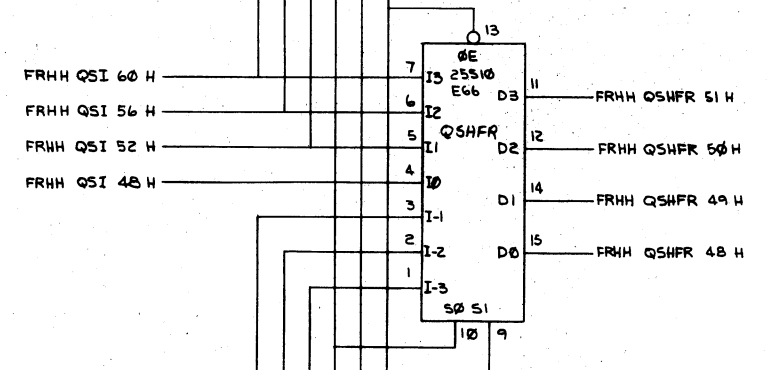
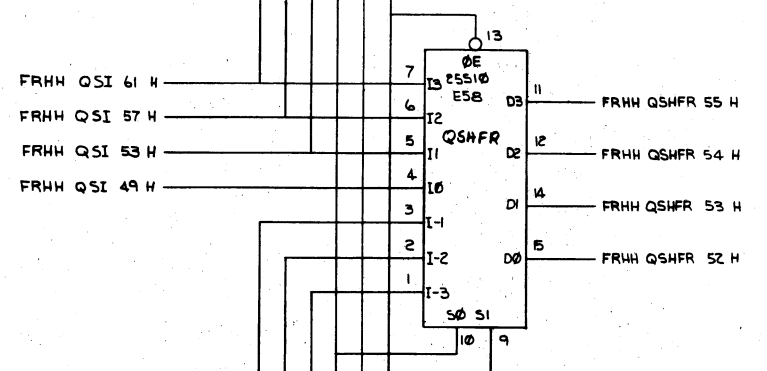
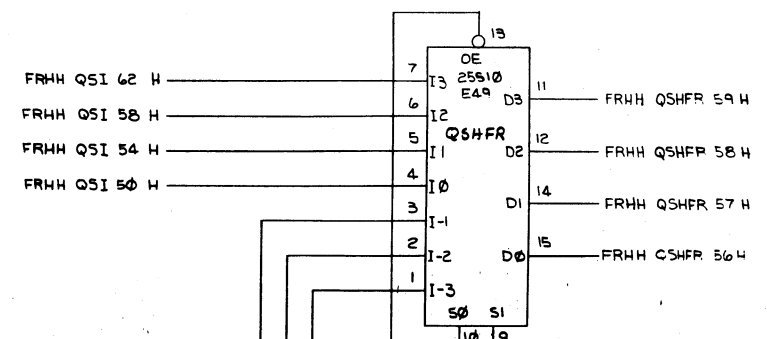
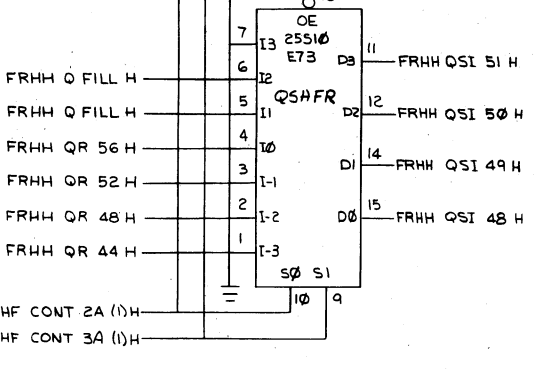
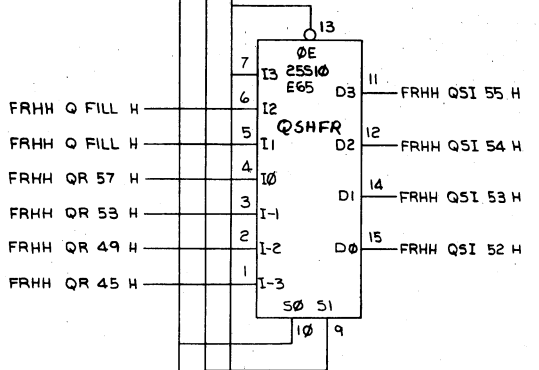
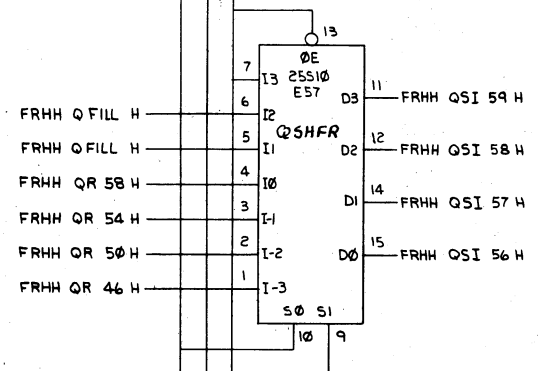
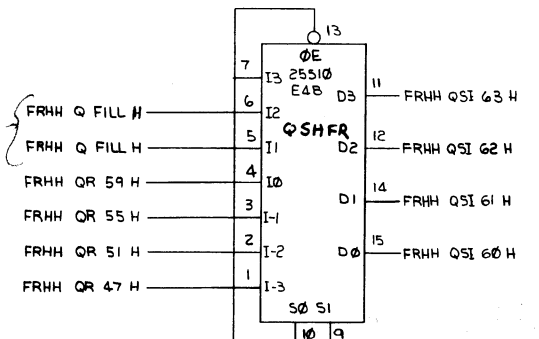
TITLE		(FRHF)		SIZE CODE	NUMBER	REV.
FRACTION PROC. HIGH ORDER		D CS		M8126-0-1		C
SCALE	SHEET 7 OF 13		DIST.			

REVISIONS		
CHK	CHANGE NO.	REV.

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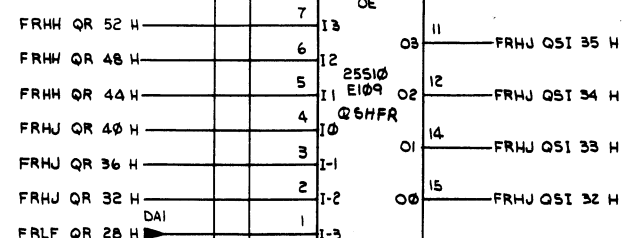
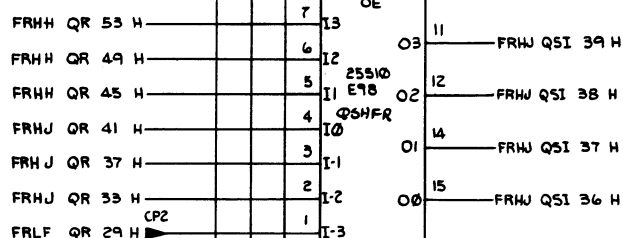
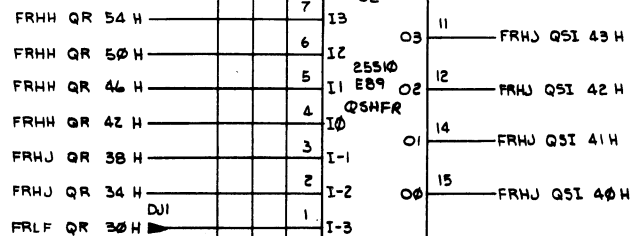
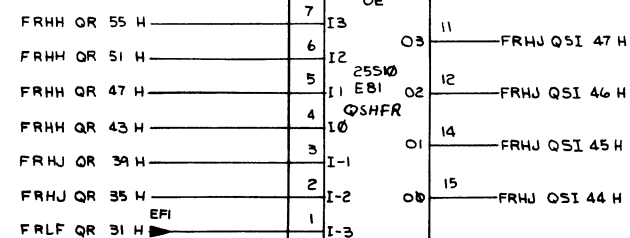
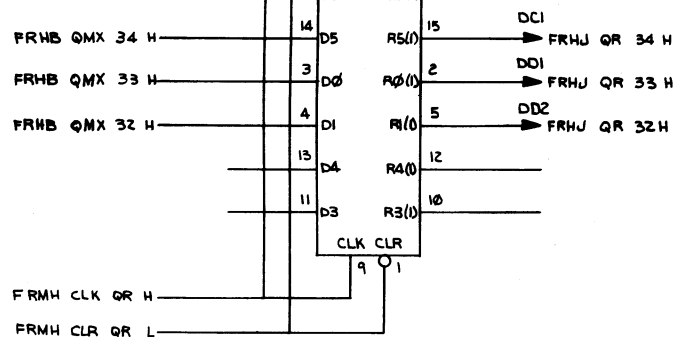
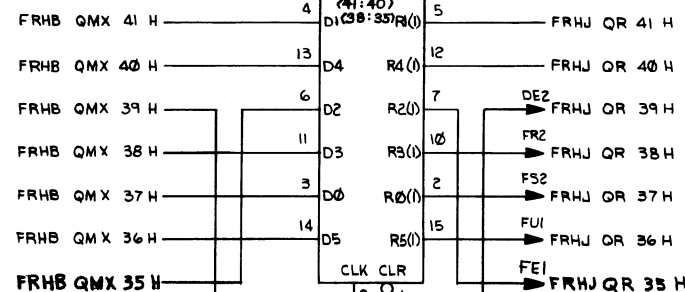
US60 BY
MOD Inst.
STCE Inst.



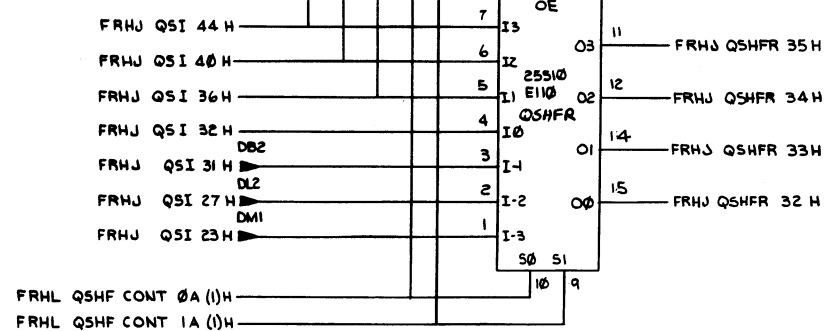
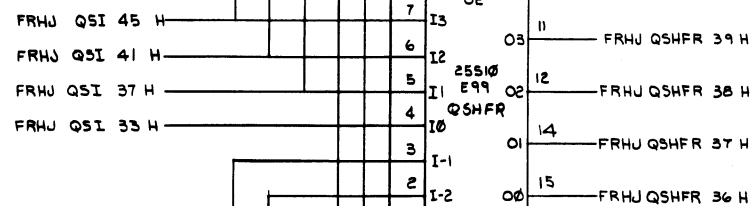
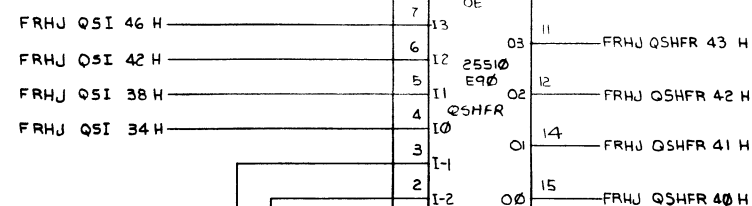
REVISIONS		
CHK	CHANGE NO.	REV.

DEC FORM NO. 000 130

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FRHL QSHF CONT 2A (1)H
FRHL QSHF CONT 3A (1)H



FRHL QSHF CONT 0A (1)H
FRHL QSHF CONT 1A (1)H

A/Q SHF CONT	ASHFR/QSHFR FUNCTION
3 2 1 0	
0 0 0 0	SHIFT RIGHT 8 PLACES
0 0 0 1	" " 7 "
0 0 1 0	" " 6 "
0 0 1 1	" " 5 "
0 1 0 0	" " 4 "
0 1 0 1	" " 3 "
0 1 1 0	" " 2 "
0 1 1 1	" " 1 "
1 0 0 0	NO SHIFT
1 0 0 1	SHIFT LEFT 1 PLACE
1 0 1 0	" " 2 "
1 0 1 1	" " 3 "
1 1 0 0	" " 4 "
1 1 0 1	" " 5 "
1 1 1 0	" " 6 "
1 1 1 1	" " 7 "

(SLOT 02)

REVISIONS		
CHK	CHANGE NO.	REV.

DEC FORM NO. 138

TITLE FRACTION (FRHJ)
PROC. HIGH ORDER

SIZE CODE DCSM8126-0-1

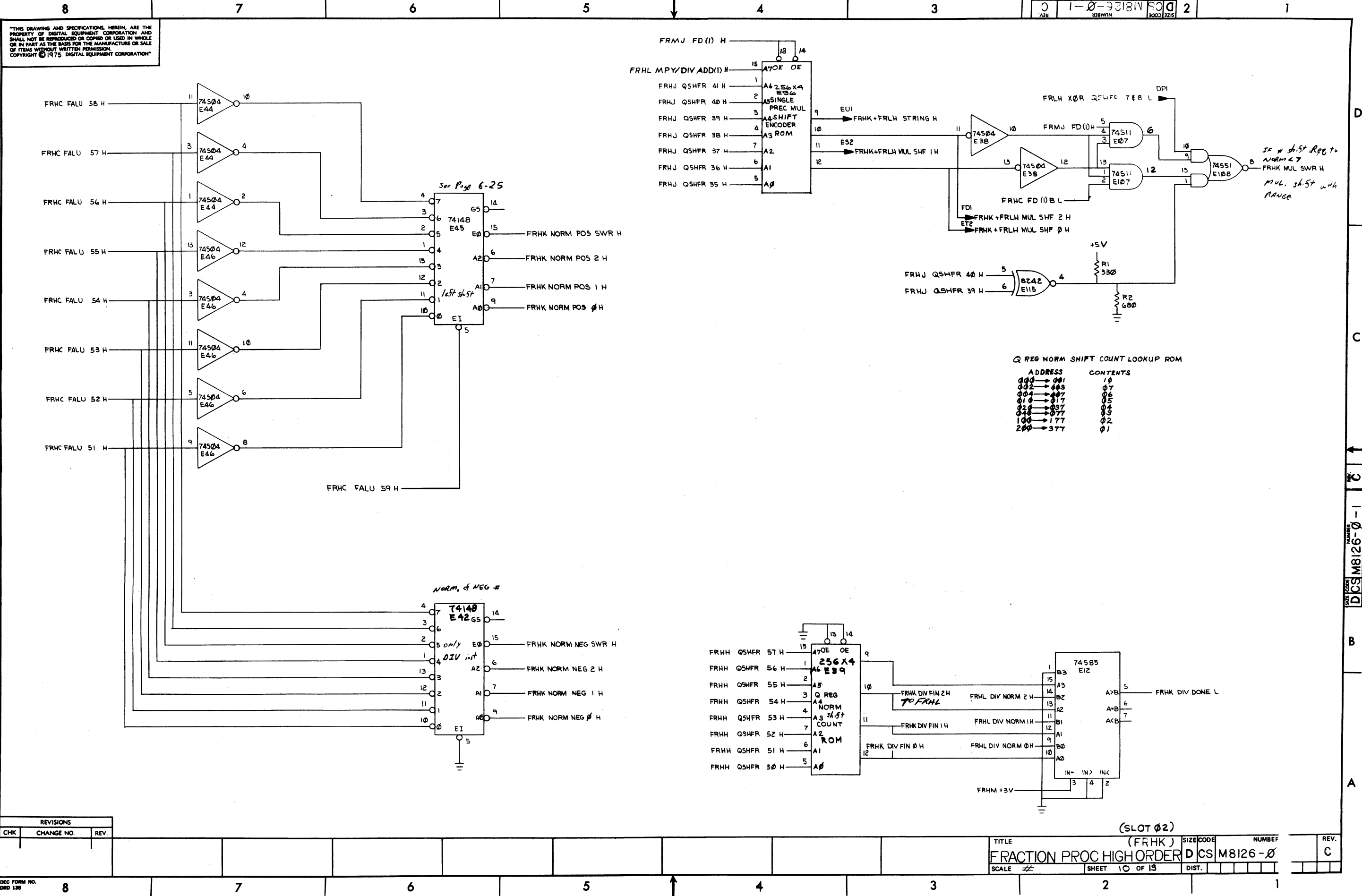
NUMBER

REV. C

SCALE 1 1/2" = 1"

SHEET 9 OF 12

DIST.



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D

C

B

A

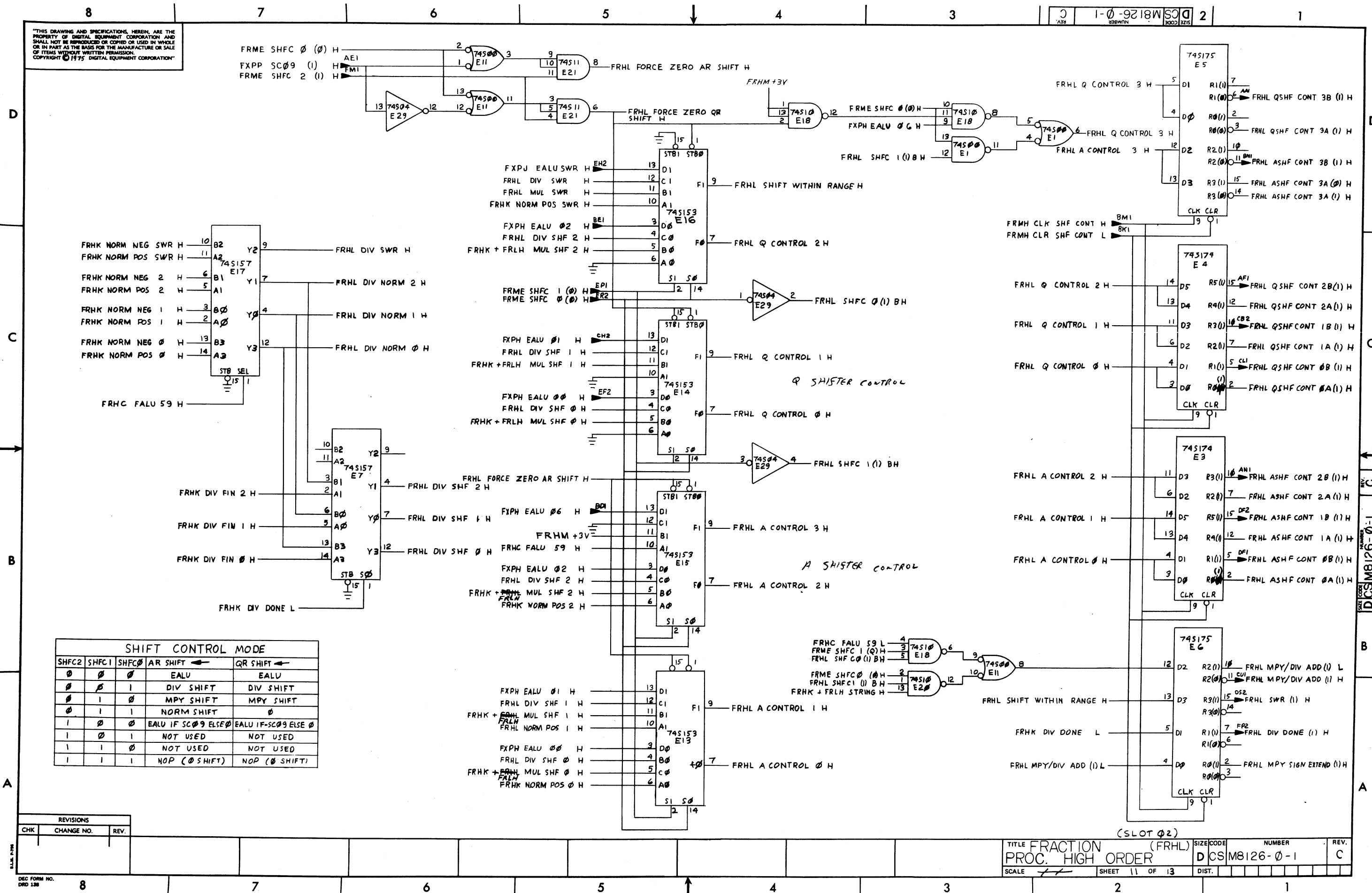
D

C

B

A

1-0-9218W DCS M8126-0-1

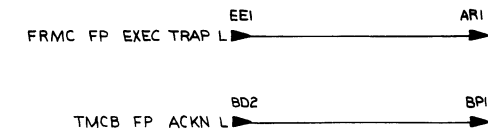
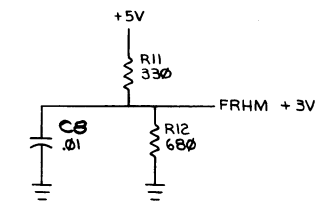
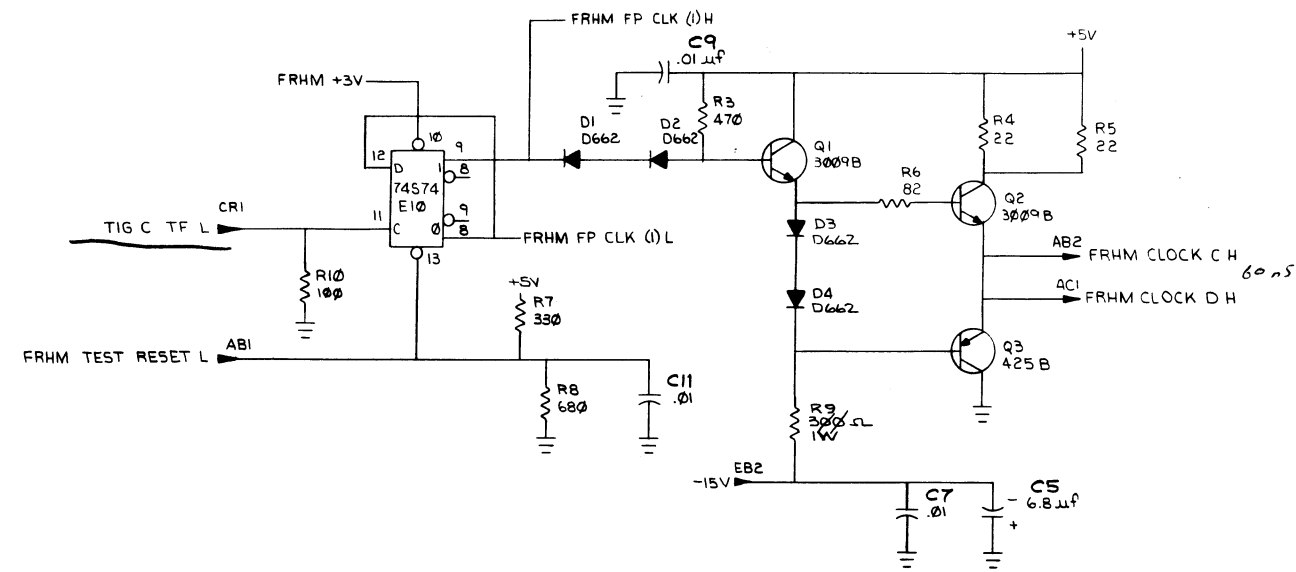
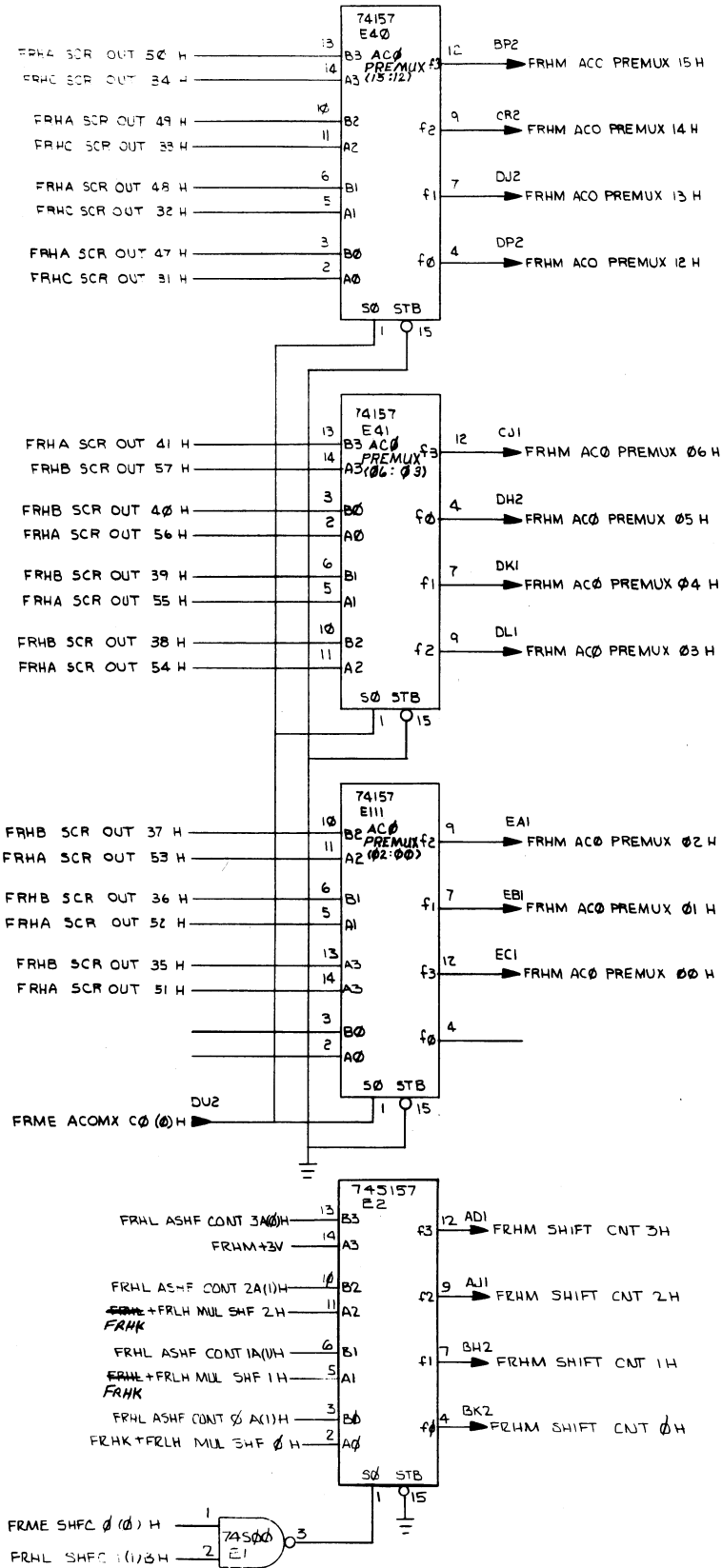


SHIFT CONTROL MODE					
SHFC2	SHFC1	SHFC0	AR SHIFT	QR SHIFT	
0	0	0	EALU	EALU	
0	0	1	DIV SHIFT	DIV SHIFT	
0	1	0	MPY SHIFT	MPY SHIFT	
0	1	1	NORM SHIFT	0	
1	0	0	EALU IF SC09 ELSE 0	EALU IF SC09 ELSE 0	
1	0	1	NOT USED	NOT USED	
1	1	0	NOT USED	NOT USED	
1	1	1	NOP (0 SHIFT)	NOP (0 SHIFT)	

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE FRACTION PROC. HIGH ORDER (FRHL) SIZE CODE NUMBER REV. DCS M8126-0-1 C

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SHIFT COUNT MUX FUNCTION

SHFC1	SHFC0	FRHM SHIFT CNT (3:0) ←	FRHL ASHF CONT (3:0)
0	0	FRHM SHIFT CNT (3:0) ←	FRHL ASHF CONT (3:0)
0	1	FRHM SHIFT CNT (3:0) ←	FRHL ASHF CONT (3:0)
1	0	FRHM SHIFT CNT (3:0) ←	FRHL ASHF CONT (3:0)
1	1	FRHM SHIFT CNT (3:0) ←	FRHL ASHF CONT (3:0)

(SLOT 02)

TITLE	SIZE	CODE	NUMBER	REV.
FRACTION PROC. HIGH ORDER	D	C	M8126-0-1	C
SCALE	+	+	SHEET 12 OF 13	DIST.

CHK	CHANGE NO.	REV.

DEC FORM NO. 000 138

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MULTIPLY CONTROL LOOKUP ROM

ADDRESS (OCTAL)	CONTENTS (OCTAL)	FUNCTION	
		SHIFT BY	OPERATION
000	07	1	ADD
001	06	2	ADD
002	17	1	SUB
003	05	3	ADD
004	07	1	ADD
005	16	2	SUB
006	17	1	SUB
007	04	4	ADD
010	07	1	ADD
011	06	2	ADD
012	17	1	SUB
013	15	3	SUB
014	07	1	ADD
015	16	2	SUB
016	17	1	SUB
017	03	5	ADD
020	07	1	ADD
021	06	2	ADD
022	17	1	SUB
023	15	3	SUB
024	07	1	ADD
025	16	2	SUB
026	17	1	SUB
027	14	4	ADD
030	07	1	ADD
031	06	2	ADD
032	17	1	SUB
033	15	3	SUB
034	07	1	ADD
035	16	2	SUB
036	17	1	SUB
037	02	6	ADD
040	07	1	ADD
041	06	2	ADD
042	17	1	SUB
043	05	3	ADD
044	07	1	ADD
045	16	2	SUB
046	17	1	SUB
047	04	4	ADD
050	07	1	ADD
051	06	2	ADD
052	17	1	SUB
053	15	3	SUB
054	07	1	ADD
055	16	2	SUB
056	17	1	SUB
057	13	5	SUB
060	07	1	ADD
061	06	2	ADD
062	17	1	SUB
063	05	3	ADD
064	07	1	ADD
065	16	2	SUB
066	17	1	SUB
067	14	4	ADD
070	07	1	ADD
071	06	2	ADD
072	17	1	SUB
073	15	3	SUB
074	07	1	ADD
075	16	2	SUB
076	17	1	SUB
077	12	6	SUB

ADDRESS (OCTAL)	CONTENTS (OCTAL)	FUNCTION	
		SHIFT BY	OPERATION
100	07	1	ADD
101	06	2	ADD
102	17	1	SUB
103	05	3	ADD
104	07	1	ADD
105	16	2	SUB
106	17	1	SUB
107	04	4	ADD
110	07	1	ADD
111	06	2	ADD
112	17	1	SUB
113	15	3	SUB
114	07	1	ADD
115	16	2	SUB
116	17	1	SUB
117	03	5	ADD
120	07	1	ADD
121	06	2	ADD
122	17	1	SUB
123	05	3	ADD
124	07	1	ADD
125	16	2	SUB
126	17	1	SUB
127	14	4	ADD
130	07	1	ADD
131	06	2	ADD
132	17	1	SUB
133	15	3	SUB
134	07	1	ADD
135	16	2	SUB
136	17	1	SUB
137	12	6	ADD
140	07	1	ADD
141	06	2	ADD
142	17	1	SUB
143	05	3	ADD
144	07	1	ADD
145	16	2	SUB
146	17	1	SUB
147	04	4	ADD
150	07	1	ADD
151	06	2	ADD
152	17	1	SUB
153	15	3	SUB
154	07	1	ADD
155	16	2	SUB
156	17	1	SUB
157	13	5	SUB
160	07	1	ADD
161	06	2	ADD
162	17	1	SUB
163	05	3	ADD
164	07	1	ADD
165	16	2	SUB
166	17	1	SUB
167	14	4	ADD
170	07	1	ADD
171	06	2	ADD
172	17	1	SUB
173	15	3	SUB
174	07	1	ADD
175	16	2	SUB
176	17	1	SUB
177	12	6	SUB

ADDRESS (OCTAL)	CONTENTS (OCTAL)	FUNCTION	
		SHIFT BY	OPERATION
200	02	6	ADD
201	07	1	ADD
202	06	2	ADD
203	17	1	SUB
204	05	3	ADD
205	07	1	ADD
206	16	2	SUB
207	17	1	SUB
210	04	4	ADD
211	07	1	ADD
212	06	2	ADD
213	17	1	SUB
214	15	3	SUB
215	07	1	ADD
216	16	2	SUB
217	17	1	SUB
220	03	5	ADD
221	07	1	ADD
222	06	2	ADD
223	17	1	SUB
224	05	3	ADD
225	07	1	ADD
226	16	2	SUB
227	17	1	SUB
230	14	4	ADD
231	07	1	ADD
232	06	2	ADD
233	17	1	SUB
234	15	3	SUB
235	07	1	ADD
236	16	2	SUB
237	17	1	SUB
240	02	6	ADD
241	07	1	ADD
242	06	2	ADD
243	17	1	SUB
244	05	3	ADD
245	07	1	ADD
246	16	2	SUB
247	17	1	SUB
250	04	4	ADD
251	07	1	ADD
252	06	2	ADD
253	17	1	SUB
254	15	3	SUB
255	07	1	ADD
256	16	2	SUB
257	17	1	SUB
260	13	5	SUB
261	07	1	ADD
262	06	2	ADD
263	17	1	SUB
264	05	3	ADD
265	07	1	ADD
266	16	2	SUB
267	17	1	SUB
270	14	4	ADD
271	07	1	ADD
272	06	2	ADD
273	17	1	SUB
274	15	3	SUB
275	07	1	ADD
276	16	2	SUB
277	17	1	SUB

ADDRESS (OCTAL)	CONTENTS (OCTAL)	FUNCTION	
		SHIFT BY	OPERATION
300	02	6	ADD
301	07	1	ADD
302	06	2	ADD
303	17	1	SUB
304	05	3	ADD
305	07	1	ADD
306	16	2	SUB
307	17	1	SUB
310	04	4	ADD
311	07	1	ADD
312	06	2	ADD
313	17	1	SUB
314	15	3	SUB
315	07	1	ADD
316	16	2	SUB
317	17	1	SUB
320	03	5	ADD
321	07	1	ADD
322	06	2	ADD
323	17	1	SUB
324	05	3	ADD
325	07	1	ADD
326	16	2	SUB
327	17	1	SUB
330	14	4	ADD
331	07	1	ADD
332	06	2	ADD
333	17	1	SUB
334	15	3	SUB
335	07	1	ADD
336	16	2	SUB
337	17	1	SUB
340	02	6	ADD
341	07	1	ADD
342	06	2	ADD
343	17	1	SUB
344	05	3	ADD
345	07	1	ADD
346	16	2	SUB
347	17	1	SUB
350	04	4	ADD
351	07	1	ADD
352	06	2	ADD
353	17	1	SUB
354	15	3	SUB
355	07	1	ADD
356	16	2	SUB
357	17	1	SUB
360	13	5	SUB
361	07	1	ADD
362	06	2	ADD
363	17	1	SUB
364	05	3	ADD
365	07	1	ADD
366	16	2	SUB
367	17	1	SUB
370	14	4	ADD
371	07	1	ADD
372	06	2	ADD
373	17	1	SUB
374	15	3	SUB
375	07	1	ADD
376	16	2	SUB
377	17	1	SUB

REVISIONS		
CHK	CHANGE NO.	REV.

(SLOT 02)

TITLEFRACTION PROC.
HIGH ORDER

SIZE CODED

NUMBERCS M8126-0-1

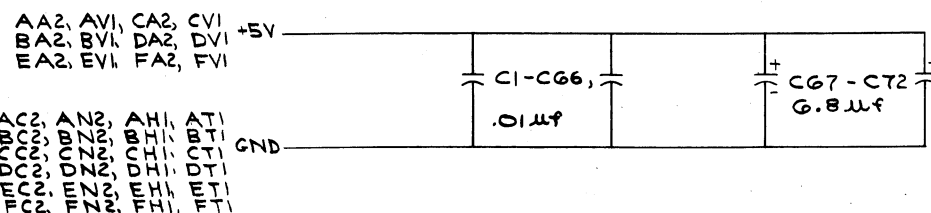
REV.C

SCALE

SHEET13 OF 13

DIST.

NOTES:



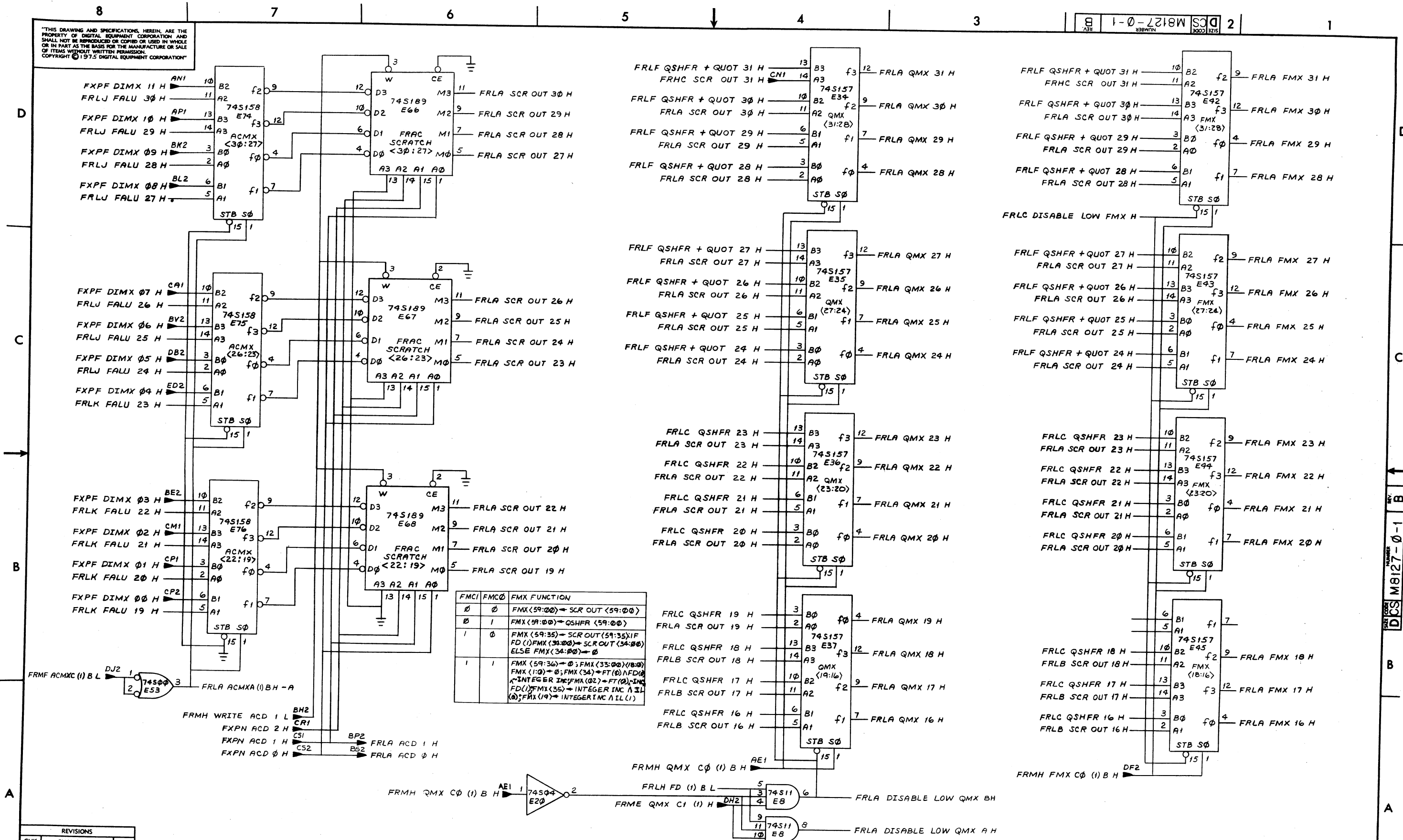
San Northrup 5 Jan 76	T. NORTHROP	H. Denmore 2 JAN '76	<i>2</i> M8127-00001 B			
				ORIGINATED	A	
				CHANGE NO.	REV.	
				CHK		REVISIONS

FIRST USED ON OPTION MODEL	
FPII-C	
DEC NO.	EIA NO.
SEMICONDUCTOR	

QTY	REF. DESIGNATION	DESCRIPTION										PART NO.	ITEM NO.
PARTS LIST													
ETCH BOARD REV.		C											
		DRIN.	DATE		digital								
		CHD.	DATE										
		ENG.	DATE		TITLE FRACTION PROC. LOW ORDER								
		PROJ. ENG.	DATE										
		PROJ.	DATE		SIZE CODE NUMBER D CS M8127-0-1								
		NEXT HIGHER ASSY											
DEC NO.		EIA NO.	B-LD-FP11-C		REV. B								
			SCALE										
CONVERSION CHART		SHEET	OF	14	DIST.								

DEC FORM NO
DBD 135.B

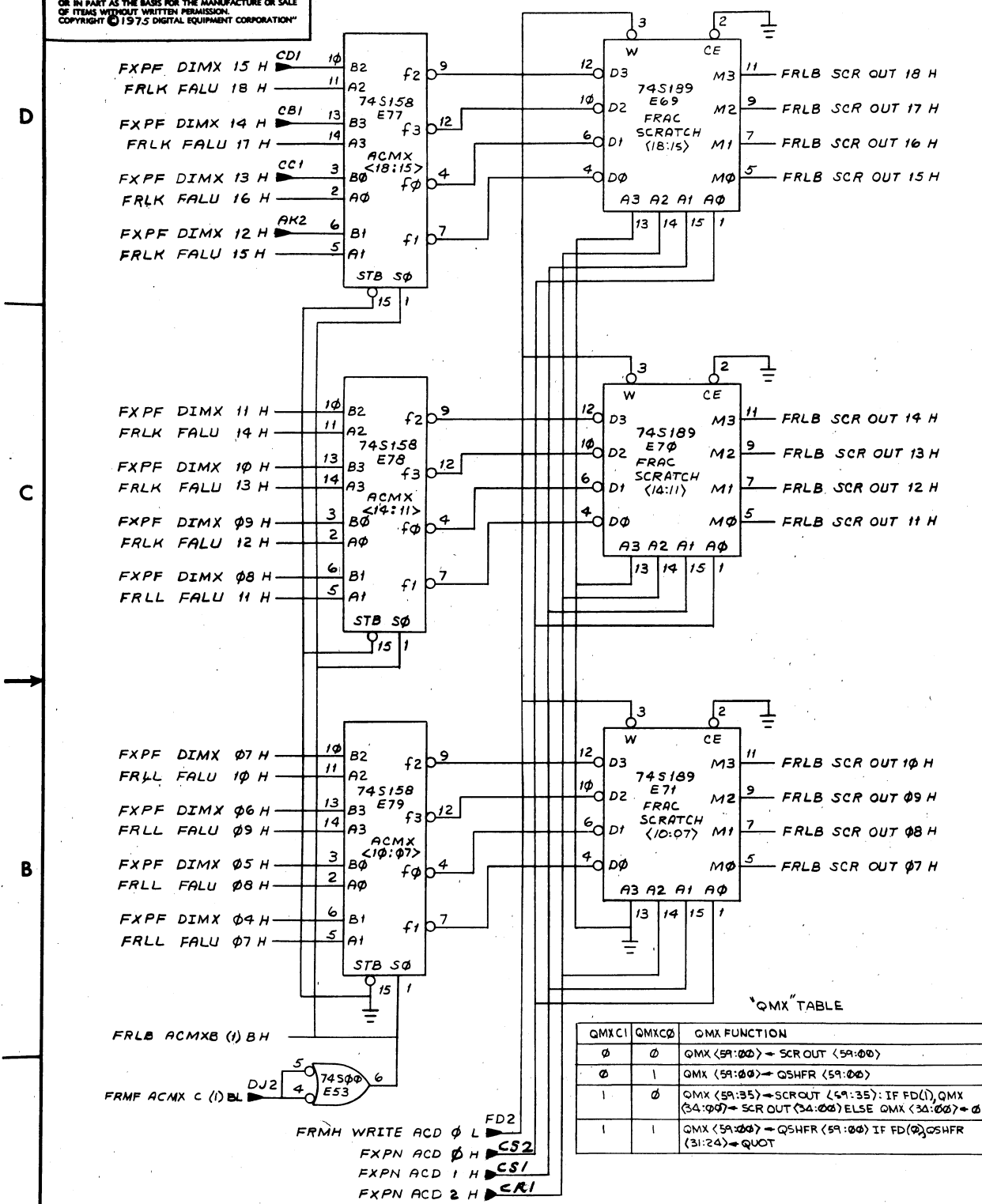
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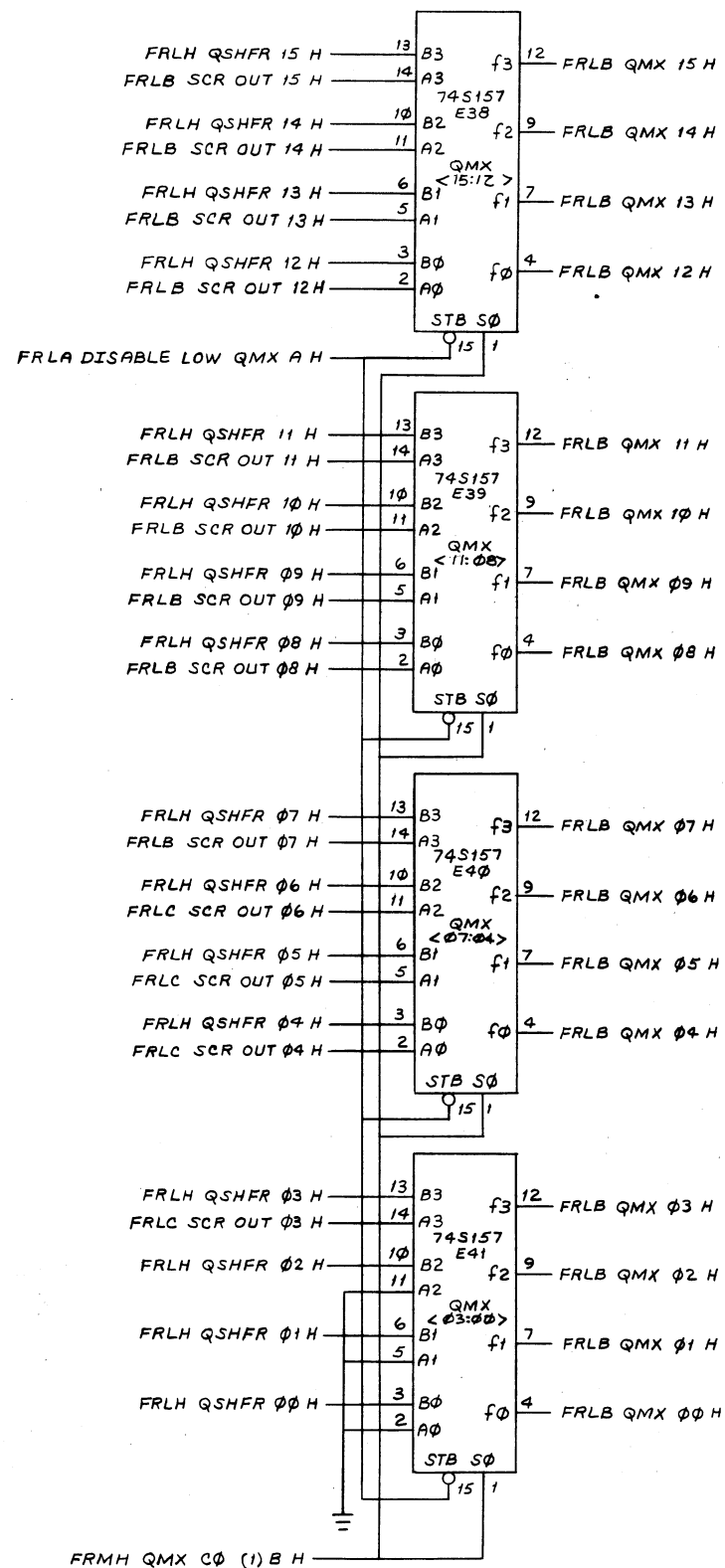
REVISIONS		
CHK	CHANGE NO.	REV.

TITLE FRACTION PROC. LOW ORDER (FRLA)		SIZE CODE DCS	NUMBER M8127-0-1	REV. B
SCALE 1/1	SHEET 2 OF 14	DIST.		

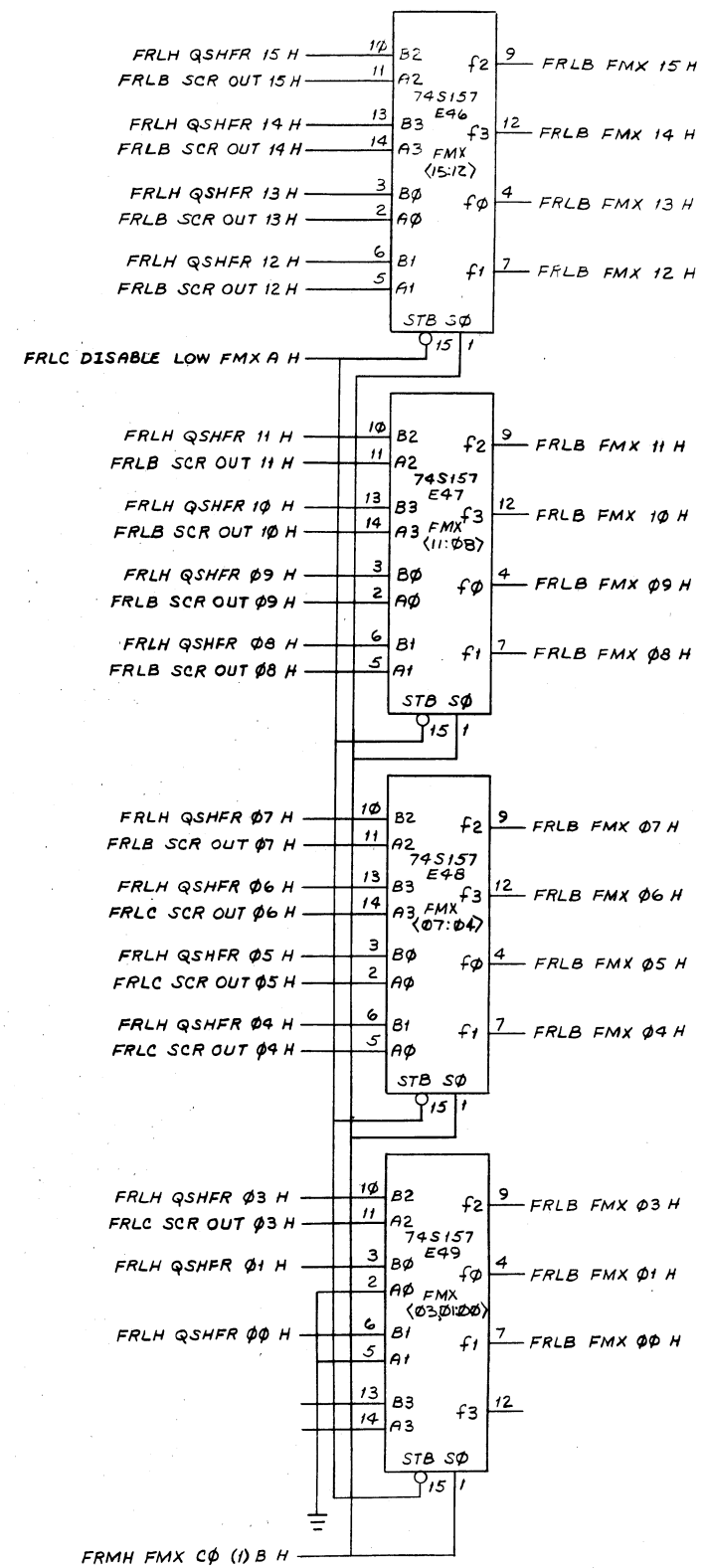
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"QMX" TABLE		
QMXC1	QMXC0	QMX FUNCTION
0	0	QMX (<59:00> → SCR OUT (<59:00>)
0	1	QMX (<59:00> → Q5HFR (<59:00>)
1	0	QMX (<59:35> → SCR OUT (<59:35>); IF FD(1), QMX (<34:00> → SCR OUT (<34:00>)) ELSE QMX (<34:00>) = 0
1	1	QMX (<59:00>) → Q5HFR (<59:00>); IF FD(0), Q5HFR (<31:24>) → QUOT



"QMX" TABLE		
QMXC1	QMXC0	QMX FUNCTION
0	0	QMX (<59:00> → SCR OUT (<59:00>)
0	1	QMX (<59:00> → Q5HFR (<59:00>)
1	0	QMX (<59:35> → SCR OUT (<59:35>); IF FD(1), QMX (<34:00> → SCR OUT (<34:00>)) ELSE QMX (<34:00>) = 0
1	1	QMX (<59:00>) → Q5HFR (<59:00>); IF FD(0), Q5HFR (<31:24>) → QUOT



"QMX" TABLE		
QMXC1	QMXC0	QMX FUNCTION
0	0	QMX (<59:00> → SCR OUT (<59:00>)
0	1	QMX (<59:00> → Q5HFR (<59:00>)
1	0	QMX (<59:35> → SCR OUT (<59:35>); IF FD(1), QMX (<34:00> → SCR OUT (<34:00>)) ELSE QMX (<34:00>) = 0
1	1	QMX (<59:00>) → Q5HFR (<59:00>); IF FD(0), Q5HFR (<31:24>) → QUOT

(SLOT 3)

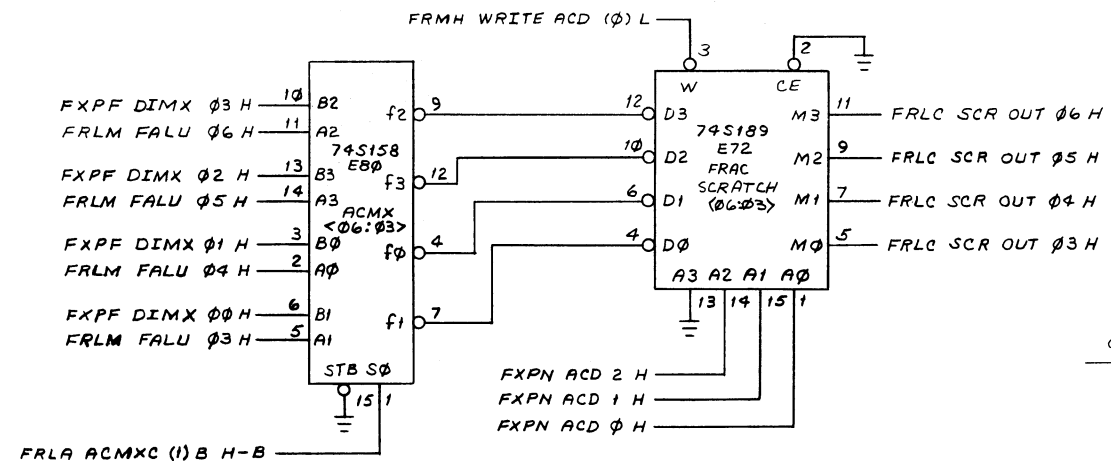
REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		FRACTION PROC. LOW ORDER (FRLB)		SIZE	CODE	NUMBER				REV.
				D	CS	M8127-0-1				B
SCALE	—/—	SHEET	3 OF 14	DIST.						

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D
C
B
A

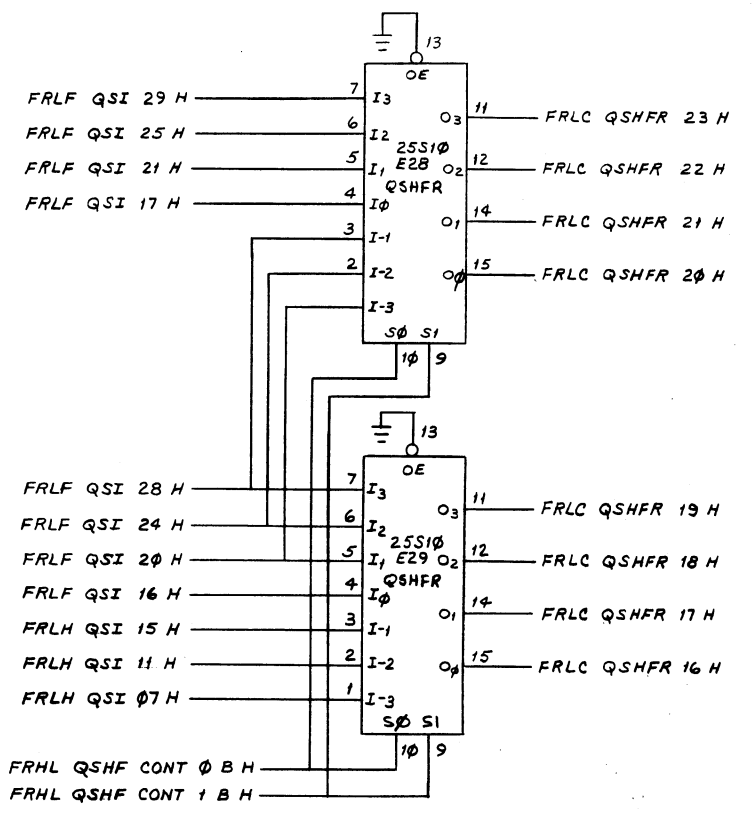
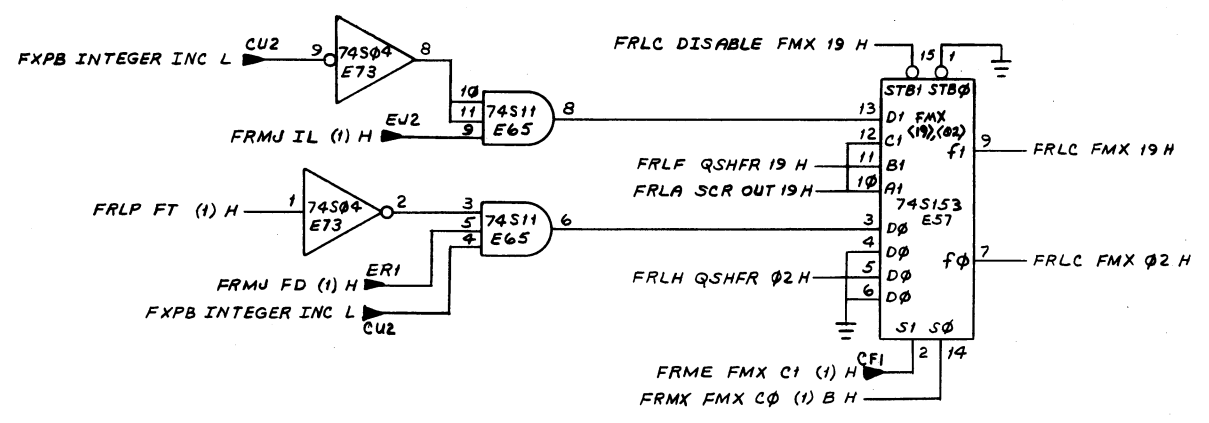
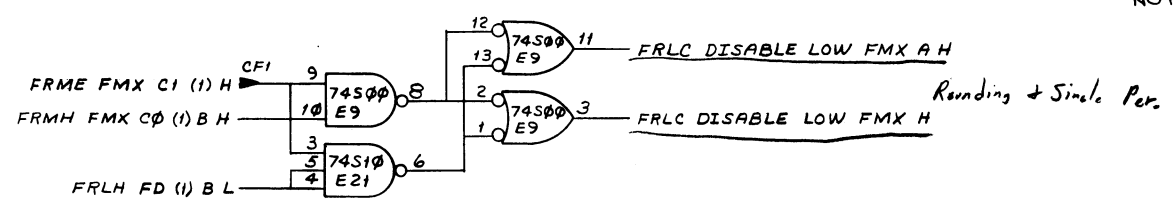
D
C
B
A



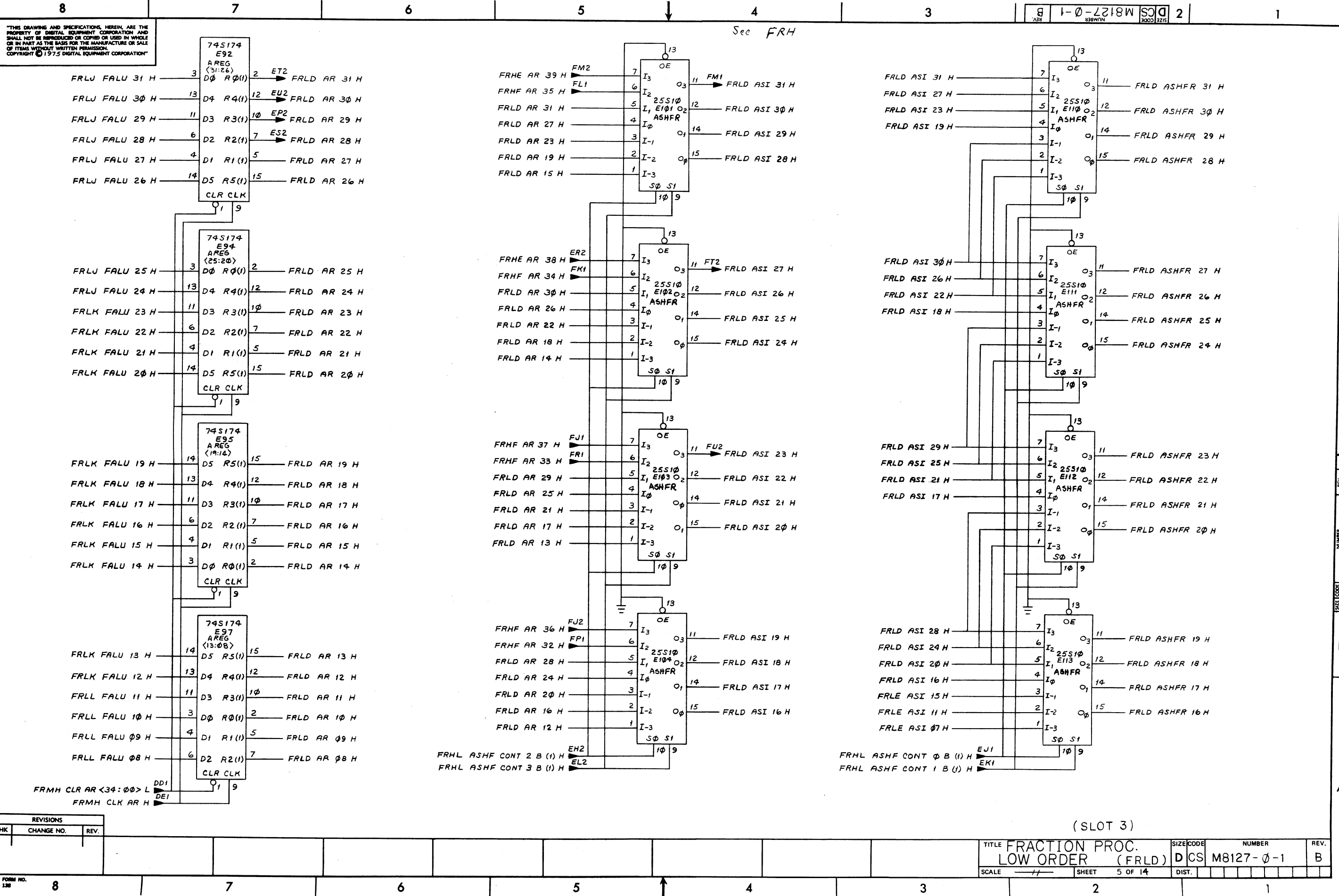
TRUTH TABLE FOR DEC 25510

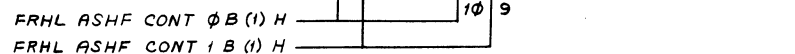
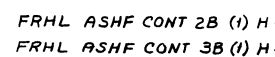
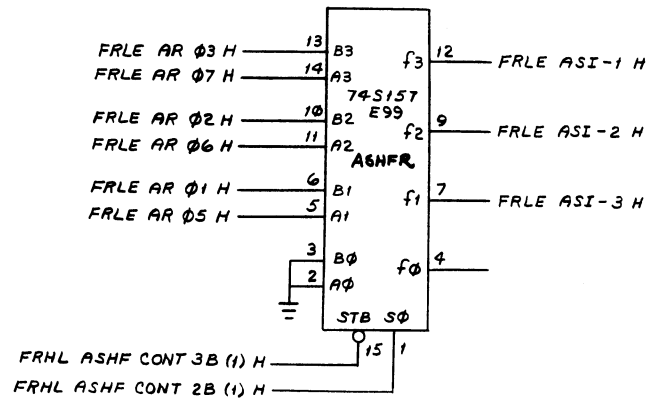
O _E	S ₁	S ₀	O ₀	O ₁	O ₂	O ₃
0	0	0	I ₀	I ₁	I ₂	I ₃
0	0	1	I-1	I ₀	I ₁	I ₂
0	1	0	I-2	I-1	I ₀	I ₁
0	1	1	I-3	I-2	I-1	I ₀
1	X	X	Z	Z	Z	Z

NOTES: X=DONT CARE
Z=OUTPUT 'OFF' I.E. IN HIGH IMPEDANCE STATE

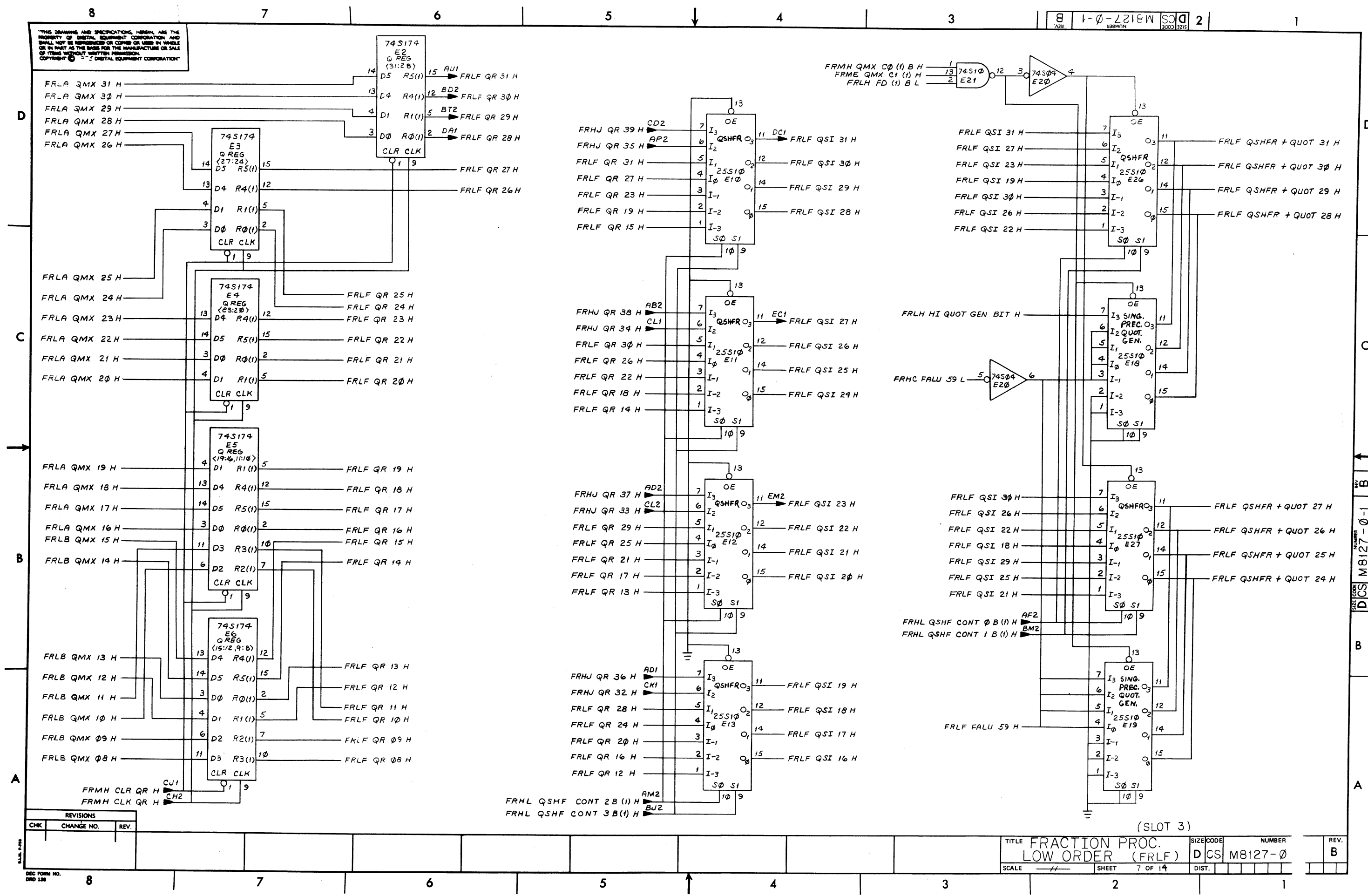


(SLOT 3)





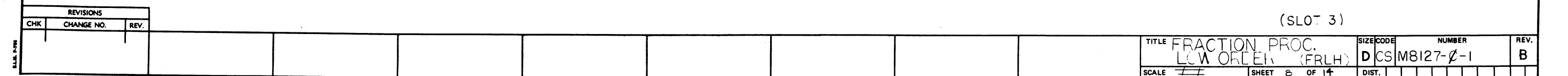
TITLE		FRACTION PROC. LOW ORDER (FRLE)		SIZE	CODE	NUMBER				REV.
				D	CS	M8127-Ø-1				B.
SCALE	—/—	SHEET	6 OF 14	DIST.						



TITLE FRACTION PROC.
LOW ORDER (FRLF)
SCALE 1/8"=1"

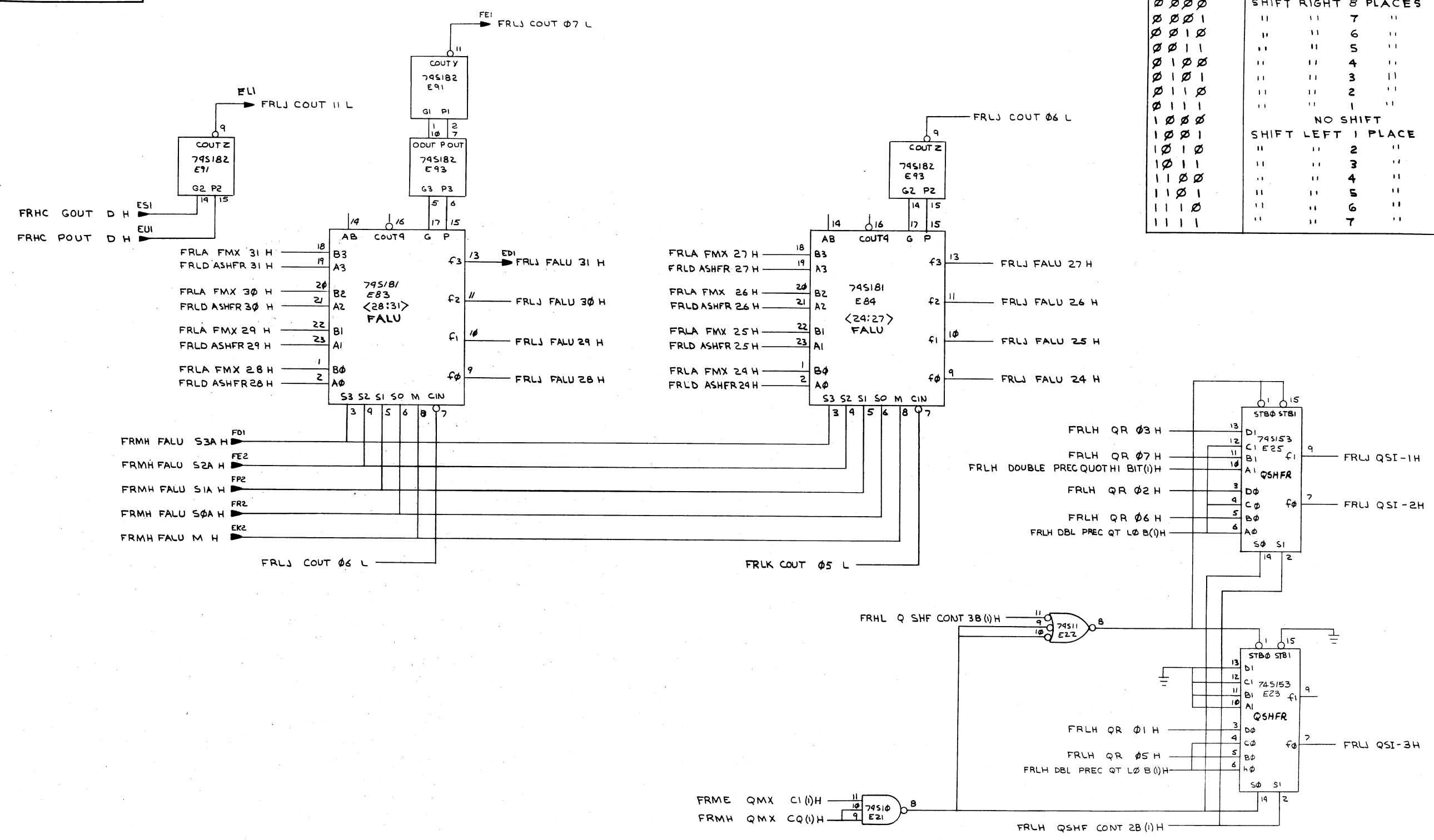
SIZE CODE DCS
SHEET 7 OF 14
DIST.

NUMBER M8127-0
REV. B



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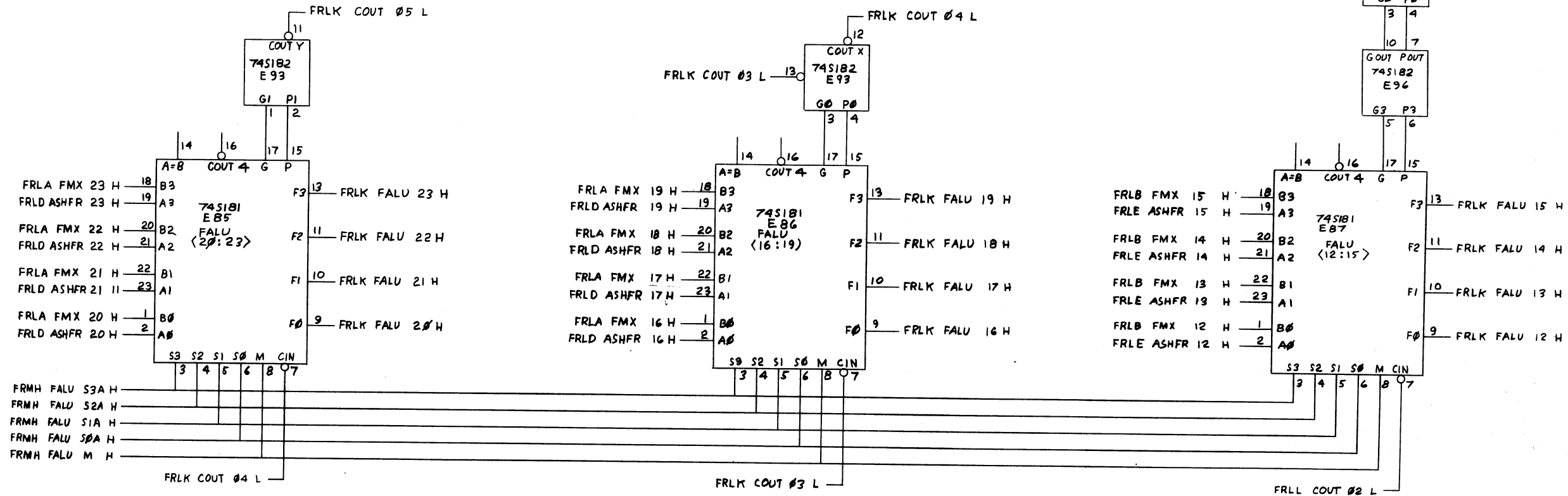
A/Q SHF CONT	ASHFR/QSHFR FUNCTION
3 2 1 0	SHIFT RIGHT 8 PLACES
0 0 0 0	" " 7 "
0 0 0 1	" " 6 "
0 0 1 0	" " 5 "
0 0 1 1	" " 4 "
0 1 0 0	" " 3 "
0 1 0 1	" " 2 "
0 1 1 0	" " 1 "
0 1 1 1	NO SHIFT
1 0 0 0	SHIFT LEFT 1 PLACE
1 0 0 1	" " 2 "
1 0 1 0	" " 3 "
1 0 1 1	" " 4 "
1 1 0 0	" " 5 "
1 1 0 1	" " 6 "
1 1 1 0	" " 7 "
1 1 1 1	" " "



REV.	CHANGE NO.	CHK

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REV. 8 DCSM8127-0-1 2



(SLOT 3)

REVISIONS		
CHK	CHANGE NO.	REV.

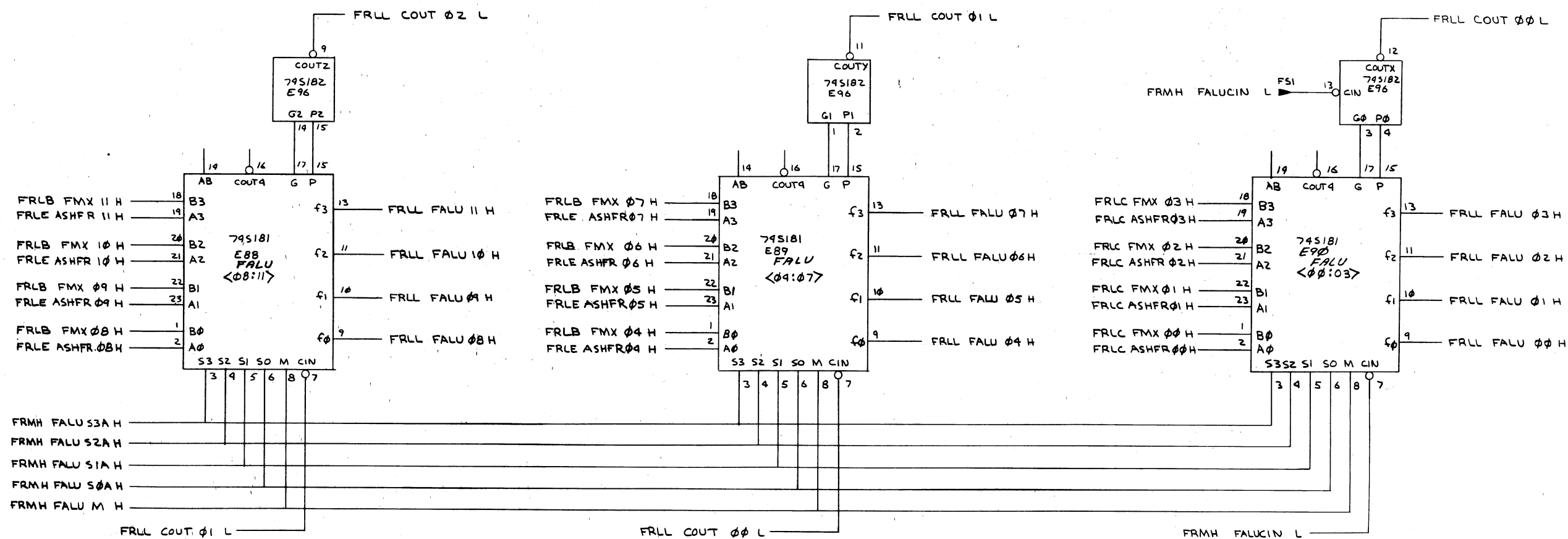
DEC FORM NO. DRD 138

TITLE FRACTION (FRLK) PROC LOW ORDER SIZE CODE DCSM8127-0-1 NUMBER REV. B

SCALE 1/1 SHEET 10 OF 14 DIST.

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1-0-721671 2 1

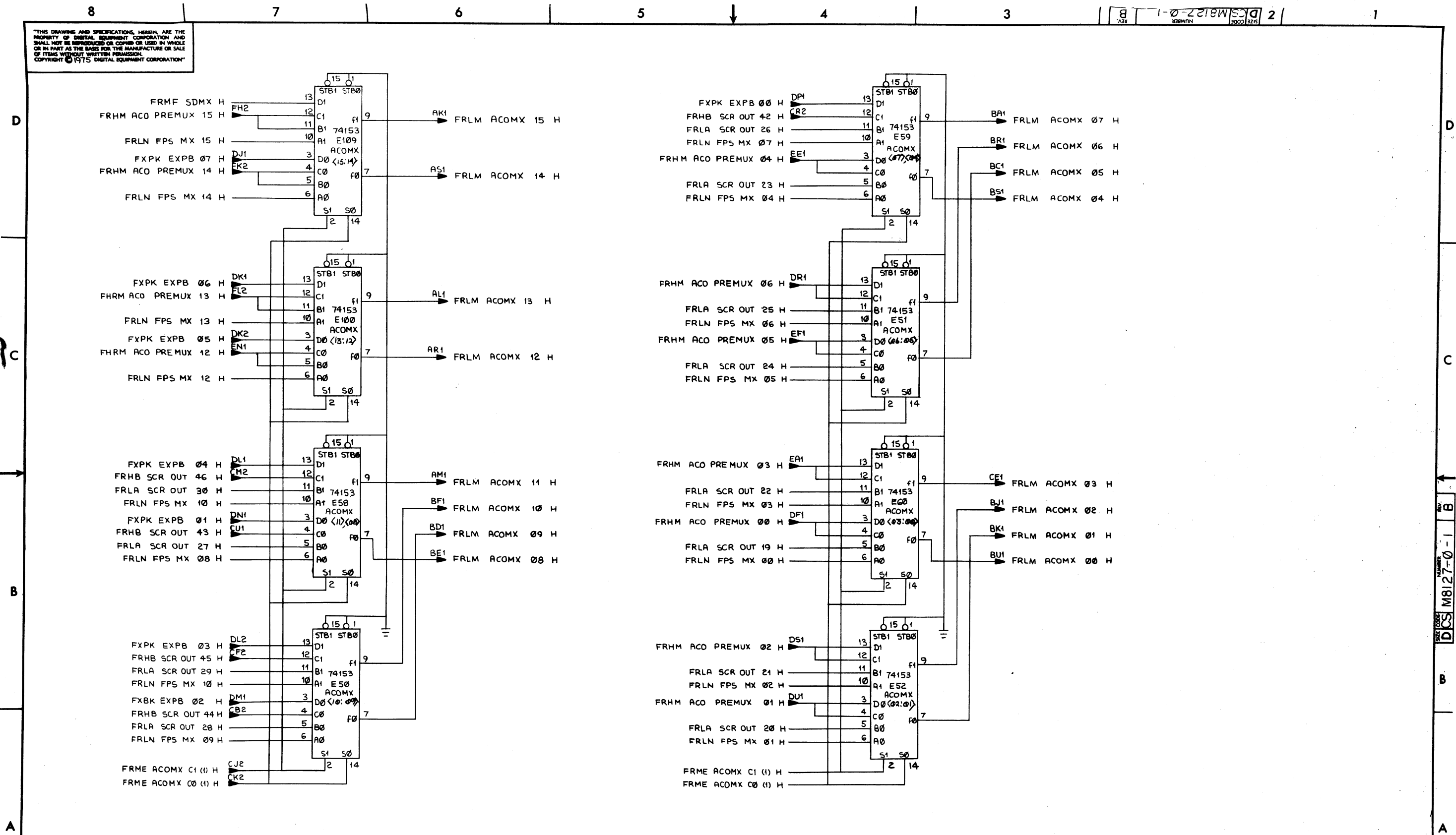


REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		SIZE CODE	NUMBER	REV.
FRACTION PROC		D	CSM8127-0-1	B
LOW ORDER (FRL)		DIST.		
SCALE		SHEET	11 OF 14	

DEC FORM NO. 130

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REVISIONS		
CHK	CHANGE NO.	REV.

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MULTIPLY CONTROL LOOKUP ROM

ADDRESS (OCTAL)	CONTENTS (OCTAL)	FUNCTION	
		SHIFT BY	OPERATION
000	07	1	ADD
001	06	2	ADD
002	17	1	SUB
003	05	3	ADD
004	07	1	ADD
005	16	2	SUB
006	17	1	SUB
007	04	4	ADD
010	07	1	ADD
011	06	2	ADD
012	17	1	SUB
013	15	3	ADD
014	07	1	ADD
015	16	2	SUB
016	17	1	SUB
017	03	5	ADD
020	07	1	ADD
021	06	2	ADD
022	17	1	SUB
023	15	3	ADD
024	07	1	ADD
025	16	2	SUB
026	17	1	SUB
027	14	4	SUB
030	07	1	ADD
031	06	2	ADD
032	17	1	SUB
033	15	3	ADD
034	07	1	ADD
035	16	2	SUB
036	17	1	SUB
037	02	6	ADD
040	07	1	ADD
041	06	2	ADD
042	17	1	SUB
043	05	3	ADD
044	07	1	ADD
045	16	2	SUB
046	17	1	SUB
047	04	4	ADD
050	07	1	ADD
051	06	2	ADD
052	17	1	SUB
053	15	3	ADD
054	07	1	ADD
055	16	2	SUB
056	17	1	SUB
057	13	5	SUB
060	07	1	ADD
061	06	2	ADD
062	17	1	SUB
063	05	3	ADD
064	07	1	ADD
065	16	2	SUB
066	17	1	SUB
067	14	4	SUB
070	07	1	ADD
071	06	2	ADD
072	17	1	SUB
073	15	3	ADD
074	07	1	ADD
075	16	2	SUB
076	17	1	SUB
077	12	6	SUB

ADDRESS (OCTAL)	CONTENTS (OCTAL)	FUNCTION	
		SHIFT BY	OPERATION
100	07	1	ADD
101	06	2	ADD
102	17	1	SUB
103	05	3	ADD
104	07	1	ADD
105	16	2	SUB
106	17	1	SUB
107	04	4	ADD
110	07	1	ADD
111	06	2	ADD
112	17	1	SUB
113	15	3	ADD
114	07	1	ADD
115	16	2	SUB
116	17	1	SUB
117	03	5	ADD
120	07	1	ADD
121	06	2	ADD
122	17	1	SUB
123	05	3	ADD
124	07	1	ADD
125	16	2	SUB
126	17	1	SUB
127	14	4	SUB
130	07	1	ADD
131	06	2	ADD
132	17	1	SUB
133	15	3	ADD
134	07	1	ADD
135	16	2	SUB
136	17	1	SUB
137	12	6	SUB
140	07	1	ADD
141	06	2	ADD
142	17	1	SUB
143	05	3	ADD
144	07	1	ADD
145	16	2	SUB
146	17	1	SUB
147	04	4	ADD
150	07	1	ADD
151	06	2	ADD
152	17	1	SUB
153	15	3	ADD
154	07	1	ADD
155	16	2	SUB
156	17	1	SUB
157	13	5	SUB
160	07	1	ADD
161	06	2	ADD
162	17	1	SUB
163	05	3	ADD
164	07	1	ADD
165	16	2	SUB
166	17	1	SUB
167	14	4	SUB
170	07	1	ADD
171	06	2	ADD
172	17	1	SUB
173	15	3	ADD
174	07	1	ADD
175	16	2	SUB
176	17	1	SUB
177	12	6	SUB

ADDRESS (OCTAL)	CONTENTS (OCTAL)	FUNCTION	
		SHIFT BY	OPERATION
200	02	6	ADD
201	07	1	ADD
202	06	2	ADD
203	17	1	SUB
204	05	3	ADD
205	07	1	ADD
206	16	2	SUB
207	17	1	SUB
210	04	4	ADD
211	07	1	ADD
212	06	2	ADD
213	17	1	SUB
214	15	3	ADD
215	07	1	ADD
216	16	2	SUB
217	17	1	SUB
220	03	5	ADD
221	07	1	ADD
222	06	2	ADD
223	17	1	SUB
224	05	3	ADD
225	07	1	ADD
226	16	2	SUB
227	17	1	SUB
230	14	4	SUB
231	07	1	ADD
232	06	2	ADD
233	17	1	SUB
234	15	3	ADD
235	07	1	ADD
236	16	2	SUB
237	17	1	SUB
240	02	6	ADD
241	07	1	ADD
242	06	2	ADD
243	17	1	SUB
244	05	3	ADD
245	07	1	ADD
246	16	2	SUB
247	17	1	SUB
250	04	4	ADD
251	07	1	ADD
252	06	2	ADD
253	17	1	SUB
254	15	3	ADD
255	07	1	ADD
256	16	2	SUB
257	17	1	SUB
260	13	5	SUB
261	07	1	ADD
262	06	2	ADD
263	17	1	SUB
264	05	3	ADD
265	07	1	ADD
266	16	2	SUB
267	17	1	SUB
270	14	4	SUB
271	07	1	ADD
272	06	2	ADD
273	17	1	SUB
274	15	3	ADD
275	07	1	ADD
276	16	2	SUB
277	17	1	SUB

ADDRESS (OCTAL)	CONTENTS (OCTAL)	FUNCTION	
		SHIFT BY	OPERATION
300	02	6	ADD
301	07	1	ADD
302	06	2	ADD
303	17	1	SUB
304	05	3	ADD
305	07	1	ADD
306	16	2	SUB
307	17	1	SUB
310	04	4	ADD
311	07	1	ADD
312	06	2	ADD
313	17	1	SUB
314	15	3	ADD
315	07	1	ADD
316	16	2	SUB
317	17	1	SUB
320	03	5	ADD
321	07	1	ADD
322	06	2	ADD
323	17	1	SUB
324	05	3	ADD
325	07	1	ADD
326	16	2	SUB
327	17	1	SUB
330	14	4	SUB
331	07	1	ADD
332	06	2	ADD
333	17	1	SUB
334	15	3	ADD
335	07	1	ADD
336	16	2	SUB
337	17	1	SUB
340	12	6	SUB
341	07	1	ADD
342	06	2	ADD
343	17	1	SUB
344	05	3	ADD
345	07	1	ADD
346	16	2	SUB
347	17	1	SUB
350	04	4	ADD
351	07	1	ADD
352	06	2	ADD
353	17	1	SUB
354	15	3	ADD
355	07	1	ADD
356	16	2	SUB
357	17	1	SUB
360	13	5	SUB
361	07	1	ADD
362	06	2	ADD
363	17	1	SUB
364	05	3	ADD
365	07	1	ADD
366	16	2	SUB
367	17	1	SUB
370	14	4	SUB
371	07	1	ADD
372	06	2	ADD
373	17	1	SUB
374	15	3	ADD
375	07	1	ADD
376	16	2	SUB
377	17	1	SUB

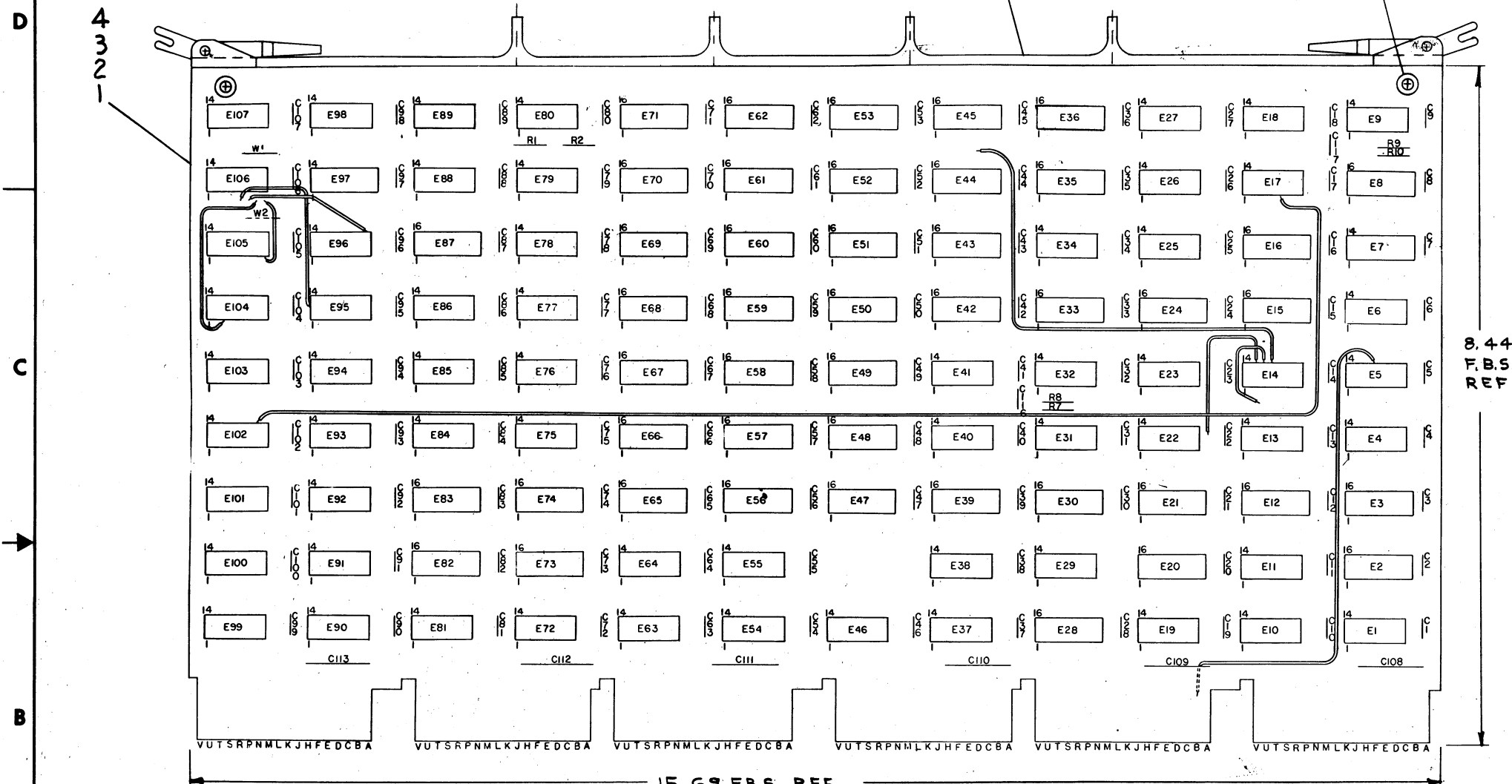
REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		SIZE CODE	NUMBER	REV.
FRACTION PROC. LOW ORDER (FRLP)		DCS	M8127-0-1	B
SCALE	SHEET 14 OF 14	DIST.		

(SLOT 3)

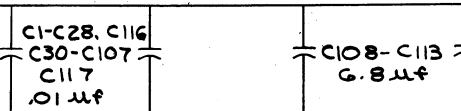
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NOTES: 1) E8, AND E73 ARE SPARE IC LOCATIONS
2) REF. DES. C29 IS NOT USED



AA2, AV1, CA2, CV1
BA2, BV1, DA2, DV1 +5V
EA2, EV1, FA2, FV1

AA2, AV1, CA2, CV1
BA2, BV1, DA2, DV1 +5V
EA2, EV1, FA2, FV1
GND



IC	TYPE	QTY	LOC
IC 74175	8	16	
256x4 ROM	8	16	
IC 74S175	8	16	
IC 74S151	8	16	
IC 74157	8	16	
IC 74S174	8	16	
IC 74S112	8	16	
IC 74193	8	16	
IC 74151	8	16	
IC TYPE	GND	+5V	

GND AND 5V ARE USUALLY PIN 7 AND 14
RESPECTIVELY EXCEPTIONS ARE STATED ABOVE

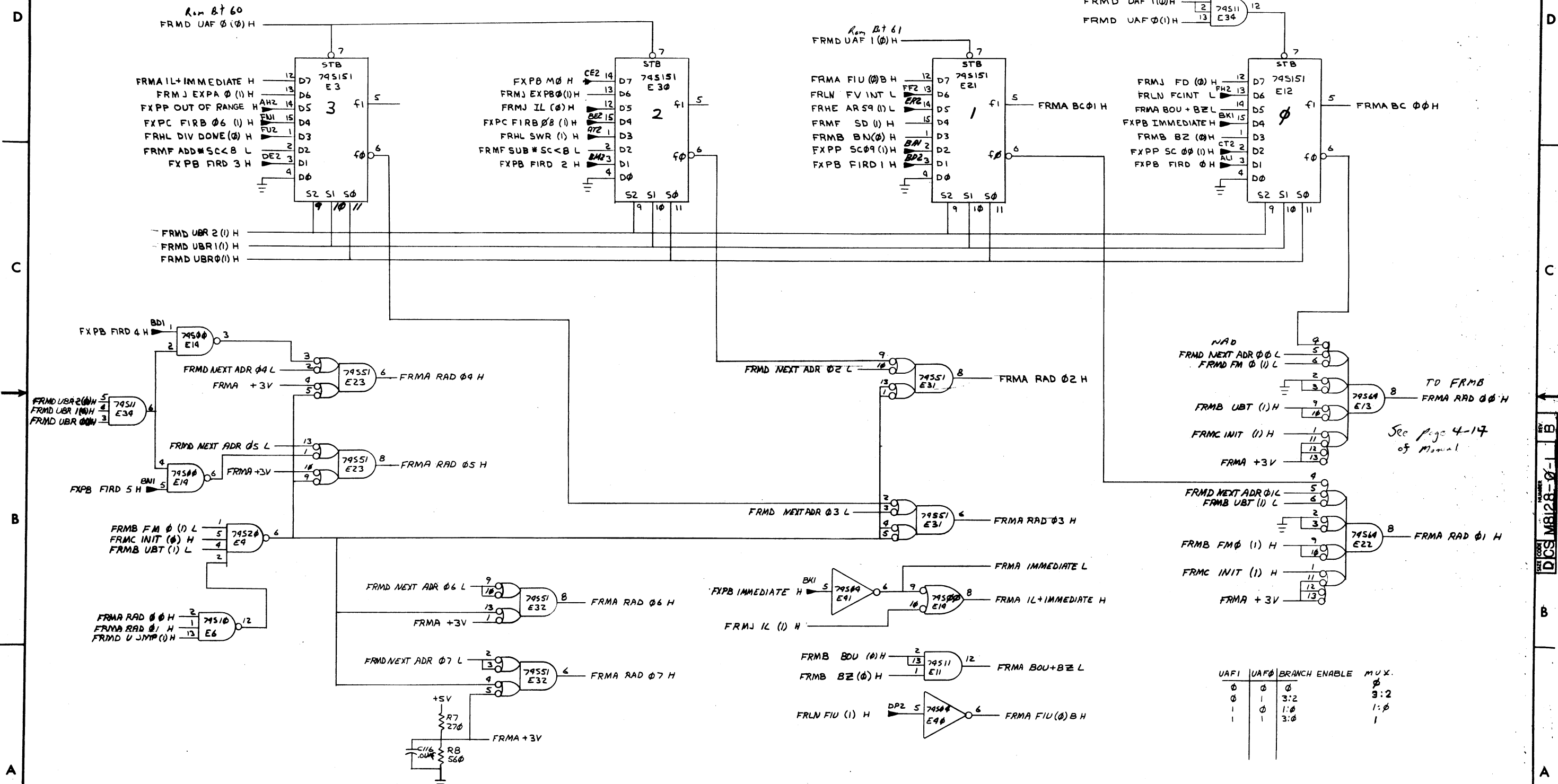
IC PIN LOCATIONS

DEC FORM NO.
DND 135.8

REF	X-Y COORDINATE HOLE LOCATION	K-CO-M8128-0-4	1
REF	ASSY/DRILLING HOLE LAYOUT	D-AH-M8128-0-5	2
REF	MODULE ECO HISTORY	B-MH-M8128-0-6	3
1	ETCHED CIRCUIT BOARD	5011872	4
108	C1 THRU C28, C30 THRU C107, C116, C117	CAPACITOR, .01µf	5
6	C108 THRU C113	CAPACITOR, 0.8µf	6
1	HANDLE HEX MODULE	1210711-2	7
1	R1	SPARE	8
2	R8, R10	RESISTOR, 560 OHM, 1/4W, 5%	9
1	R2	RESISTOR, 100 OHM, 1/4W, 5%	10
2	R7, R9	RESISTOR, 270 OHM, 1/4W, 5%	11
3	E35, E36, E87	I.C. DEC 74151	12
1	E27	I.C. DEC 7486	13
1	E16	I.C. DEC 74193	14
9	E14, E17, E55, E75, E78, E84, E98, E100, E106	I.C. DEC 74S00	15
6	E28, E40, E41, E64, E96, E101	I.C. DEC 74S04	16
7	E6, E18, E72, E80, E89, E94, E102	I.C. DEC 74S10	17
4	E11, E34, E95, E7	I.C. DEC 74S11	18
2	E4, E93	I.C. DEC 74S20	19
9	E29, E37, E88, E46, E90, E99, E103, E20, E79	I.C. DEC 74S40	20
3	E13, E22, E54	I.C. DEC 74S64	21
9	E1, E5, E9, E10, E19, E63, E81, E105, E107	I.C. DEC 74S74	22
3	E82, E83, E104	I.C. DEC 74S112	23
7	E76, E77, E85, E86, E91, E92, E38	I.C. DEC 74S140	24
3	E15, E24, E33	I.C. DEC 74S174	25
1	E28	I.C. DEC 74157	26
4	E3, E12, E21, E30	I.C. DEC 74S151	27
5	E2, E39, E42, E65, E66	I.C. DEC 74S175	28
5	E23, E25, E31, E32, E97	I.C. DEC 74S51	29
1	E74	I.C. DEC 8251-1	30
1	E47	ROM	31
1	E48	ROM	32
1	E49	ROM	33
1	E50	ROM	34
1	E51	ROM	35
1	E52	ROM	36
1	E53	ROM	37
1	E56	ROM	38
1	E57	ROM	39
1	E58	ROM	40
1	E59	ROM	41
1	E60	ROM	42
1	E61	ROM	43
1	E62	ROM	44
12	EYELET	9006732	45
8	E42, E45, E67, E69, E70, E71	I.C. DEC 74175	46
1	W1	JUMPER	47
A/R	WIRE, 30 AWG	9105740-55	48
			49

QTY	REF. DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
ETCH BOARD REV. C				
FIRST USED ON OPTION MODEL FPII-C				
TITLE FP ROM CONTROL				
D E S I G N A T I O N				
M8128-0-1				
REV. B				
SEMICONDUCTOR CONVERSION CHART				
SHEET 1 OF 11				

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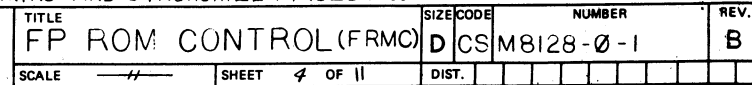


UAF1	UAF0	BRANCH ENABLE	MUX
0	0	0	0
0	1	3:2	3:2
1	0	1:0	1:0
1	1	3:0	1

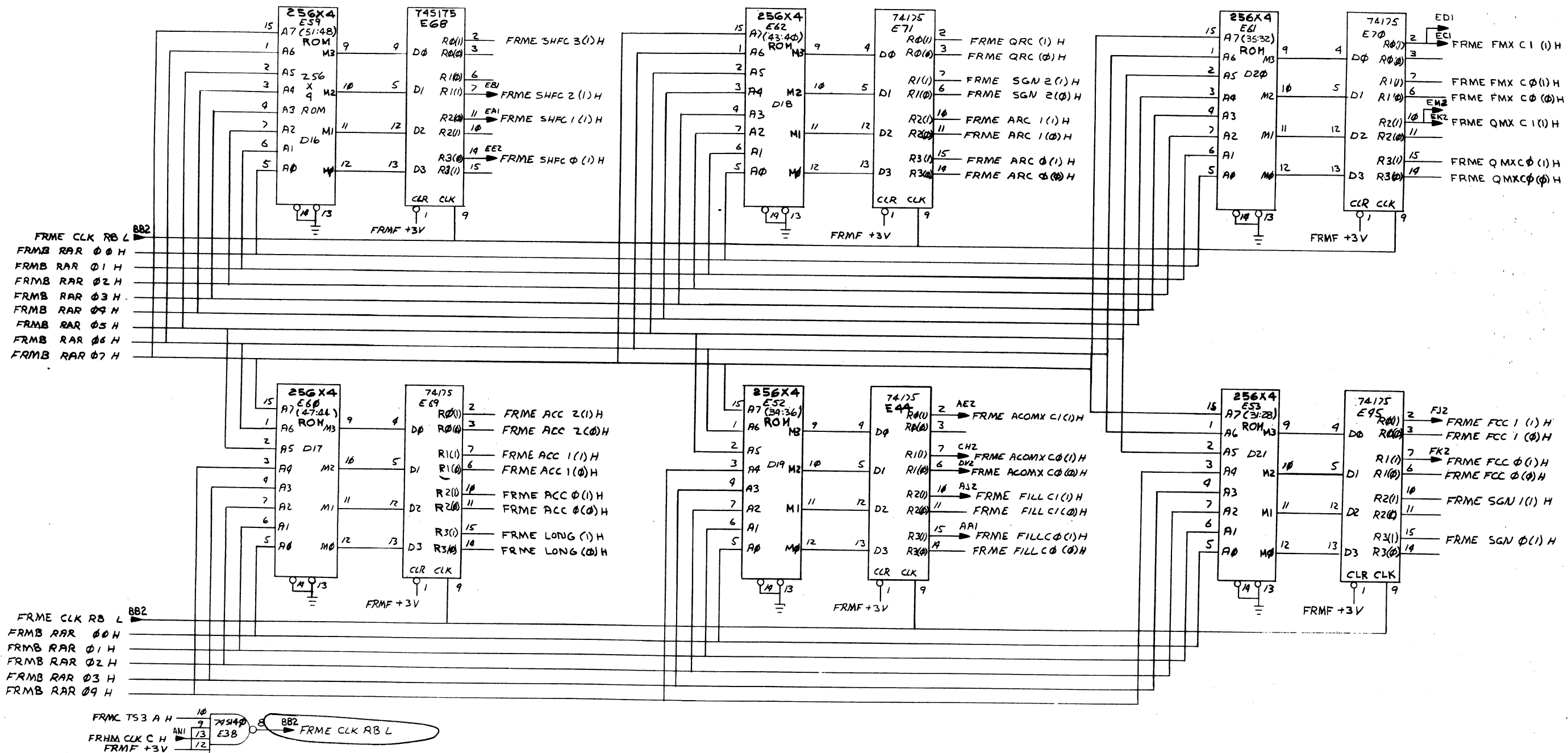
REVISIONS		
CHK	CHANGE NO.	REV.

(BRANCH CONTROL (SLOT 4)

TITLE		(FRMA)	SIZE	CODE	NUMBER				REV.
FP ROM CONTROL			D	CS	M8128-0-1				B.
SCALE	1/2	SHEET	2	OF	11	DIST.			

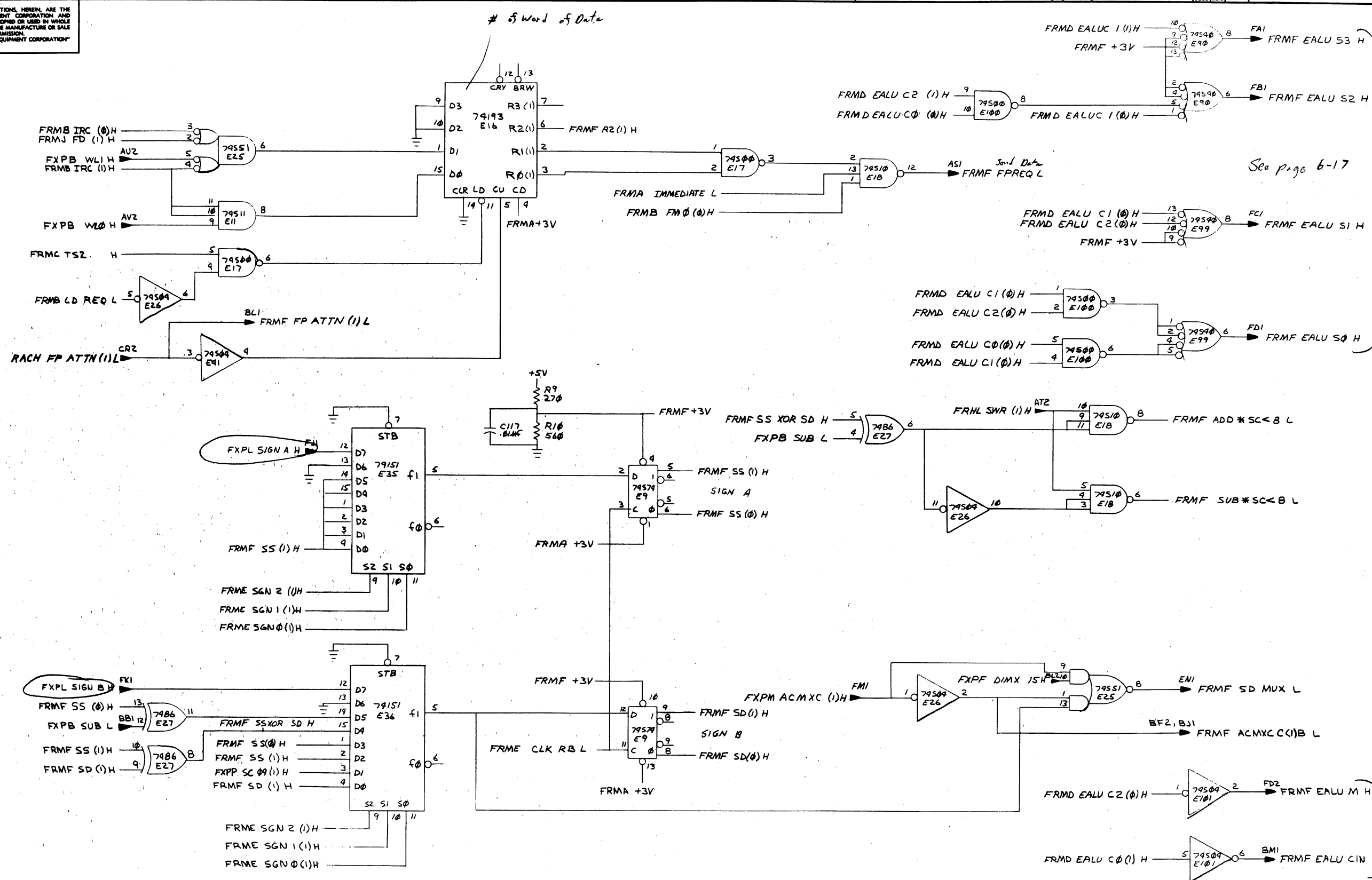


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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		(FRME)	SIZE CODE	NUMBER	REV.
FP ROM CONTROL			DCS	M8128-0-1	B
SCALE	SHEET 6 OF 11		DIST.		

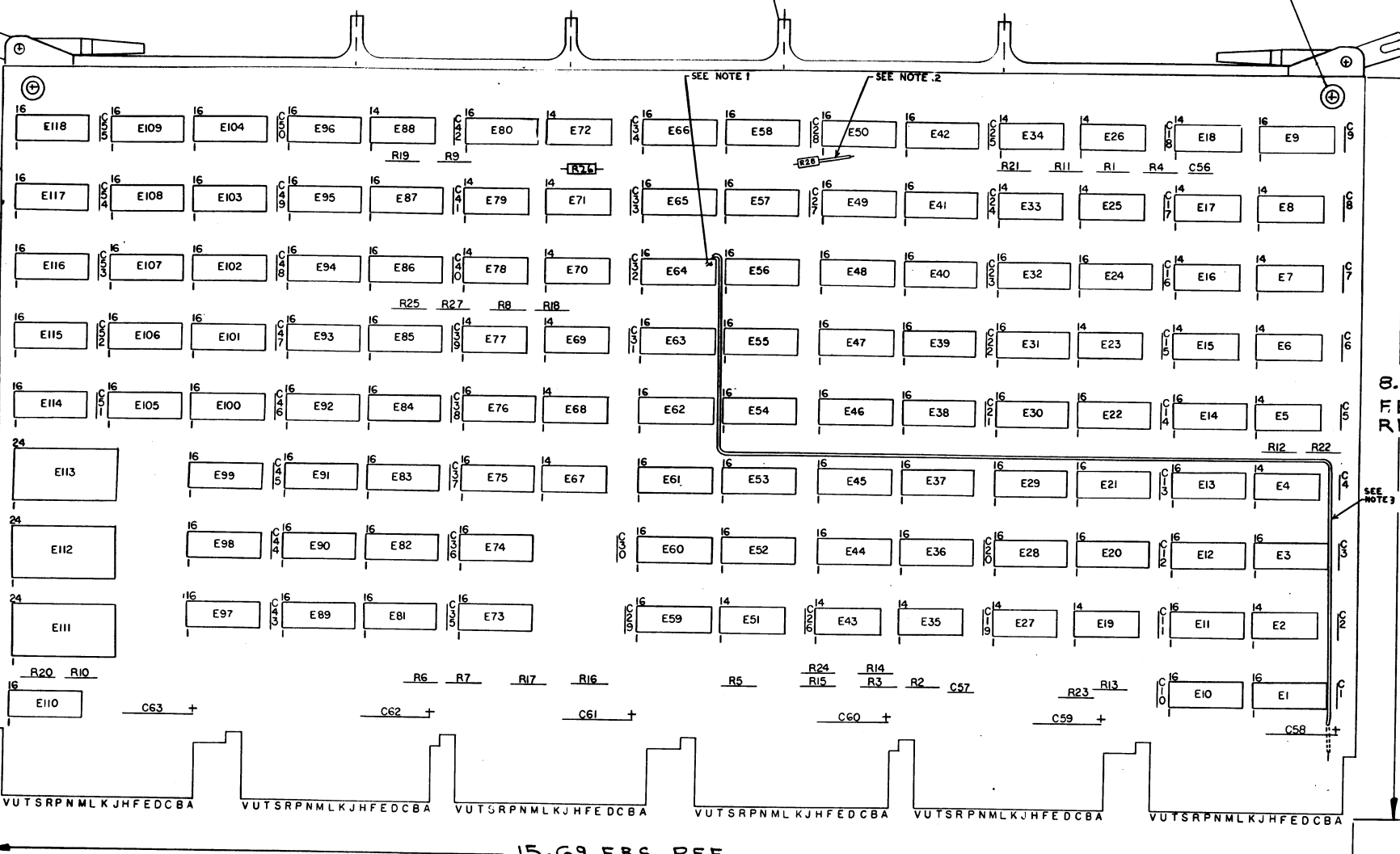


REVISIONS		
CHK	CHANGE NO.	REV.

(SIGN CONTROL, EALU CONTROL)(SLOT 4)									
TITLE FP ROM CONTROL (FRMF)				SIZE CODE D CS		NUMBER M8128-0-1		REV. B	
SCALE <u>1/1</u>		SHEET <u>7</u> OF <u>11</u>		DIST.					

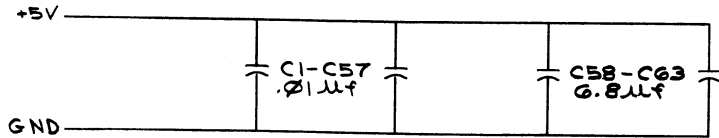
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NOTES: E110 IS SPARE I/O LOCATION



AA2, AV1, CA2, CV1
BA2, BV1, DA2, DV1
EA2, EV1, FA2, FV1

AC2, AN2, AH1, AT1
BC2, BN2, BH1, BT1
CC2, CN2, CH1, CT1
DC2, DN2, DH1, DT1
EC2, EN2, EH1, ET1
FC2, FN2, FH1, FT1



IC 74S182	8	16
IC 74S133	8	16
IC 74S175	8	16
IC 74157	8	16
IC 74174	8	16
IC 74175	8	16
IC 74S174	8	16
IC 74S158	8	16
IC 74S153	8	16
IC 74S181	12	24
IC 7485	8	16
IC 74153	8	16
IC 74151	8	16
IC TYPE	GND	+5V

GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY EXCEPT AS STATED ABOVE

DEC FORM NO. DRD 135-B

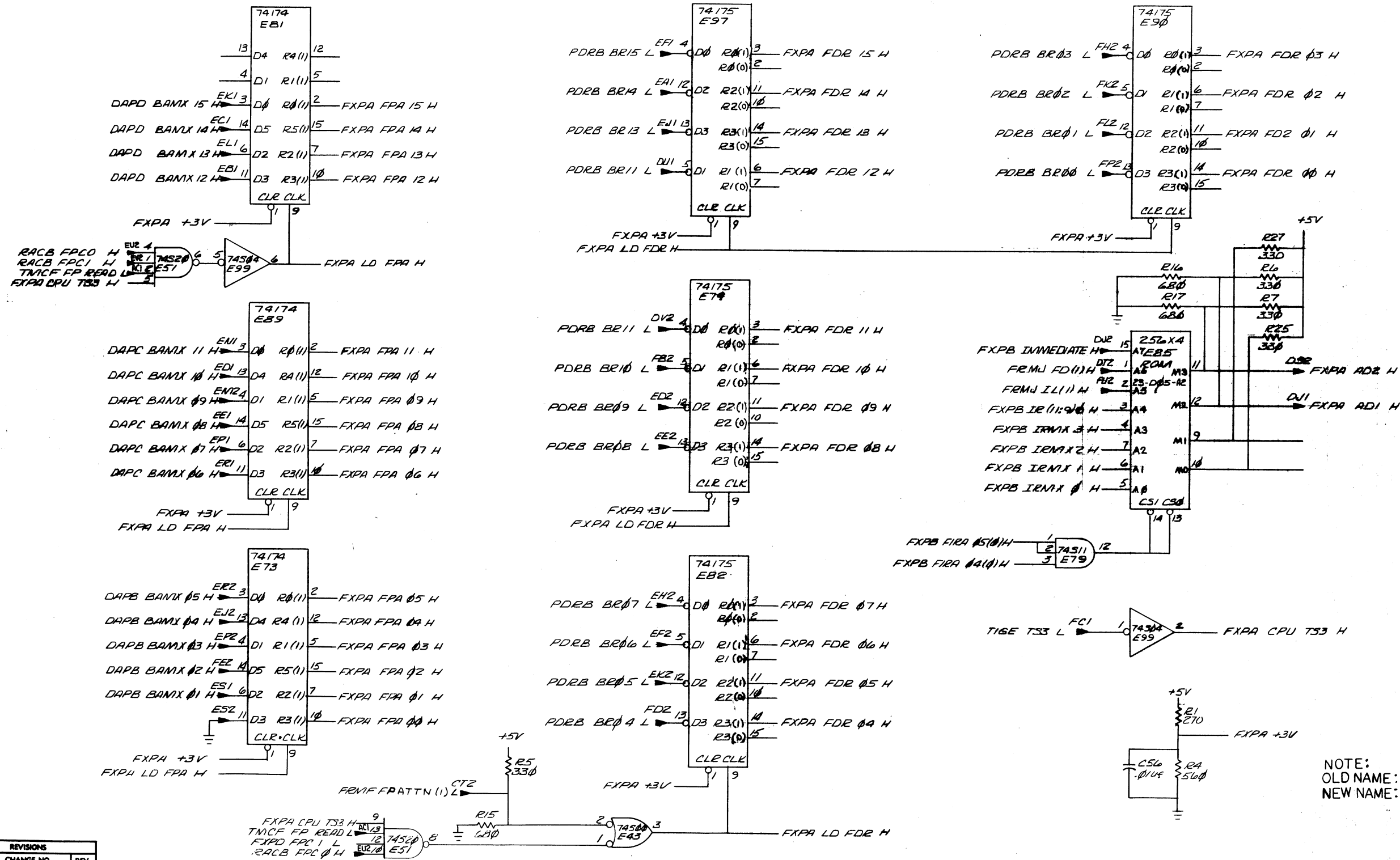
REVISIONS
A. HELENUS
31 DEC 75
M8129-00001 R
CHK CHANGE NO. REV.

FIRST USED ON OPTION MODEL
FPII-C

SEMICONDUCTOR CONVERSION CHART

REF	DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
REF	X-Y COORDINATE HOLE LOCATION		K-CO-M8129-B-4	1
REF	ASSY/DRILLING HOLE LAYOUT		D-AH-M8129-B-5	2
REF	MODULE ECO HISTORY		B-MH-M8129-B-6	3
1	ETCHED CIRCUIT BOARD		5011873	4
57	C1 THRU C57	CAPACITOR, .01uF	1001610-00	5
6	C58 THRU C63	CAPACITOR, 6.8uF	1005306	6
1	HANDLE HEX MODULE		1210711-2	7
12	R5 THRU R14, R25, R27	RESISTOR, 330 OHM, 1/4W, 5%	1300295	8
2	R3, R4	RESISTOR, 560 OHM, 1/4W, 5%	1301890	9
10	R15 THRU R24	RESISTOR, 680 OHM, 1/4W, 5%	1301424	10
2	R1, R2	RESISTOR, 270 OHM, 1/4W, 5%	1301972	11
1	R26	RES, 100u, 1/4W, 5%	1300229	12
2	E33, E69	I.C. DEC 8242	1909712	13
1	E84	I.C. DEC 74151	1909936	14
8	E45 THRU E48, E53 THRU E56	I.C. DEC 74153	1909937	15
2	E42, E50	I.C. DEC 7485	1910224	16
3	E111 THRU E113	I.C. DEC 74S181	1910531	17
5	E7, E9, E15, E43, E80	I.C. DEC 74S88	1910532	18
2	E23, E99	I.C. DEC 74S84	1910534	19
6	E2, E4, E5, E19, E27, E35	I.C. DEC 74S85	1910535	20
5	E16, E67, E70, E71, E78	I.C. DEC 74S18	1910536	21
3	E25, E72, E79	I.C. DEC 74S11	1910537	22
3	E51, E68, E88	I.C. DEC 74S28	1910539	23
3	E6, E8, E18	I.C. DEC 74S148	1910546	24
17	E21, E22, E29 THRU E31, E37, E38, E39, E104 THRU E107, E114 THRU E118	I.C. DEC 74S153	1910547	25
1	E26	I.C. DEC 74S74	1910544	26
1	E17	I.C. DEC 74SG4	1910542	27
5	E20, E28, E36, E100, E102	I.C. DEC 74S174	1910550	28
11	E4, E11, E12, E13, E14, E15, E16, E17, E18, E19, E20, E21, E22, E23, E24, E25, E26, E27, E28, E29, E30, E31, E32, E33, E34, E35, E36, E37, E38, E39, E40, E41, E42, E43, E44, E45, E46, E47, E48, E49, E50, E51, E52, E53, E54, E55, E56, E57, E58, E59, E60, E61, E62, E63, E64, E65, E66, E67, E68, E69, E70, E71, E72, E73, E74, E75, E76, E77, E78, E79, E80, E81, E82, E83, E84, E85, E86, E87, E88, E89, E90, E91, E92, E93, E94, E95, E96, E97, E98, E99, E100, E101, E102, E103, E104, E105, E106, E107, E108, E109, E110, E111, E112, E113, E114, E115, E116, E117, E118	I.C. DEC 74175	1910651	29
9	E2, E3, E4, E5, E6, E7, E8, E9, E10, E11, E12, E13, E14, E15, E16, E17, E18, E19, E20, E21, E22, E23, E24, E25, E26, E27, E28, E29, E30, E31, E32, E33, E34, E35, E36, E37, E38, E39, E40, E41, E42, E43, E44, E45, E46, E47, E48, E49, E50, E51, E52, E53, E54, E55, E56, E57, E58, E59, E60, E61, E62, E63, E64, E65, E66, E67, E68, E69, E70, E71, E72, E73, E74, E75, E76, E77, E78, E79, E80, E81, E82, E83, E84, E85, E86, E87, E88, E89, E90, E91, E92, E93, E94, E95, E96, E97, E98, E99, E100, E101, E102, E103, E104, E105, E106, E107, E108, E109, E110, E111, E112, E113, E114, E115, E116, E117, E118	I.C. DEC 74174	1910652	30
1	E94	I.C. DEC 74157	1910655	31
4	E4, E75, E89, E91	I.C. DEC 74S175	1910957	32
2	E34, E77	I.C. DEC 74S51	1911712-BW-S	33
1	E12	I.C. DEC 74S133	1911983-BW-S	34
3	E44, E52, E86	I.C. DEC 74S158	1910549	35
6	E1, E3, E10, E11, E13, E14	I.C. DEC 74S189	191266	36
1	E98	I.C. DEC 74S182	1912097	37
12	EYELET		9006732	38
1	E63	ROM	23D2GA2	39
1	E65	ROM	23D2BA2	40
1	E66	ROM	23D2TA2	41
1	E87	ROM	23D81A2	42
1	E93	ROM	23D82A2	43
1	E96	ROM	23D83A2	44
1	E95	ROM	23D84A2	45
1	E85	ROM	23D85A2	46
1	E108	ROM	23D87A2	47
1	E109	ROM	23D88A2	48
1	E59	ROM	23D24A2	49
1	E61	ROM	23D25A2	50
1	R28	RES 150u 1/4W 5%	1300250	51
A/R	WIRE, 30AWG.		9105740-B5	52
A/R	TUBING		9105278-04	53
QTY	REF. DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
ETCH BOARD REV. C				
DRN. DATE 1/15/75				
CHK'D. DATE 12/2/75				
PROJ. ENG. DATE 12/2/75				
PROD. DATE 12/2/75				
NEXT HIGHER ASSY B-DD-FPII-C				
DEC NO.	EIA NO.	DEC NO.	EIA NO.	SCALE
SHEET 1 OF 15				
TITLE FRACTION PROC EXPON PATH				
SIZE CODE NUMBER REV. B				
D/C S M8129-0-1				
DIST.				

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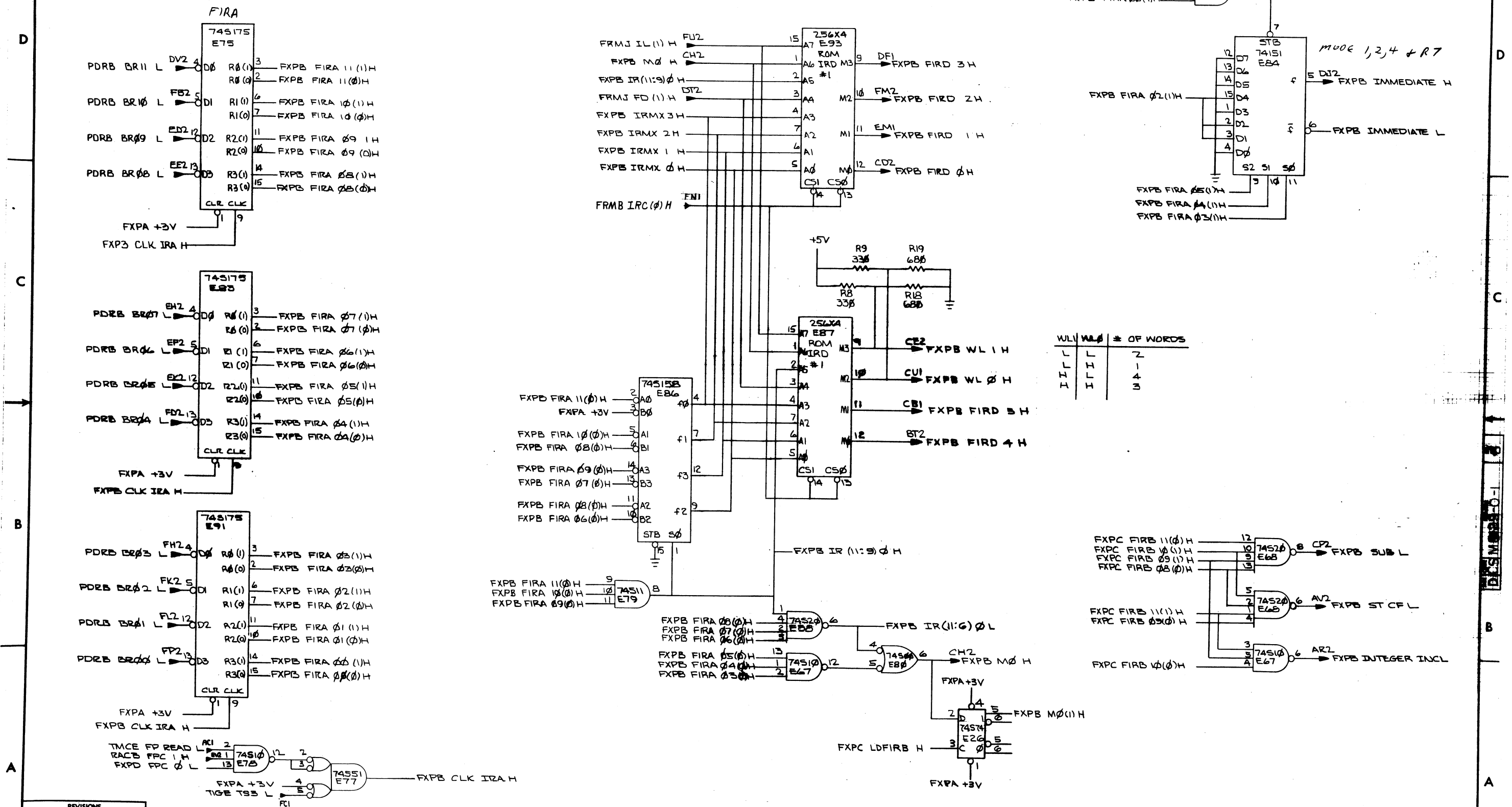


NOTE:
OLD NAME: FXP EALU XXH
NEW NAME: FRLM ACOMX XXH

ADD	AD1	ADD + SUB
L	L	10
L	H	4
H	L	2
H	H	0

REV.	CHANGE NO.	CHK.
8		
7		
6		
5		
4		
3		
2		
1		

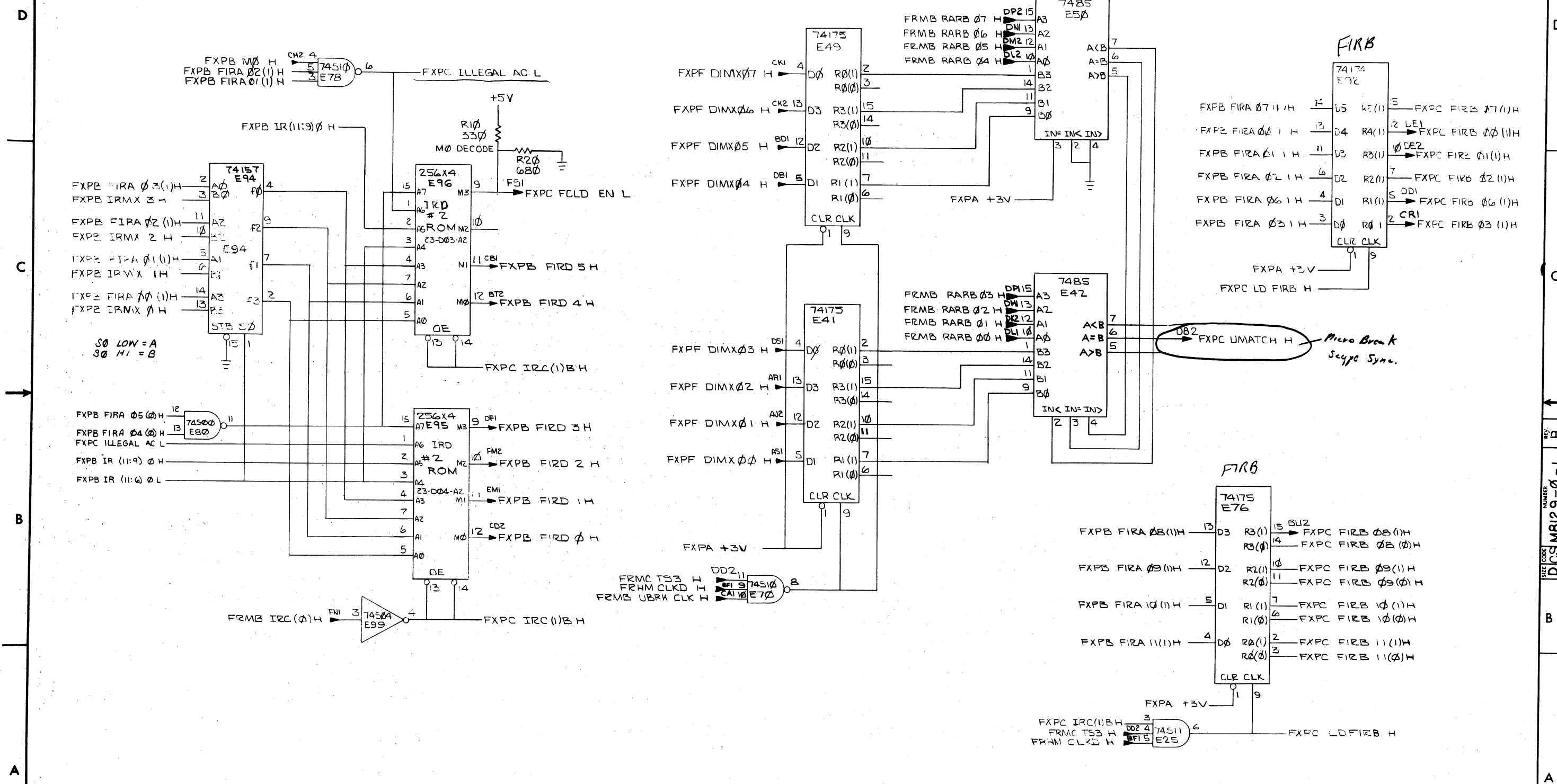
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FRACTION PROC. EXPN. PATH. (FXPA)	D CS	M8129-0-1	8
SCALE	1/1	SHEET 2 OF 15	



WLI	WLI	# OF WORDS
L	L	2
L	I	1
I	L	4
I	I	3

REVISIONS		
CHK	CHANGE NO.	REV.

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REVISIONS		
CHK	CHANGE NO.	REV.

10

**B****B**

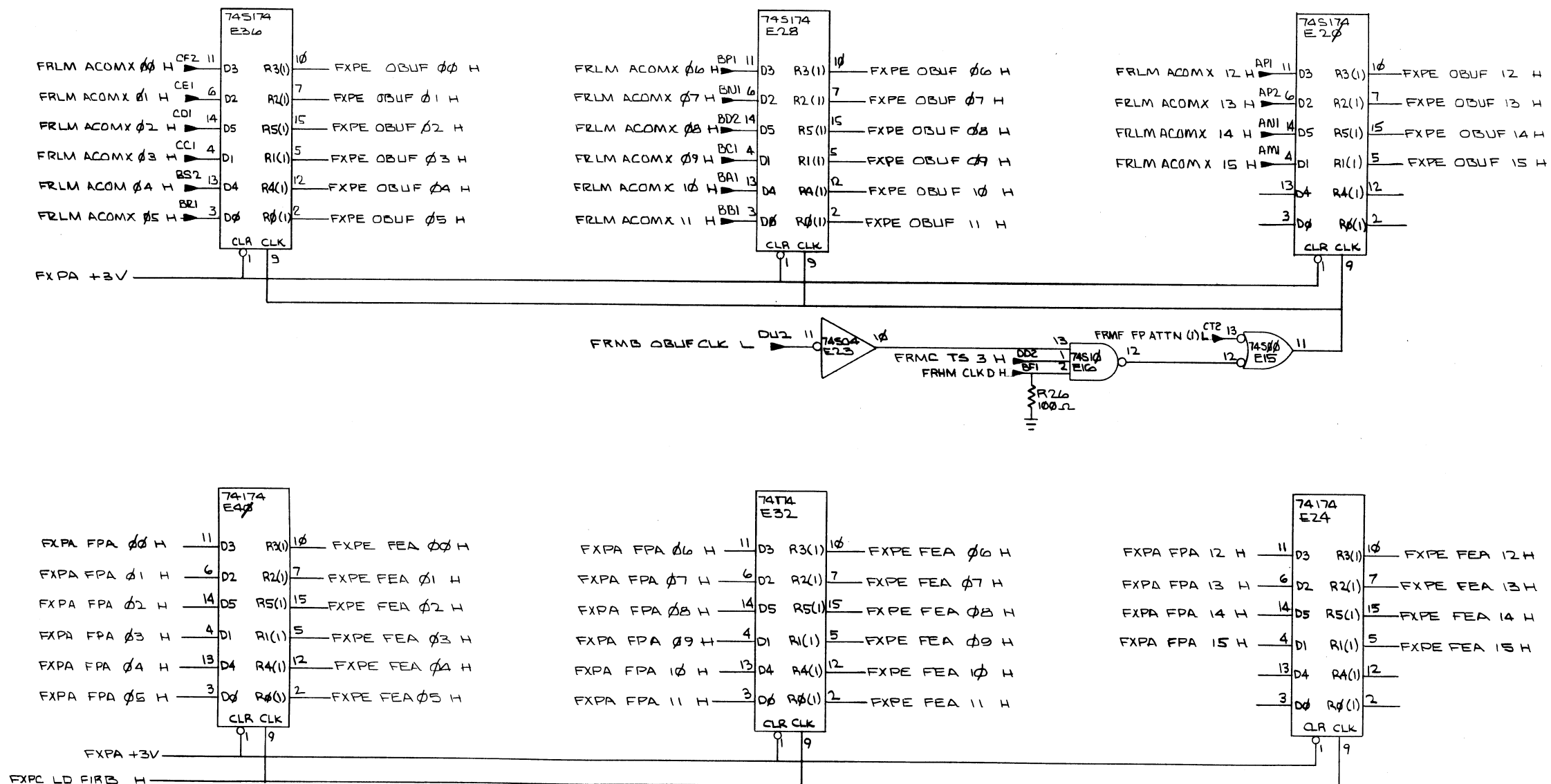
A

(DOMX (SLOT 5))

TITLE FRACTION PFC. EXPN. PATH. (FXPD)	SIZE D	CODE CS	NUMBER M8129-0-1	REV B
SCALE ##	SHEET 5 OF 15		DIST.	

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REV. 1-7-75
NUMBER 3002 3275
D CS ME129-0-1



REVISIONS		
CHK	CHANGE NO.	REV.

DEC FORM NO. 500 128

TITLE FRACTION PROC.
EXPON. PATH. (FXPE)

SIZE CODE D CS
NUMBER ME129-0-1
SCALE 1/16
SHEET 6 OF 15
DIST. 1

(OBUF/FEA(SLOT 5))

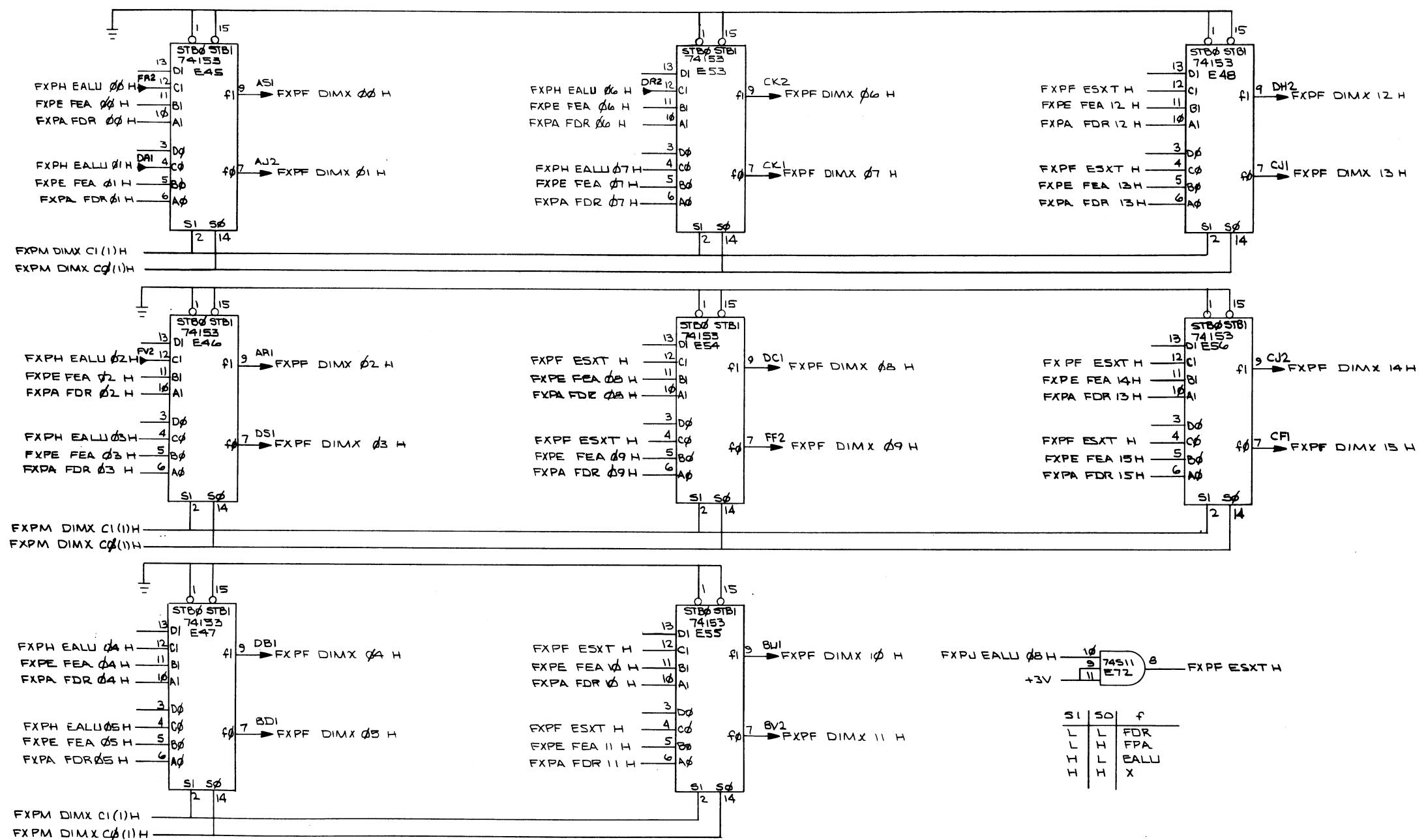
REV. B

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8 7 6 5 4 3 2 1
1-0-6218W DCS M8129-0-1

D
C
B
A

D
C
B
A

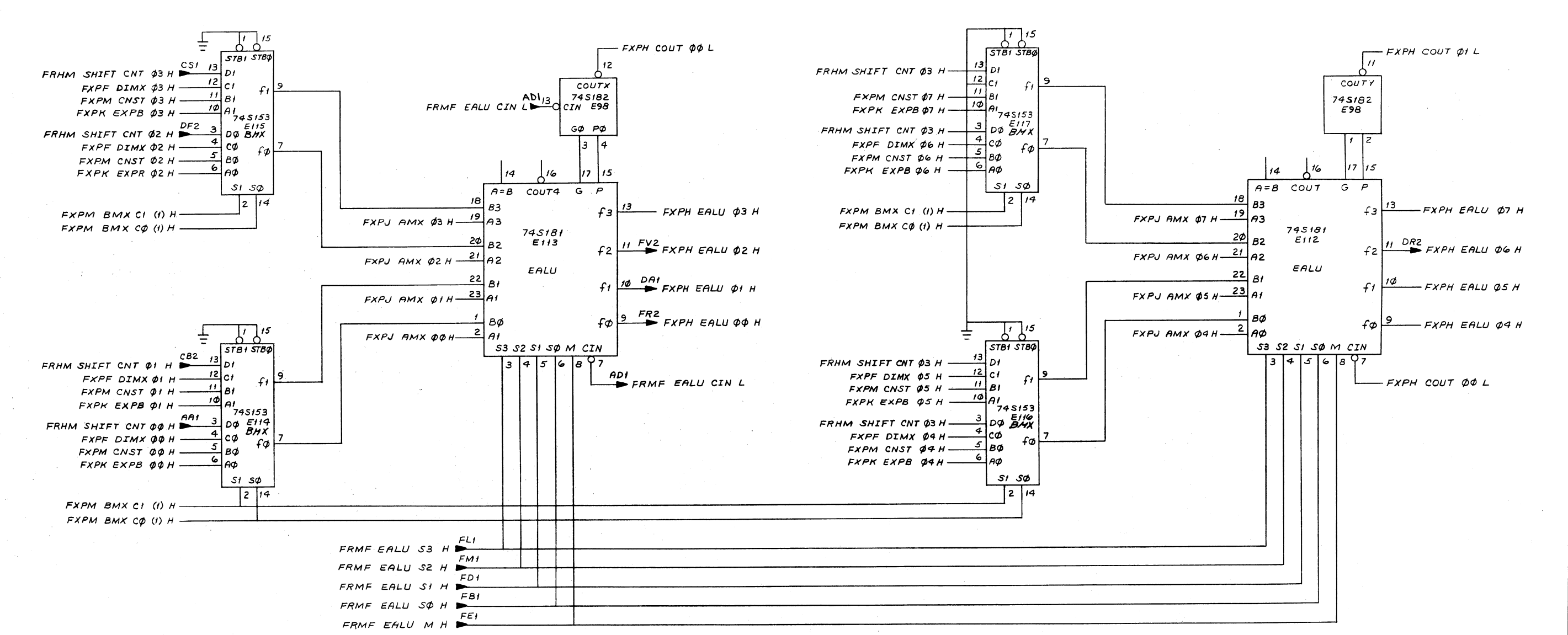


REVISIONS		
CHK	CHANGE NO.	REV.

DWG FORM NO. 138

TITLE FRACTION PROC. EXPN. PATH. (FXPF)		SIZE CODE DCS M8129-0-1	NUMBER 1	REV. B
SCALE 1/1	SHEET 7	OF 15	DIST.	

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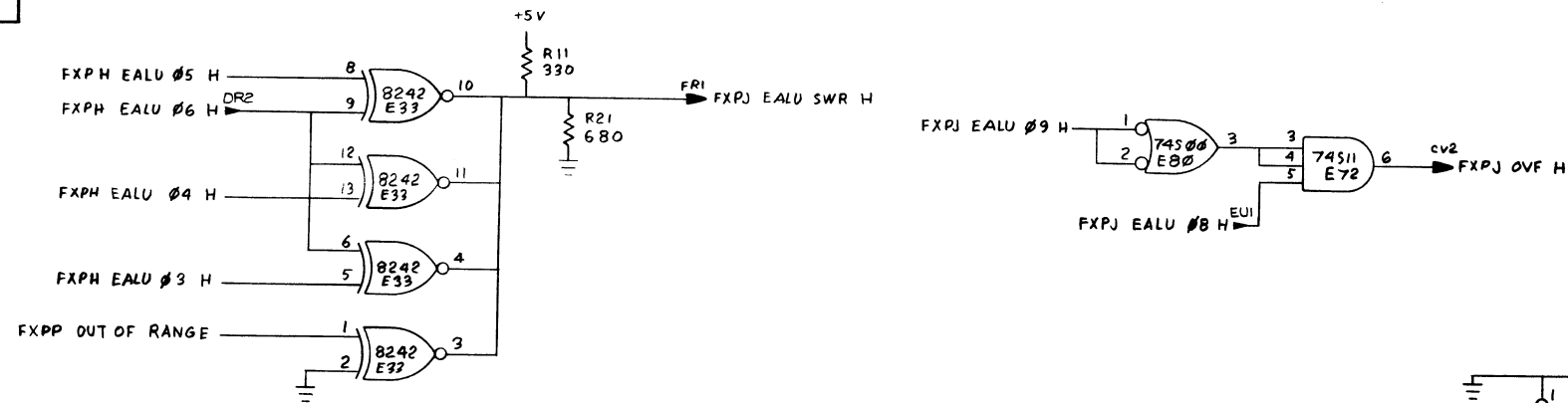


BMX	S1	S0	f
L	L	L	EXP
L	H	L	CNST
H	L	L	DIMX
H	H	L	SHIFT CONT.

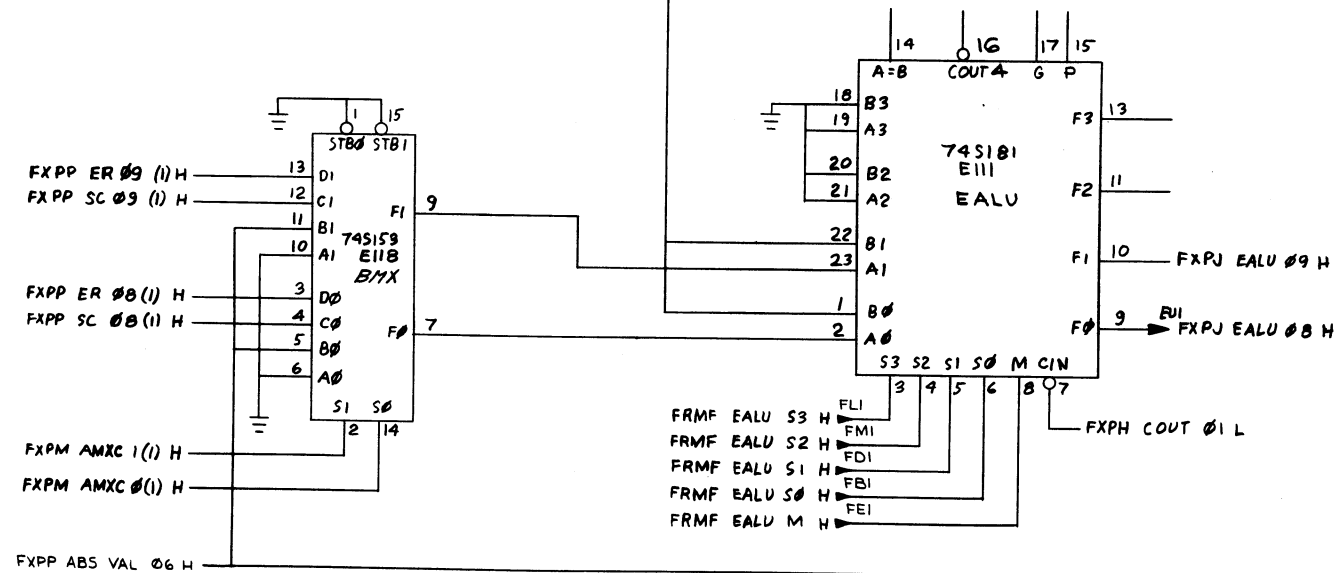
REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		SIZE CODE	NUMBER	REV.
FRACTION PROC. EXPON. PATH (FXPH)		D CS	M8129-0-1	B
SCALE	SHEET	DIST.		
1/1	8 OF 15			

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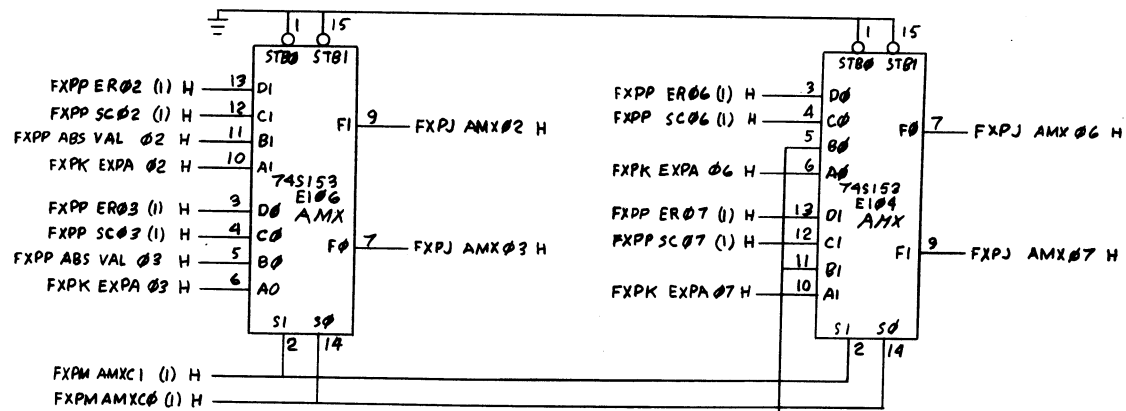
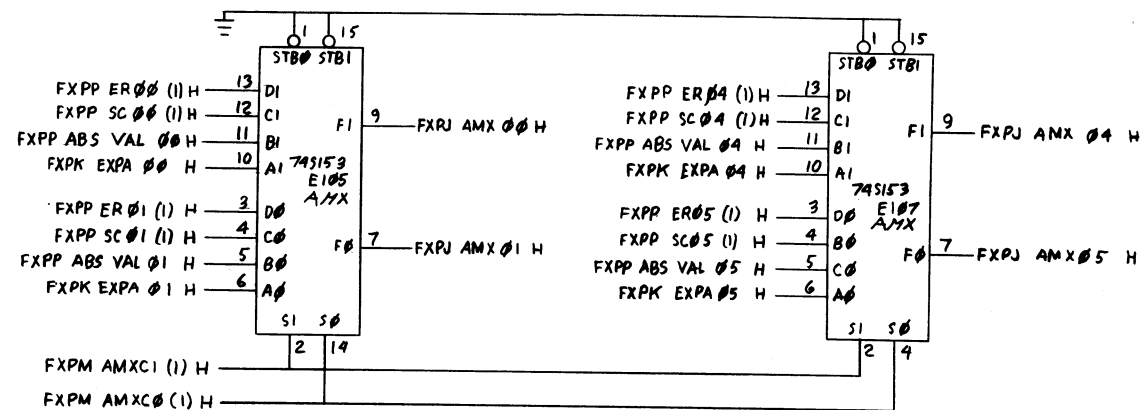


FXPM BMXC1 (1) H
FRHM SHIFT CNT 03 H
FXPM BMXC0 (0) H



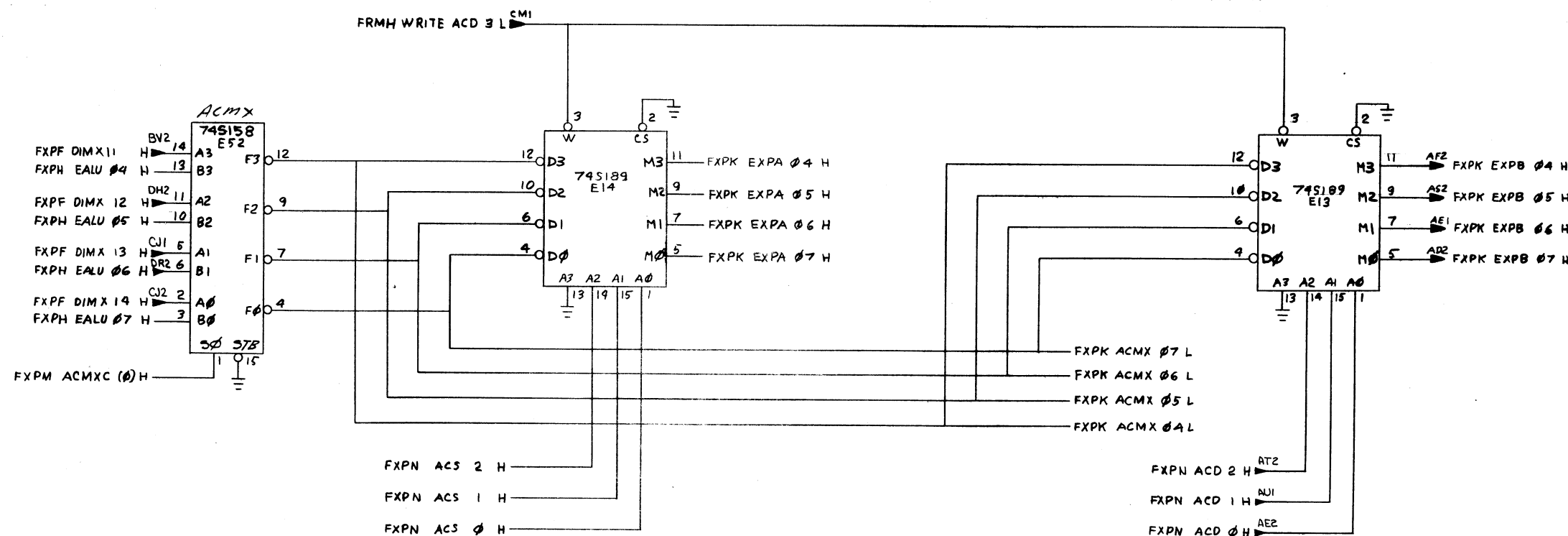
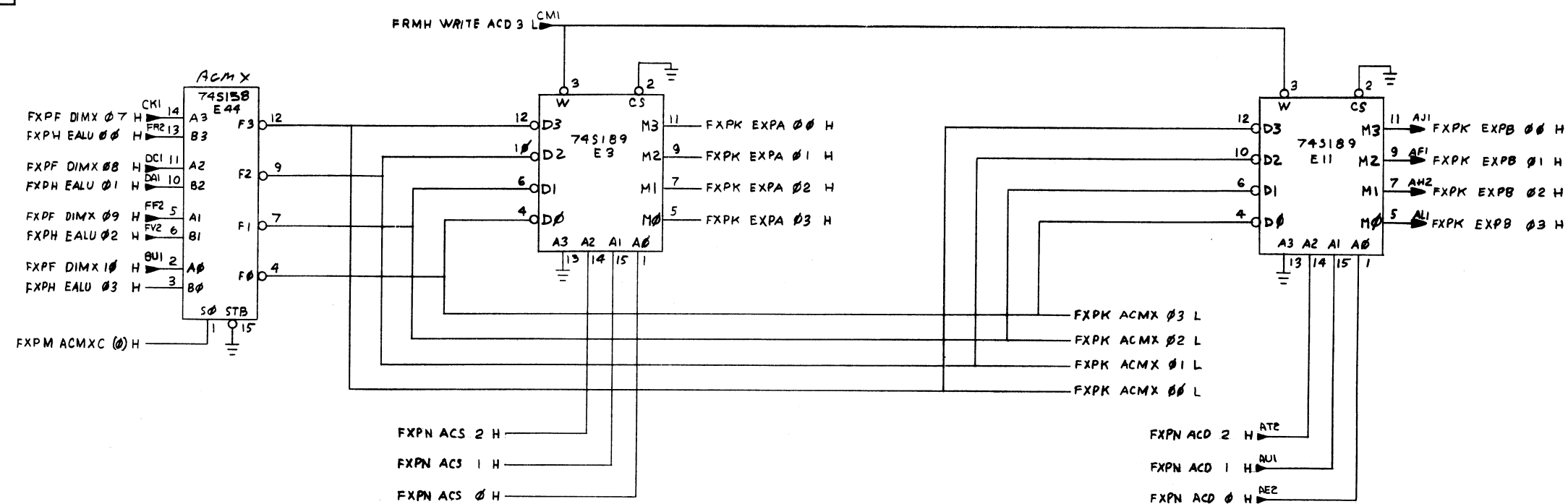
FRMF EALU S3 H
FRMF EALU S2 H
FRMF EALU S1 H
FRMF EALU S0 H
FRMF EALU M H

AMX	S1	S0	F
L	L	L	EXP
L	H	H	ABS SC
H	L	L	SC
H	H	H	ER



(AMX, EALU HI (SLOT 5))

TITLE	SIZE	CODE	NUMBER	REV.
FRACTION (FXPJ) PROC EXPON PATH	D	CS	M8129-0-1	B
SCALE	++	SHEET 9	OF 15	DIST.

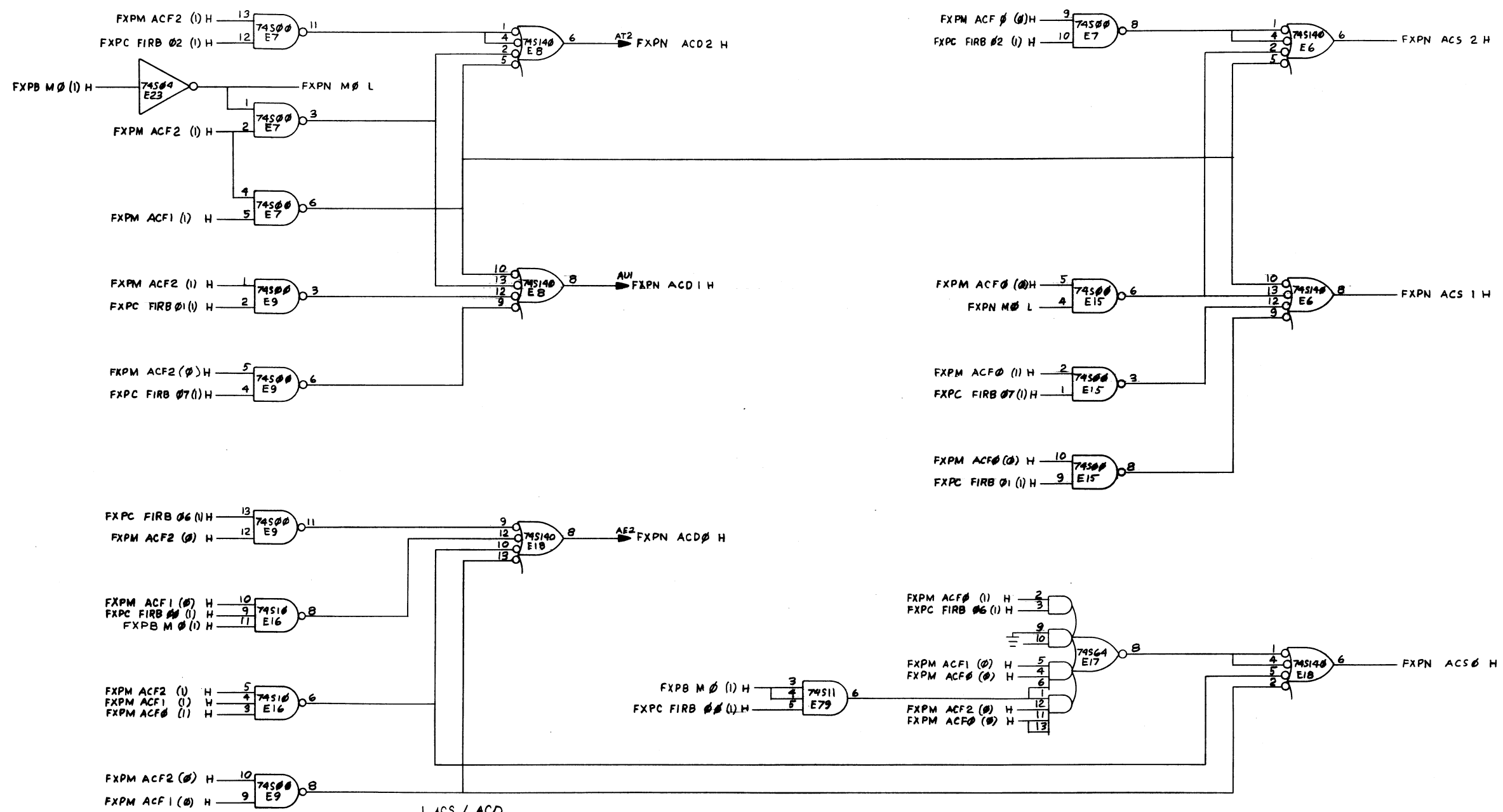


REVISIONS		
CHK	CHANGE NO.	REV.

(EXP SCRATCH PADS (SLOT 5)

TITLE FRACTION PROC (FXPK) EXPON. PATH		SIZE D	CODE CS	NUMBER M8129-0-1				REV B
SCALE 11	SHEET 10 OF 15	DIST.						

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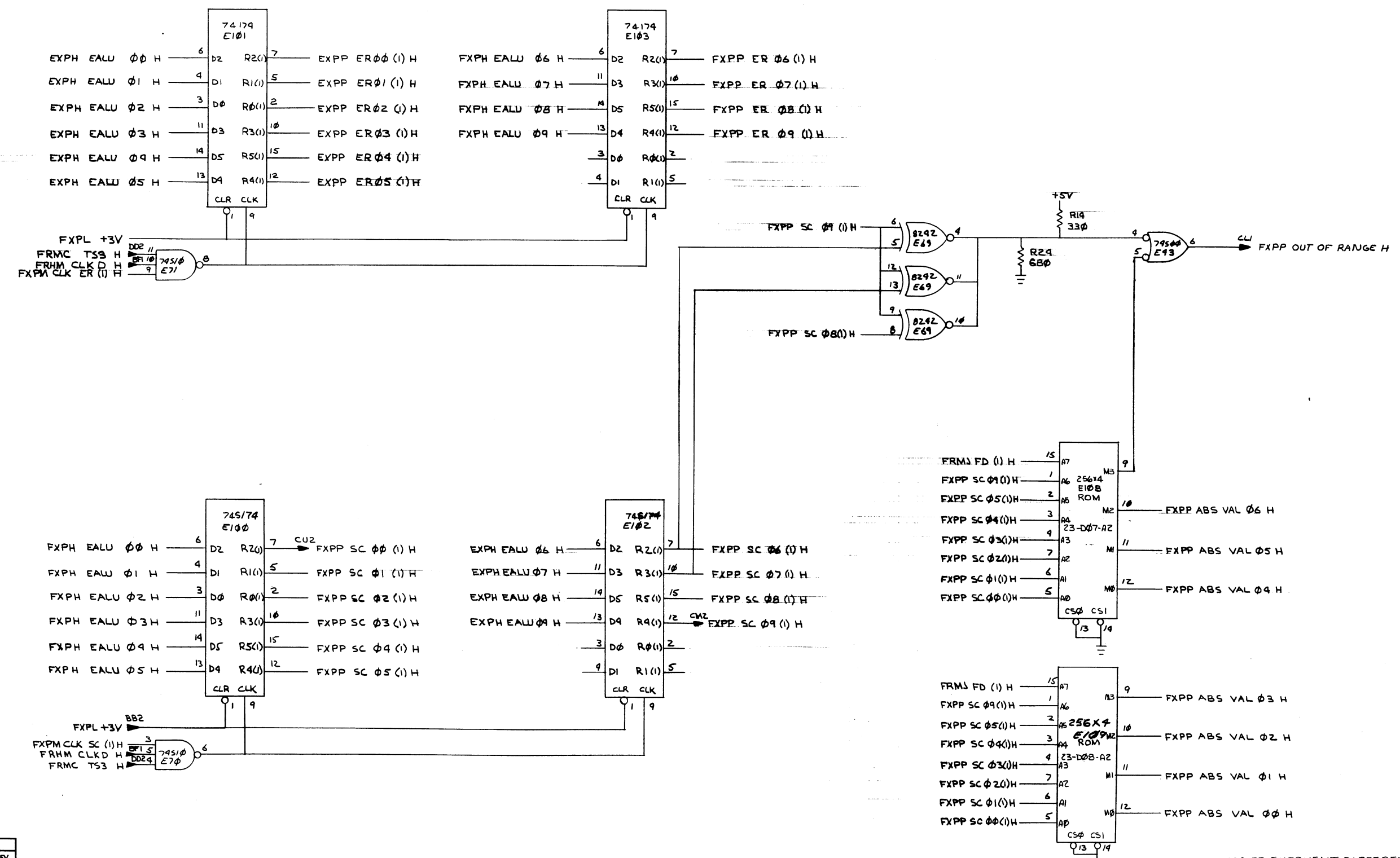
REVISIONS		
CHK	CHANGE NO.	REV.

(ACCUMULATOR ADDRESS (SLOT 5))

TITLE	FRACTION (FXPN)	SIZE	CODE	NUMBER	REV.
SCALE	++	SHEET	13	OF	15
DIST.					

DCS M8129-0-1

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REVISIONS		
CHK	CHANGE NO.	REV.

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IR DECODE #1 FxPB

	IL*MO*FD	IL*MO*FD	IL*MO*FD	IL*MO*FD	IL*MO*FD	IL*MO*FD	IL*MO*FD	IL*MO*FD	IL*MO*FD
	ADRS DATA WL	ADRS DATA WL	ADRS DATA	ADRS DATA	ADRS DATA WL	ADRS DATA WL	ADRS DATA WL	ADRS DATA	ADRS DATA
MUL	2 11 2	22 20 4	102 30	122 30	202 11 2	222 20 4	302 30	322 30	342 30
MOD	3 11 2	23 20 4	103 30	123 30	203 11 2	223 20 4	303 30	323 30	343 30
ADD	4 11 2	24 20 4	104 30	124 30	204 11 2	224 20 4	304 30	324 30	344 30
LOAD	5 12 2	25 21 4	105 30	125 30	205 12 2	225 21 4	305 30	325 30	345 30
SUB	6 11 2	26 20 4	106 30	126 30	206 11 2	226 20 4	306 30	326 30	346 30
CMP	7 11 2	27 20 4	107 30	127 30	207 11 2	227 20 4	307 30	327 30	347 30
STORE	10 13 2	30 13 4	110 30	130 30	210 13 2	230 13 4	310 30	330 30	350 30
DIV	11 11 2	31 20 4	111 30	131 30	211 11 2	231 20 4	311 30	331 30	351 30
STEXP	12 50 1	32 50 1	112 50	132 50	212 50 1	232 50 1	312 50	332 50	352 50
STCI	13 51 1	33 51 1	113 51	133 51	213 51 2	233 51 2	313 51	333 51	353 51
STCF	14 44 4	34 44 2	114 44	134 44	214 44 4	234 44 2	314 44	334 44	354 44
LDEXP	15 15 1	35 15 1	115 30	135 30	215 15 1	235 15 1	315 30	335 30	355 30
LDCI	16 41 1	36 41 1	116 30	136 30	216 41 2	236 41 2	316 30	336 30	356 30
LDCF	17 17 3	37 17 2	117 30	137 30	217 17 3	237 17 2	317 30	337 30	357 30
LDFPS	41 22 1	61 22 1	141 30	161 30	241 22 1	261 22 1	341 30	361 30	381 30
STFPS	42 82 1	62 62 1	142 62	162 62	242 62 1	262 62 1	342 62	362 62	382 62
STST	43 24 2	63 24 2	143 54	163 54	243 24 2	263 24 2	343 54	363 54	383 54
CLR	44 25 2	64 25 4	144 30	164 30	244 25 2	264 25 4	344 30	364 30	384 30
TST	45 26 1	65 26 1	145 30	165 30	245 26 1	265 26 1	345 30	365 30	385 30
ABS	46 27 2	66 27 2	146 30	166 30	246 27 2	266 27 2	346 30	366 30	386 30
NEG	47 27 2	67 27 2	147 30	167 30	247 27 2	267 27 2	347 30	367 30	387 30
NO MEM CLASS	-	-	140 30	160 30	-	-	340 30	360 30	-

ABS Rom

FxPP

ADRS	ABS	VAL	ADRS	ABS	VAL	ADRS	ABS	VAL	ADRS	ABS	VAL	ADRS	ABS	VAL	ADRS	ABS	VAL
000	200	054	000	130	000	204	374	260	320	334	334	001	377	055	000	131	000
002	376	056	000	132	000	206	372	262	316	336	336	003	375	057	000	133	000
004	374	060	000	134	000	210	370	264	314	340	340	005	373	061	000	135	000
006	372	062	000	136	000	212	366	266	312	342	342	007	371	063	000	137	000
010	370	064	000	140	000	214	364	270	310	344	344	011	367	065	000	141	000
012	366	066	000	142	000	216	362	272	308	346	346	013	365	067	000	143	000
014	364	070	000	144	000	220	360	274	304	350	350	015	363	071	000	145	000
016	362	072	000	146	000	222	356	276	302	352	352	017	361	073	000	147	000
020	360	074	000	150	350	224	354	278	300	354	354	021	357	075	000	151	351
022	356	076	000	152	352	226	352	280	298	356	356	023	355	077	000	153	353
024	354	100	000	154	354	230	350	284	296	360	360	025	353	101	000	155	355
026	352	102	000	156	356	232	348	286	294	362	362	027	351	103	000	157	357
030	350	104	000	160	360	234	346	290	292	364	364	031	349	105	000	161	361
032	348	106	000	162	362	236	344	292	290	366	366	033	347	107	000	163	363
034	346	108	000	164	364	240	342	294	288	368	368	035	345	110	000	166	366
036	344	110	000	166	366	242	340	296	286	370	370	037	343	111	000	167	367
040	342	114	000	170	370	244	338	298	284	372	372	041	341	115	000	171	371
042	340	116	000	172	372	246	336	300	282	374	374	043	339	117	000	173	373
044	338	120	000	174	374	250	334	304	280	376	376	045	337	121	000	175	375
046	336	122	000	176	376	252	332	306	278	378	378	047	335	123	000	177	377
050	334	124	000	180	380	254	330	308	276	380	380	051	333	125	000	179	379
052	332	126	000	182	382	256	328	310	274	382	382	053	331	127	000	181	381
054	330	127	000	183	383	257	327	311	273	383	383						

IR DECODE #2 FxPC

NO MEM DECODE

	MO+1*ILL AC	MO+1*ILL AC	MO+1*ILL AC
	ADRS DATA	ADRS DATA	ADRS DATA
MUL	322 61	122 61	22 52
MOD	323 61	123 61	23 52
ADD	324 65	124 65	24 52
LOAD	325 42	125 42	25 52
SUB	326 65	126 65	26 52
CMP	327 67	127 67	27 52
STORE	330 43	130 43	30 52
DIV	331 73	131 73	31 52
STEXP	332	132	32
STCI	333	133	33
STCF	334 35	134 35	34 32
LDEXP	335 45	135 45	35 45
LDCI	336 40	136 40	36 40
LDCF	337 46	137 46	37 52
LDFPS	361 60	161 60	61 80
STFPS	362	162	62
STST	363	163	63
CLR	364 55	164 55	64
TST	365 56	165 56	65
ABS	366 57	166 57	66
NEG	367 57	167 57	67
CFCC		140 3	
SETF		141 33	
SETI		142 33	
LDUB		143 34	
MSN		144 37	
STAB		145 36	
STQB		147	47 53
SETD		151 33	
SETL		152 33	

ADX ROM IR DECODE #2 FxPA

	IM*FD*IL	IM*FD*IL	IM*FD*IL	IM*FD*IL	IM*FD*IL	IM*FD*IL	IM*FD*IL	IM*FD*IL	IM*FD*IL
	ADRS DATA	ADRS DATA	ADRS DATA	ADRS DATA	ADRS DATA	ADRS DATA	ADRS DATA	ADRS DATA	ADRS DATA
MUL	2 4	42 4	102 10	142 10	202 2	242 2	302 2	342 2	382 2
MOD	3 4	43 4	103 10	143 10	203 2	243 2	303 2	343 2	383 2
ADD	4 4	44 4	104 10	144 10	204 2	244 2	304 2	344 2	384 2
LOAD	5 4	45 4	105 10	145 10	205 2	245 2	305 2	345 2	385 2
SUB	6 4	46 4	106 10	146 10	206 2	246 2	306 2	346 2	386 2
CMP	7 4	47 4	107 10	147 10	207 2	247 2	307 2	347 2	387 2
STORE	10 4	50 4	110 10	150 10	210 2	250 2	310 2	350 2	390 2
DIV	11 4	51 4	111 10	151 10	211 2	251 2	311 2	351 2	391 2
STEXP	12 2	52 2	112 2	152 2	212 2	252 2	312 2	352 2	392 2
STCI	13 2	53 4	113 2	153 4	213 2	253 2	313 2	353 2	393 2
STCF	14 10	54 10	114 4	154 4	214 2	254 2	314 2	354 2	394 2
LDEXP	15 2	55 2	115 2	155 2	215 2	255 2	315 2	355 2	395 2
LDCI	16 2	56 4	116 2	156 4	216 2	256 2	316 2	356 2	396 2
LDCF	17 10	57 10	117 4	157 4	217 2	257 2	317 2	357 2	397 2
LDFPS	21 2	61 2	121 2	161	221 2	261 2	321 2	361 2	401 2
STFPS	22 2	62 2	122 2	162	222 2	262 2	322 2	362 2	402 2
STST	23 4	63 4	123 4	163	223 2	263 2	323 2	363 2	403 2
CLR	24 4	64 4	124 10	164	224 2	264 2	324 2	364 2	404 2
TST	25 4	65 4	125 10	165	225 2	265 2	325 2	365 2	405 2
ABS	26 4	66 4	126 10	166	226 2	266 2	326 2	366 2	406 2
NEG	27 4	67 4	127 10	167	227 2	267 2	327 2	367 2	407 2

NOTE: FOR Mode 1, the chip select is disabled for constant of 0

CHK	CHANGE
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DIGITAL EQUIPMENT CORPORATION

MAYNARD, MASSACHUSETTS

PARTS LIST

MADE BY D. HEALY	CHECKED D. HEALY	SECTION
DATE 12/15/75	DATE 12/15/75	1
ENG <i>Donna A. Ho</i>	PROD <i>Don W. Galt</i>	ISSUED SECT.
DATE 12/22/75	DATE 12-22-75	1

QUANTITY / VARIATION

[illegible][illegible]

TITLE		ASSY NO.	SIZE	CODE	NUMBER	REV.	ECO NO.
SOFTWARE LIST FP11-C	NONE	A PL			FP11-C-3		
SHEET 1 OF 1	DIST.						

DIGITAL EQUIPMENT CORPORATION

MAYNARD, MASSACHUSETTS

PARTS LIST

MADE BY	D. HEALY
DATE	12/16/75

CHECKED	D. HEALY
DATE	12/16/75

SECTION	1
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ENG *Thomas A. Moore*
DATE *12/22/75*

DATE	12/18/75
PROD	Don Weaver
DATE	12-22-75

ISSUED SECT.	1
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ITEM NO.	DWG NO. / PART NO.
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DESCRIPTION

EK-FP11C-MM

FLOATING POINT PROC MAINT. MANUAL

FP11-C

QUANTITY/VARIATION

TITLE	SHIPPING LIST	FP11-C
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ASSY NO.	NONE
----------	------

SIZE	CODE
A	PL

NUMBER
FP11-C-4

REV.	ECO NO.
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SHEET 1 OF 1

	DIST.
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DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE 15 DEC 75

TITLE FP11-C INSTALLATION AND ACCEPTANCE PROCEDURE

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE

ENG <i>Thomas A. Ho</i> 12/16/75	APPD	SIZE A	CODE SP	NUMBER FP11-C-5	REV
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DEC 16-(392)-1079A-R873
DRA 107A

SHEET 1 OF 3

ENGINEERING SPECIFICATION

digital

CONTINUATION SHEET

TITLE FP11-C INSTALLATION AND ACCEPTANCE PROCEDURE

EQUIPMENT REQUIRED

- 11/70 MA (115V), 11/70 MB (230V), 11/45, 11/50, or 11/55
- FP11-C Floating Point Processor

TEST EQUIPMENT REQUIRED

- Oscilloscope, Tektronix 453 or equivalent
- Digital Voltmeter (DVM)

INSTALLATION PROCEDURE

- Run Processor diagnostic:

11/70	11/45
DEKQC	T-17
DEKBA	
DEKBB	
- Verify that Processor into which FP11-C is to be installed is KB11-C (11/70) or KB11-D (11/45, 11/50, or 11/55).

Quick verification is as follows:

 - RAC module (slot 9) is M8123.
 - There is continuity between backplane pins.

11/70:	E13R2 and C02R1, or
11/45, 11/50, 11/55:	E15R2 to C02R1

NOTE: If the Processor is found not to be one of the above, it must be upgraded to accept the FP11-C. Upgrade kits (which include an FP11-C) are FP-45-CU (11/45) and FP-70-CU (11/70).
- With system power off, install the floating point +5V regulator (H744), (included with the FP11-C), in the upper system bulk power supply regulator A position. (See Figure 1.) Connect the regulator by inserting the provided male mate-n-lock connector from the system power harness (P9 for 11/70, P17 for 11/45, 11/50, 11/55) into the female mate-n-lock receptacle on the regulator.
- Apply power to the system and measure the voltage present at pin A02A2 of the Processor backplane. It should be between 5.0 and 5.3 volts. Adjust the regulator (position A) output, if necessary, to obtain the required voltage.

DEC FORM NO DEC 16-(381)-1022-N370
DRA 108

SHEET 2 OF 3

ENGINEERING SPECIFICATION

digital

CONTINUATION SHEET

TITLE FP11-C INSTALLATION AND ACCEPTANCE PROCEDURE

5. Remove power from the system.
 6. Plug in the four FP11-C modules as follows:

Slot 2	M8126
Slot 3	M8127
Slot 4	M8128
Slot 5	M8129
 7. Remove IRC module (M8132 for 11/70, 11/45, 11/50, 11/55) from the system. Verify board is rev. B or higher, and that jumper W1 has been removed from the board. If it has not been, remove it. Replace the module.
 8. Turn on system power and, using the DVM, measure the voltage present at backplane pin A02A2. Adjust the regulator (position A) output until the voltage measured is $+5.0V \pm .05V$.
 9. Load and run each of the following diagnostics for the prescribed times:
 - a. MAINDEC-11-DEFPA - 5 passes
 - b. MAINDEC-11-DEFPB - 5 passes
- No error printouts should be experienced.
10. Run Processor diagnostics:

<u>11/70</u>	<u>11/45</u>
DEKQC	T17
DEKBA	
DEKBB	

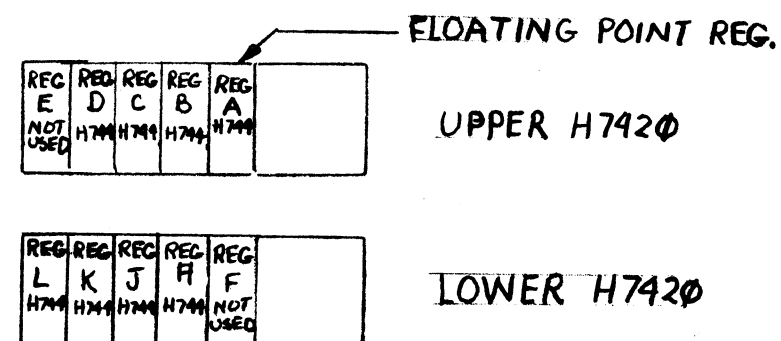


FIGURE1 INSTALLATION OF FP11-C REGULATOR

SIZE
ACODE
SPNUMBER
FP11-C-5

REV



3

4