

FIELD MAINTENANCE PRINT SET

TABLE OF CONTENTS

**UNIT VARIATIONS
COVERED BY THIS
PRINT SET**

KDJ11-A

Field Maintenance Print Set

MP-Ø189Ø-Ø1

EN-01124-16-N675.(327

DRB 106A

AUTOMATED BY PRTLST.4Q(50)

PARTS LIST

SHEET A1 OF A1

LINE ITEM TOP DOCUMENT

PART NUMBER MIN REV DESCRIPTION

QTY PER VARIATION

VARIATION REVISION LEVEL: AA A1

1 1 D-UA-M8192-0-0 M8192-00 DUAL HEIGHT CPU BRD (KDJ11-AA),Q 1

REVISION HISTORY			BASIC PART NO: KDJ11		D I G I T A L	
ENG:	ECO NUMBER	REV	SECTION A OF A	DRN: D. HEALY	DATE: 04-JAN-84	
	INITIAL	A	SECTION VARIATION INDEX [A]AA	CHK'D: D. HEALY	DATE: 05-JAN-84	TITLE PARTS LIST KDJ11-A OPTION
			[B]	DES.ENG: P. RUBINFELD	DATE: 05-JAN-84	DOCUMENT NUMBER
			[C]	RESP.ENG.: H. DROBNER	DATE: 05-JAN-84	SIZE: CODE: NUMBER REV
			[D]	MFG.ENG.: J. VEGA	DATE: 05-JAN-84	K PL KDJ11-A-DBP A
			[E]	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER: #8-DD-KDJ11-A	RELEASE DATE: 20-JAN-84
			[F]		FILE NAME: Z8849A.PLS	EDIT 3

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AUTOMATED BY PRTLST.4Q(50)

P A R T S L I S T

SHEET A1 OF A2

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
1	1	D-MD-5015394-0-0	5015394-01	ETCH CIRCUIT BOARD	1	
2	2		1915305-00	AM 2908 TRANSCEIVER,BUS,LATCH	6	E51,E32,E27,E23,E38,E42
3	3		1919015-00	DC 021 BUS TRANSCEIVER,20PI	3	E1,E12,E7
4	4		1913671-00	74S374 FF-D,OCTAL,TR1 STATE	2	E4,E15
5	5		1910532-00	74S00 NAND GATE-QUAD 2IN	1	E3
6	6		1912801-00	LS02 NOR-GATE-QUAD 2IN	1	E5
7	7		1912824-00	LS74 FF-D DUAL,EDGE TRIGG	2	E22,E41
8	8		1912805-00	LS08 AND GATE-QUAD 2IN,PO	1	E31
9	9		1913414-00	LS14 INVERTER GATE-HEX SC	1	E17
10	10		1912837-01	LS123 ONE SHOT,DUAL,RETRIG	1	E55
11	11		1616653-01	DELAY= 100NS,10TAPS WITH TTLBU	1	E6
12	12		1912813-00	LS27 NOR GATE-TRIPLE 3IN	1	E11
13	13		1216116-00	SKT,IC 40PIN DIP GOLD CARR	1	XE35
14	14		1221158-01	SKT,IC 68PIN CHIP CARRIER	2	XE13,XE34
15	15		1920854-01	GATE ARRAY,CDP PROG.	1	E34
16	16		1920403-01	8A1664 GATE ARRAY,JSS PROG.	1	E13
17	17		1216116-01	SKT,IC 60PIN DIP	1	XE36
18	18		5719400-04	HYBRID,ASSY W/2 CHIPS 15MHZ	1	E36
19	19		1921009-01	74F241 BUFFER/LINE DRIVER,N	1	E39
20	20		1921010-01	74F373 OCTAL TRANSPARENT LA	5	E20,E54,E50,E30,E25
21	21		1921008-01	74F240 BUFFER/LINE DRIVER,I	2	E9,E45
22	22		1911676-00	74S139 DECODER-DUAL TWO-INP	1	E46
23	23		1912860-00	LS259 LATCH 8BIT	1	E26
24	24		23044K4-00	K4-01 E-18-16RL6LPC 440K4	1	E18
25	25		1920442-01	74F374 FF-D,OCTAL,TRI-STATE	2	E47,E33
26	26		1911573-00	74S280 PARITY GEN/CHKR,9BIT	2	E10,E40
27	27		1919189-00	COMPARATOR,8-BIT BIP	2	E28,E2
28	28		2117498-02	6147P RAM,4KX1,STATIC,55NS	2	E24,E29
29	29		2119250-00	8168 4KX4 STATIC RAM 55NS	7	E19,E44,E53,E49,E43,E48,E52
30	30		1913340-00	74S32 OR GATE-QUAD 2IN	1	E16

2611-83

REVISION HISTORY		BASIC PART NO: M8192		DRN: M. MCDONOUGH	DATE: 22-MAR-83	D I G I T A L			
ENG:	ECO NUMBER	REV	SECTION A OF A	CHK'D: DICK BARRIERE	DATE: 08-NOV-83	TITLE PARTS LIST			
	INITIAL	A	SECTION VARIATION INDEX	DES.ENG: P. RUBINFELD	DATE: 22-MAR-83	KDJ11-A PROCESSOR			
			[A] 00	RESP.ENG.: H. DROBNER	DATE: 22-MAR-83	DOCUMENT NUMBER			
			[B]	MFG.ENG.: JORGE VEGA	DATE: 26-OCT-83	SIZE	CODE	NUMBER	REV
			[C]	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:	K	PL	M8192-0-DBP	A
			[D]	D-UA-M8192-0-0	B-DD-M8192-0-0	FILE NAME:		EDIT #	
			[E]			Z6708A.PLS		5	
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			[H]						
			[J]						
			[K]						
			[L]						
			[M]						
			[N]						

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
31	31		1912388-00	74S02 NOR GATE-QUAD 2IN,PO	1	E8
32	32		1912096-00	DEC 74S86 XOR GATE,QUAD 2IN	1	E21
33	33		1914085-00	74S260 NOR GATE-DUAL,POS	1	E14
34	34		1316254-01	R NETWORK 7-4.7K 2.0 % 8PIN	2	Z1,Z2
35	35		1318110-00	R NETWORK 11-330 11-680 13PIN	4	Z3,Z4,Z5,Z6
36	36		1309595-00	1.0 M .25 W 5.0 % CF	1	R1
37	37		1302177-00	47.0 K .25 W 5.0 % CF	2	R2,R5
38	38		1302466-00	100.0 K .25 W 5.0 % CF	1	R3
39	39		1319471-47	14.0 2.0 W 2.0 % M.OXIDE	1	R4
40	40		1300365-00	1.0 K .25 W 5.0 % CF	1	R7
41	41		1300316-00	470.0 .25 W 5.0 % CF	1	R8
42	42		1305325-00	8.25 K .25 W 1.0 % RN55D-F10	1	R9
43	43	SEE NOTE	1120964-01	LED ASSY 4 RED 5V 8MA	1	D1,D2,D3,D4
44	44		1100114-00	PIV= 25 IO=135 MA	1	D5
45	45		1000014-00	68.0 MMF 100V 5%200PPM MICA	2	C1,C2
46	46		1013398-00	750.0 MMF 100V 5%200PPM MICA	2	C3,C5
47	47		1000076-00	39 MFD 10V 10% S.TANT	1	C4
48	48		1004812-00	15 MFD 20V 10% S.TANT	2	C8,C9
49	49		1014265-02	.33 MFD 50V +80-20% CER	27	C10-C24,C26-C34,C37-C39
50	50		1215341-08	PCB HEADER 18PIN(2X09).100CC STR	18	M1-M18
51	51		1218783-00	JUMPER 02POS(1X02).100CC	9	W1-W9
52	52		1809880-10	XTAL 15.206 MHZ	1	Y1
53	53		1213543-01	HANDLE,UNIBUS MODULE,MAGENTA	1	
54	54		1010279-00	.47 MFD 25V 20% CER	2	C7,C6
55	55		1912799-00	LS00 NAND-GATE-QUAD 2IN,P	1	E37
56	56		1320602-42	15.0 2.0 W 2.0 % M.OXIDE	1	R6
57	57		1012784-00	.047 MFD 50V +80-20% CER	2	C35,C36

58 NOTE: 1-D1,D2,D3,D4 ARE ALL PART OF SAME ASSEMBLY.

D	I	G	I	T	A	L	TITLE	KDJ11-A PROCESSOR	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
										K	PL	M8192-0-DBP	A

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COMPONENT SIDE VIEW

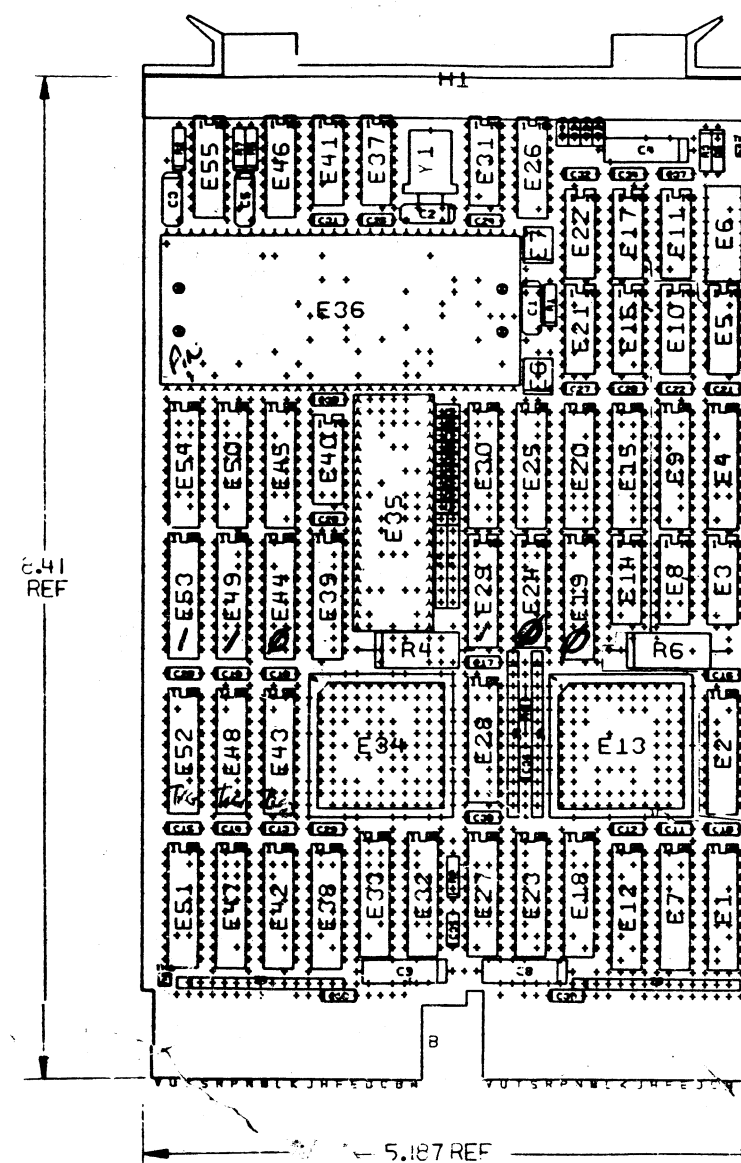
REWORK INSTRUCTION

ECO M6192-N1001

WIRE ADD SIDE ONE AS SHOWN

1-- FROM E17-9 TO E5-2

1-- FROM E16-13 THRUPTH TO E13-41 ON SIDE 2



NOTES

1. INSTALL WITH KEY

STEP 1 + Y AXIS - STEP 2 - TIMES
REPEAT + X AXIS - STEP 3 - TIMES

CHANGE NO	REV
1	1
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SIGNATURES	DATE
DRN. 1	
CHK D.	
MECH. ENG.	
PROJ. ENG.	
PROD.	
SCALE 1:1	
SHT. 1 OF 1	
NEXT HIGHER ASSY. B-22 MPT 2	

digital	REV B
TITLE	PROCESSOR
SIZE CODE	NUMBER 0
0 101	M6192-0
1 MS	822100

8

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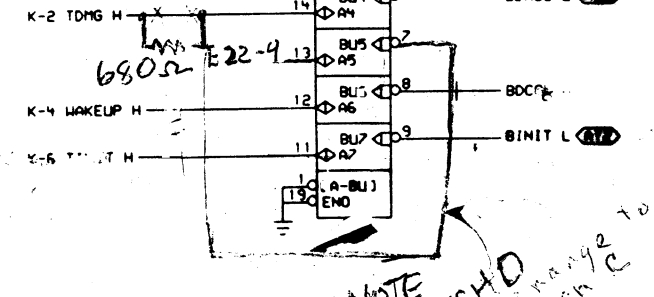
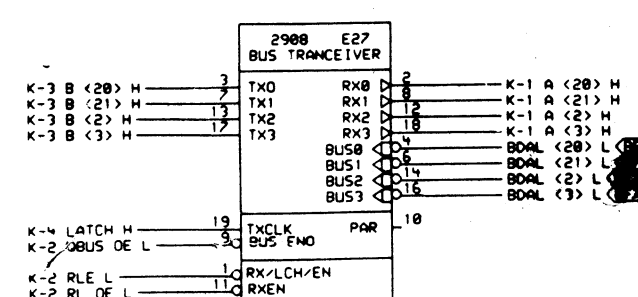
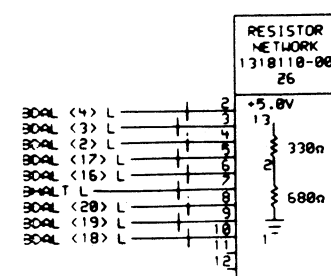
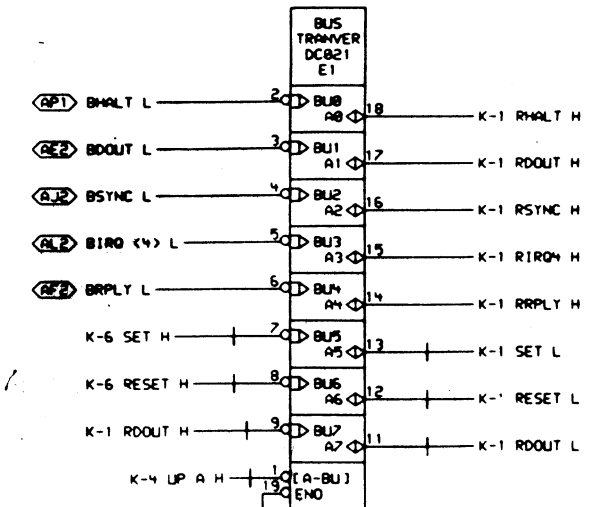
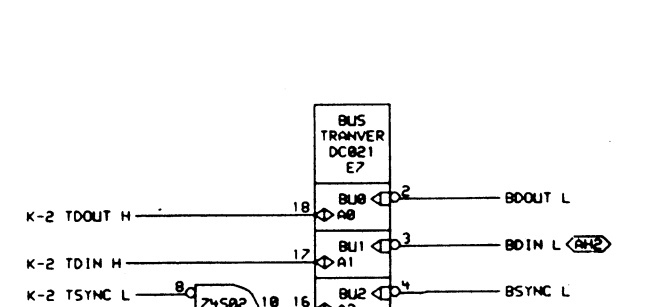
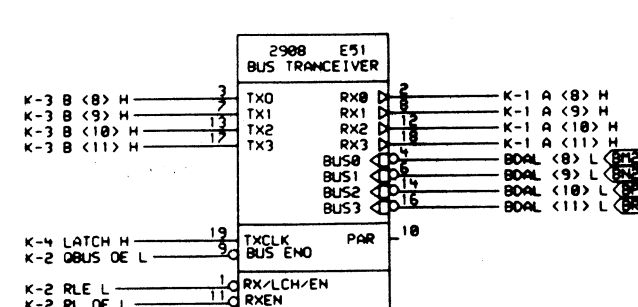
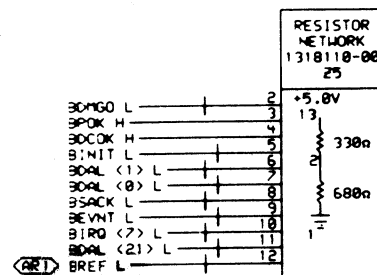
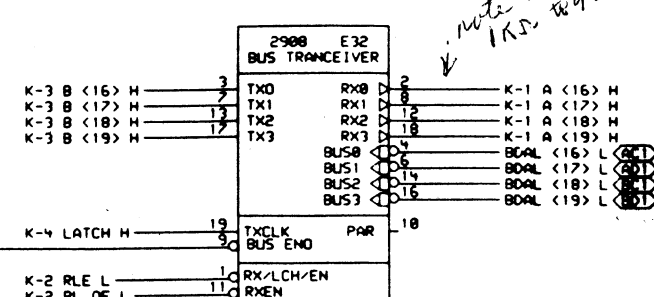
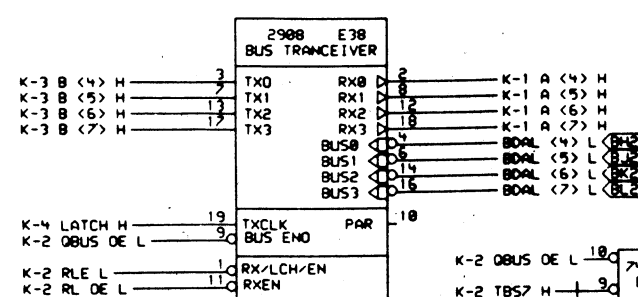
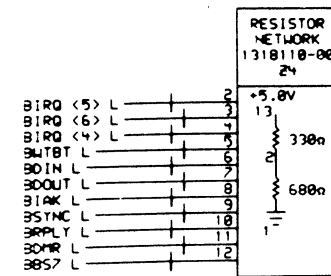
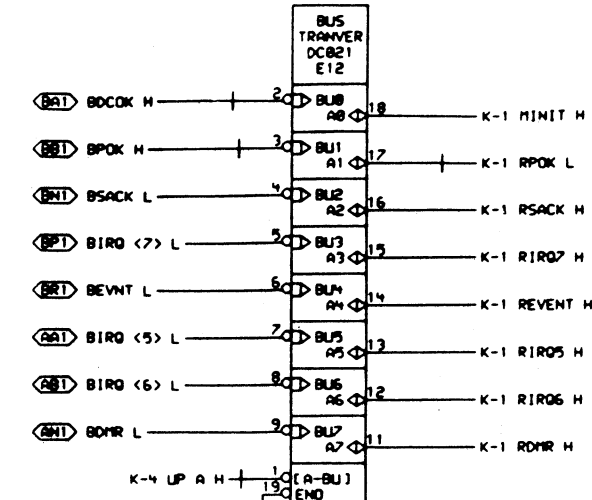
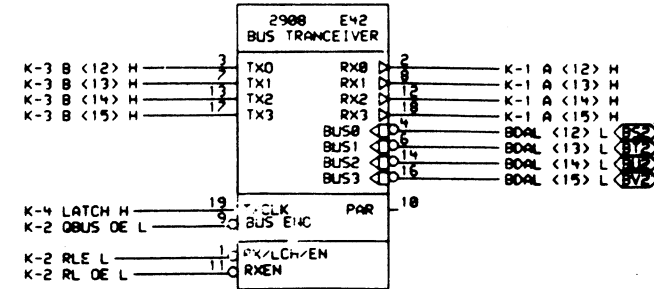
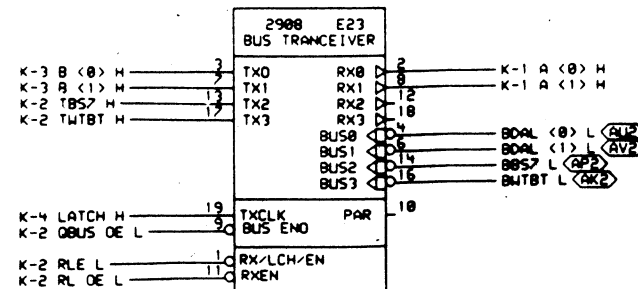
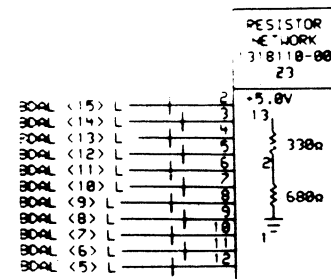
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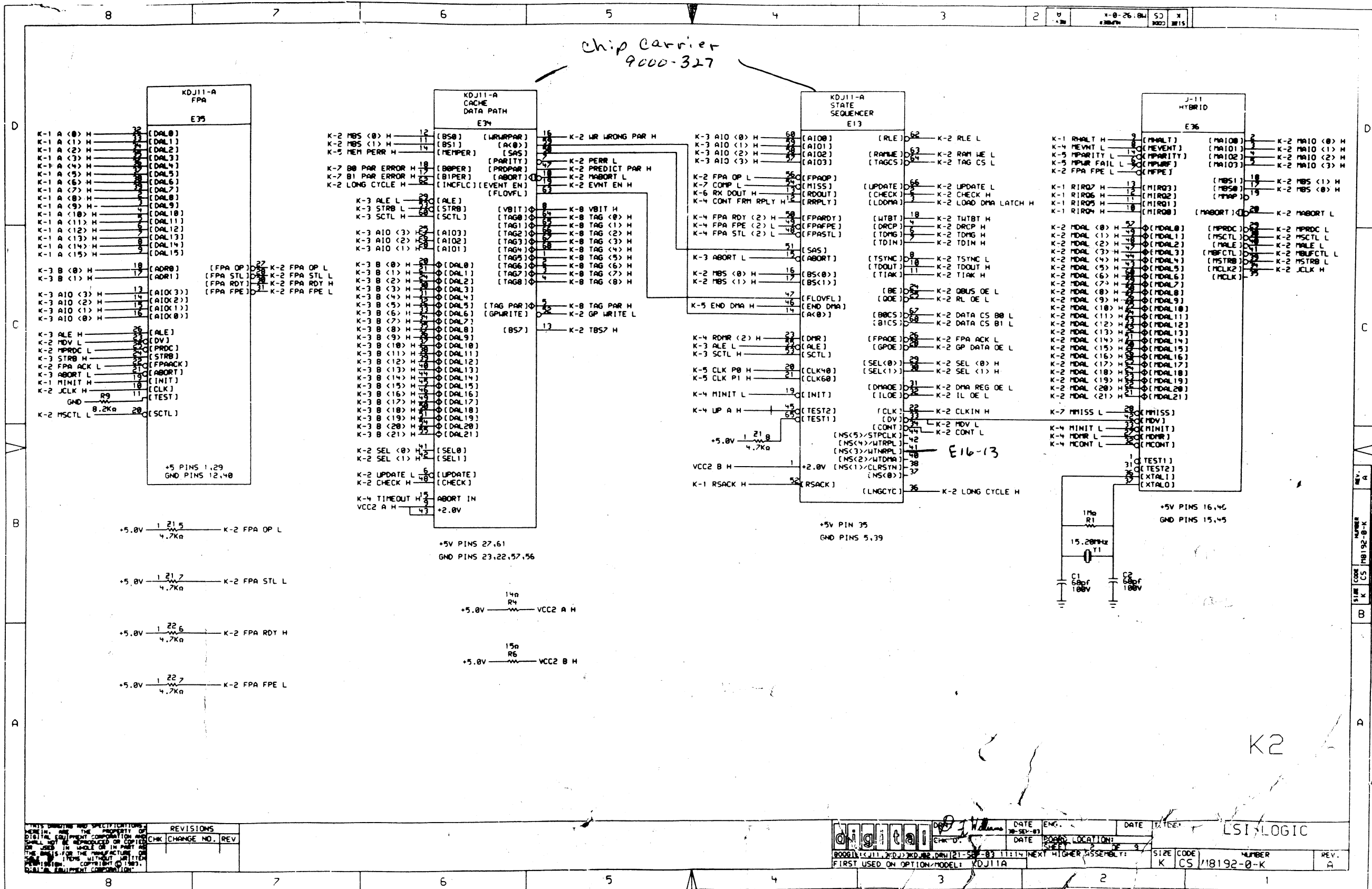


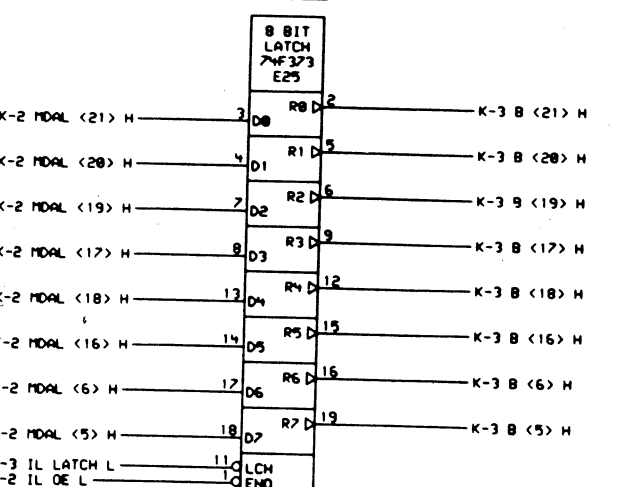
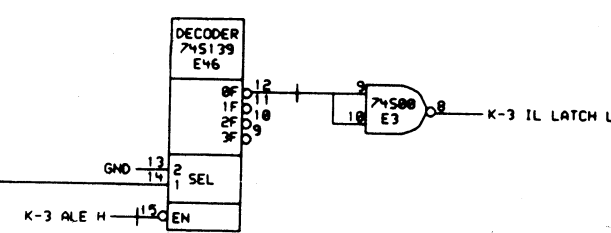
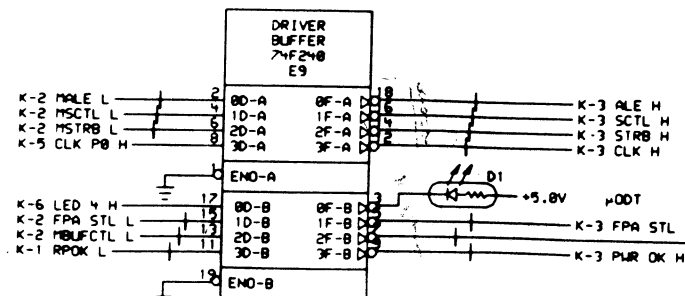
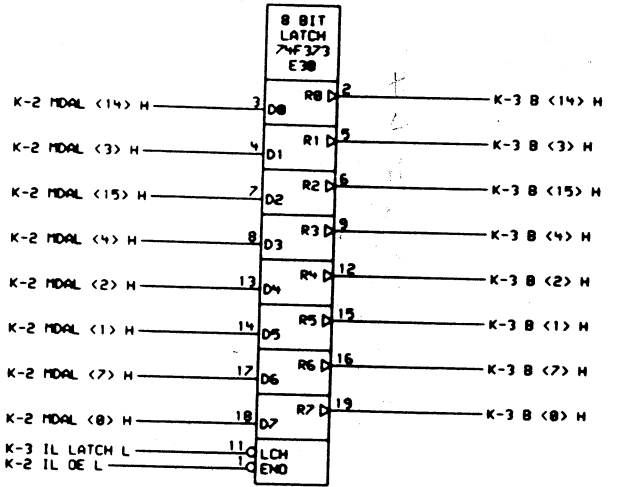
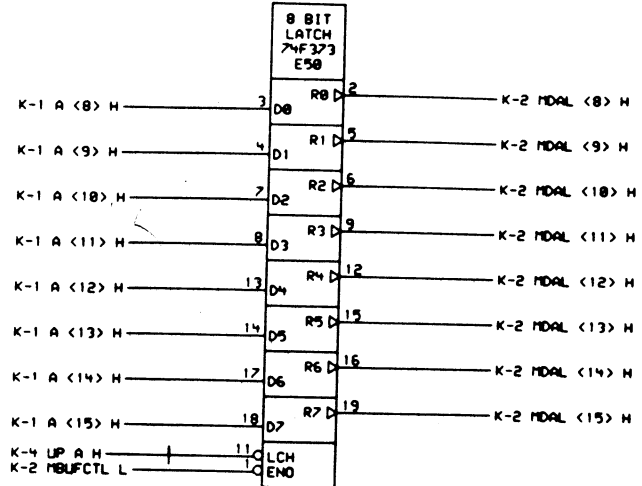
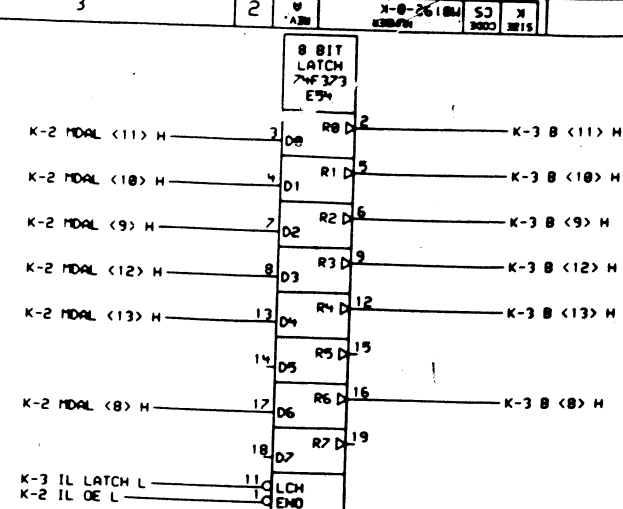
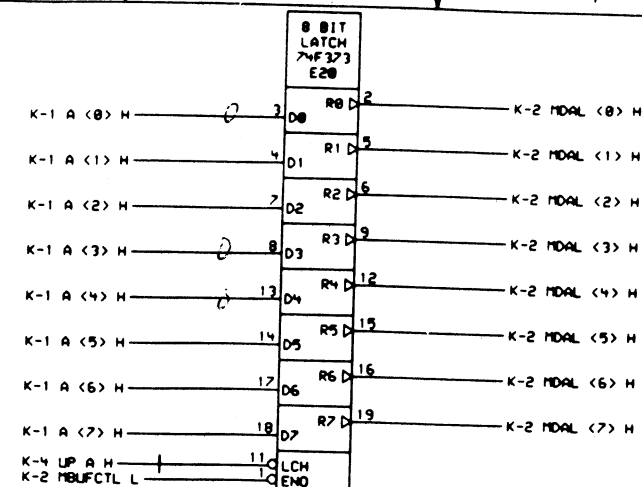
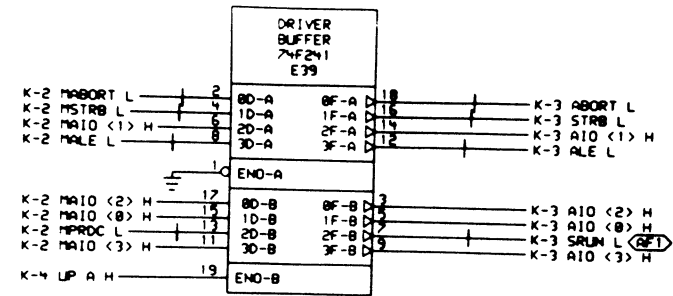
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	CHK	CHANGE NO.	REV

digit	DATE 20-SEP-83	ENG.	DATE	TITLE: Q-BUS INTERFACE
CHK'D.	DATE	BOARD LOCATION:		
FIRST USED ON OPTIC MODEL: KDJ11A				
SIZE	CODE	NUMBER	REV.	
K	CS	M8192-0-K	A	

chip carrier
9000-327





K3

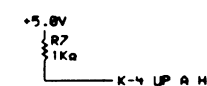
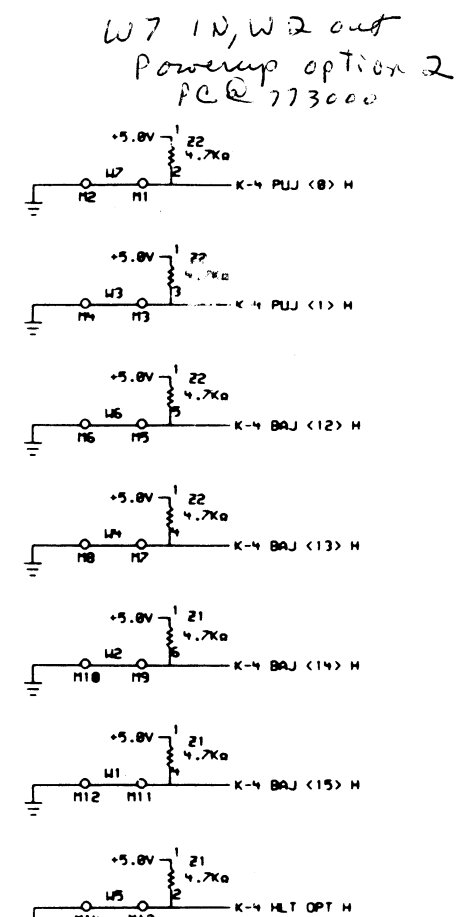
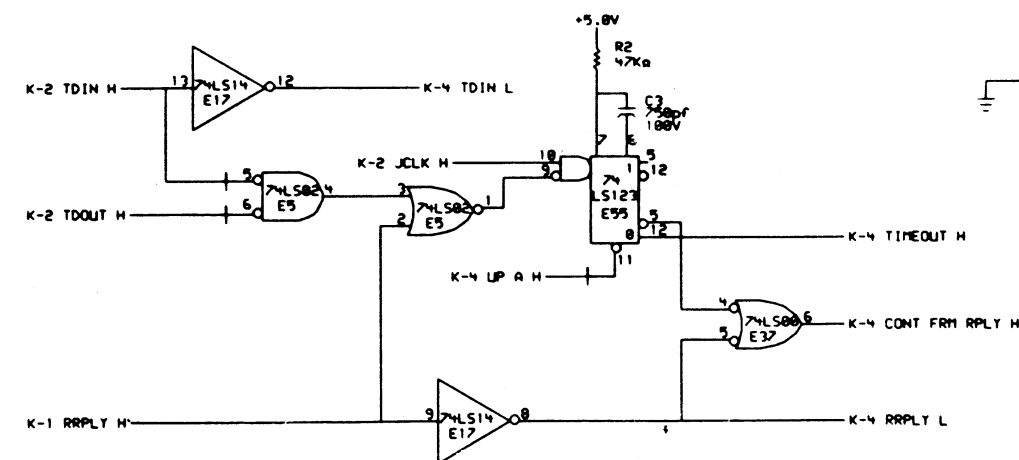
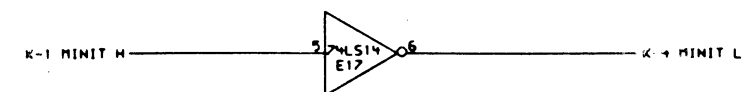
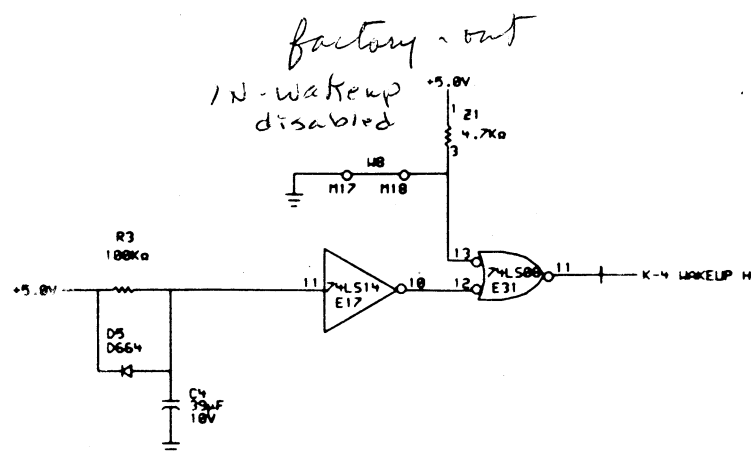
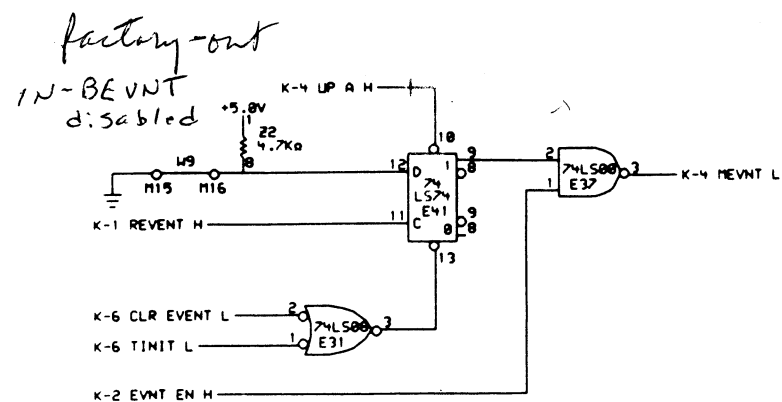
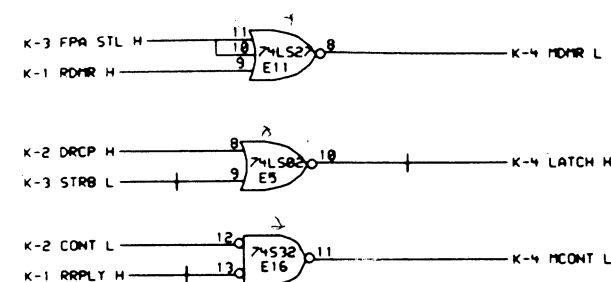
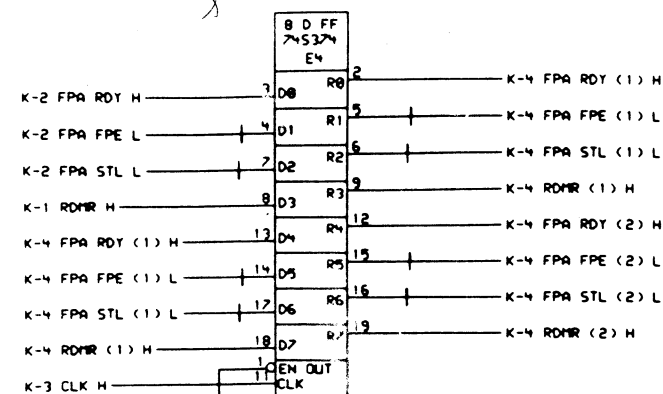
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REV.	DESCRIPTION	DATE
1	INITIAL RELEASE	10-SEP-81

digital **J-11 INTERFACE**

DATE: 10-SEP-81
 CHECKED: [Signature]
 FIRST USED ON OPTION/MODEL: KDJ11A
 BOARD LOCATION: [Blank]
 NEXT HIGHER ASSEMBLY: [Blank]
 SIZE: K CS
 NUMBER: M8192-0-K
 REV: A

8 7 6 5 4 3 2 1



K4

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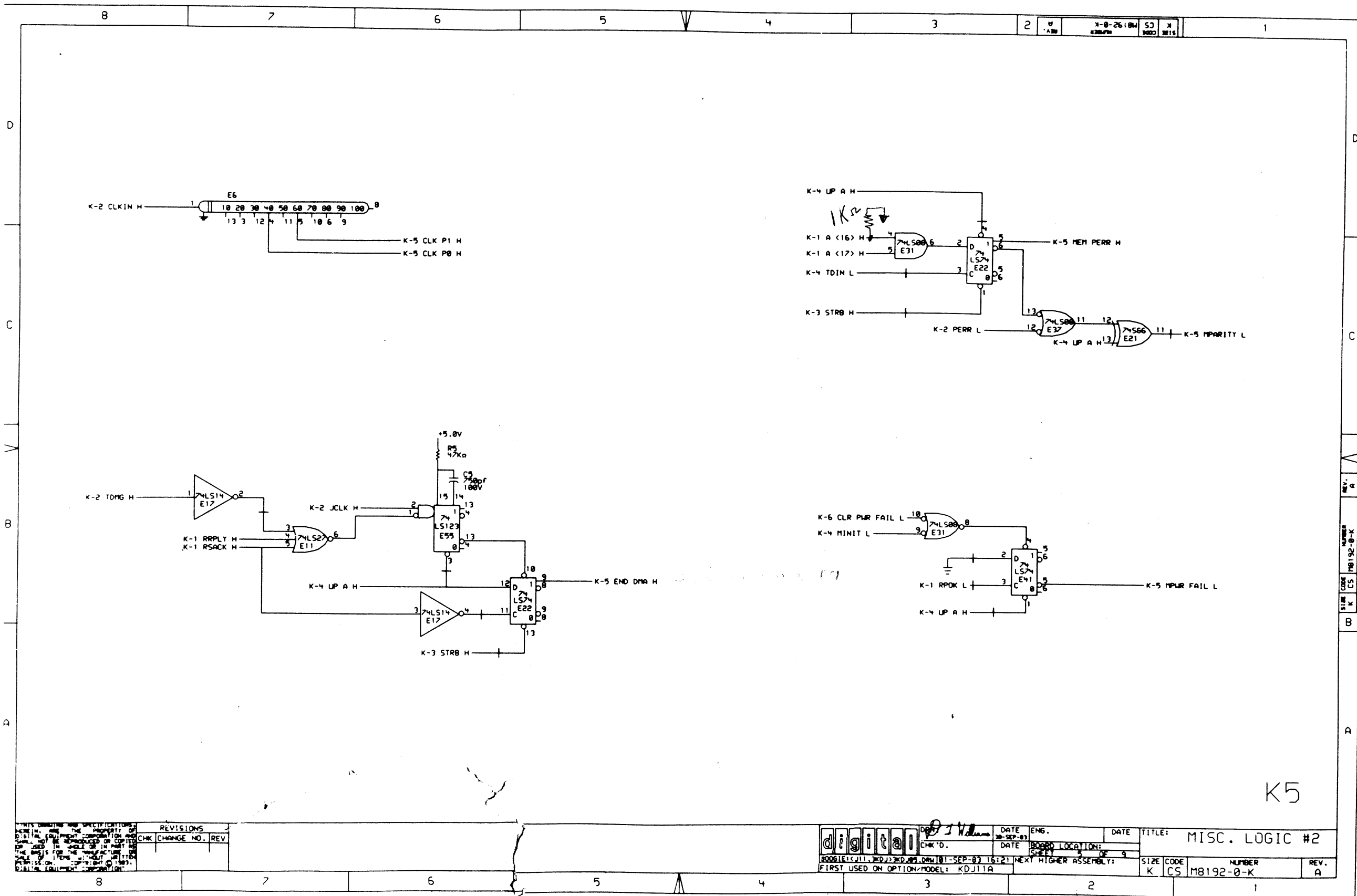
REVISIONS	
CHK	CHANGE NO. REV

digital
FIRST USED ON OPTION MODEL: KDJ11A

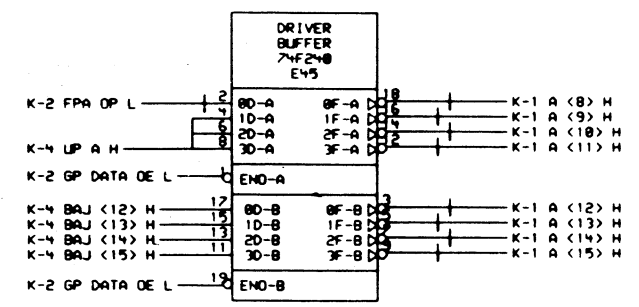
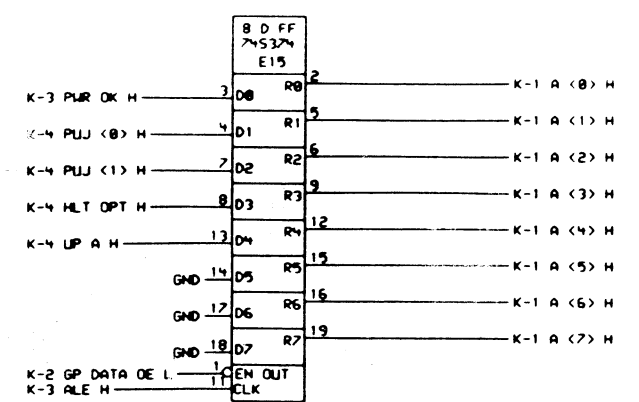
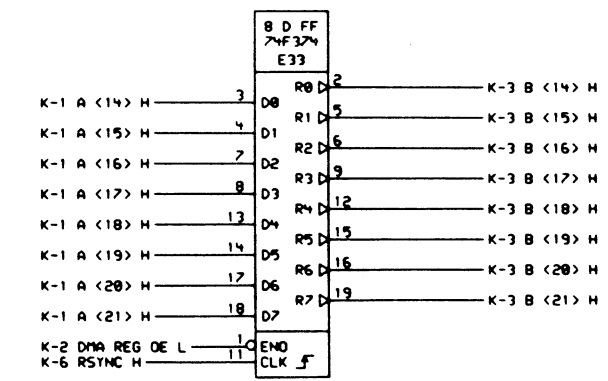
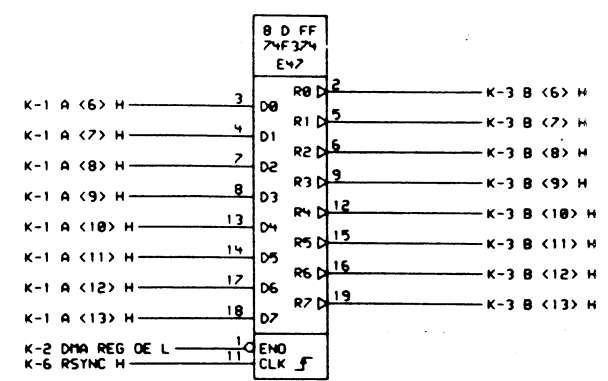
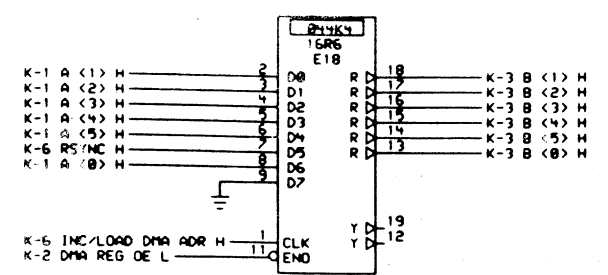
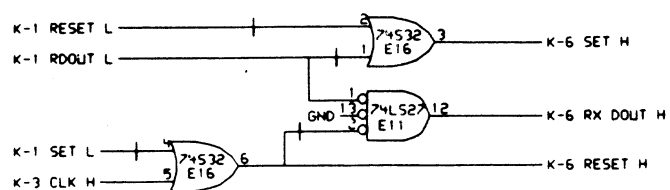
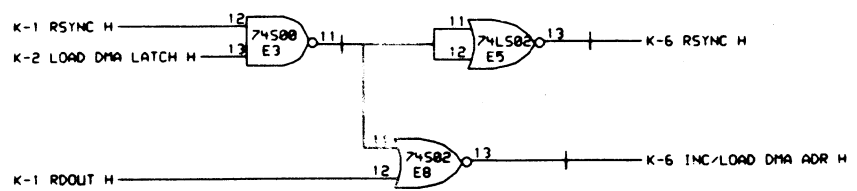
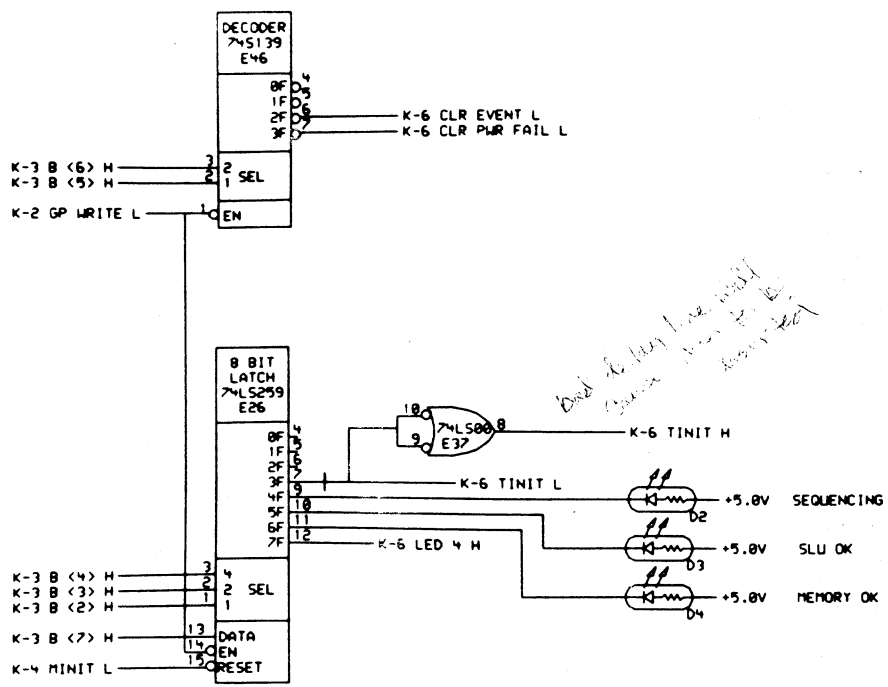
DATE	ENG.	DATE	TITLE:
30-SEP-81			MISC. LOGIC #1
DATE	BOARD LOCATION	DATE	

SIZE	CODE	NUMBER	REV.
K	CS	M8192-0-K	A

8 7 6 5 4 3 2 1



8 7 6 5 4 3 2 1



K6

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REVISIONS		
CHK	CHANGE NO.	REV.

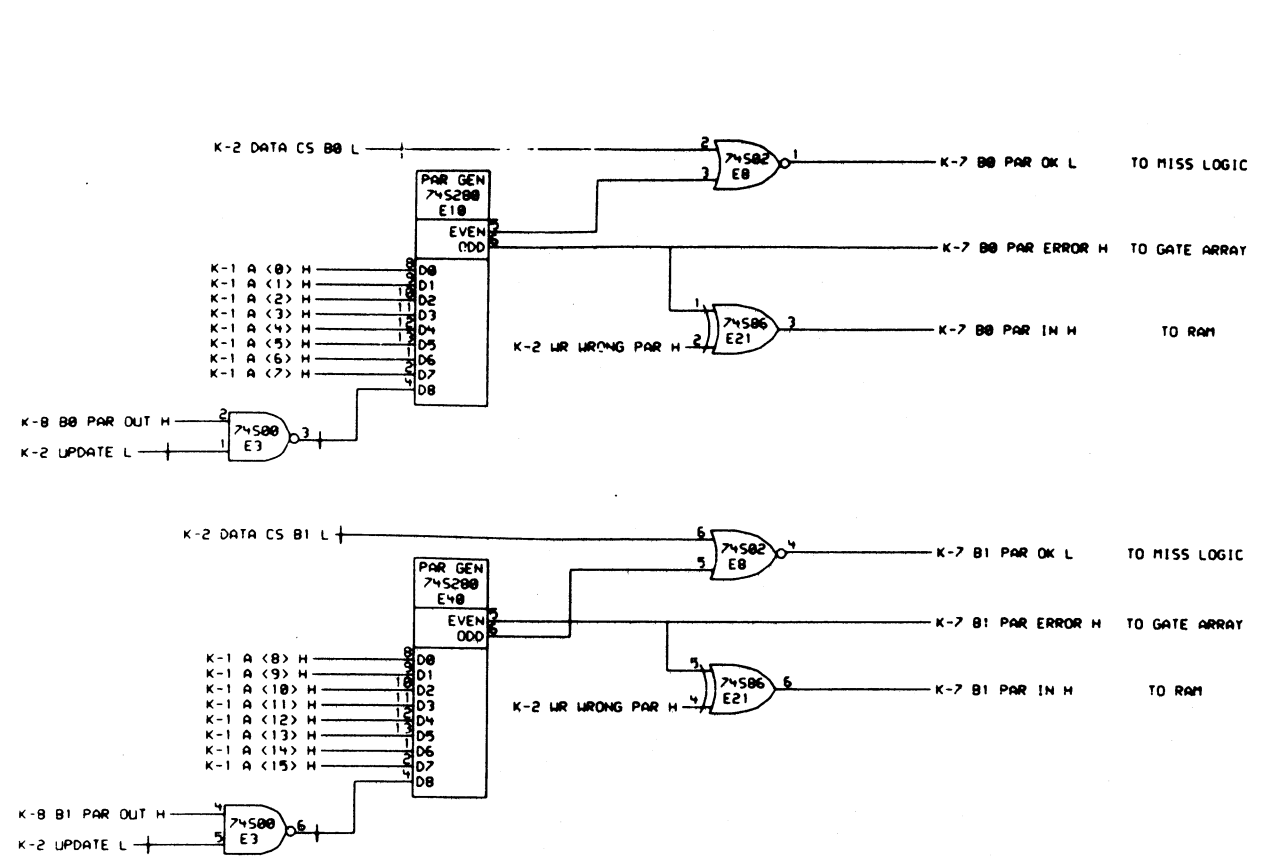
digital

DATE: 20-SEP-83
ENG: J. Williams
DATE: 20-SEP-83
CHK'D: J. Williams
DATE: 20-SEP-83
SHEET: 5 OF 9

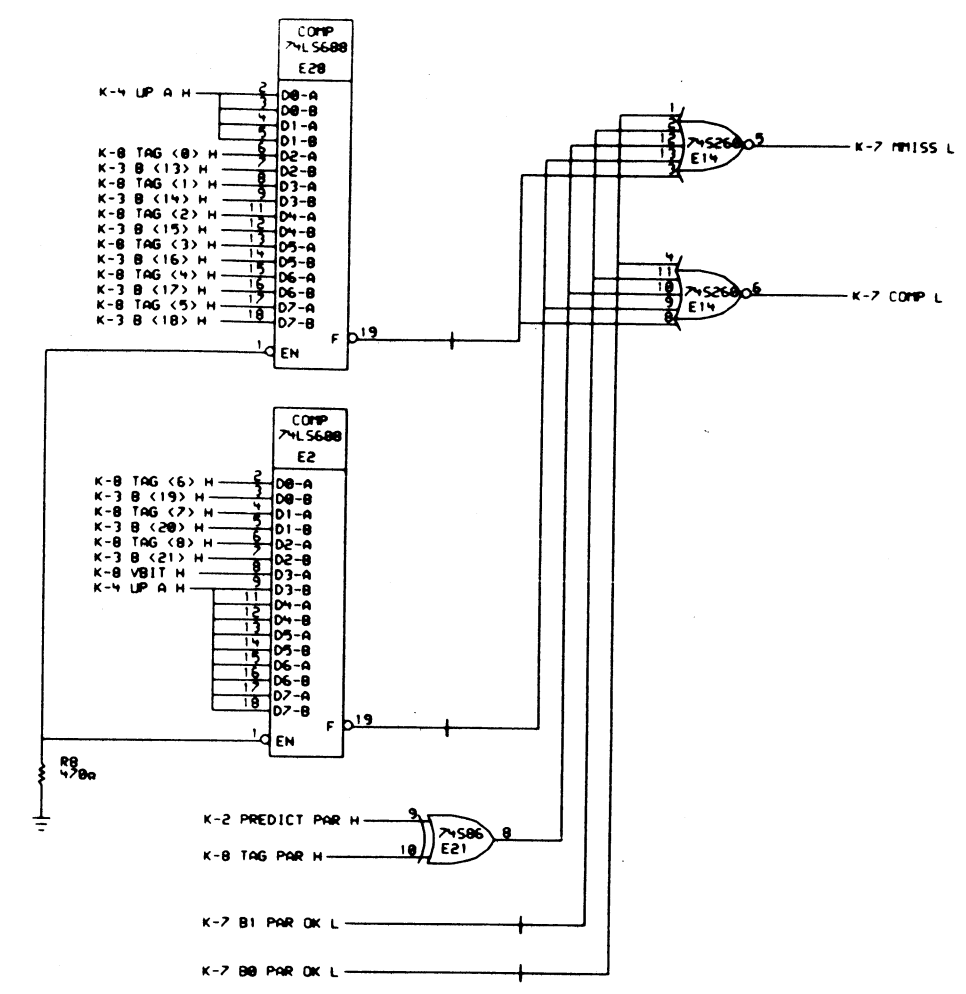
TITLE: GP LATCH & DMA LATCH LOGIC
SIZE: K
CODE: CS
NUMBER: M8192-0-K
REV: A

8 7 6 5 4 3 2 1

8 7 6 5 4 3 2 1



CACHE DATA PARITY LOGIC



HIT/MISS LOGIC

K7

REVISIONS	
CHK	CHANGE NO. REV.

digital	DATE	ENG.	DATE	TITLE: HIT MISS COMP. & CACHE DATA PARITY
	CHK'D.	DATE	BOARD LOCATION:	
FIRST USED ON OPTION MODEL: KDJ11A				SIZE CODE NUMBER REV. K CS M8192-0-K A

8 7 6 5 4 3 2 1

