



PDP-11

DATE: April 15, 1969

SUBJECT: Notes from PDP-11 Design Review 4-10-69

TO: PDP-11 Design Review FROM: Roger Cady
PDP-11 Engineers
Roger Dow
George Thissell
Larry McGowan

PRESENT: A. Kotok, G. Saviers, D. Zereski, I. Morris
R. Cady

The question of byte addressing was raised and it was suggested that a list of pro and con arguments be generated.

The committee recommended that sample subroutines be demonstrated for multiple precision add/subtracts, floating point; binary → ASCII conversion; TTY handler.

The TRAP handling problem was discussed and it was requested that the unimplemented trap handling routine be demonstrated to prove that the proper info was stored at time of trap.

It was strongly urged that in some way we should be able to clear interrupt enable in all devices by a single instruction and/or console action.

The question of bootstrap loading was raised but unanswered.

Address calculation flowchart.
Benchmark & program sequences.
Jq

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