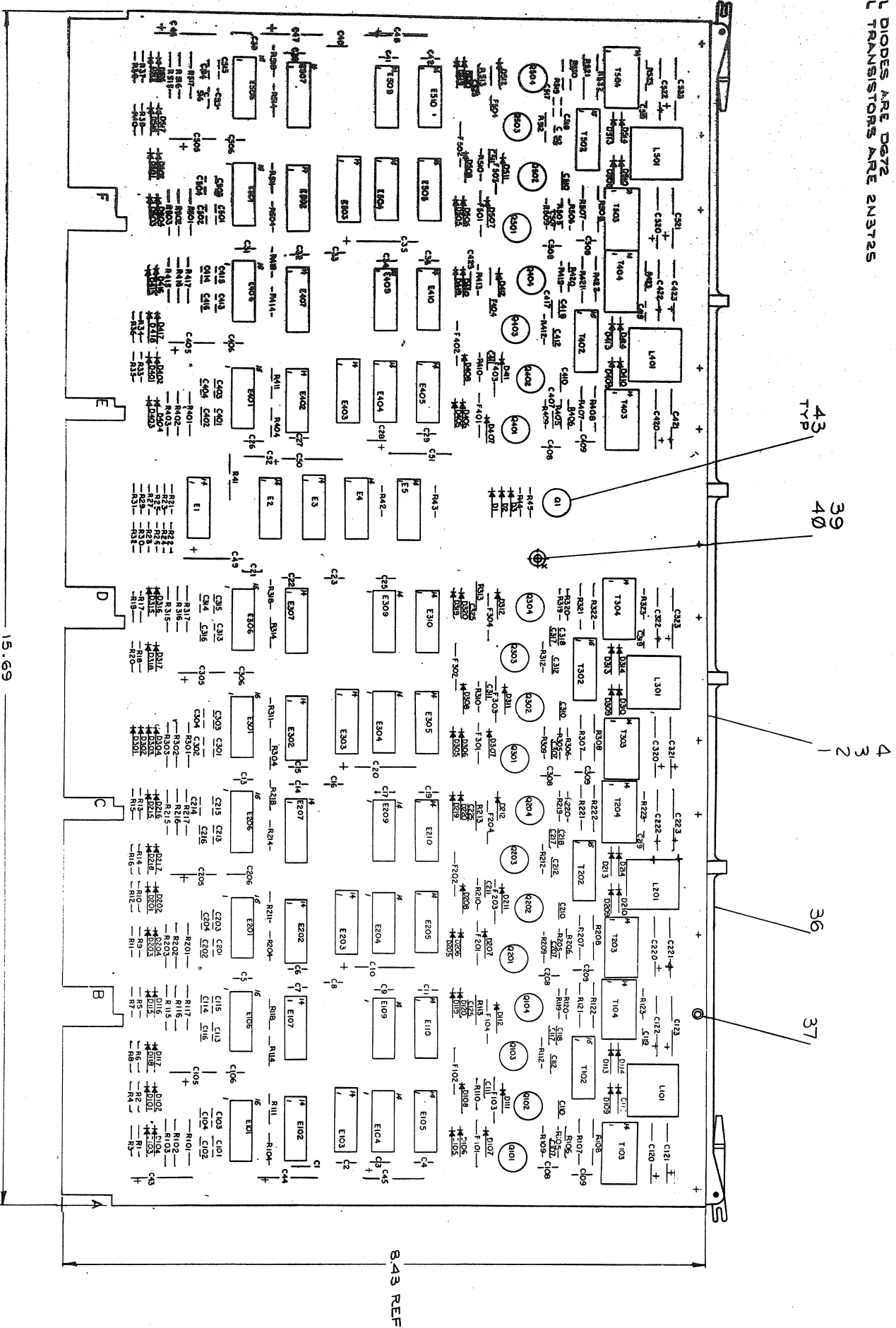


[illegible]

- NOTES:** UNLESS OTHERWISE SPECIFIED
1. ALL RESISTORS ARE IN OHMS /1W
 2. ALL CAPACITORS ARE IN MICROFARADS
 3. DATA BITS 118 ARE NOT USED IN 8 BIT SYSTEMS
 4. DATA BIT 16 IS NOT USED IN 19 BIT SYSTEMS
 5. DATA BITS 16/17/18/19 ARE NOT USED IN 16 BIT SYSTEMS
 6. ALL DIODES ARE 1N4148
 7. ALL TRANSISTORS ARE 2N3725



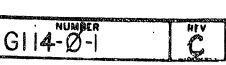
IC D1	C 7350	1	B	-
IC D1	C 360	1	B	-
IC D1	C 762B	9	16	B
IC TYPE		GND	+ 5V	- 5V

GRID AND SV ARE USUALLY PM 7 AND 14
RESPECTIVELY. EXCEPTIONS ARE STATED ABOVE

CHK		CHANGE NO.		REV		QTY		REF DESIGNATION		DESCRIPTION		PART NO.		ITEM NO.	
REVISIONS															
FIRST USED ON OPTION MODEL															
MF 11-U & MF 11-UP															
ETCH BOARD REV						B									
DEC NO.						EIA NO.	DEC NO.		EIA NO.						
SEMICONDUCTOR CONVERSION CHART															
SHEET		2		OF											
SCALE						16K									
NEXT HIGHER ASSY															
SIZE CODE						NUMBER									
DISTR.						REV.									
DIGITAL						EQUIPMENT									
CORPORATION						MAYNARD MASSCHUSETTS									
SENSE/INHIBIT															

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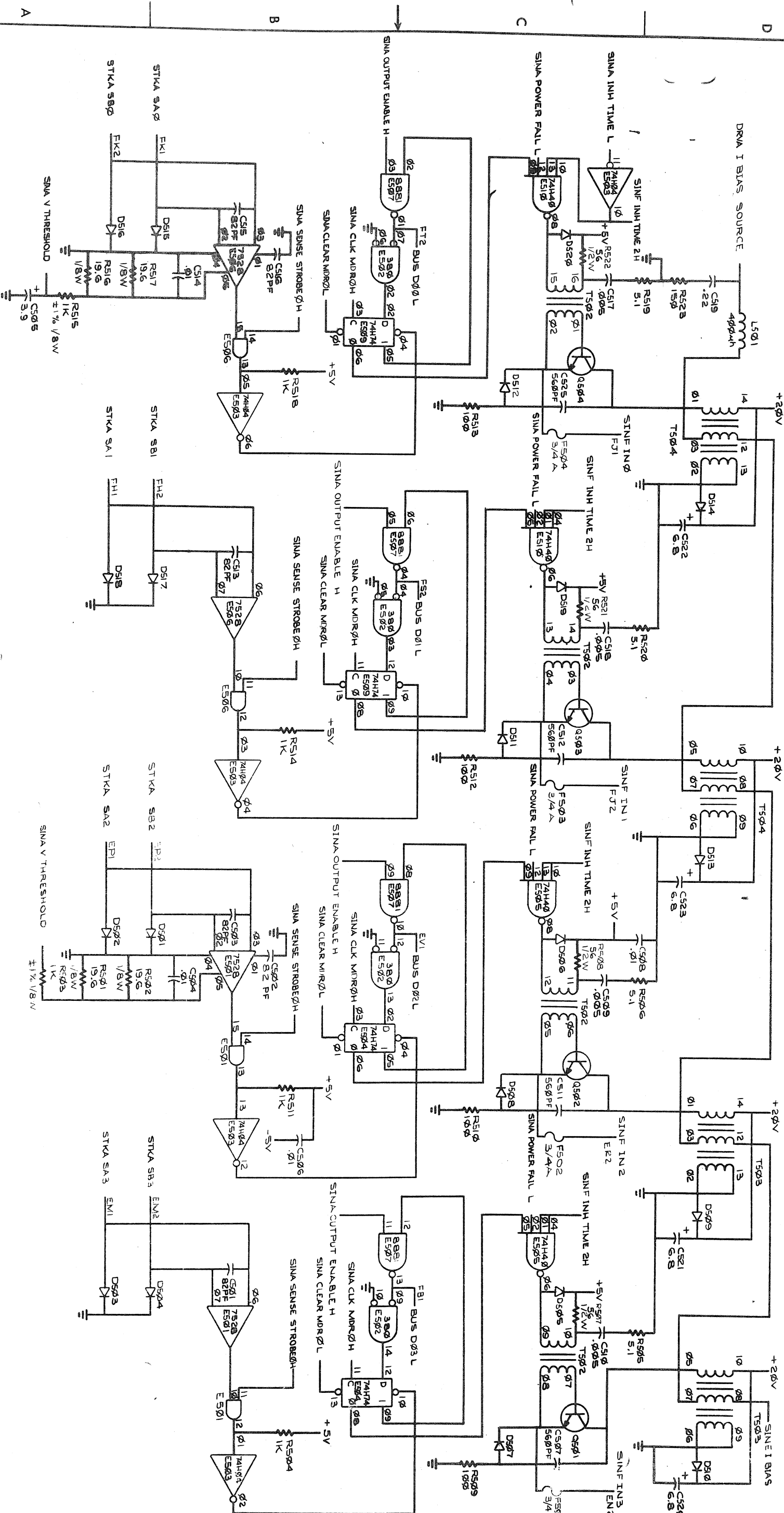
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FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
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A

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REVISIONS	
CHK	CHANGE NO.

500 SERIES SINF

TITLE		SIZE CODE		NUMBER		REV	
FOR SENSE/INHIBIT (SINF)		D05		614-0-1		C	
DATE		5		1		1	
PAGE		5		1		1	

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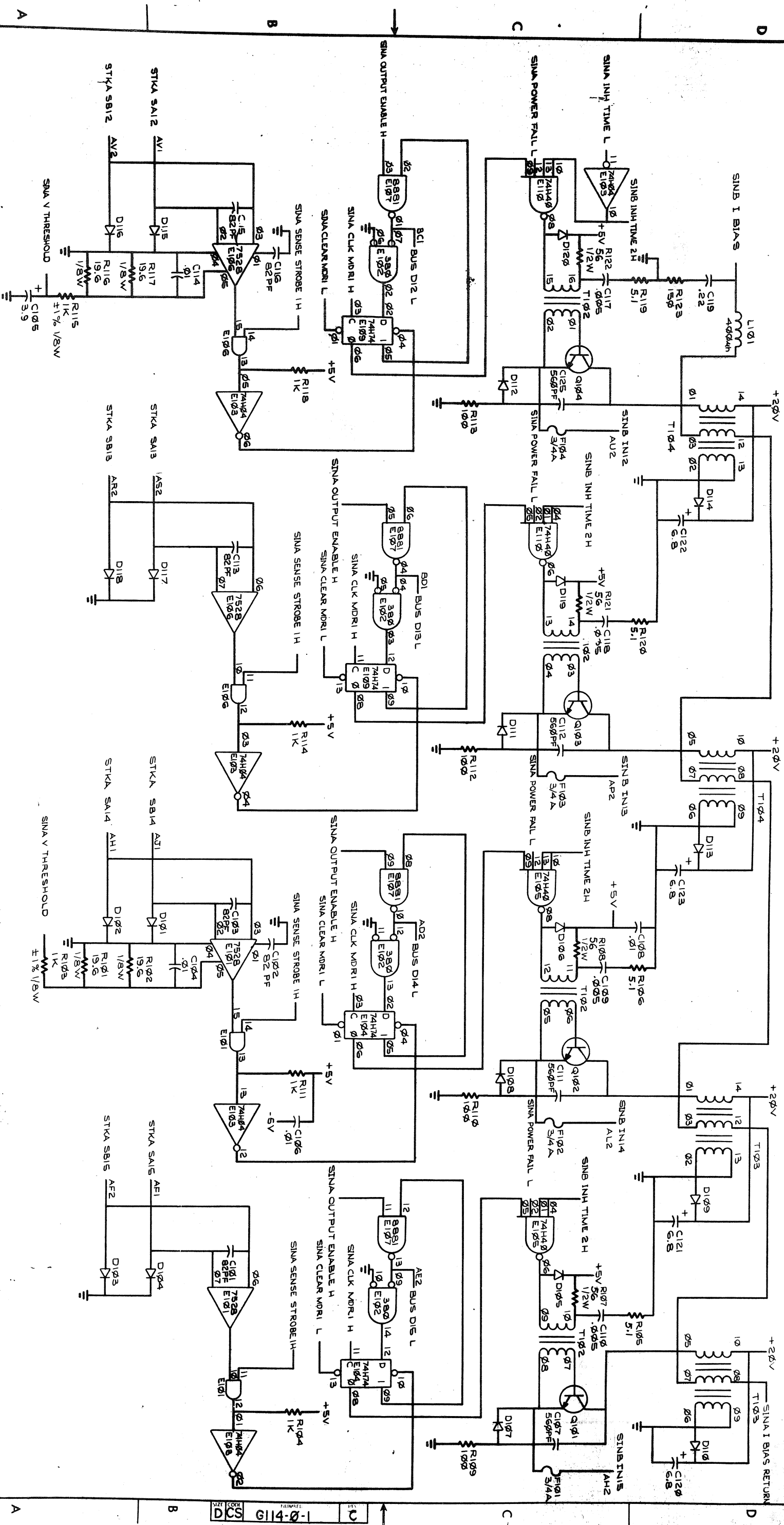
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F.E.V.

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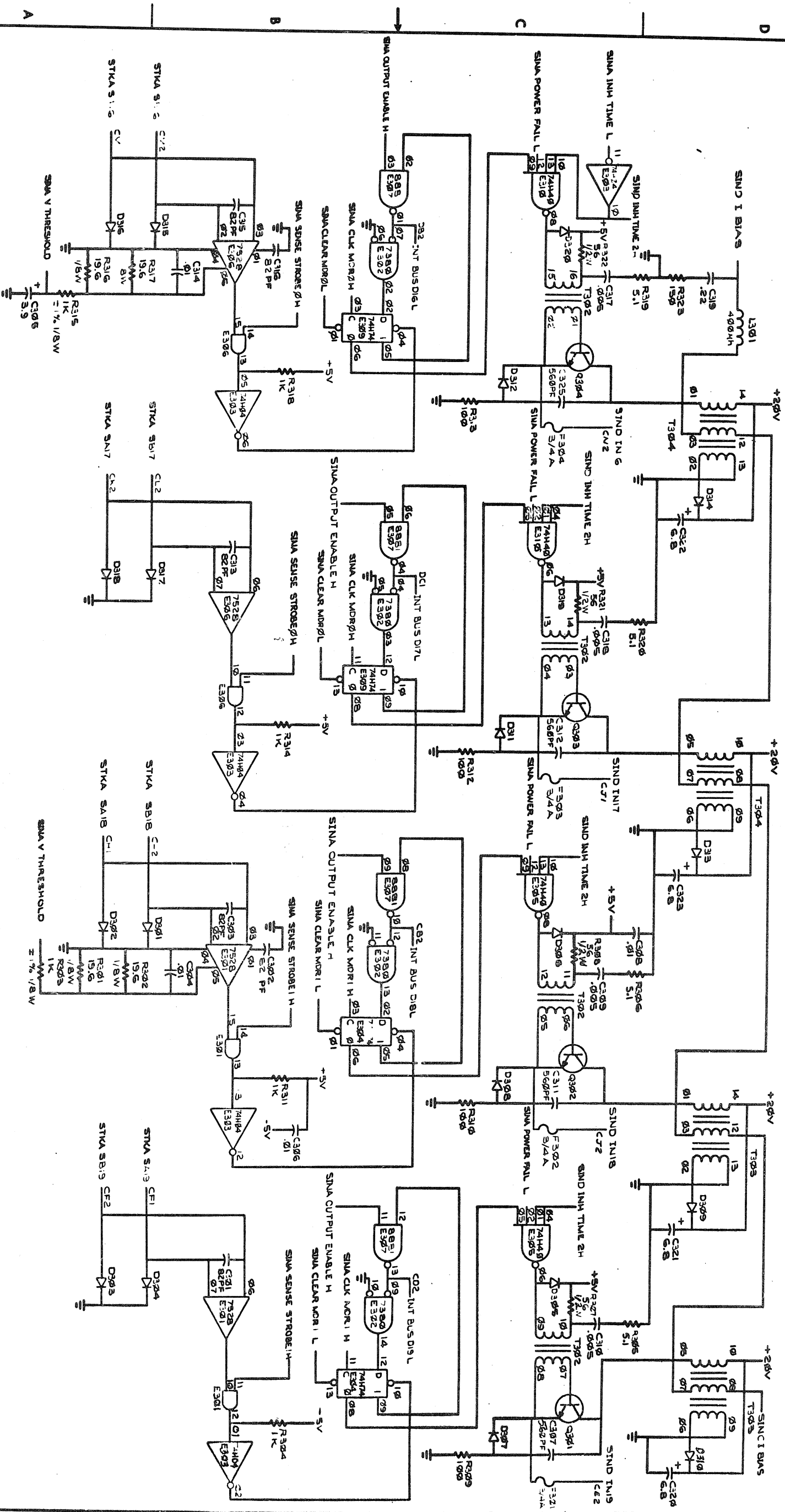
REVISIONS		
CHK	CHANGE NO.	REV.

100 SERIES SINB

OK SENSE/INHIBIT

SIZE CODE	NUMBER
-----------	--------

355



TITLE		16K SENSE/INHIBIT		SIZE CODE		NUMBER		REV	
16K SENSE/INHIBIT		DCS		G114-0-1		C			
SCALE		SHEET 9 OF		DWT					