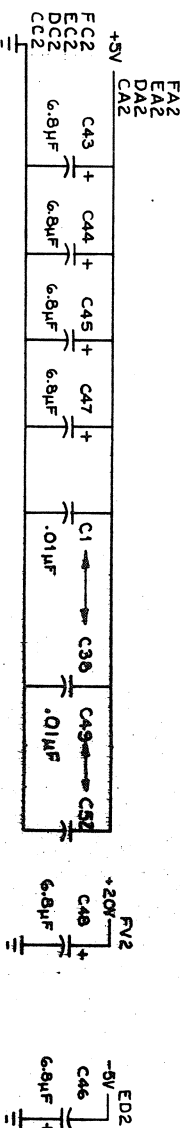
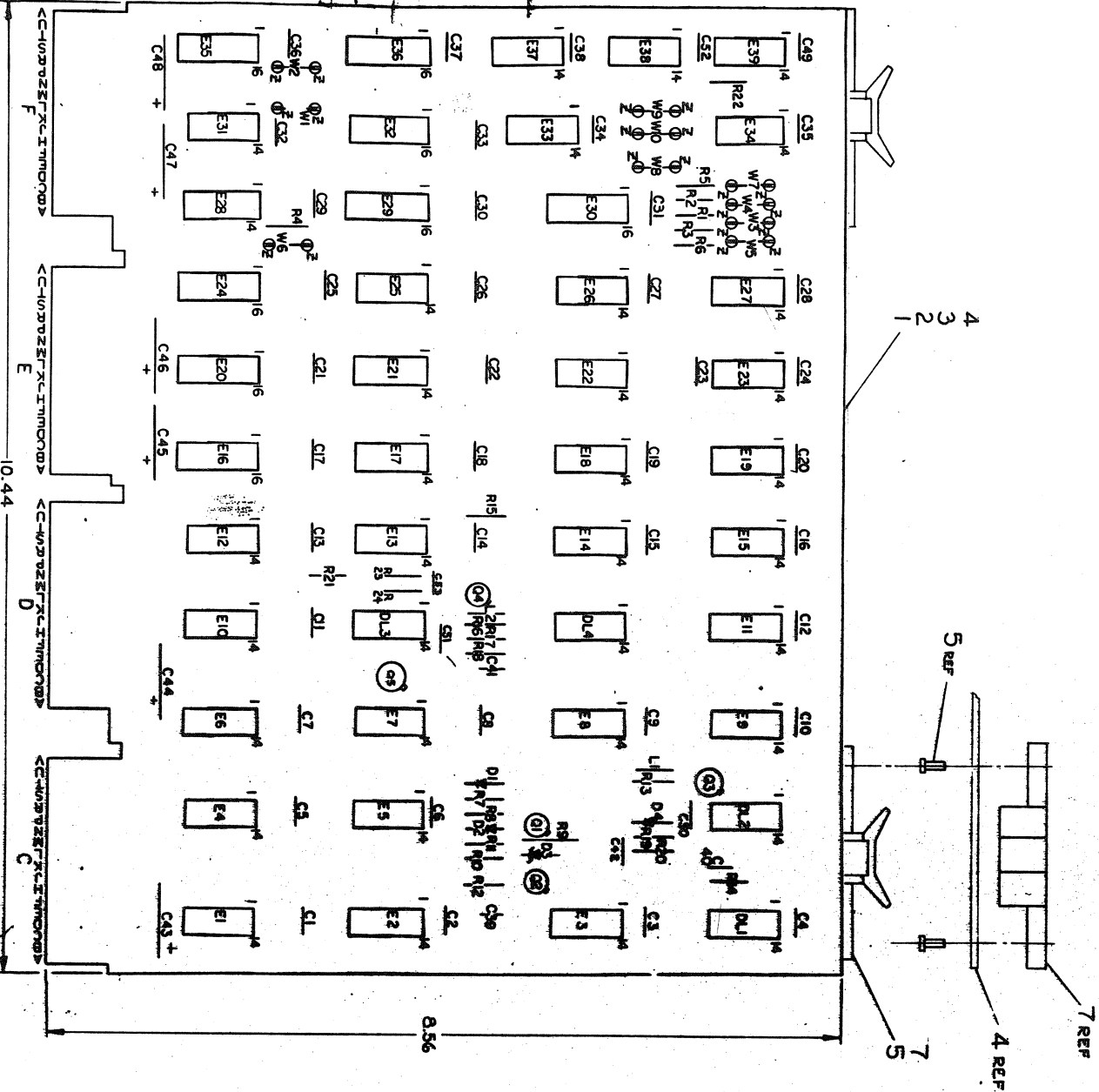


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NOTES: 1 UNLESS OTHERWISE SPECIFIED:
RESISTANCE IS IN OHMS.
CAPACITANCE IS IN MICROFARADS.



IC TYPE	IC PIN LOCATIONS
7483	12 5
7485	8 16
380	1 8
7475	12 5
GND	+ 5V

REV	CHANGE NO.	CHK
1	1	1
2	2	2
3	3	3
4	4	4
5	5	5
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REF	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
REF	1	ASSY COORDINATE HOLE LOCATION	K-CO-M8293-0-4	1
REF	2	ASSY/DRILLING HOLE LAYOUT	D-44-M8293-0-5	2
REF	3	MODULE ECO HISTORY	B-MH-M8293-0-6	3
REF	4	ETCHED CIRCUIT BOARD	5-010473	4
REF	5	EYELET GS-4-7	9006732	5
REF	6	SPLIT LUGS	9006735	6
REF	7	HANDLE FLIP-CHIP MAGENTA	9008337-6	7
REF	8	CAP 0.01UF 100V ±5% DM	1000015	8
REF	9	CAP 0.01UF 100V ±5% DM	1000042	9
REF	10	CAP 0.01UF 50V ±20%	1001610	10
REF	11	CAP 0.01UF 35V ±10% S.TANT	1005306	11
REF	12	DIODE IN746A 3.6V±5% ZENER	1104860	12
REF	13	DIODE D672	1105275	13
REF	14	RES 100 1/4W 5%	1300229	14
REF	15	RES 120 1/4W 5%	1300247	15
REF	16	RES 150 1/2W 5%	1300354	16
REF	17	RES 1K 1/4W 5%	1300365	17
REF	18	RES 2K 1/4W 5%	1302368	18
REF	19	RES 20K 1/4W 5%	1302391	19
REF	20	RES 560 1/4W 5%	1301890	20
REF	21	RES 33K 1/4W 10%	1300510	21
REF	22	TRANSISTOR DEC 6534 D	1503409	22
REF	23	INDUCTOR DEC 3009 B	1503100	23
REF	24	INDUCTOR 100UH	1610662	24
REF	25	DELAY LINE 250 NS, TAPPED	1611243	25
REF	26	IC DEC 7440	1905586	26
REF	27	IC DEC 7475	1909050	27
REF	28	IC DEC 74H00	1909056	28
REF	29	IC DEC 74H10	1909057	29
REF	30	IC DEC 74H50	1909060	30
REF	31	IC DEC 74H53	1909062	31
REF	32	IC DEC 74H55	1909063	32
REF	33	IC DEC 380	1909465	33
REF	34	IC DEC 74H04	1909531	34
REF	35	IC DEC 7483	1909932	35
REF	36	IC DEC 7485	1910224	36
REF	37	IC DEC 0085	1910449	37
REF	38	IC DEC 7427	1910878	38
REF	39	RES 330 1/4W 5%	1300295	39
REF	40	CAP 120PF 100V±5% D.M.	1000018	40

REV	CHANGE NO.	CHK
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REV	CHANGE NO.	CHK
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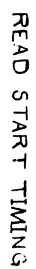
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FIRST USED ON OPTION MODEL		
M11-U		
ETCH BOARD REV	B	

QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.



SIZE	CODE	ITEM	QTY
D	CS	M8293-0-1	C

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1-0-3628W SCD 2

1

D

MATC 300 NS DLY H

MATC 325 NS DLY H

MATB START READ H

(READ EARLY FLIP-FLOP)

CDI MATC END STROBE L

MATB 150 NS DLY H

(END STROBE FLIP-FLOP)

MATH C0 H
MATH C1 L

MATC DATIP CYCLE L

MATC 300 NS DLY H

MATH C1 L

MATB MSYN H

MATB CLK MDR 0 L
MATB CLK MDR 1 L
(SSYN FLIP-FLOP)

MATC SSYN L
+5V
R23 330
C53 120 PF
R24 330
CB2 BUS SSYN L *
Q5 3000B

B

MATC 350 NS DLY H

(READ LATE FLIP-FLOP)

CMI MATC READ LATE L

MATB 250 NS DLY H
2 3 4 5 6 7 8 9 10 11 12
275 300 325 350 375 400 425 450 475
(COL TAPPED DELAY LINE)
R14 100
C41 82 PF

* THIS SIGNAL IS CALLED
BUS SSYN L' FOR MFI-U
SSYN INT BUS L' FOR MFI-U-P

A

REVISIONS	
CHK	CHANGE NO
REV	

READ TIMING

TITLE		SIZE CODE	NUMBER	REV.
ICR UNIBUS TIMING (MATC)		DCS	M8293-0-1	C
SCALE	SHEET 3 OF 70	DIST		

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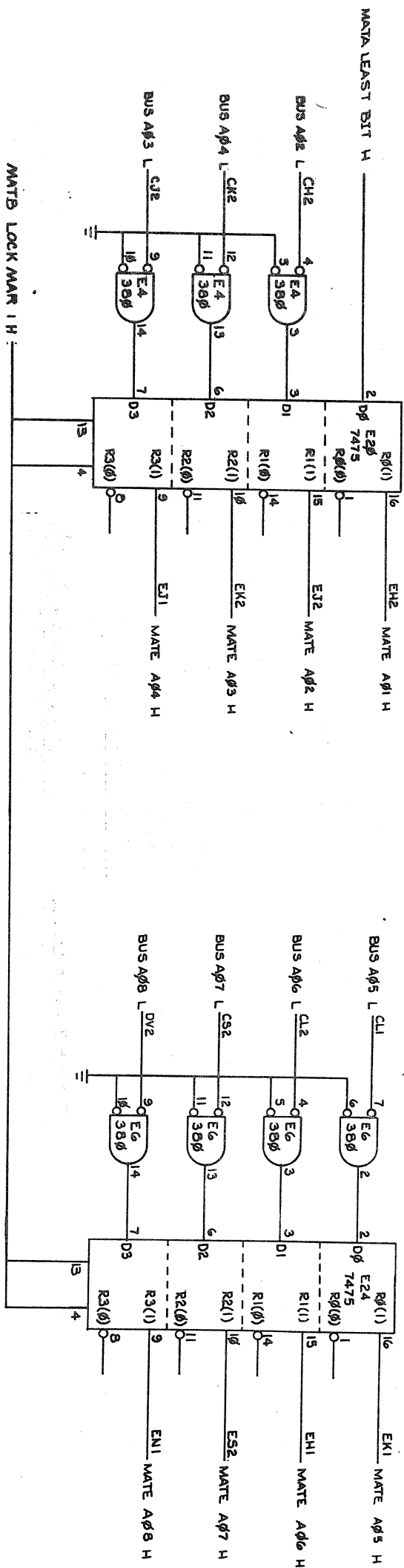
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SIZE CODE NUMBER
DCS M8293-0-1

10

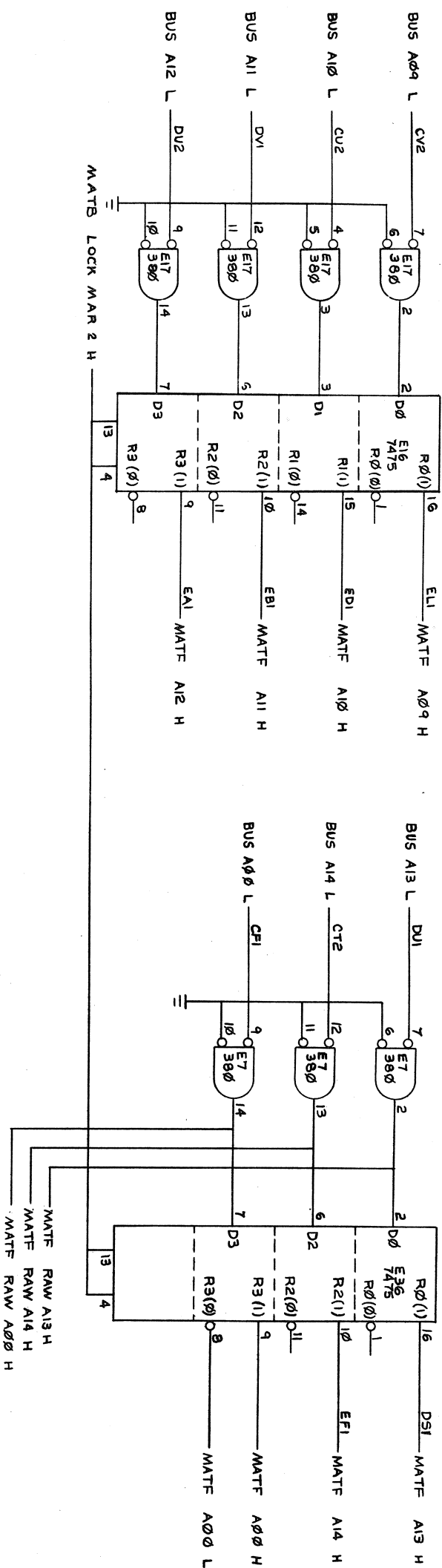
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REV. NO.		REV.	
CHK	CHANGE NO.	REV.	
8			
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TITLE		SIZE	CODE	NUMBER	REV.
A01-A08 LATCHES		D	CS	M8293-0-1	C
A01-A08 LATCHES		D	CS	M8293-0-1	C

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MATB LOCK MAR 2 H

MATF RAW A13 H
MATF RAW A14 H
MATF RAW A00 H

A00 A09 to A13 LATCHES

TITLE 16K UNIBUS TIMING (MATF)

SCALE 1/4" = 1"

SHEET 3 OF 10

DIST

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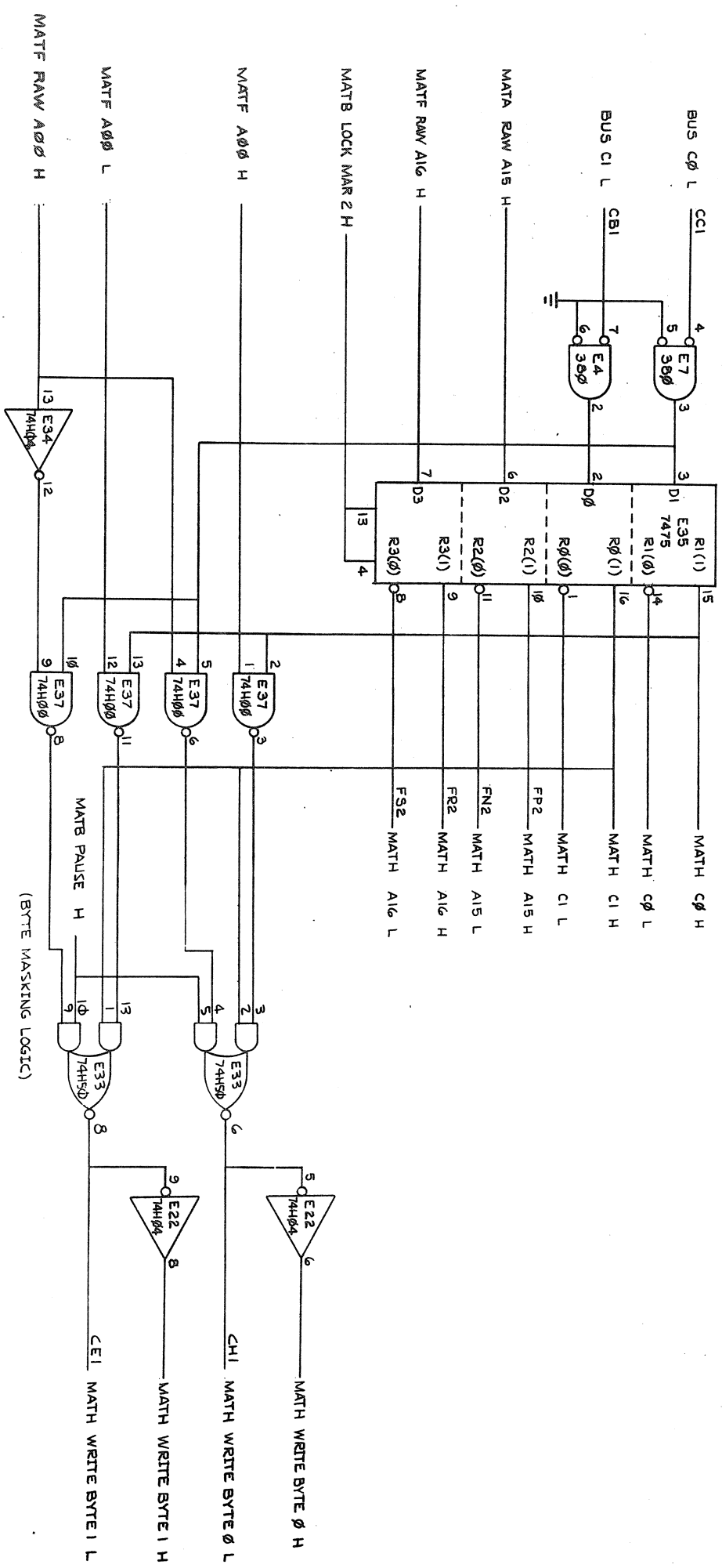
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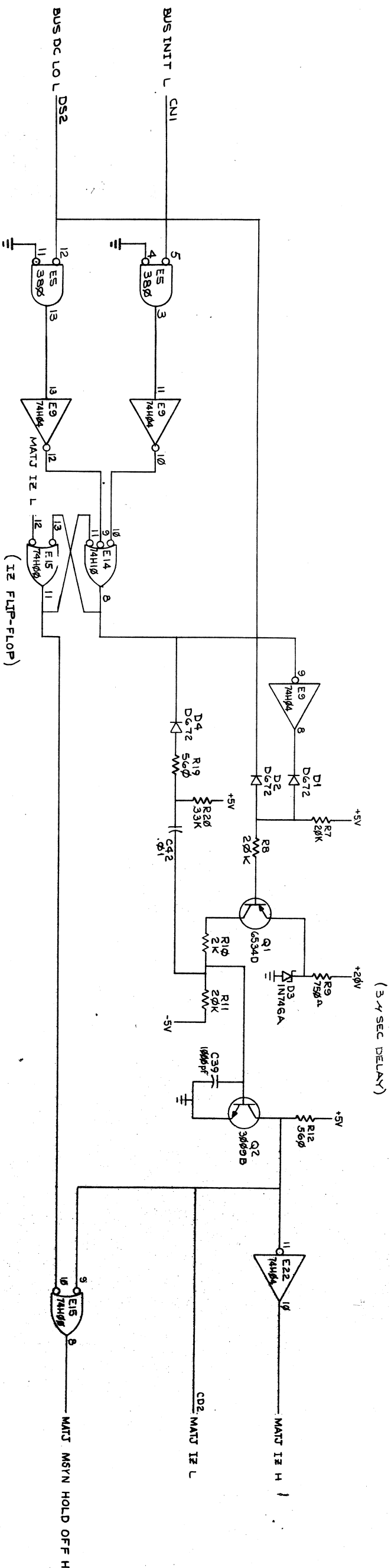


LATCHES & BYTE MASKING

LATCHES & BYTE MASKING		LATCHES & BYTE MASKING		LATCHES & BYTE MASKING	
DATE	10/1/73	DESIGNED BY	W. J. H.	CHECKED BY	W. J. H.
SHEET	9	OF	24	DIST.	
SIZE	11x17	CODE	D105	NUMBER	M8293-0-1
REV.	C				

REVISIONS		REVISIONS	
CHK	CHANGE NO.	REV	

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INIT & DCLO INITIALIZING

TITLE		SIZE CODE		NUMBER		REV.
INIT & DCLO INITIALIZING	DCS	M8293-0-1	1	1	1	

REVISIONS	
CHANGE NO.	REV.

8 7 6 5 4 3 2 1