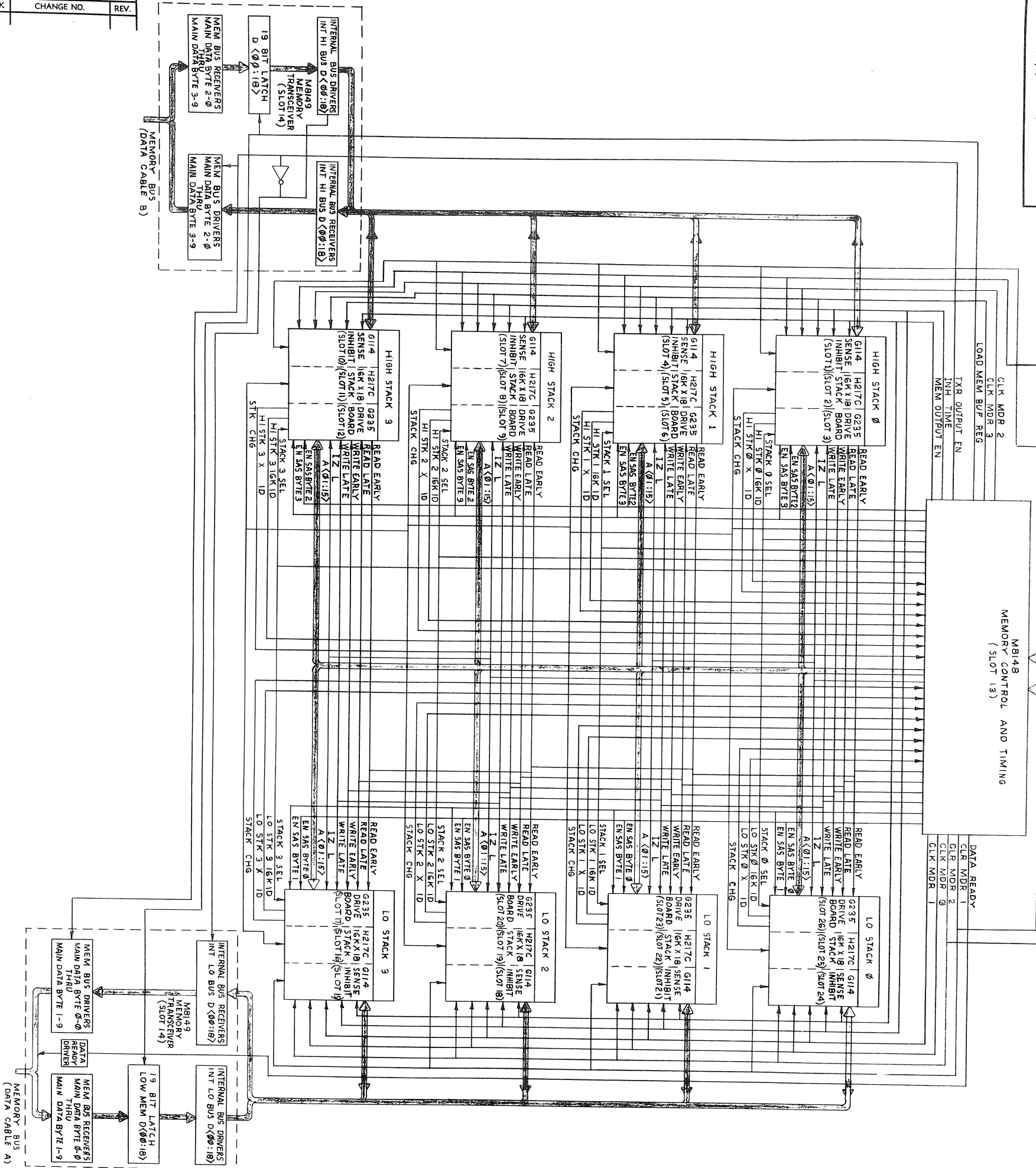


DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS					QUANTITY / VARIATION										
PARTS LIST															
MADE BY BILL BLODGETT		CHECKED D. HEALY		SECTION											
DATE 1/22/75		DATE 1/29/75		1											
ENG <i>Thomas C. No</i>		PROD DON WEAVER		ISSUED SECT.											
DATE 2/28/75		DATE 3-26-75		1											
ITEM NO.	DWG NO. / PART NO.	DESCRIPTION			MJ11-AA	MJ11-AB	MJ11-AC	MJ11-AD	MJ11-AE	MJ11-AM	MJ11-AY	MJ11-AZ	MJ11-AG	MJ11-AH	
1	E-IA-7009769-0-0	CHASSIS 10.5"			1	1	1	1	-	-	1	1	1	1	
2	D-PS-1211825-02	SLIDE, 3 POS TILT			1	1	1	1	1	1	1	1	1	1	
3	E-AD-7010694-0-0	POWER SUPPLY MJ11 115V			1	-	1	-	-	-	1	-	1	-	
4	E-AD-7010694-1-0	POWER SUPPLY MJ11 230V			-	1	-	1	-	-	-	1	-	1	
5	D-PS-1211630-0-0	CARD GUIDE			30	30	30	30	-	-	30	30	30	30	
6	E-AD-7010497-0-0	PACK PLANE (MEM)			1	1	1	1	-	-	1	1	1	1	
7	E-AD-7010497-1-0	BACK PLANE (MEM EL-FAB)													
8	D-IA-7010580-0-0	HARNESS POWER			1	1	1	1	-	-	1	1	1	1	
9	D-IA-7010581-0-0	HARNESS AC/DC LO			1	1	1	1	-	-	1	1	1	1	
10	D-IA-7009768-0-0	COVER, CHASSIS			2	2	2	2	-	-	2	2	2	2	
11	D-AD-7010824-0-0	EXP DATA CABLE ASSY (BAY Ø) 7ft.			1	1	-	-	-	-	-	-	1	1	
12	D-AD-7010824-1-0	EXP ADD. CABLE ASSY (BAY Ø) 7ft.			1	1	-	-	-	-	-	-	1	1	
13	D-AD-7010826-0-0	EXP CABLE ASSY. (INTER BAY) 10ft.			-	-	1	1	-	-	-	-	-	-	
14	D-CS-H217-C-1	H217C 16K X 18 BIT STACK			2	2	2	2	2	1	2	2	8	8	
15	D-CS-G235-Ø-1	G235 16K MEM. DRIVE BOARD			2	2	2	2	2	1	2	2	8	8	
16	9006022-3	SCR, PHL TRUSS HD #6-32 X .38			11	11	11	11	-	-	11	11	11	11	
17	9006633	WASHER INT. TOOTH LOCK #6			7	7	7	7	-	-	7	7	7	7	
18	9007651	WASHER EXT. TOOTH LOCK #10			8	8	34	34	-	-	8	8	8	8	
19	9009599-2	SCR. PHL FLAT HD #10-32 X .531 (SPECIAL)			6	6	6	6	-	-	6	6	6	6	
20	906071-3	SCR, PHL HD TRUSS #10-32 X .38			8	8	12	12	-	-	8	8	8	8	
21	D-CS-G114-Ø-1	G114 16K SENSE/INHIBIT BOARD			2	2	2	2	2	1	2	2	8	8	
MAY BE USED AS A REPLACEMENT FOR ITEM #6															
TITLE UNIT ASSY MJ11-A		ASSY NO. E-AD-MJ11-A-Ø		SIZE CODE A PL	NUMBER MJ11-A-Ø			REV. D			ECO NO. MJ11-A-00004				
SHEET 1 OF 3		DIST.													

DEC FORM DEC 16-(325)-1031-N870
DRA 110

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS					QUANTITY / VARIATION										
PARTS LIST															
MADE BY BILL BLODGETT		CHECKED D. HEALY		SECTION											
DATE 1/22/75		DATE 1/29/75		1											
ENG <i>Thomas C. No</i>		PROD DON WEAVER		ISSUED SECT.											
DATE 2/28/75		DATE 3-26-75		1											
ITEM NO.	DWG NO. / PART NO.	DESCRIPTION			MJ11-AA	MJ11-AB	MJ11-AC	MJ11-AD	MJ11-AE	MJ11-AM	MJ11-AY	MJ11-AZ	MJ11-AG	MJ11-AH	
22	B-MD-7413204-0-0	CARD GUIDE REWORK			6	6	6	6	-	-	6	6	6	6	
23	D-AD-7010532-0-0	FULL DOOR UNIT ASSY.			-	-	1	1	-	-	-	-	-	-	
24	D-AD-7010791-0-0	FRONT DOOR ASSY.			-	-	1	1	-	-	-	-	-	-	
25	D-IA-7010780-1-0	PANEL LOGO			-	-	1	1	-	-	-	-	-	-	
26	C-PL-7006501-35-0	BASIC CABINET ASSY. 19"			-	-	1	1	-	-	-	-	-	-	
27	E-UA-861-D-0	POWER CONTROL 115V			-	-	1	-	-	-	-	-	-	-	
28	E-UA-861-E-0	POWER CONTROL 230V			-	-	-	1	-	-	-	-	-	-	
29	9006073-3	SCR, PHL TRUSS HD #10-32 X .50			-	-	20	20	-	-	-	-	-	-	
30	9007786	NUT, TINNER MAN #10-32			-	-	26	26	-	-	-	-	-	-	
31	9007086	CLAMP, CABLE			1	1	1	1	-	-	1	1	1	1	
32	907031	WRAP, TIE			-	-	6	6	-	-	-	-	-	-	
33	D-CS-M8148-Ø-1	M8148 MEM. CONT & TIMING MODULE			1	1	1	1	-	-	1	1	1	1	
34	D-CS-M8149-Ø-1	M8149 MEM. TRANCEIVER CARD			1	1	1	1	-	-	1	1	1	1	
35	9006077-3	SCR, PHL HD TRUSS #10-32 X 1.00			-	-	2	2	-	-	-	-	-	-	
36	B-MD-7413664-0-0	BRKT. LOCKING			-	-	2	2	-	-	-	-	-	-	
37	9006634	WASH, INT. TOOTH #8			1	1	1	1	-	-	1	1	1	1	
38	9006418-1	SCR PH HD PAN SELF TAP #8-32 x .38			1	1	1	1	-	-	1	1	1	1	
39	9006658	WASH. FLAT #6			12	12	12	12	-	-	12	12	12	12	
40	D-AD-7011027-0-0	CABLE, TROUGH ASSY			-	-	2	2	-	-	-	-	-	-	
41	3612425-02	DECAL, CABINET DESIGNATION			-	-	1	1	-	-	-	-	-	-	
42	3612423-00	MJ11 MEMORY STICKER			1	1	1	1	-	-	1	1	1	1	
43	9006565	NUT KEPS #10-32			2	2	-	-	-	-	2	2	2	2	
TITLE UNIT ASSY. MJ11-A		ASSY NO. E-AD-MJ11-A-Ø		SIZE CODE A PL	NUMBER MJ11-A-Ø			REV. D			ECO NO.				
SHEET 2 OF 3		DIST.													

DEC FORM DEC 16-(325)-1031-N870
DRA 110



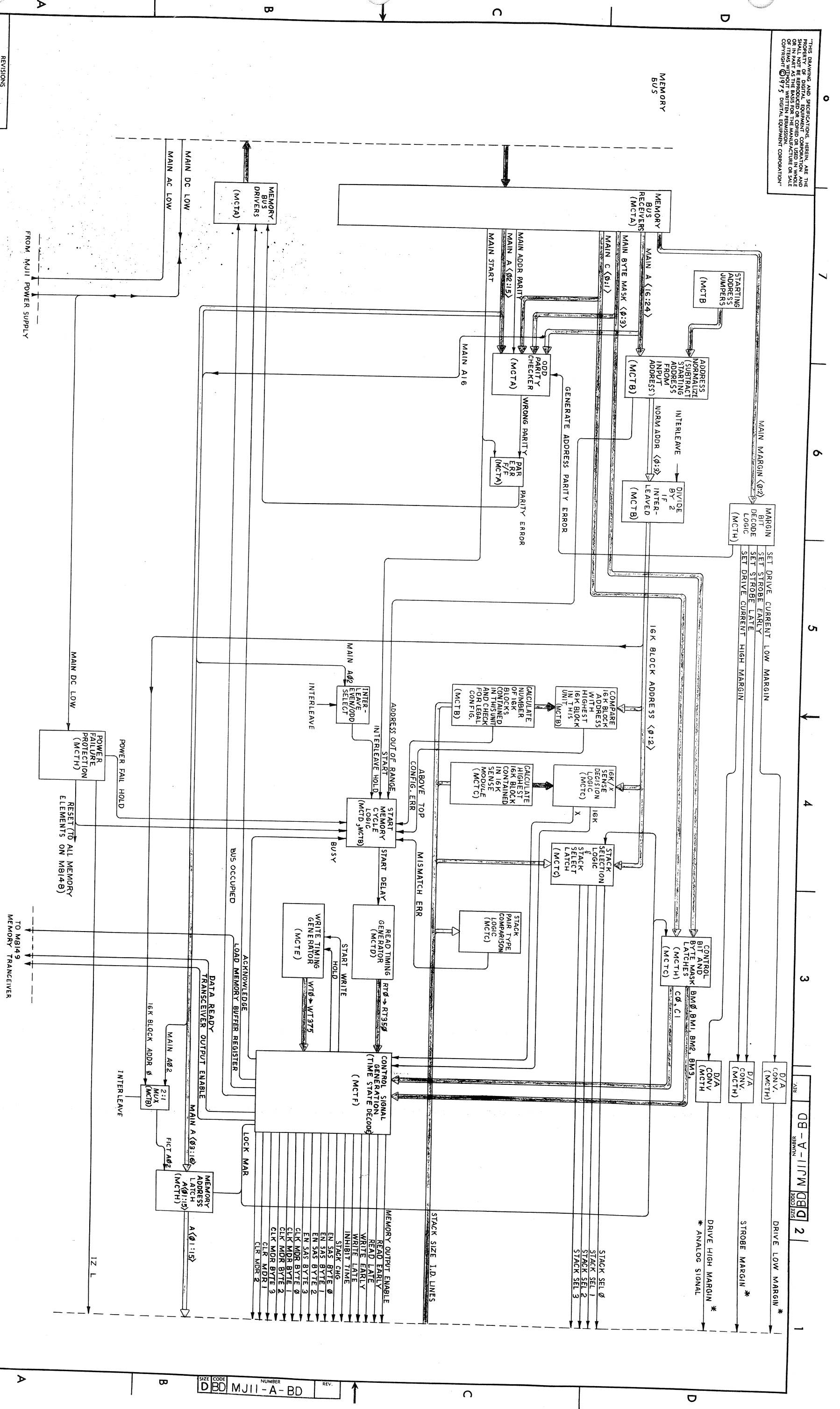
MEMORY BUS NAME		INTERNAL BUS NAME (MHI BACK PANEL)
MAIN DATA BYTE 0-0	LOW BUS D00	
MAIN DATA BYTE 0-1	LOW BUS D01	
MAIN DATA BYTE 0-2	LOW BUS D02	
MAIN DATA BYTE 0-3	LOW BUS D03	
MAIN DATA BYTE 0-4	LOW BUS D04	
MAIN DATA BYTE 0-5	LOW BUS D05	
MAIN DATA BYTE 0-6	LOW BUS D06	
MAIN DATA BYTE 0-7	LOW BUS D07	
MAIN DATA BYTE 0-8	LOW BUS D08	
MAIN DATA BYTE 0-9	LOW BUS D09	
MAIN DATA BYTE 1-0	LOW BUS D10	
MAIN DATA BYTE 1-1	LOW BUS D11	
MAIN DATA BYTE 1-2	LOW BUS D12	
MAIN DATA BYTE 1-3	LOW BUS D13	
MAIN DATA BYTE 1-4	LOW BUS D14	
MAIN DATA BYTE 1-5	LOW BUS D15	
MAIN DATA BYTE 1-6	LOW BUS D16	
MAIN DATA BYTE 1-7	LOW BUS D17	
MAIN DATA BYTE 1-8	LOW BUS D18	
MAIN DATA BYTE 1-9	LOW BUS D19	
MAIN DATA BYTE 2-0	LOW BUS D20	
MAIN DATA BYTE 2-1	LOW BUS D21	
MAIN DATA BYTE 2-2	LOW BUS D22	
MAIN DATA BYTE 2-3	LOW BUS D23	
MAIN DATA BYTE 2-4	LOW BUS D24	
MAIN DATA BYTE 2-5	LOW BUS D25	
MAIN DATA BYTE 2-6	LOW BUS D26	
MAIN DATA BYTE 2-7	LOW BUS D27	
MAIN DATA BYTE 2-8	LOW BUS D28	
MAIN DATA BYTE 2-9	LOW BUS D29	
MAIN DATA BYTE 3-0	LOW BUS D30	
MAIN DATA BYTE 3-1	LOW BUS D31	
MAIN DATA BYTE 3-2	LOW BUS D32	
MAIN DATA BYTE 3-3	LOW BUS D33	
MAIN DATA BYTE 3-4	LOW BUS D34	
MAIN DATA BYTE 3-5	LOW BUS D35	
MAIN DATA BYTE 3-6	LOW BUS D36	
MAIN DATA BYTE 3-7	LOW BUS D37	
MAIN DATA BYTE 3-8	LOW BUS D38	
MAIN DATA BYTE 3-9	LOW BUS D39	

- TO THE INTERNAL BUS SIGNAL NAME TO SIGNAL NAME ON g14_CIRCUIT SCHEMATIC (1) LOCATE DATA BIT ELECTRONICS ON SENSE/INHIBIT
- (1) DETERMINE WHETHER DATA BIT IS IN HIGH OR LOW WORD
- (2) DETERMINE WHICH "STACK" BIT IS IN.
- (3) TO LOCATE BIT LOGIC, DELETE "H" OR "L" PREFIX FROM INTERNAL BUS SIGNAL NAME (E.G. LOW BUS D09 BECOMES BUS D09)
- AGREES WITH g14_CIRCUIT SCHEMATIC.
- IF NOT, PROCEED TO STEP 4.
- IF YES, PROCEED TO STEP 5.
- IF YES, PROCEED TO STEP 6.
- IF YES, PROCEED TO STEP 7.
- IF YES, PROCEED TO STEP 8.
- IF YES, PROCEED TO STEP 9.
- IF YES, PROCEED TO STEP 10.
- IF YES, PROCEED TO STEP 11.
- IF YES, PROCEED TO STEP 12.
- IF YES, PROCEED TO STEP 13.
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- IF YES, PROCEED TO STEP 97.
- IF YES, PROCEED TO STEP 98.
- IF YES, PROCEED TO STEP 99.
- IF YES, PROCEED TO STEP 100.

[illegible]

FIRST USED ON OPTION/MODEL		QTY.		DESCRIPTION		PART NO.		NEW	
M/JII				PARTS LIST					
DIMENSIONAL TOLERANCE				DATE					
DIMENSIONS ARE MILLIMETERS				DATE					
UNLESS OTHERWISE SPECIFIED				DATE					
MILLIMETERS		INCHES		ANGLES					
XXX = ±0.10 XX = ±0.05 X = ±0.02 X = ±0.01		XXX = ±.005 XX = ±.002 X = ±.001		±0° 30'					
THIRD ANGLE PROJECTION				REMOVE BURS AND BREAK SHARP CORNERS SURFACE QUALITY		✓			
MATERIAL				FINISH					
FINISH				SCALE					
B - DD - M/JII - A - 0				SHEET 1 OF 2					
NEXT HIGHER ASSY.				DATE					
TITLE				DATE					
BLOCK DIAGRAM				DATE					
M/JII				DATE					
SIZE CODE				NUMBER		REV.			
D BD				M/JII - A - BD					

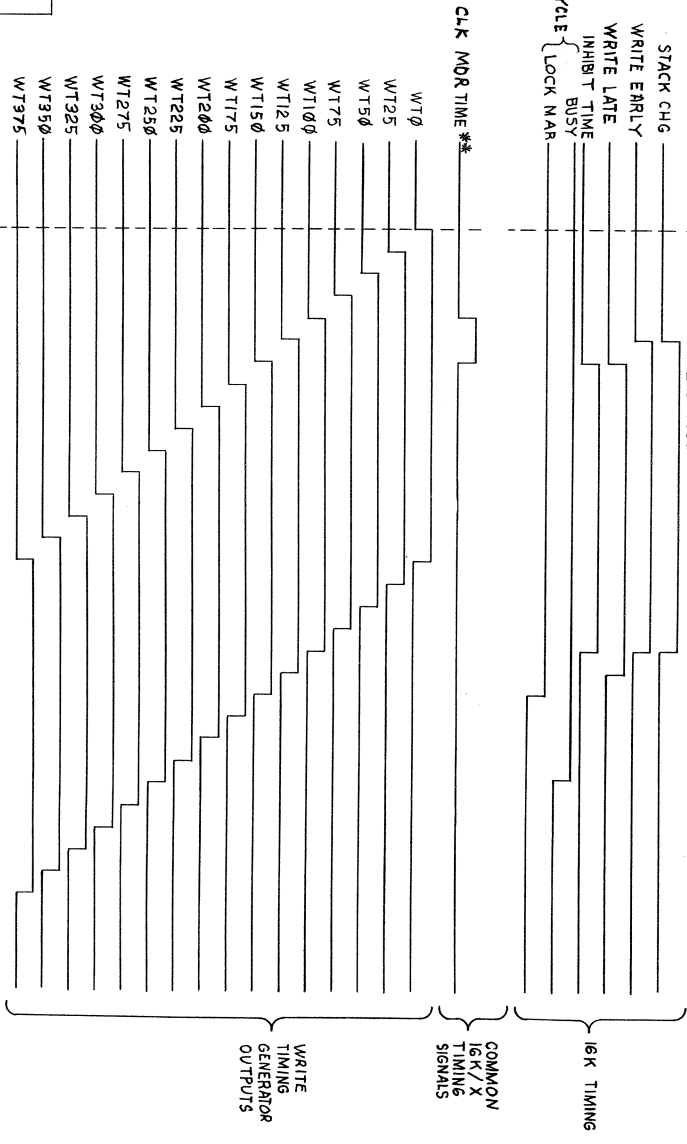
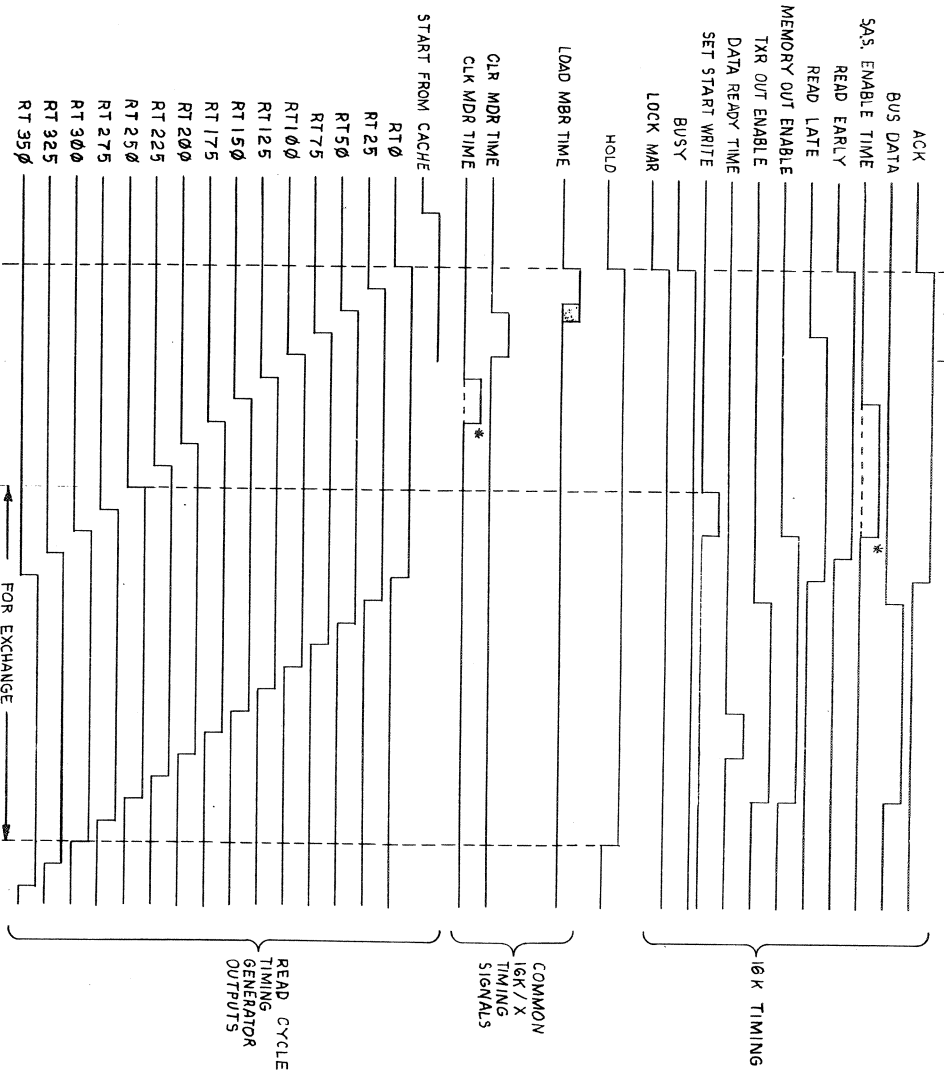
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REVISIONS	
CHK	CHANGE NO.

TITLE: MJ11-A-BD
BLOCK DIAGRAM (MR148)
SIZE CODE: DBD
NUMBER: A-BD
REV: 1

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* WRITE CYCLE ONLY - CLK MDR FOR BYTES SELECTED BY BYTE MASK BITS
- DISABLE SAS FOR BYTES SELECTED BY BYTE MASK BITS
** EXCHANGE CYCLE ONLY - CLK MDR FOR BYTES SELECTED BY BYTE MASK BITS

REVISIONS	
REV.	CHANGE NO.

CHK	

REV. DT - A - 111W DTD 2

REV. DTD MJ11-A-TD

MEMORY SYSTEM TIMING (WRITE CYCLE)

MEMORY SYSTEM TIMING (READ CYCLE)

FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
MJ11					
DIMENSIONAL TOLERANCE		DATE			
DIMENSIONS ARE MILLIMETERS UNLESS OTHERWISE SPECIFIED		2/17/75			
MILLIMETERS	INCHES	ANGLES	DATE		
XXX ± 0.10	XXX ± .005	50° 30'	2/17/75		
XX ± .05	XX ± .02		DATE		
X ± .25	X ± .1		2/17/75		
THIRD ANGLE PROJECTION		DATE			
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY V		2/17/75			
MATERIAL		TITLE			
FINISH		MJ11 MEMORY TIMING			
SCALE		SIZE CODE			
SHEET 1 OF 1		D TD MJ11-A-TD			

NOTES:



1	E10	IC DEC 75453	1911036	62
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MJ11	ET
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RESPECTIVELY EXCEPTIONS ARE STATED ABOVE

ALL C2,T1 PINS

$$+2\phi V \quad \frac{FV2}{\quad}$$

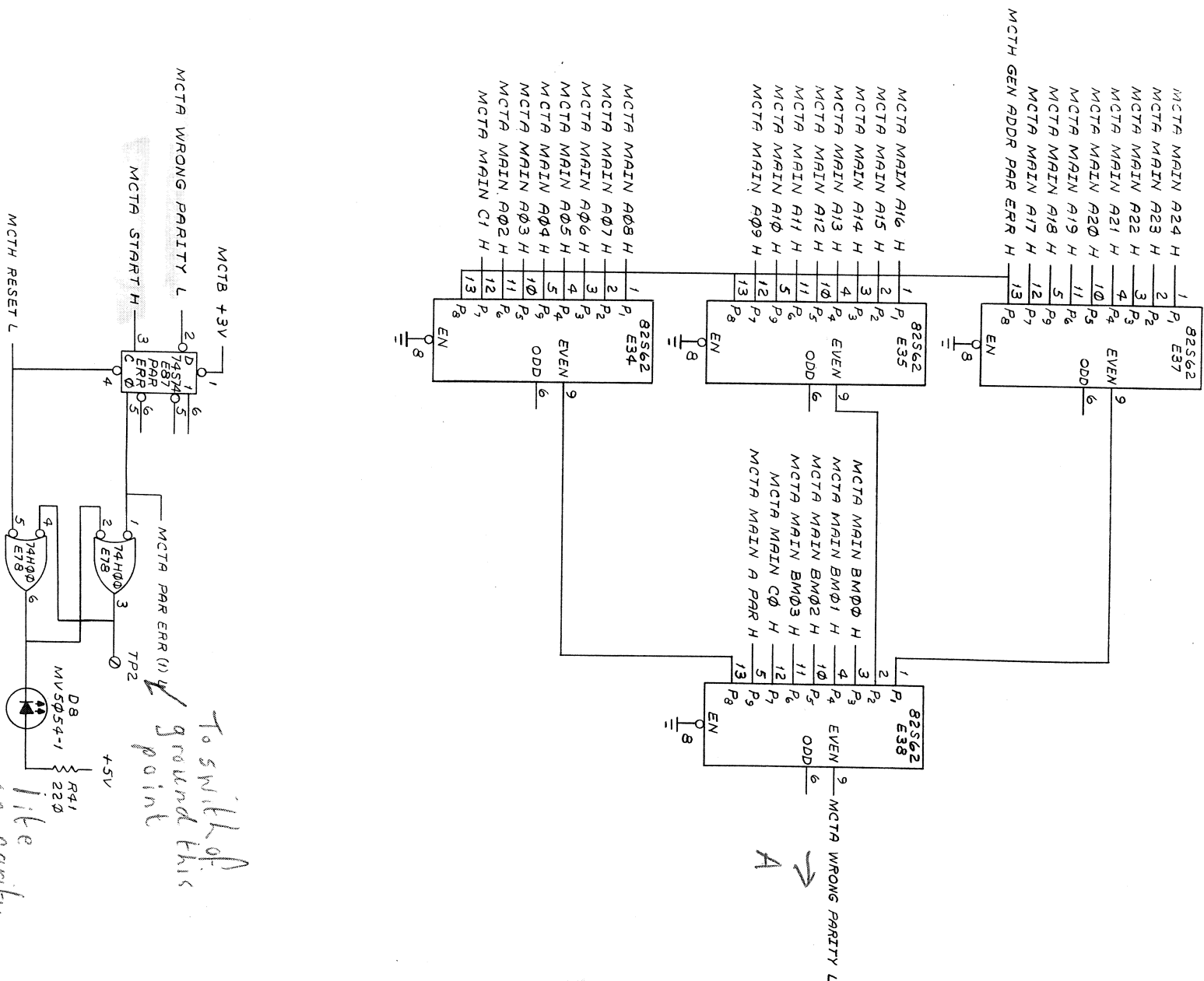
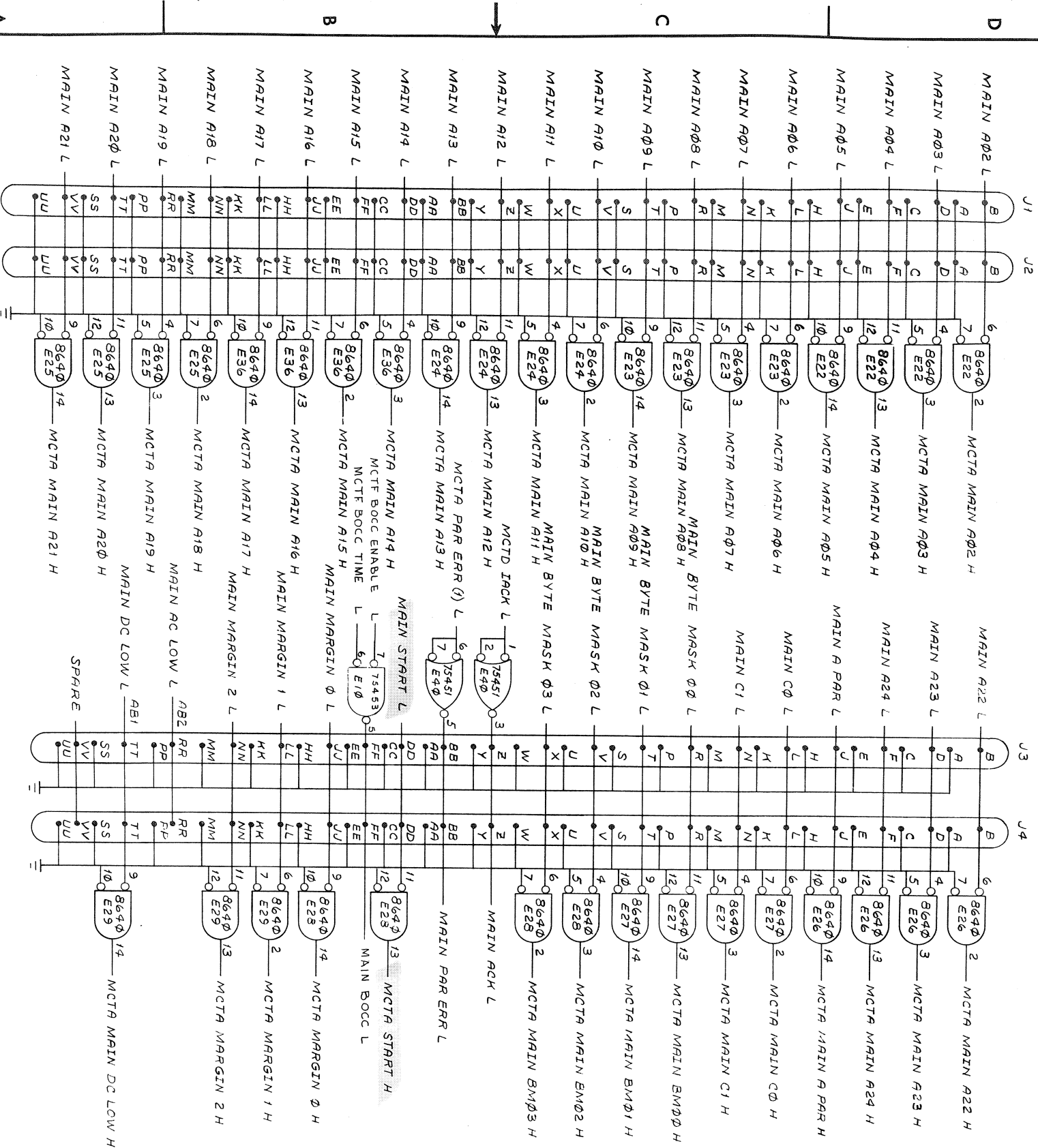
7	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100	101	102	103	104	105	106	107	108	109	110	111	112	113	114	115	116	117	118	119	120	121	122	123	124	125	126	127	128	129	130	131	132	133	134	135	136	137	138	139	140	141	142	143	144	145	146	147	148	149	150	151	152	153	154	155	156	157	158	159	160	161	162	163	164	165	166	167	168	169	170	171	172	173	174	175	176	177	178	179	180	181	182	183	184	185	186	187	188	189	190	191	192	193	194	195	196	197	198	199	200	201	202	203	204	205	206	207	208	209	210	211	212	213	214	215	216	217	218	219	220	221	222	223	224	225	226	227	228	229	230	231	232	233	234	235	236	237	238	239	240	241	242	243	244	245	246	247	248	249	250	251	252	253	254	255	256	257	258	259	260	261	262	263	264	265	266	267	268	269	270	271	272	273	274	275	276	277	278	279	280	281	282	283	284	285	286	287	288	289	290	291	292	293	294	295	296	297	298	299	300	301	302	303	304	305	306	307	308	309	310	311	312	313	314	315	316	317	318	319	320	321	322	323	324	325	326	327	328	329	330	331	332	333	334	335	336	337	338	339	340	341	342	343	344	345	346	347	348	349	350	351	352	353	354	355	356	357	358	359	360	361	362	363	364	365	366	367	368	369	370	371	372	373	374	375	376	377	378	379	380	381	382	383	384	385	386	387	388	389	390	391	392	393	394	395	396	397	398	399	400	401	402	403	404	405	406	407	408	409	410	411	412	413	414	415	416	417	418	419	420	421	422	423	424	425	426	427	428	429	430	431	432	433	434	435	436	437	438	439	440	441	442	443	444	445	446	447	448	449	450	451	452	453	454	455	456	457	458	459	460	461	462	463	464	465	466	467	468	469	470	471	472	4
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SEMICONDUCTOR CONVERSION CHART

SCALE	—H—	D	CS	M8148	-	Ø	-	1	C
HEIGHT	1 OF 2	DIST							

§ TIMING (MCT)

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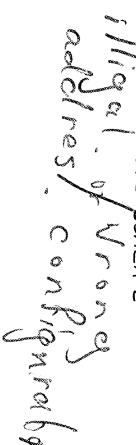
MEMORY BUS INTERFACE AND ADDRESS PARITY CHECKER

REVISIONS	
CHANGE NO.	REV.

TITLE		SIZE		NUMBER	
MEMORY CONTROL AND ADDRESS PARITY CHECKER		D		CS	
TIMING (MCTA)		D		CS	
SCALE		1/4"		SHEET 2 OF 9	

life on parity error.

To switch of ground this point.

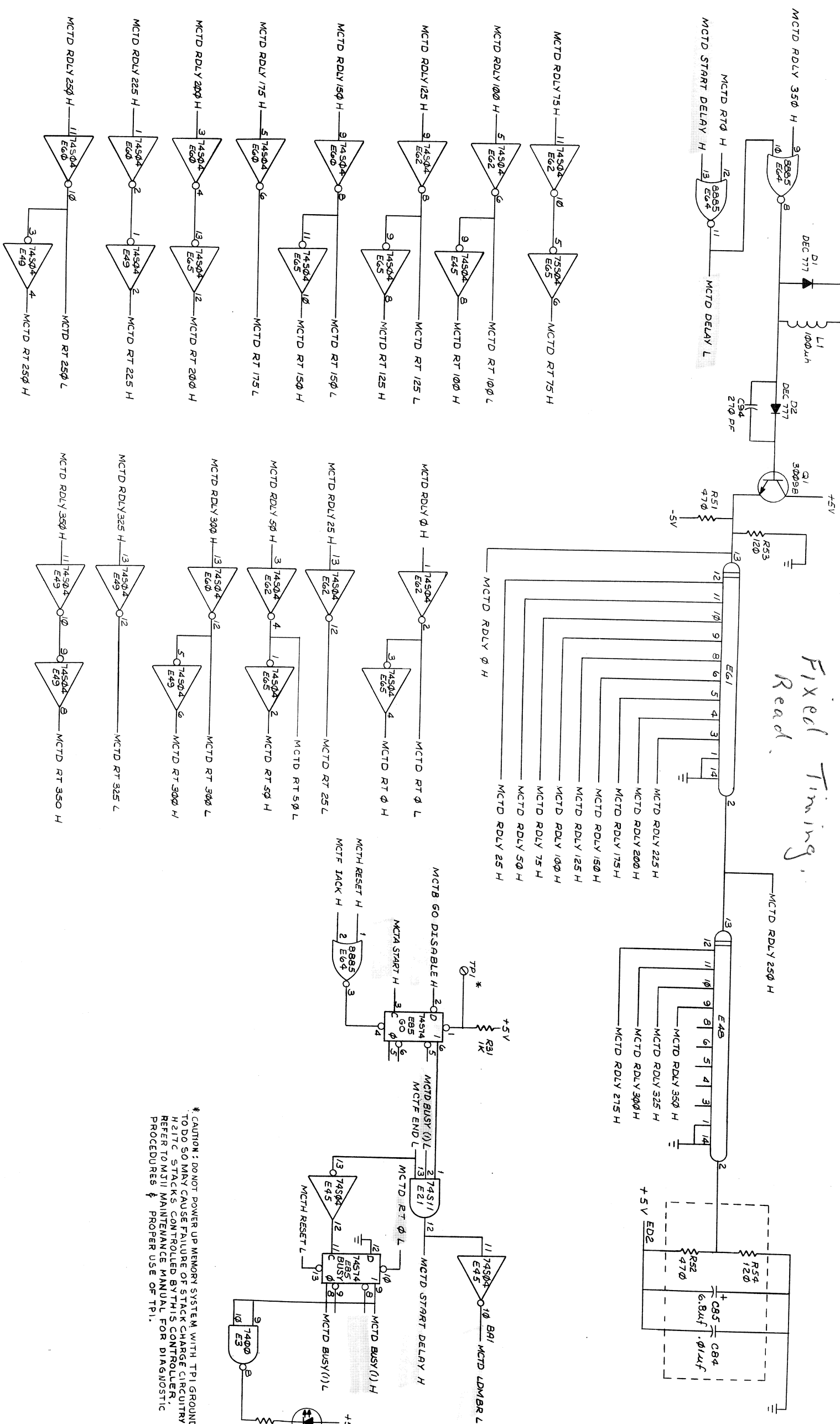


* SWITCHES<SI-1>:S2-2> DEFINE THE UPPER 9 BITS OF THE STARTING ADDRESS OF THE MEMORY CONTROLLER SWITCH CLOSED = 0 SWITCH OPEN = 1

** SEE EQUIVALENCY TABLE ON MCTC FOR SIGNAL SOURCE

	INTERLEAVE	MPXR FUNCTION
H	NORM ADDR <1:3> → 16K BLK ADDR <Φ:2> NORM ADDR Φ → FICT Φ02	
L	NORM ADDR <Φ:2> → 16K BLK ADDR <Φ:2> MAIN Φ02 → FICT Φ02	

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* CAUTION: DO NOT POWER UP MEMORY SYSTEM WITH TPI GROUNDING. TO DO SO MAY CAUSE FAILURE OF STACK CHARGE CIRCUITRY ON HZTC STACKS CONTROLLED BY THIS CONTROLLER. REFER TO M311 MAINTENANCE MANUAL FOR DIAGNOSTIC PROCEDURES & PROPER USE OF TPI.

like M8148-0-1

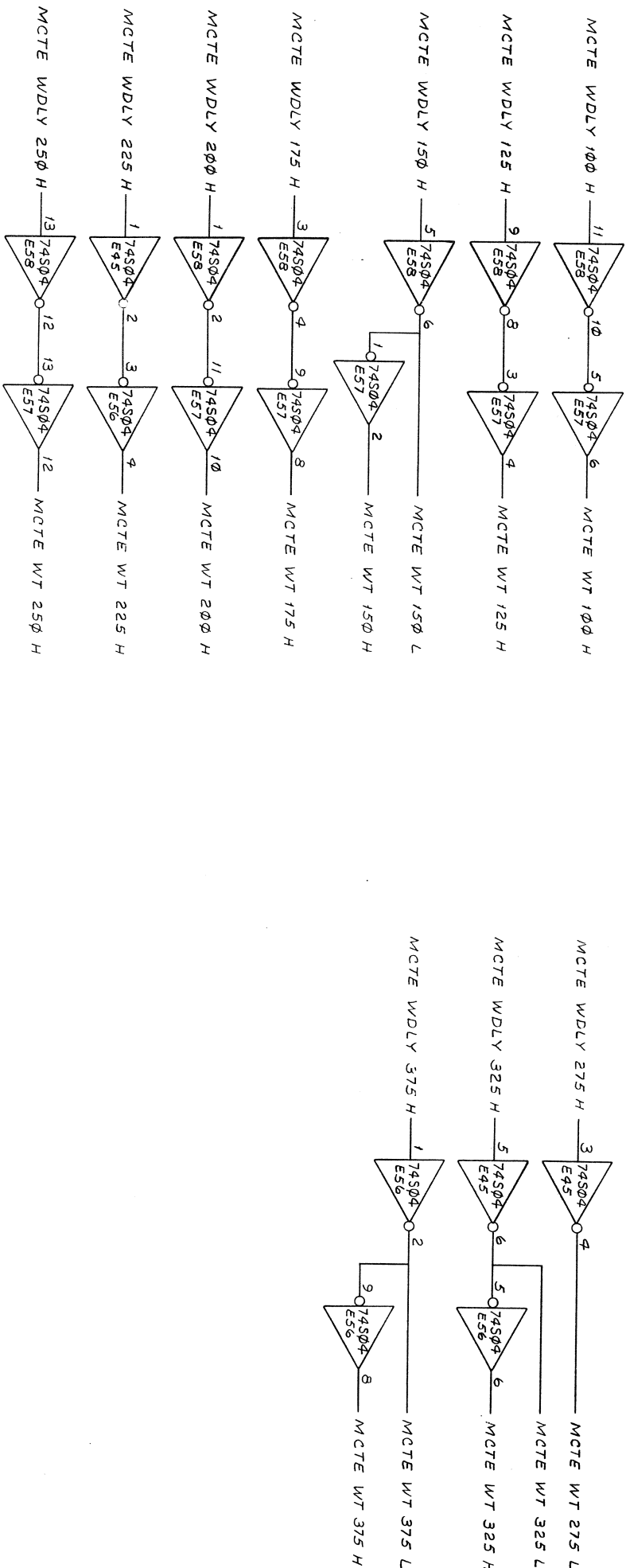
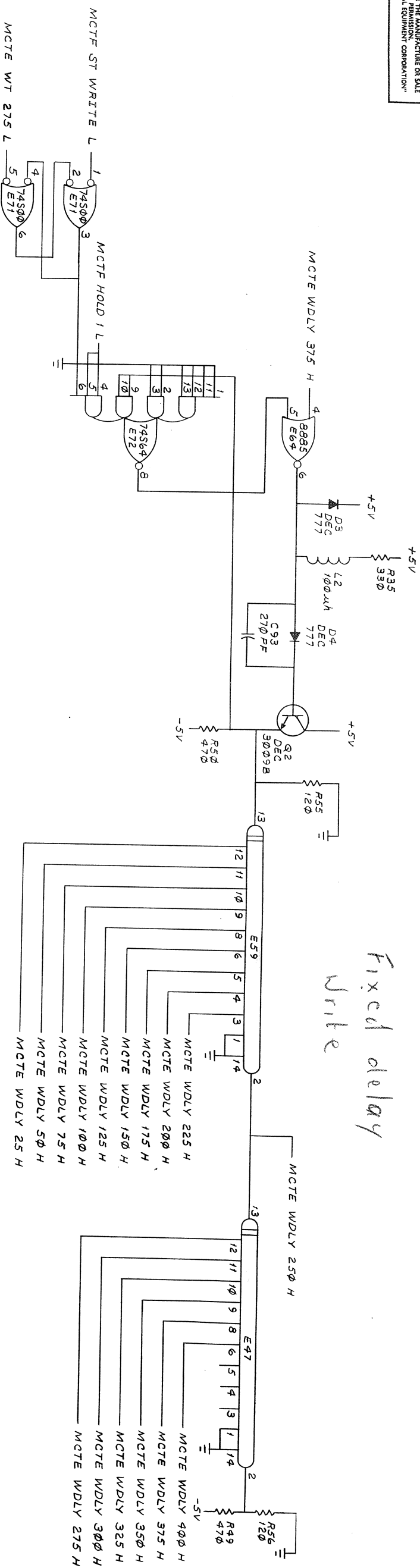
READ TIMING GENERATOR

TITLE	SIZE/CODE	NUMBER	REV.
MEMORY CONTROL AND TIMING (MCTD)	D	CS M8148-0-1	C

SCALE	SHEET	OF	1
1:1	5	9	1

REVISIONS	CHK	CHANGE NO.	REV.

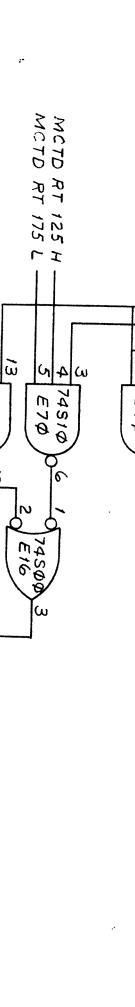
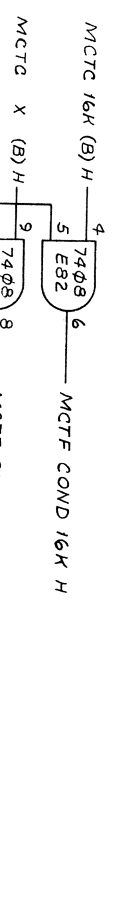
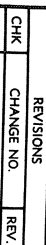
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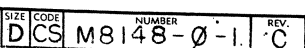


REVISIONS	
CHK	CHANGE NO.

TITLE	
MEMORY CONTROL AND TIMING (MCTE)	
SIZE CODE	NUMBER
D CS	M8148-0-1
DIST	REV.

SHEET	
2 OF 9	
SCALE	





TITLE	SIZE CODE	NUMBER	REV.
MEMORY CONTROL AND TIMING (MCTH)	D CS	M8148-0-1	C
SCALE ————	SHEET 8 OF 9	DIST.	

2	SIZE CODE	NUMBER	REV.
D	CS	M 148-1	C

NOTE: ALL OTHER COMBINATIONS OF ROW INPUTS RESULT IN ALL OUTPUT PINS LOW

ROM OUTPUTS

ROM INPUTS

[illegible]

TRUTH TABLE FOR MEM TOP LOOKUP ROM [E3 - MCTB]

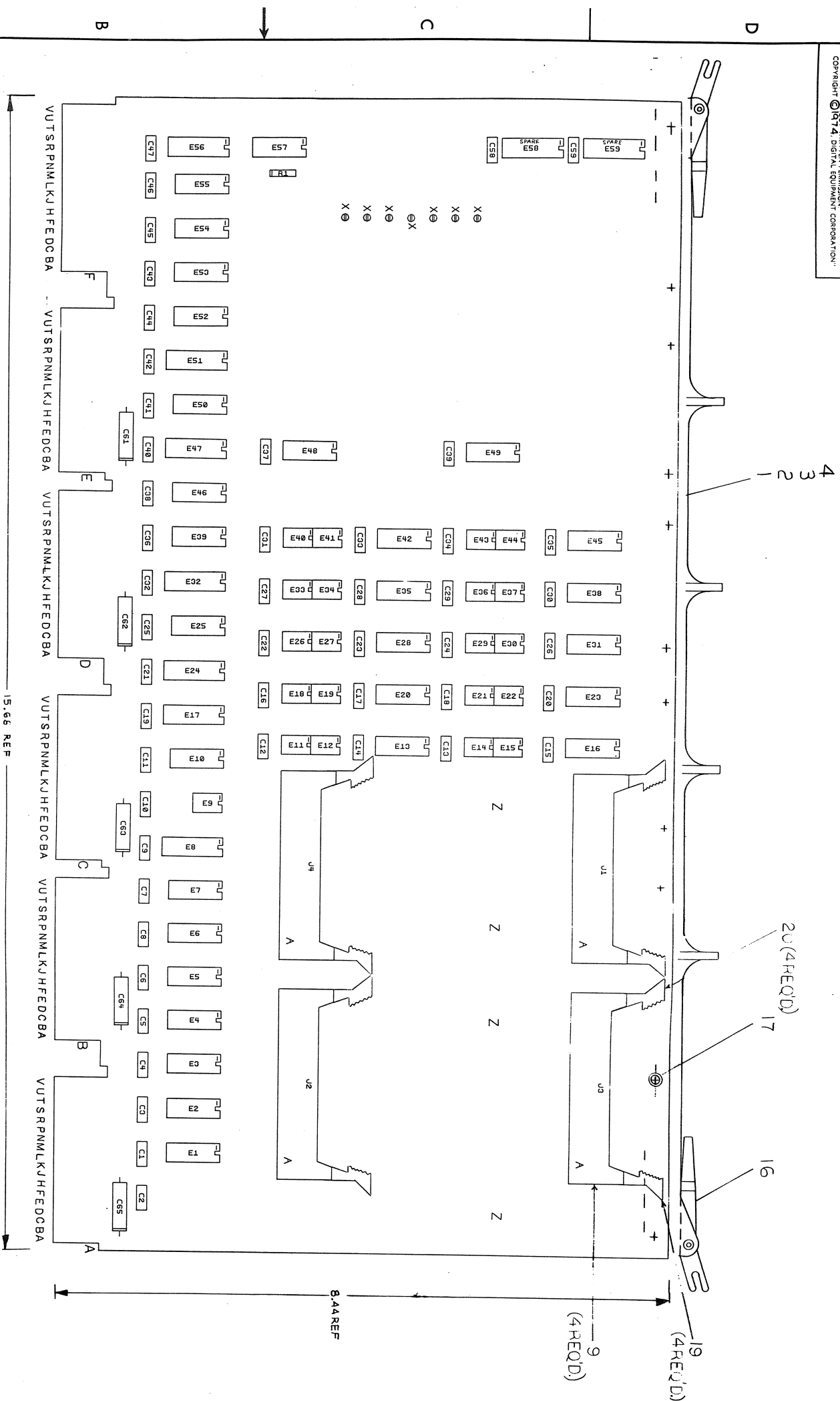
REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		SIZE CODE		NUMBER		REV	
MEMORY CONTROL AND							
TIMING							
D		CS		M6148-Ø-1		C	
SCALE		1/1		SHEET		9 OF 3	
DIST.							

SIZE D	CODE CS	NUMBER M8148-0-1	REV. C
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NOTES: C58 & C59 ARE SPARES



IC DEC 75453	4	8
IC DEC 8640	1	8
IC DEC 75452	4	8
IC DEC 745174	8	16
IC TYPE	GND	+ 5V

GND AND 5V ARE USUALLY PIN 7 AND 14
RESPECTIVELY. EXCEPTIONS ARE STATED ABOVE

IC PARTS LISTED IN ORDER OF INCREASING PRICE

AA2, BA2, CA2
DA2, EA2

+ 5V

GND

C58-C62
6.8uF

C1-C47
.01uF

<i>Foley & Clark</i>		<i>12/14/75</i>	
E. CLARK			
<i>P.O. #2869045</i> <i>9-03-75</i>			
M8149-00003		C	
<i>Thomas A. Northrup</i> <i>3/21/75</i>			
T. NORTHROP			
<i>J. Kirkland</i> <i>1-24-79</i>			
M	M8149-00002	B	
<i>Thomas A. Northrup</i> <i>1/16/75</i>			
T. NORTHROP			
<i>J. Kirkland</i> <i>1-15-75</i>			
M	M8149-00001	A	
CHK	CHANGE NO.	REV	

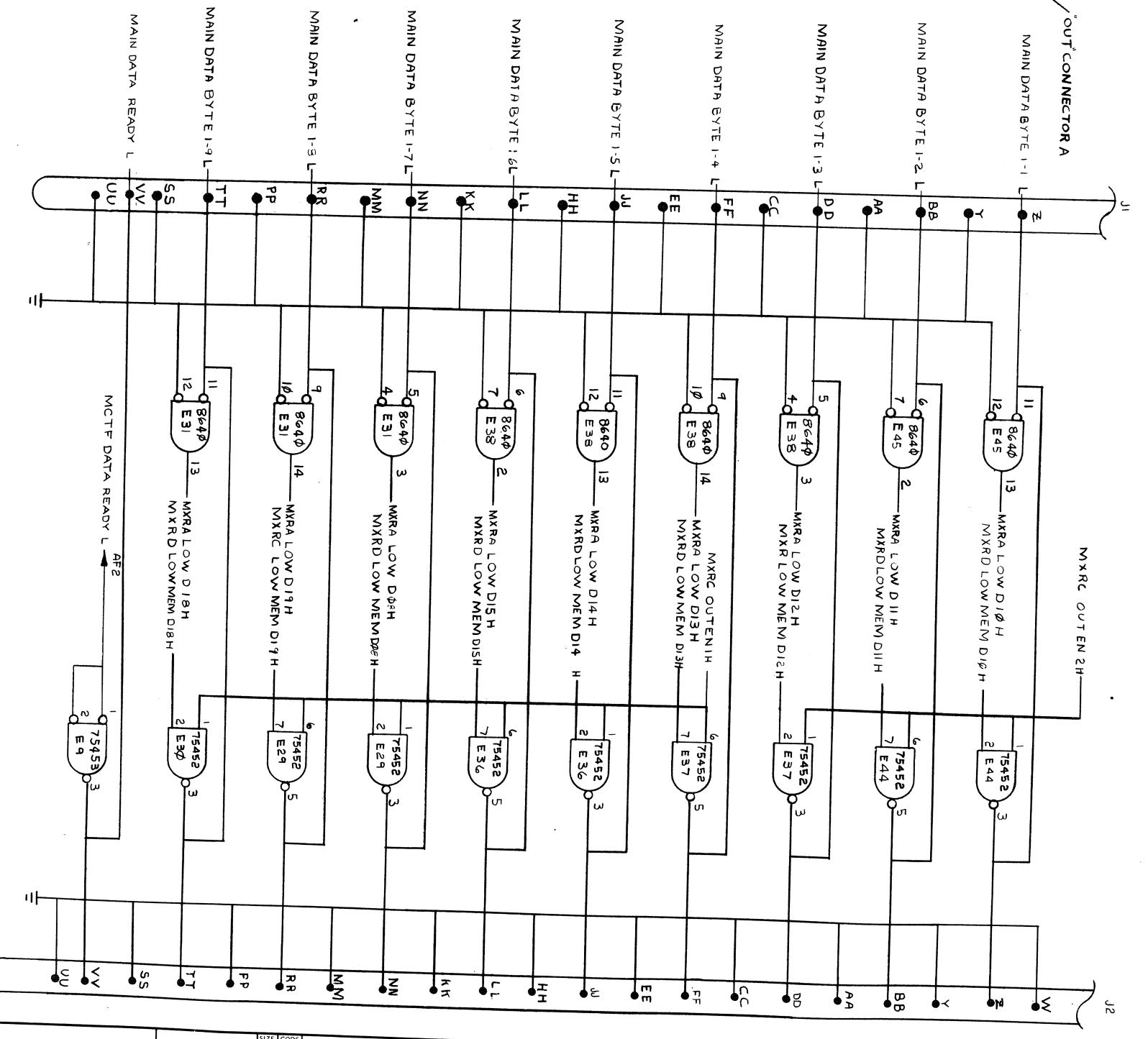
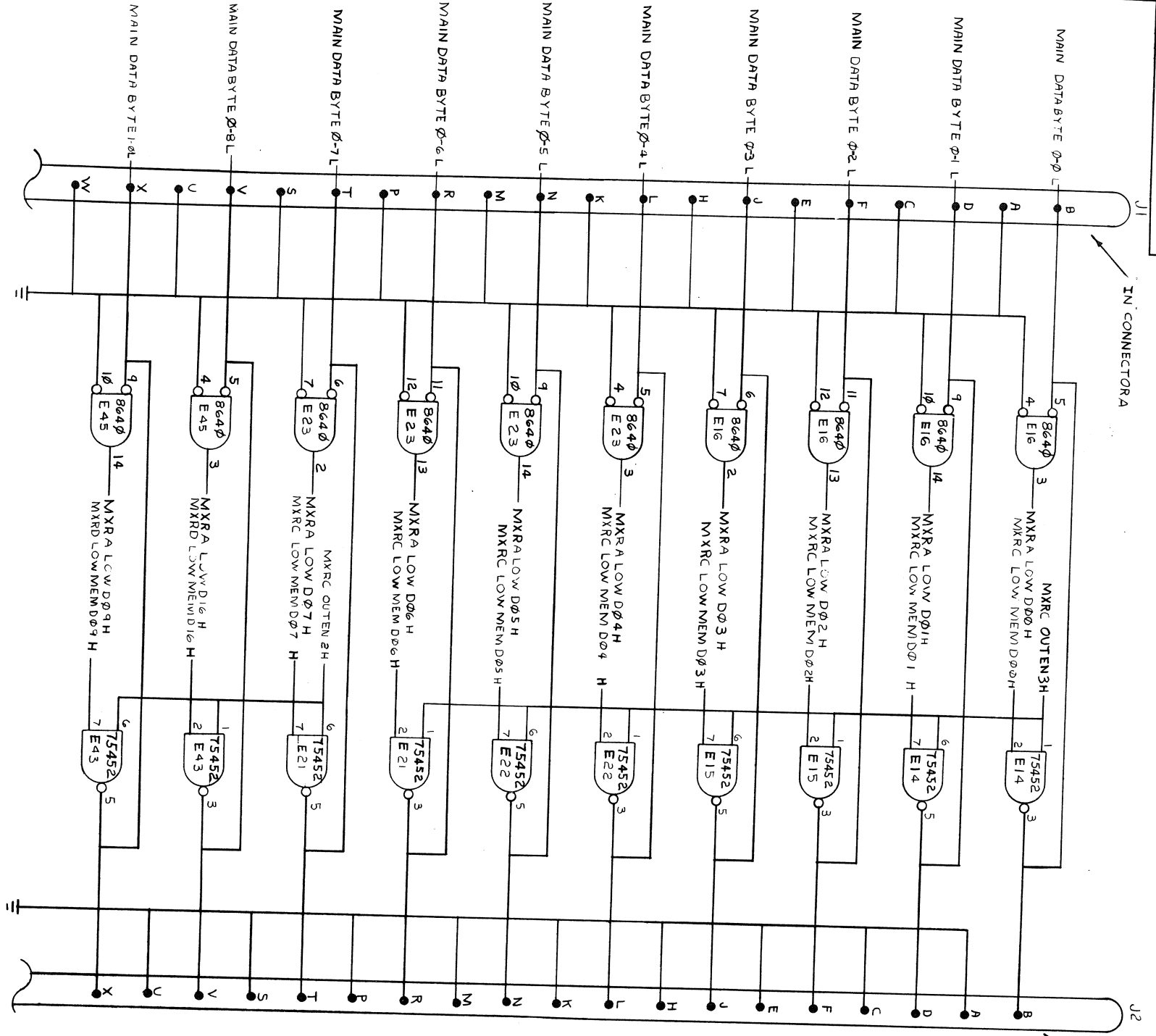
REVISIONS	
DEC NO.	EIA NO.

SEMICONDUCTOR C

[illegible]

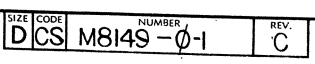
REF	X-Y COORDINATE	HOLE LOCATION	K-CO-MB149-Ø-4
REF	ASSY/DRILL HOLE LAYOUT		D-AH-MB149-Ø-5
REP	MODULE ECO HISTORY		B-MH-MB149-Ø-5
1	ETCHED CIRCUIT BOARD		501290
5	C61-C65	CAP 6.8 MFD 35V 10%	1005306
47	C1-CAT	CAP .01 MFD 100V	1001610-01
1	R1	RES 1K 1/4W 5%	1300365
7	SPLIT LUG		9306735
4	U1 THRU U4	RT. ANGLE HEADER, 40 PIN	1209441-02
7	E01E1/E24,E32,E47,E51,E56	IC DEC. 745174	1410550
10	E2,E3,B5,E6,E25,E 39, E50	IC DEC.74H01-1	1409849
	E52,E54,E57		
E1,E12,E14,E15,E18,E19,E21,E22			
20	E26,E27,E29,E30,E33,E34,E36	IC DEC. 75452	1410645
	E37,E40,E41,E43,E44		
9	E1,E4,E7,E0E46,E48,E49,E55,E53	IC DEC. 745Ø4	1410534
10	E13,E16,E20,E23,E28,E31,E35	IC DEC. C. Ø640	1411469
	E38,E42,E45		
1	E9	IC DEC. C. 75453	1411036
1		HANDLE ASSY	1410711-02
12		EYELETS, GS-47	9006732
4		RT. ANGLE HEADER 40 PIN LATCH LEFT	1209341-03
4		RT. ANGLE HEADER 40 PIN LATCH RIGHT	1209341-04

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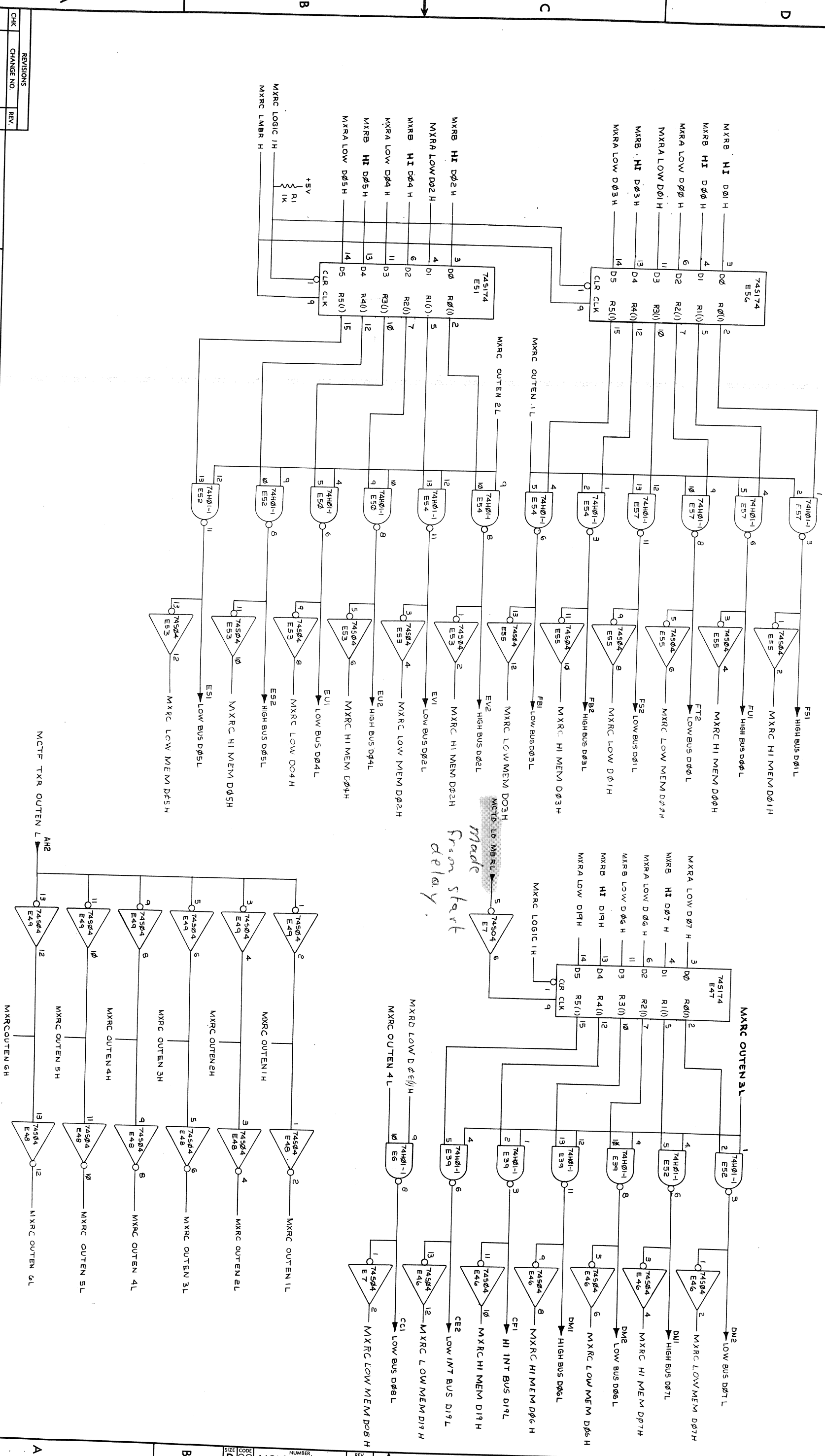
REVISIONS	
CHK	CHANGE NO

8	7	6	5	4	3	2	1
---	---	---	---	---	---	---	---



TITLE	SIZE CODE	NUMBER	REV
MEMORY (MXRB) TRANSCIVER CARD	D C5	M8149-01	C
SCALE $\frac{1}{4}$ - $\frac{1}{4}$	SHEET 3	OF 5	DIST.

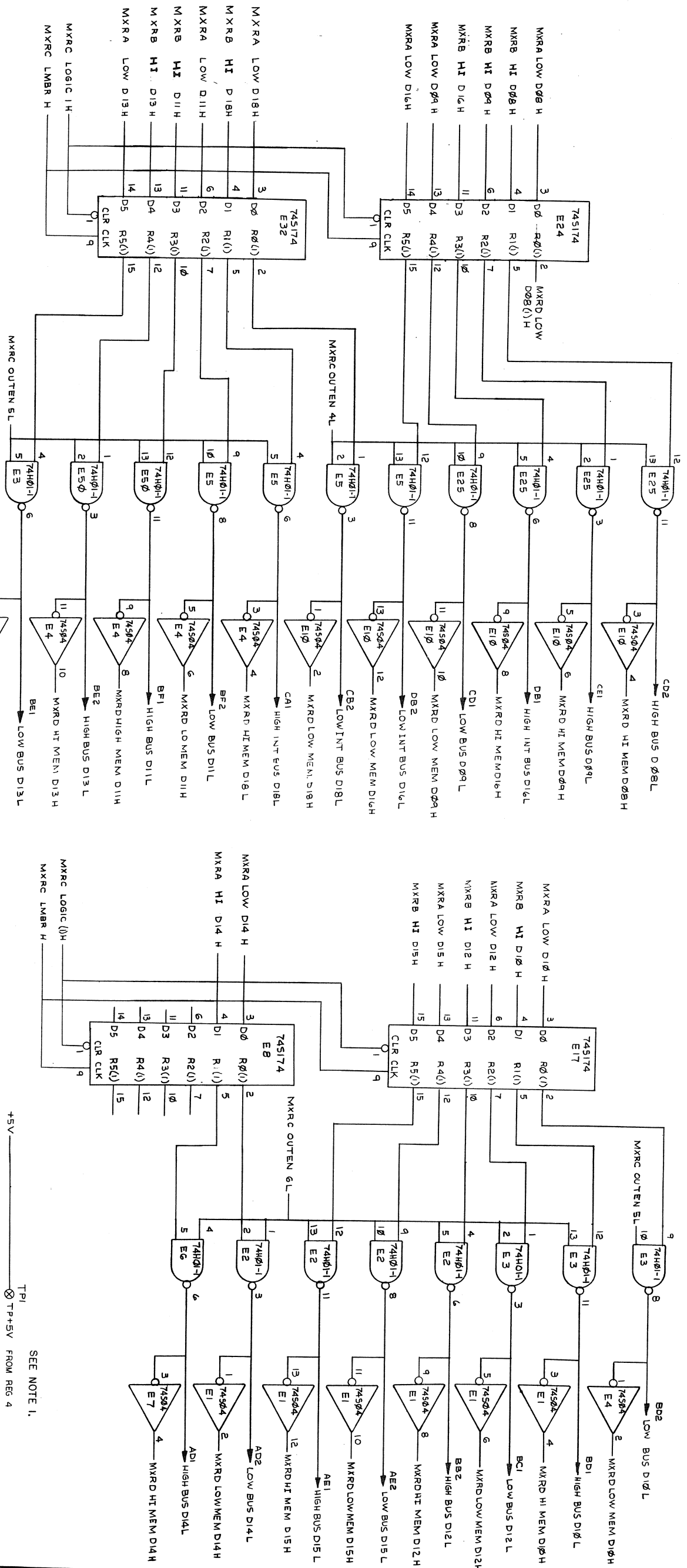
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REVISIONS	
CHANGE NO.	REV.

CHK	8	7	6	5	4	3	2	1
TITLE: MEMORY TRANSCEIVER CARD								
SCALE: 1/4"								
SHEET: 4 OF 5								
DIST:								
NUMBER: MB149-0-1								
REV: C								

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NOTE 1.
REGULATOR NUMBERS ARE
SHOWN ON PRINT
E-AD-7010694-0-0
(POWER SUPPLY M111).



SEE NOTE 1.

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		MEMORY (MXRD)		SIZE CODE		NUMBER		REV.	
TRANSCIVER CARD		D		CS M8149-0-1		C			
SCALE		SHEET		5 OF 5		DIST.			
8		7		6		5		4	
3		2		1					