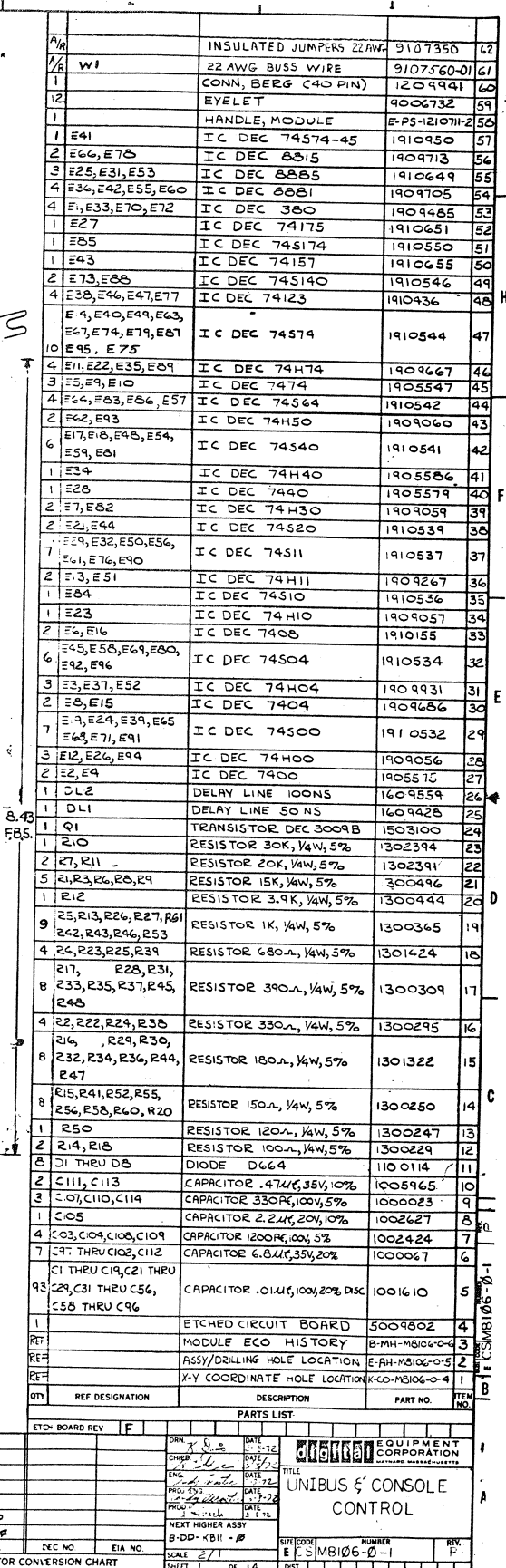




1. UNLESS OTHERWISE NOTED RESISTANCE IS IN Ω AND CAPACITANCE IS IN PICO FARADS. CAPS. WITHOUT VALUE NOTED ARE .01 MFD

2. RESISTOR R19, R49, R51, R54 AND R59 ARE SPARES—NO VALUES ASSIGNED

AA2, AV1, BA2, BV1,
CA2, CV1, DA2, DV1,
EA2, EV1, FA2, FV2

AC2, AN2, AH1, AT1, BC2, BN2,
BH1, BT1, CC2, CN2, CH1, CT1,
DC2, DN2, DH1, DT1, EC2, EN2,
EH1, ET1, FC2, FN2, FH1, FT1

+ 5 V

6.8M Ω

C97 THRU C102, C112

.01 MFD.

C1 THRU

C1 THRU C19, C21 THRU C29,
C31 THRU C56, C58 THRU C90

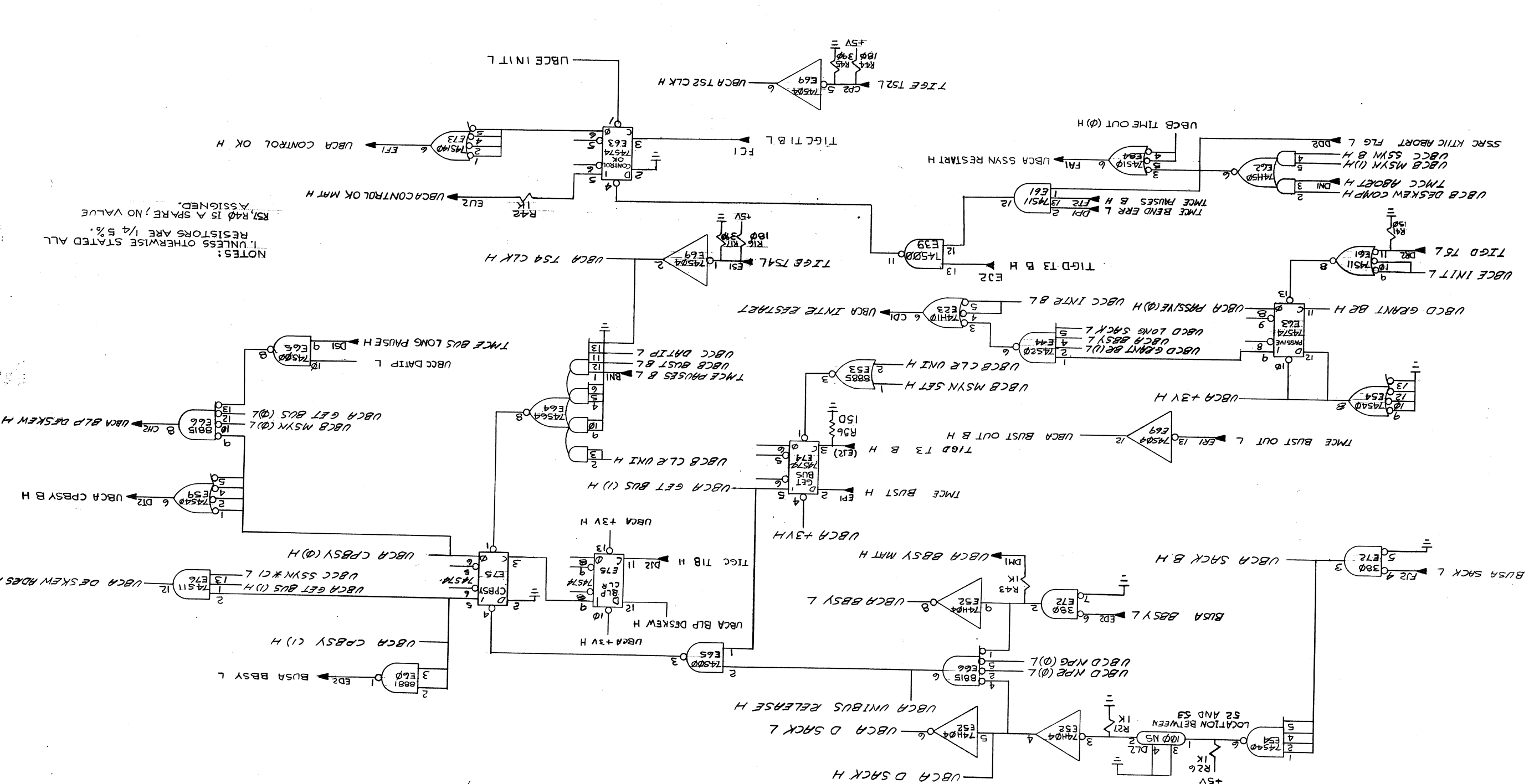
[illegible]

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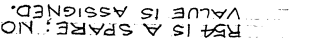
REV	CHANGE NO	REVISIONS

FIRST USED ON OPTION/MODEL		11/45
QTY.	DESCRIPTION	PART NO.
PARTS LIST		
UNLESS OTHERWISE SPECIFIED		
DIMENSION IN INCHES		
TOLERANCES		
DECIMALS		
ANGLES		
X = .1		
XX = .05		
XXX = .02		
REMOVE BURRS AND BREAK SHARP		
CORNERS SURFACE QUALITY		
NEXT HIGHER ASSY.		
B-DD-KB11-0		
SIZE CODE		
DCS		
NUMBER		
M8106-0-1		
REV.		
FINISH		
SCALE		
SHEET 2 OF 14		
DIST.		

CP BUSY, CONTROL OK, TIG RESTARTS

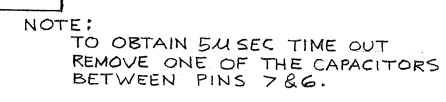


NOTES:
1. UNLESS OTHERWISE STATED ALL RESISTORS ARE 1/4 5%.
2. R57, R40 IS A SPARE, NO VALUE ASSIGNED.



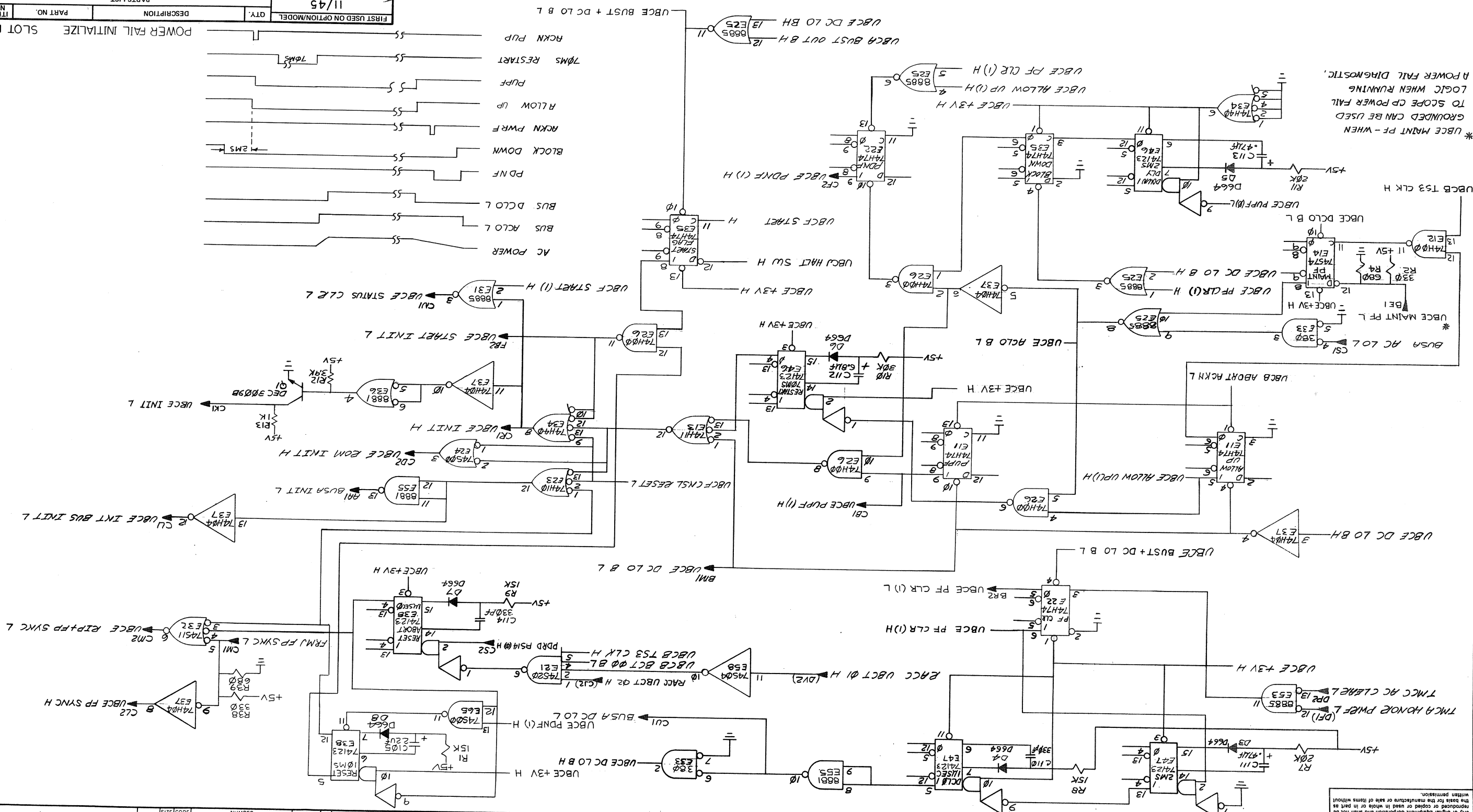
2

3 2E	CODE	NUMBER	REF.
D	CS	M8106-0-1	P

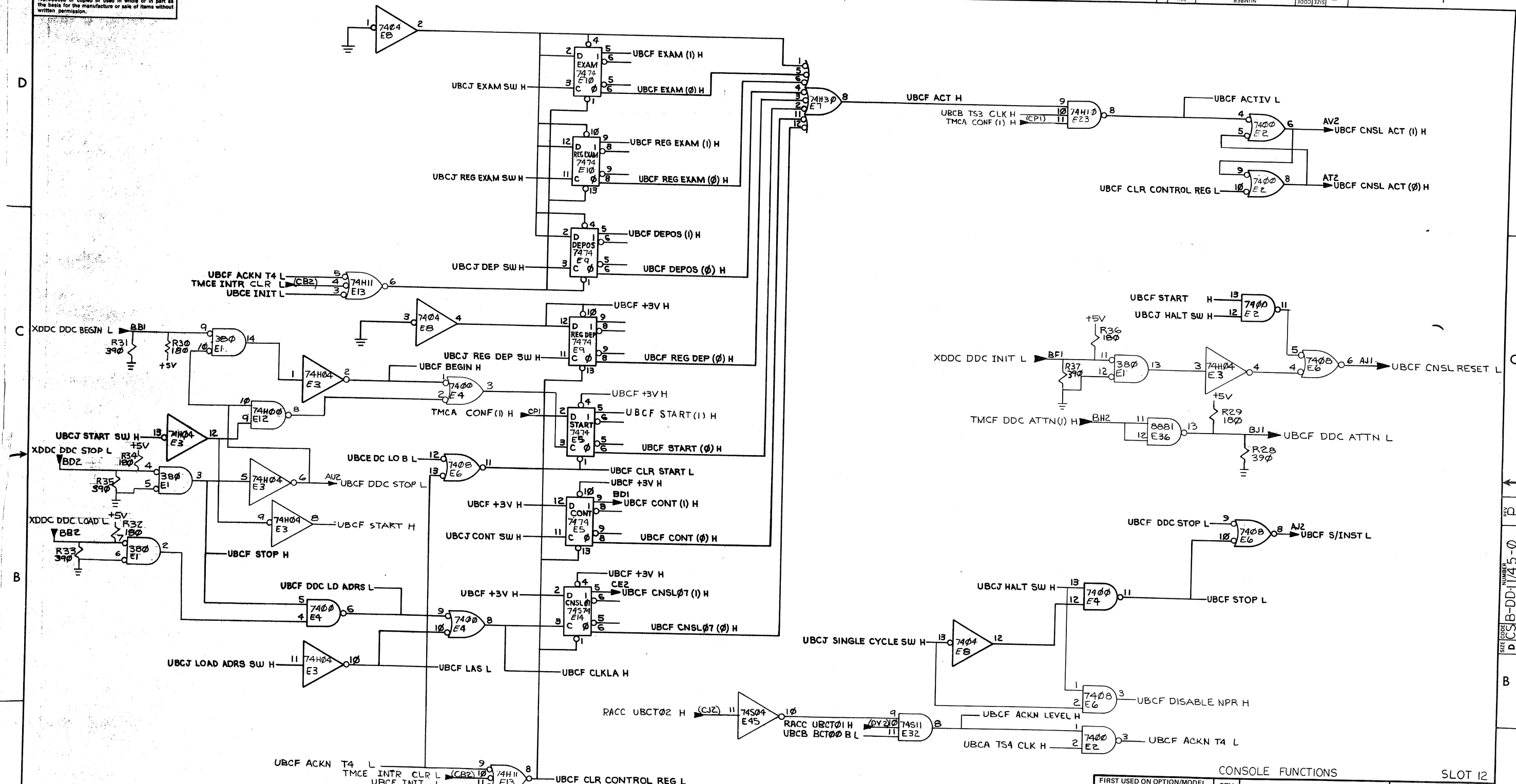


FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
11/45					
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		DRN 2.25 CHRY 3.00 ENG 1.00 PRO 1.00 PROP 1.00	DATE 6/24/77 DATE 3/3/78 DATE 3-3-78 DATE 3-3-78 DATE 3-15-78	PARTS LIST digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS TITLE UNIBUS & CONSOLE CONTROL (UBCD)	
DECIMALS	ANGLES	REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ☒			
.XXX = .005 XX = .02 X = .1	± 0° 30'	MATERIAL + / -			
FINISH + / -		NEXT HIGHER ASSY. B-DD-KB11-0 SCALE SHEET 5 OF 14		SIZE/CODE DCS M8106-0-1	NUMBER REV P

1	2	SIZE	CODE	NUMBER	REV	
	D	CS	M8106-0-1	P		



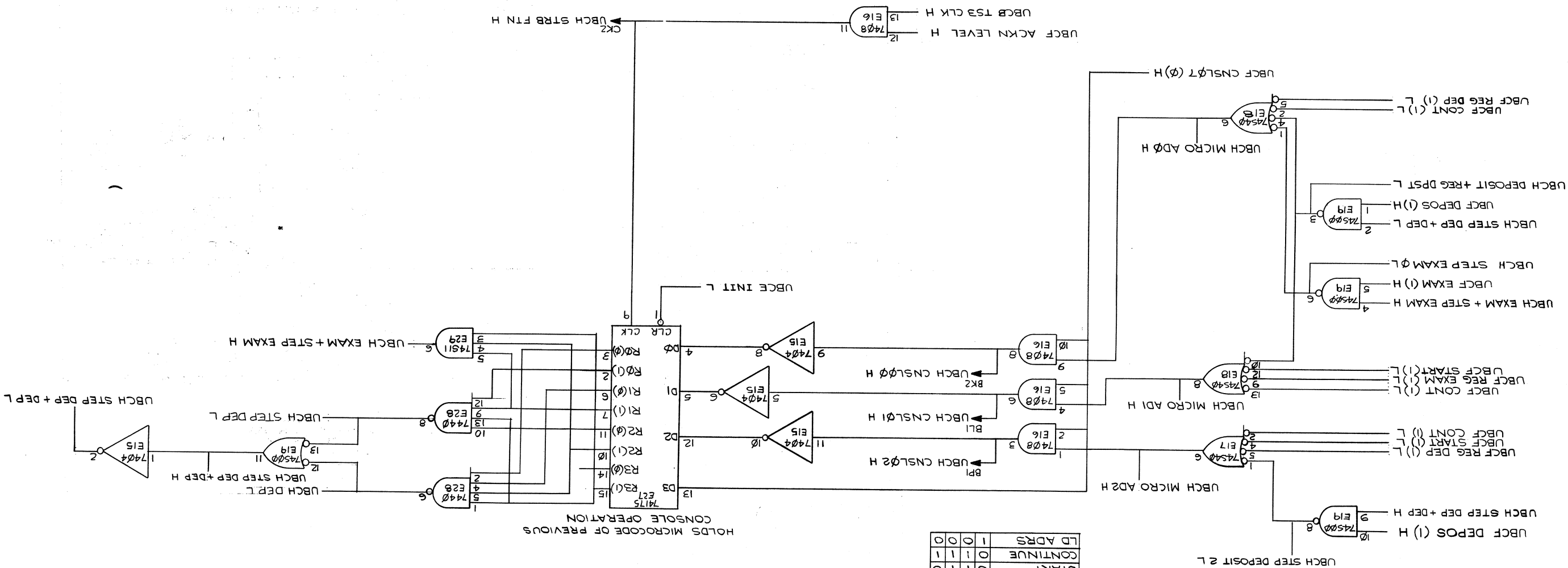
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REV		CHANGE NO.		CHK	
1					
2					
3					
4					
5					
6					
7					
8					

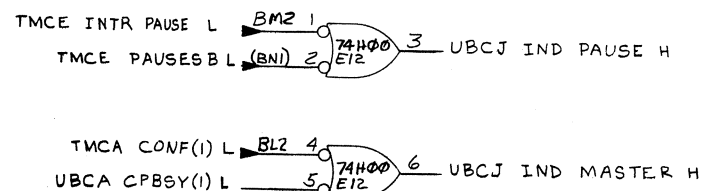
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11/45					
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		DRN	DATE	PARTS LIST	
DECIMALS	ANGLES	CHKD	DATE	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
.XXX = .005	± 0° 30'	ENG	DATE	TITLE	
.XX = .02		PROZ/ENG	DATE	UNIBUS & CONSOLE CONTROL	
.X = .1		PROB	DATE	(UBCF)	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY					
MATERIAL		NEXT HIGHER ASSY.		SIZE CODE	
FINISH		B-DD-KB11-0		NUMBER	
		SCALE		DCS M8106-0-1	
		SHEET 7 OF 14		REV. P	

MICRO	
CONSOLE	7 2 1 0
FUNCTION	EXAM
	0 0 0 0
	STEP EXAM
	0 0 0 1
	REGS EXAM
	0 0 1 0
	DEPOSIT
	0 0 1 1
	STEP DEPOSIT
	0 1 0 0
	REGS DEPOSIT
	0 1 0 1
	START
	0 1 1 0
	CONTINUE
	0 1 1 1
	LD ADRS
	1 0 0 0



FIRST USED ON OPTION/MODEL						11/45	
UNLESS OTHERWISE SPECIFIED							
DIMENSION IN INCHES							
TOLERANCES							
DECIMALS				ANGLES			
X.XXX = .005				° ± .30'			
X.X = .02							
X = .1							
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY							
MATERIAL							
FINISH							
B-DD-KB-11				SCALE			
SHEET 8 OF 14				DIST.			
SIZE CODE				D CS			
NUMBER				M8106-φ-1			
REV.				P			
TITLE							
INIBUS & CONSOLE CONTROL							
EQUIPMENT CORPORATION WAYLAND MASSACHUSETTS							
PARTS LIST							
QTY.		DESCRIPTION		PART NO.		ITEM NO.	
		CONSOLE & ADPS		SLOT 12			

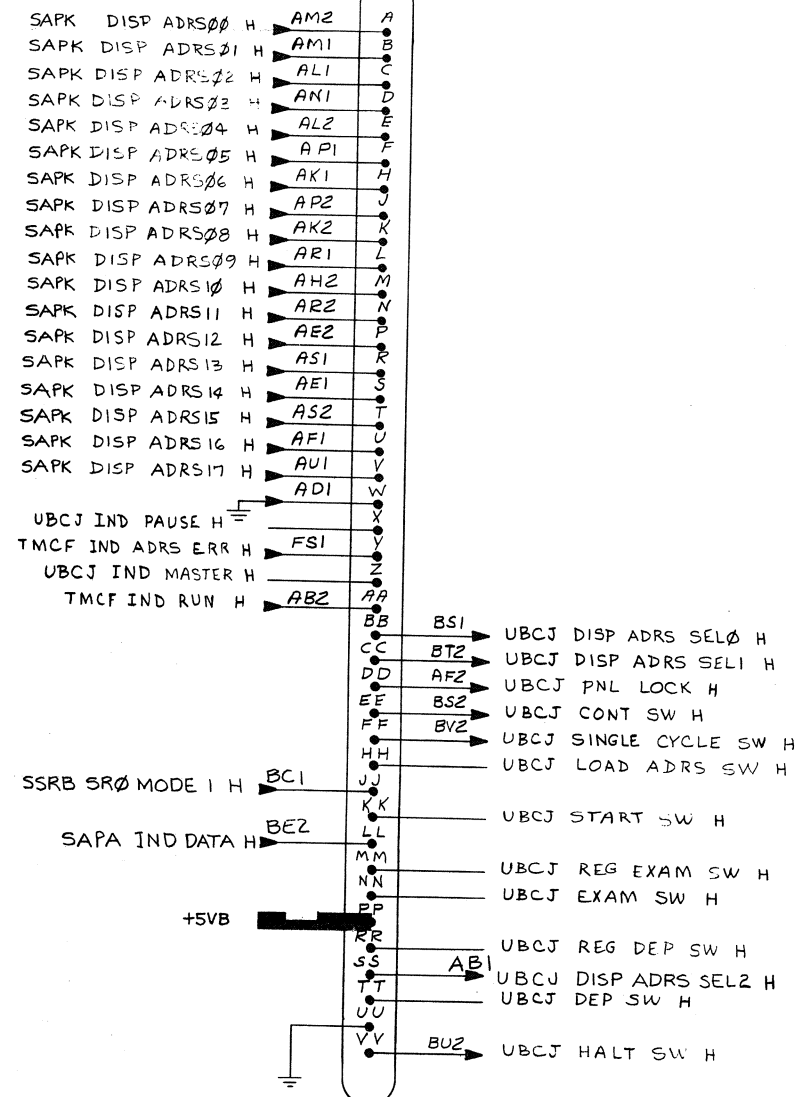
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UBC/KNL INTERFACE

SIGNALS TO CONSOLE

SIGNALS FROM CONSOLE



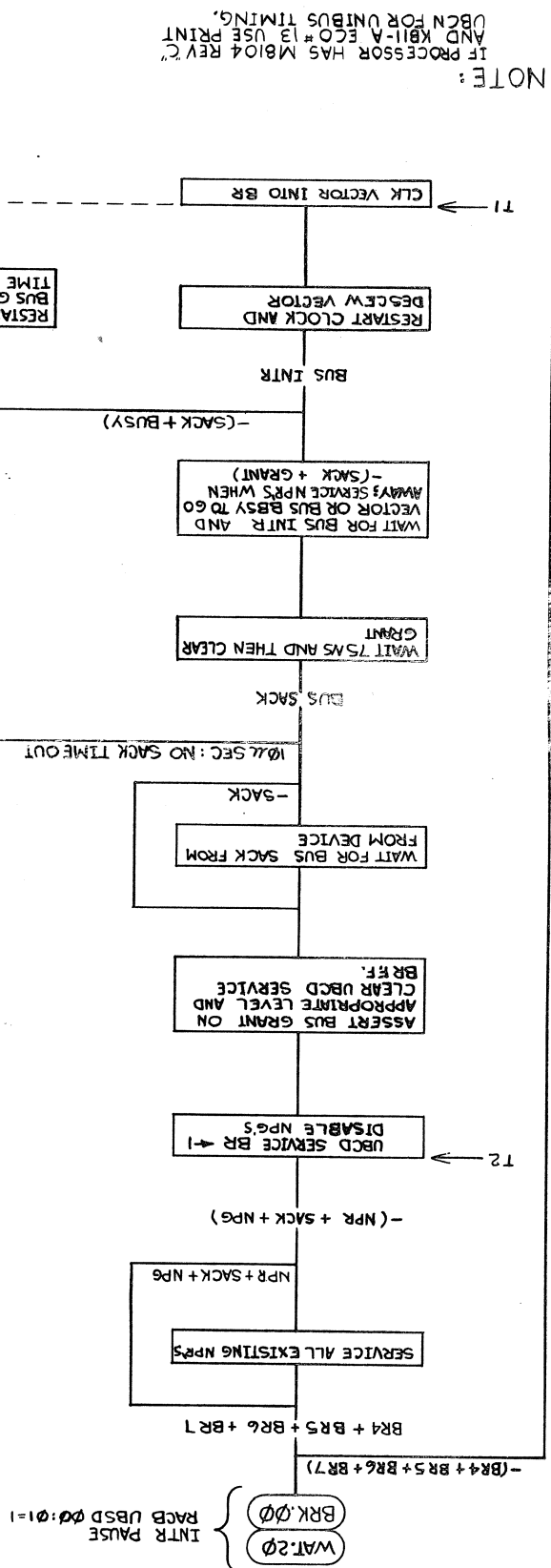
7008864

CONSOLE CABLE; INDICATORS

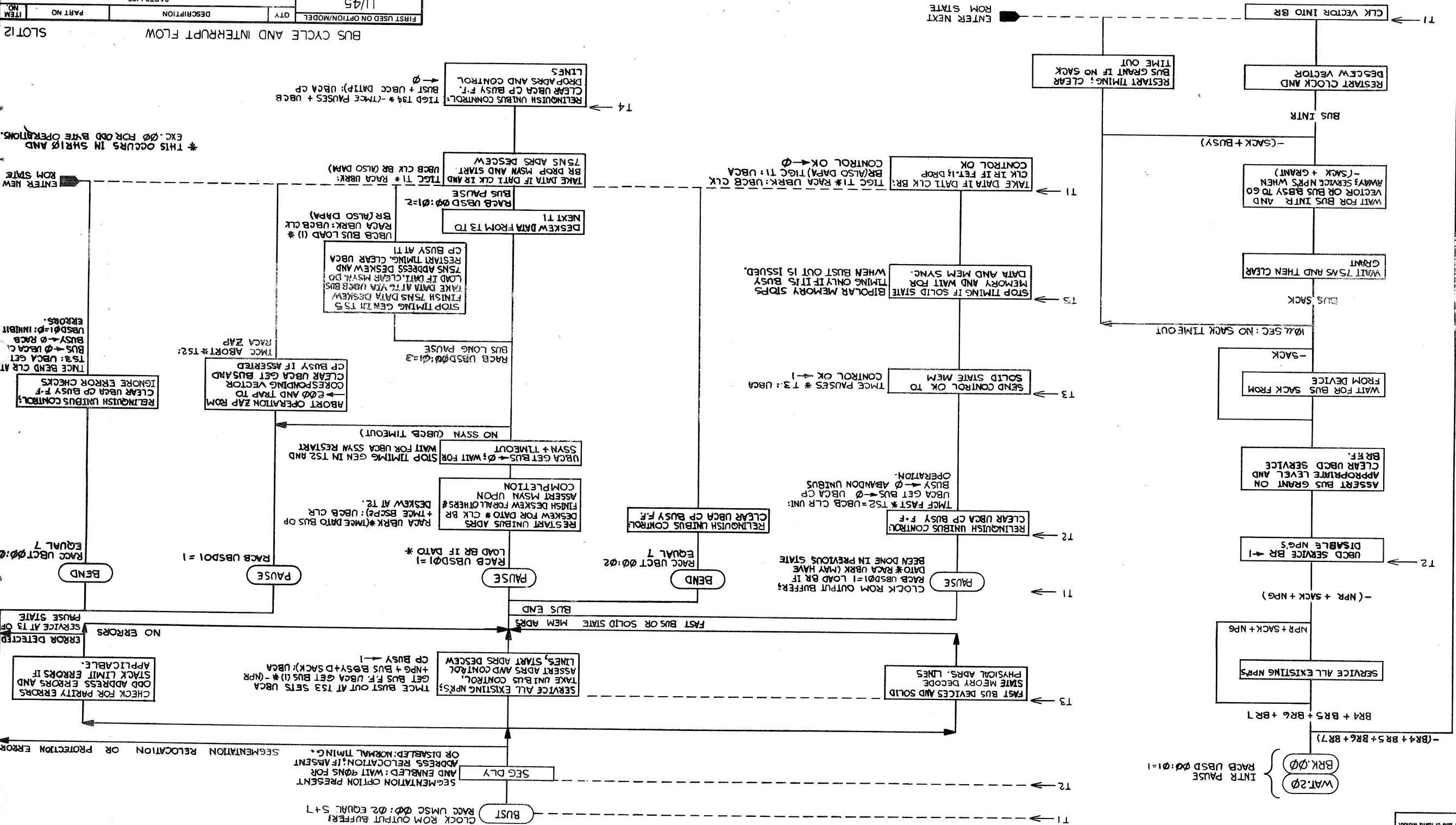
SLOT 12

FIRST USED ON OPTION/MODEL 11/45	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES	DRN DATE 6-25-71	DATE 3-3-72	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
DECIMALS .XXX = .005 .XX = .02 .X = .1	ENG DATE 3-3-72	DATE 3-3-72	TITLE UNIBUS & CONSOLE CONTROL (UBCJ)	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	PROB DATE 3-15-72	DATE 3-15-72	SIZE CODE B-DD-KB11-0	
MATERIAL ++	NEXT HIGHER ASSY.	SCALE ++	NUMBER DCS M8106-0-1	REV. p
FINISH ++	SHEET 9 OF 14	DIST.		

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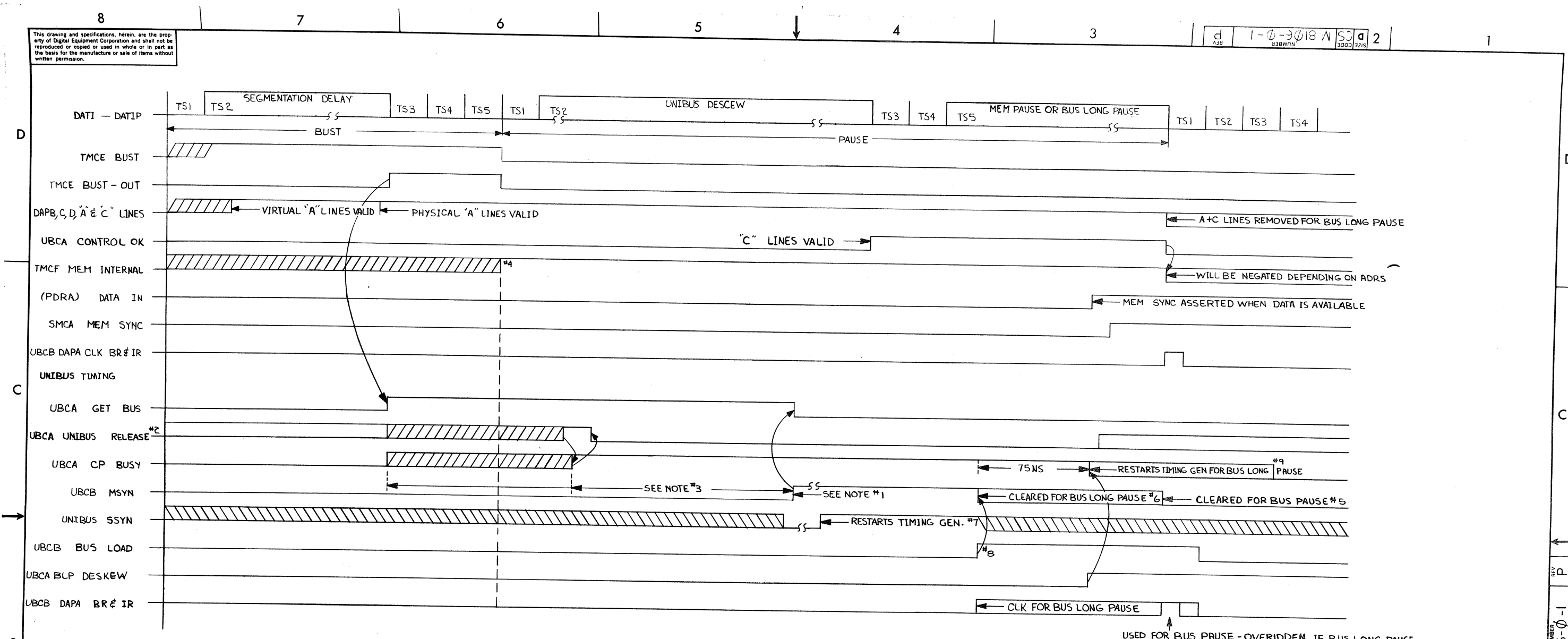
NOTE:
IF PROCESSOR HAS M8104 REV "C"
AND KB11-A ECO #13 USE PRINT
UBCN FOR UNITBUS TIMING.



* THIS OCCURS IN SHRIØ AND
EXC. ØØ FOR ØDD BYTE OPERATIONS.

FIRST USED ON OPTION/MODEL		11145	
QTY		PARTS LIST	
INLESS OTHERWISE SPECIFIED		DIMENSION IN INCHES	
TOLERANCES		DECIMALS	
ANGLES		$\pm .005$ $\pm .001$ $\pm .002$	
REMOVE SURF AND BREAK CHART CORNER SURFACE QUALITY		FINE DATE 1/1/78 DATE 1/1/78 DATE 1/1/78 DATE 1/1/78 DATE 1/1/78	
MATERIAL		NEXT HIGHER ASSY.	
B-DD-KB11-A SCALE 1 OF 1		DCS M8106-0-1 NUMBER (UBCK)	
FINISH		SIZE CODE DIST.	
UNIBUS & CONSOLE CONTROL TITLE EQUIPMENT CORPORATION LYNN MA 01903		DATE 1/1/78 DATE 1/1/78 DATE 1/1/78 DATE 1/1/78 DATE 1/1/78	

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1. MSYN IS NOT SET IF ODD ADDR, SEGMENTATION, PARITY OR STACK LIMIT ERROR HAS OCCURED. ERROR CONDITION RESTARTS TIMING GENERATOR AND ROM IS FORCED TO ZAP. 00
2. UNIBUS RELEASE = -(NPG + NPR + DSACK + BBSY)
3. MSYN IS SET 150 NS AFTER CP BUSY IS SET OR WHENEVER SSYN IS NOT PRESENT AFTER THE 150NS DESCEW
4. IF ADDR IS INTERNAL OR SOLID STATE MEMORY UNIBUS CNTL LOGIC IS CLEARED, DESCEW STOPPED, MSYN NOT ASSERTED.
5. BUS PAUSE - ADDR WILL NOT BE CHANGED IMMEDIATELY UPON EXITING PAUSE STATE

6. BUS LONG PAUSE - 75NS ADDR DESCEW AFTER NEGATION OF MSYN MUST BE DONE BEFORE TIMING GEN. RESTARTS AT TS OF PAUSE CYCLE BECAUSE "A" & "C" LINES WILL CHANGE UPON EXITING PAUSE STATE.
7. IF NON-EXISTANT MEMORY, TIMEOUT RESTARTS CLOCK.
8. BUS LOAD IS SET ONE CLOCK AFTER TS TO ALLOW COMPLETION OF WRITE TO SCRATCHPAD FOR PREVIOUS CYCLE BEFORE NEW TR & BR ARE LOADED
9. CLEARED 75NS AFTER MSYN → 0: T4 FOR BUS PAUSE

NOTE:
IF PROCESSOR HAS M8104 REV C
AND KBII-A ECO #13 USE PRINT
UBCP FOR UNIBUS TIMING.

DATI - DATIP TIMING DIAGRAM				SLOT 12	
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.	
PARTS LIST					
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES		DATE 4/12/72	digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS		
DECIMALS XXX = .005 XX = .02 X = .1	ANGLES ± 0° 30'	DATE 4/12/72	TITLE UNIBUS & CONSOLE CONTROL (UBCL)		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		DATE 5-1-72	REV. P		
MATERIAL	NEXT HIGHER ASSY.	DATE	NUMBER		
FINISH	SCALE	DATE	DISTRIBUTION		
SHEET 11 OF 14		DISTRIBUTION			

BRUNING 40-522 15840
REV. 1
CHANGE NO.
CHK

DEC FORM NO
DRD 102-B

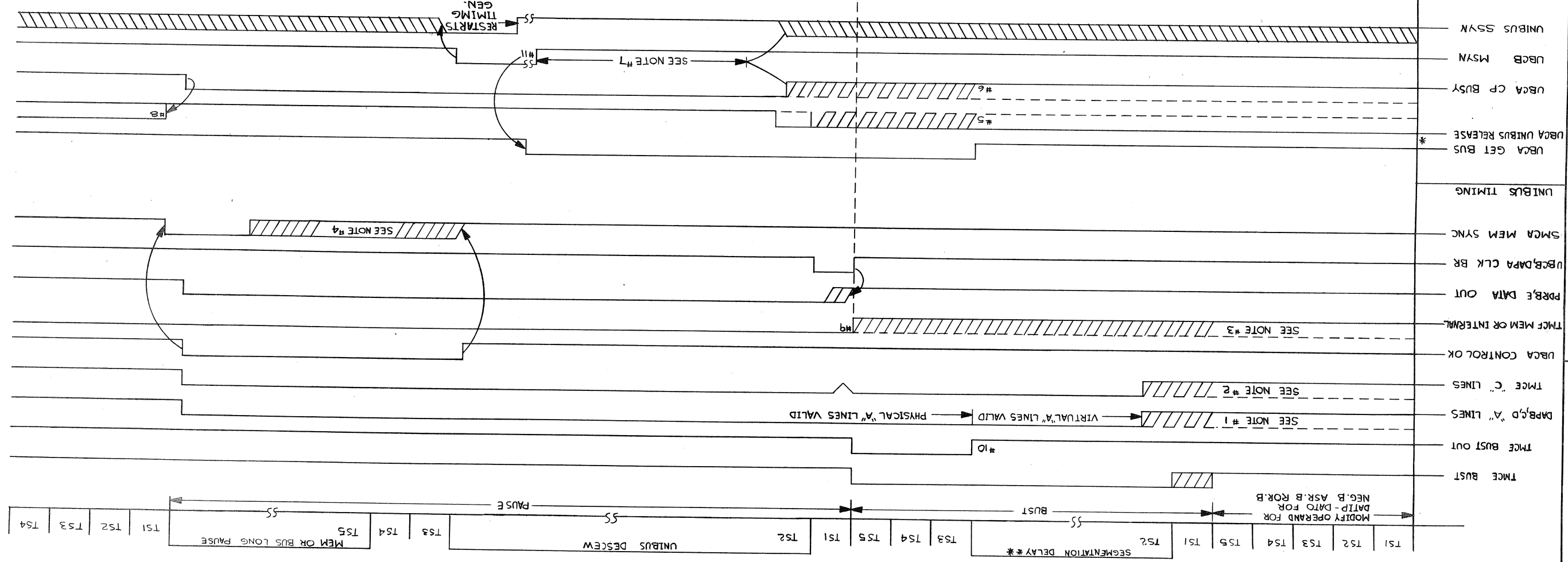
NOTE:
IF PROCESSOR HAS M8104 REV "C"
AND KB11-A ECO #13, USE PRINT
UBCP FOR UNIBUS TIMING.

1. ADDRESS WILL NOT BE CHANGING IF DATP IS PRECEDED BY DATIP (DOTTED LINE SHOWS DATIP)
2. CONTROL LINES WILL SHOW DATP OR ELSE ARE INVALID UNTIL CONTROL OK
3. MEM OR INTERNAL WILL BE TRUE UPON ENTERING DATP IF ADDRESS WAS SEMI CONDUCTOR MEMORY OR INTERNAL FROM PREVIOUS DATIP
4. MEM SYNC IS ASSERTED BY SEMI CONDUCTOR MEMORY AS SOON AS DATA IS TAKEN-IF DATA IS TAKEN AND MEM SYNC ASSERTED PRIOR TO STOPPED IN TS
5. UNIBUS RELEASE WILL NOT BE TRUE IF PREVIOUS OPERATION WAS A DATIP TO A UNIBUS ADDRESS.
6. CP BUSY WILL BE ASSERTED UPON ENTRY INTO DATP IF PREVIOUS OPERATION WAS A DATIP TO A UNIBUS ADDRESS

UNIBUS RELEASE = $-(NPR + NPG + BBSY + SAC)$

7. DESKEW FOR "A" "C" "D" LINES IS 150 NS STARTING WHEN CP BUSY IS SET AND SSYN FROM THE PREVIOUS BUS OPERATION IS NEGATED.
8. UNIBUS RELEASE BECOMES TRUE IF NO NPR DEVICE HAS BEEN GIVEN CONTROL OR IS REQUESTING PRIOR TO CP BSY GETTING CLEARED
9. IF ADDRESS IS SEMI CONDUCTOR MEMORY OR INTERNAL, UNIBUS LOGIC IS CLEARED DESKEW IS STOPPED AND MSYN IS NOT ASSERTED.
10. BUST OUT IS INHIBITED IN IRD00 FOR CONDITIONAL BUST (MSC=5) IF BIN * SM123 + BR INST * BRQ TRUE + DM123 * -(BIN * SM1 + BR INST)
11. MSYN IS NOT SET IF AN ODD ADDR, STACK LIMIT RED SEGMENTATION OR PARITY ERROR HAS OCCURRED

NOTE: 1. ALL SIGNALS ARE SHOWN TRUE WHEN HIGH



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FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
11/45					
UNLESS OTHERWISE SPECIFIED		PARTS LIST			
DIMENSION IN INCHES					
TOLERANCES					
DECIMALS					
ANGLES					
X = .1					
.XX = .005					
.XXX = .001					
REMOVE BURNS AND BREAK SHARP CORNERS SURFACE QUALITY					
MATERIAL					
NEXT HIGHER ASSY.					
B-DD-KB11-A					
SCALE					
SHEET 12 OF 14					
REV					
P					
NUMBER					
M 8106-0-1					
SIZE CODE					
D CS					
TITLE					
UNIBUS & CONSOLE CONTROL					
DIGITAL CORPORATION					
MAYNARD MASSACHUSETTS					

DATO TIMING DIAGRAM

SLOT 12

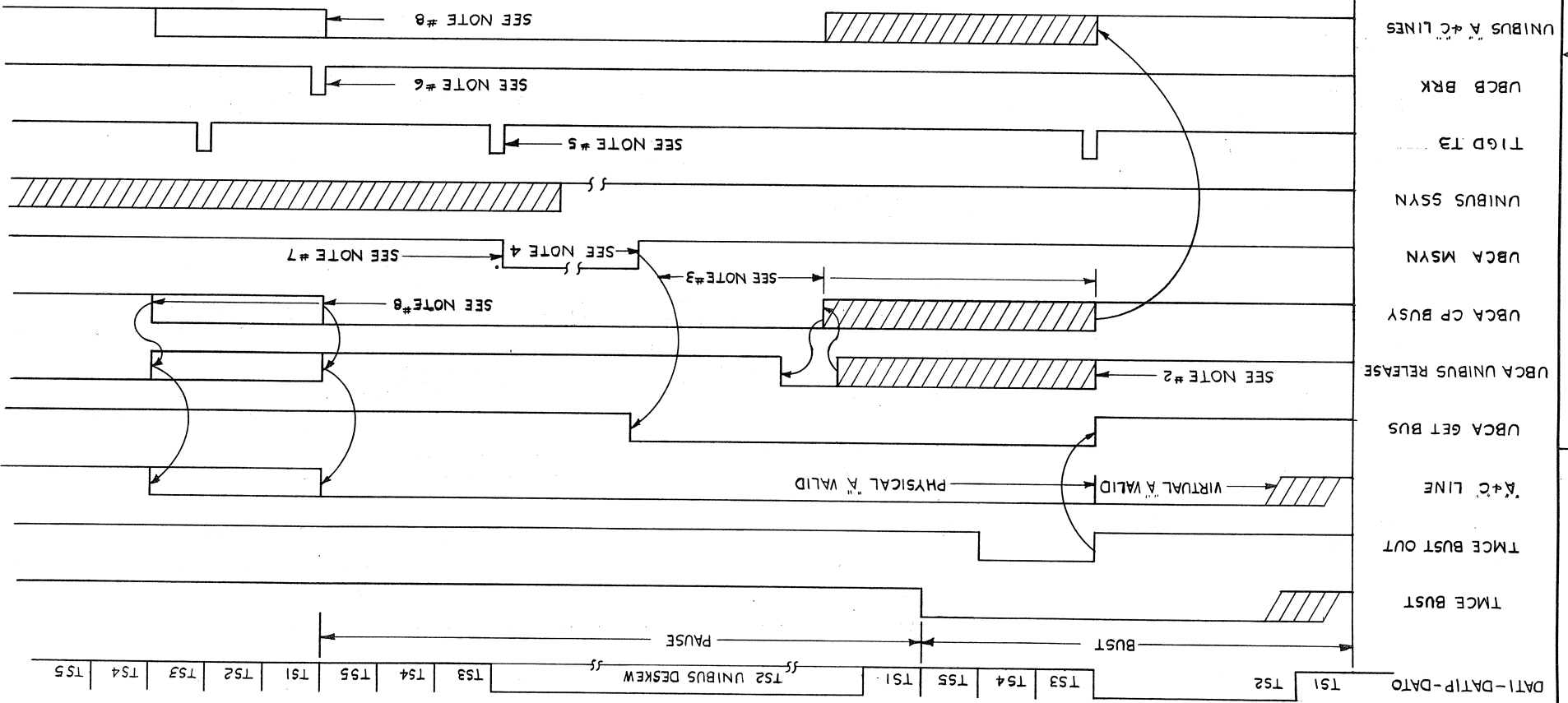
SIZE CODE
D CS
NUMBER
M 8106-0-1
REV
P



A	BRUNING 40-522 15940	
	CHK	REV
	REVISIONS	CHANGE NO.
	DEC FORM NO	
	DRD 102-B	

REV. NO.	1
CHANGE NO.	
CHK	

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- NOTES:
- #1 MSYN IS NOT SET IF ODD ADDRESS, KTIC, PARITY OR STACK LIMIT HAS OCCURRED. ERROR CONDITION RESTARTS TIMING GEN. AND ROM IS FORCED TO ZAP.00
 - #2 UNIBUS RELEASE = -(NPG + NPR + SACK + B BUSY)
 - #3 MSYN IS SET 150 NS AFTER CP BUSY OR WHENEVER SSYN IS NOT PRESENT AFTER 150 NS DESKEW.
 - #4 IF ADDR IS INTERNAL OR SOLID STATE MEMORY, UNIBUS CONTROL LOGIC IS CLEARED, DESKEW STOPPED, MSYN NOT ASSERTED.
 - #5 T3 STROBES UNIBUS DATA TO BUS BUFFER REGISTER EVERY T3. FOR DAT1-DAT1P THE BR CLOCK IS NEXT T1
 - #6 BRK IS DEPENDENT ON ROM TO LOAD BUS TO BR.
 - #7 T5NS DESKEW FOR MSYN IS OBTAINED BY THE FACT THAT SSYN MUST GO THROUGH 2 RANKS OF SYNCHRONIZATION
 - #8 FOR BUS LONG PAUSE ADDRESS + CONTROL IS DESKEWED FROM T3 TO T1. FOR BUS PAUSE THEY ARE HELD TO T54 OF THE NEXT ROM CYCLE.

FINISH		SCALE		SHEET 14 OF 14	
MATERIAL		B-DD-KB11-A		D CS	
NEXT HIGHER ASSY.		UNIBUS CONTROL		M8106-0-1	
REMOVE BURNS AND BREAK SHARP CORNERS SURFACE QUALITY		PROJ. ENG.		DATE	
DECIMALS		ANGLES		DATE	
TOLERANCES		DIMENSION IN INCHES		DATE	
UNLESS OTHERWISE SPECIFIED		PARTS LIST		DATE	
FIRST USED ON OPTION/MODEL		QTY.		DESCRIPTION	
ITEM NO.		PART NO.		REV.	
12		SLOT 12		P	