

digitalEQUIPMENT
CORPORATION
MAYNARD, MASSACHUSETTS**MASTER DRAWING LIST**

CODE	DWG. NO.		REV. LET.	NO. OF SHEETS	TITLE		
BS	D-1C-49-2		-	1	MEMORY ADDRESS REGISTER & DECODER & BUS DRIVER FOR EXTENTION MEMORY 171		
CL	A-1C-49-3		-	1	MA TO MEMORY MB TO MEM		
CL	A-1C-49-4		-	2	(MAB) INPUTS OVER 16K		
BS	D-1C-49-5		-	1	PROGRAM COUNTER		
BS	D-1C-49-6		-	1	ACCUMULATOR CONTROL		
TL	A-1C-49-7		-	6	TERMINATIONS		
WD	D-1C-49-8		-	1	1A-MAFF 1B-PCFF 1C-AC CONTROL		
WD	D-1C-49-9		-	1	MEMORY ADDRESS BUFFER & MB TIE POINTS USED OVER 16K EXTENTION MEMORY		
BS	D-1C-49-10		-	1	MA, MB, IO & PC TRANSFER LOGIC		
BS	D-1C-49-11		-	1	SH/RO LOGIC, PROGRAM FLAG & COUNT LOGIC, ONE CHANNEL SEQUENCE BREAK		
BS	D-1C-49-12		-	1	INSTRUCTION REGISTER & DECODERS		
CL	A-1C-49-13		-	1	MBD → IOT (4113 INVERTERS)		
CL	A-1C-49-14		-	1	PF INDICATORS, SENSE SW'S, SING CYC LOW SP		
CL	A-1C-49-15		-	2	1R INDICATOR		
WD	D-1C-49-16		-	1	1D- 41 PC, SH/RO 1E-PF, SH/RO		
BS	D-1C-49-17		-	1	GENERAL CONTROL FUNCTIONS		
CL	A-1C-49-18		-	2	CONTROL SW'S		
WD	D-1C-49-19		-	1	1H TIME PULSE 1J PBC SPG IOT 1K, RUN CYCLE DF BREAK		
BS	D-1C-49-20		-	2	ACCUMULATOR		
CL	A-1C-49-21		-	1	TW LINES		
CL	A-1C-49-22		-	1	AC INDICATORS		
WD	D-1C-49-23		-	1	2A-AC SHIFT, 2B-AC FF 2C-AC-LOGIC		
BS	D-1C-49-24		-	1	MEMORY BUFFER REGISTER		
CL	A-1C-49-25		-	1	MB INDICATORS		
CL	A-1C-49-26		-	1	MEM CABLE SA ↔ MB		
WD	D-1C-49-27		-	1	WIRING DIAGRAM 2D-MBF 2E-MBG		
BS	D-1C-49-28		-	1	IN-OUT INPUT MIXER		
BS	D-1C-49-29		-	1	IN-OUT REGISTER		
CL	A-1C-49-30		-	1	IO INDICATORS		
WD	D-1C-49-31		-	1	2F-IO SHIFT, 2H-IO FF, 2J		
CL	A-1C-49-32		-	1	NAC & MEMORY LITES		
TL	A-1C-49-33		-	1	TERMINATORS FOR NAC & MEMORY LITES		
CL	A-1C-49-34		-	2	MEM, EXT. TYPE 171 TRANSFER SELECTION PULSES TO BAY 2		
REV.	ECO	ENG	DATE	MADE BY	CHECKER	ENG	5/15/64
A	304	EH	10/21/64	M. Mariano	A. Yunker	E. Hammond	
B	309	EH	11/18/64				
				TITLE			
				COMPUTER ASS'Y.			
				FOR			
				PDP-1C-49			
				SHEET 1 OF 3		CODE MDL	DWG. NO. A-1C-49-2
						REV. LET.	B

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MAYNARD MASSACHUSETTS**MASTER DRAWING LIST**

CODE	DWG. NO.	REV. LET.	NO. OF SHEETS	TITLE
CL	A-1C-49-35	-	1	171 MEMORY EXTENTION OF PULSES TO MEMORY(B)
CL	A-1C-49-36	-	1	171 MEMORY EXTENTION OF PULSES TO MEMORY(C)
CL	A-1C-49-37	-	2	MEM EXT. TYPE 171 & 15 TRANSFER & SELECTION PULSES TO BAY 1
CL	A-1C-49-38	-	1	PC INDICATOR WITH MEMORY EXTENTION TYPE 171
CL	A-1C-49-39	-	1	TA LINES WITH MEMORY EXTENTION
CL	A-1C-49-40	-	2	TERMINATION RESISTOR LIST & DIODE LIST FOR MEMORY EXTENTION
CL	A-1C-49-41	-	1	CAPACITOR LIST FOR MEMORY EXTENTION TYPE 171
CL	A-1C-49-42	-		TYPE 171 MEMORY EXTENTION OF PULSES TO MEMORY (A)
CL	A-1C-49-43	-	1	TYPE 171 MEMORY EXTENTION OF PULSE TO MEMORY (D)
BS	D-1C-49-44	-	1	STANDARD IN-OUT TRANSFER CONTROL
CL	A-1C-49-45	-	15	GENERAL WIRING SHEETS
CL	A-1C-49-46	-	1	4113 INVERTERS
WD	D-1C-49-47	-	1	IOT CONTROL
WD	D-1C-49-48	-	1	READER CONTROL
BS	D-1C-49-49	-	1	TELETYPE BRPE PUNCH
BS	D-1C-49-50	-	1	TYPEWRITER BUFFER #0 & CONTROL
CL	A-1C-49-51	-	1	READER INTO READER LOGIC
CL	A-1C-49-52	-	1	SOCKET WIRING (PUNCH INTO PUNCH LOGIC)
CL	A-1C-49-53	-	1	TYPEWRITER INTO TYPEWRITER LOGIC
CL	A-1C-49-54	-	2	RDR INDICATORS
CL	A-1C-49-55	-	2	SOCKET WIRING (IO OUTPUTS)
CL	A-1C-49-56	-	2	SOCKET WIRING PUN & TYPE INDICATORS
CL	A-1C-49-57	-	1	READER TO READER LOGIC CABLE
CL	A-1C-49-58	-	1	TELETYPE PUNCH TO PUNCH
WD	D-1C-49-59	-	1	RECK 1 LF SCR DRIVER 830
WD	D-1C-49-60	-	1	TELETYPE READER PUNCH & TYP CONTROL
CL	A-1C-49-61	-	1	HIGH SPEED CHANNEL OPTION
CL	A-1C-49-62	-	1	MA MIXER HIGH SPEED CHANNEL CONTROL TYPE 19
CL	A-1C-49-63	-	1	HIGH SPEED CHANNEL CONTROL TYPE 19
REV.				ECO
				ENG
				DATE
				MADE BY
				M. Mariano
				CHECKER
				A. Yurkstas
				ENG
				E. Harwood
				TITLE
				COMPUTER ASS'Y
				FOR
				PDP-1C-49
				CODE
				DWG. NO.
				A-1C-49-2
				REV.
				LET.
				B
				SHEET 2 OF 3
				MDL

MASTER DRAWING LIST

CODE	DWG. NO.			REV. LET.	NO. OF SHEETS	TITLE	
CL	A-1C-49-64			-	1	HIGH SPEED CHANNEL CONTROL TYPE 19	
CL	A-1C-49-65			-	1	HIGH SPEED CHANNEL CONTROL TYPE 19	
CL	A-1C-49-66			-	1	HSEB---TYPE 170 MEMORY SYSTEM	
CL	A-1C-49-67			-	1	MEMORY TYPE 170 HIGH SPEED BUFFER MIXER	
						INPUTS	
CL	A-1C-49-68			-	1	HIGH SPEED CHANNEL CONTROL TYPE 19	
BS	D-1C-49-69			-	2	HIGH SPEED CHANNEL CONTROL TYPE 19 BUFFER	
						MIXER & ADDRESS MIXER	
WD	D-1C-49-70			-	1	HSC CONTROL & MIXER MA MIXER	
BS	D-1C-49-71			-	1	HIGH SPEED CHANNEL TYPE 19 REQUEST &	
						TRANSFER LOGIC	
BS	D-1C-49-72			-	1	MEMORY EXTENTION CONTROL TYPE 171 TRANSFER	
						& SELECTION LOGIC	
BS	D-1C-49-73			-	1	BREAK SYSTEM PRIORITY CHAIN	
BS	D-1C-49-74			-	1	BREAK SYSTEM CONTROL	
BS	D-1C-49-75			-	1	CHANNEL REGISTER & PRIORITY CHAIN 00-07	
BS	D-1C-49-76			-	1	CHANNEL REGISTER & PRIORITY CHAIN 01-07	
BS	D-1C-49-77			-	1	SELECTION FOR 256 PULSES	
BS	D-1C-49-78			-	1	SELECTION FOR 256 PULSES	
BS	D-1C-49-79			-	1	SIMPLEX INPUT MIXING & OUTPUT DECOUPLING	
CL	A-1C-49-80			-	4	ADX SEQUENCE BREAK INDICATORS	
CL	A-1C-49-81			-	8	LINE UNIT OUTPUT DECOUPLING & INPUT MIXING	
						256 SEQ. BRK	
CL	A-1C-49-82			A	32	FLAGS	
WD	D-1C-49-83			-	1	PRIORITY CHAIN IN-OUT BREAK SYSTEM CONTROL	
WD	D-1C-49-84			-	1	CHANNEL & PRIORITY - 0R-0C-0-17	
WD	D-1C-49-85			-	1	TAPER PIN PATCH FOR 256 SEQUENCE BREAK	
						SYSTEM	
WD	D-1C-49-86			-	1	256 PULSE SELECTION & CONTROL 0E-0F	
WD	D-1C-49-87			-	1	256 PULSE SELECTION 0H,0J & 0K	
WD	D-1C-49-88			-	1	SIMPLEX INPUT MIXING & OUTPUT DECOUPLING	
BS	D-1C-49-89			-	1	COMPLETION PULSES TRAPS	
BS	D-1C-49-91			-	1	LINE PRINTER CONTROL LOGIC ADX	
MDL	170ABC-0-1			C	1	MASTER DRAWING LIST 16K MEMORY TYPE 170	
MDL	A-52-12-1				3	MASTER DRAWING LIST TAPE CONTROL 52	
CL	A-1C-49-94				2	MMP TO LINE PRINTER	
WD	D-1C-49-90				1	TAPER PIN PANEL & IN-OUT PLUGS FOR	
						OPTIONAL EQUIP.	
WD	D-1C-49-92				1	COMPLETION PULSE TRAPS ADX	
WD	D-1C-49-93				1	LINE PRINTER CONTROL INTERFACE	
REV.	ECO	ENG	DATE	MADE BY		CHECKER	ENG
				M. Mariano		A. Yurkstas	E. Harwood
				TITLE			
				COMPUTER ASS'Y.			
				FOR			
				PDP-1C-49			
				SHEET 3 OF 3		CODE	DWG. NO.
				MDL		A-1C-49-2	REV LET. B