

TSV05

TSV05 CTRL LT1
CVTSABO

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IDENTIFICATION

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1.0 GENERAL INFORMATION

1.1 PROGRAM ABSTRACT

THIS IS A LSI-11 RESIDENT DIAGNOSTIC WHICH CHECKS THE FUNCTIONALITY OF A TSV05 MAGTAPE SUBSYSTEM WHILE CONNECTED TO A LSI-11/23 SYSTEM (QBUS). THE PROGRAM PROVIDES ERROR MESSAGES WHICH IDENTIFY FAILING FUNCTIONS THAT AID IN THE REPAIR OF THE DEVICE. THIS DIAGNOSTIC CONSIST OF ELEVEN TEST WHICH ARE EXECUTED IN SEQUENCE.

THIS DIAGNOSTIC HAS BEEN WRITTEN FOR USE WITH THE DIAGNOSTIC RUNTIME SERVICES SOFTWARE (SUPERVISOR). THESE SERVICES PROVIDE THE INTERFACE TO THE OPERATOR AND TO THE SOFTWARE ENVIRONMENT. THIS PROGRAM CAN BE USED WITH XXDP+, ACT, APT, SLIDE AND PAPER TAPE. FOR A COMPLETE DESCRIPTION OF THE RUNTIME SERVICES, REFER TO THE XXDP+ USER'S MANUAL. THERE IS A BRIEF DESCRIPTION OF THE RUNTIME SERVICES IN SECTION 2 OF THIS DOCUMENT.

1.2 SYSTEM REQUIREMENTS

LSI-11 PROCESSOR AND MEMORY
CAUTION: DIAGNOSTIC REQUIRES 32K WORDS OF MEMORY
(28K USEABLE I.E. 4K FOR I/O PAGE)
TSV05 MAGTAPE SUBSYSTEM (DRIVE AND CONTROLLER)
CONSOLE TERMINAL
PDP-11 DIAGNOSTIC SUPERVISOR (MSAAA.SYS VERSION 34 OR LATER)
PDP-11 DIAGNOSTIC LOADER/MONITOR (XXDP+)

1.3 RELATED DOCUMENTS AND STANDARDS

DIGITAL EQUIPMENT CORPORATION DOCUMENTS:

1. CHQUS XXDP+ USERS GUIDE; DOCUMENT NUMBER AC-F348E-MC
DATE: 14 JULY 1980.
2. TSV05 TRANSPORT SUBSYSTEM USER'S GUIDE; DOCUMENT NUMBER EK-TSV05-UG-001
DATE: AUGUST 1983
3. TSV05 TRANSPORT SUBSYSTEM TECHNICAL MANUAL; DOCUMENT NUMBER EK-TSV05-TM-001
DATE: AUGUST 1983
4. TSV05 TRANSPORT SUBSYSTEM INSTALLATION MANUAL; DOCUMENT NUMBER EK-TSV05-IN 001
DATE: AUGUST 1983

1.4 DIAGNOSTIC HIERARCHY PREREQUISITES

FUNCTIONAL LSI-11 CENTRAL PROCESSOR AND MEMORY
FUNCTIONAL CONSOLE TERMINAL
FUNCTIONAL STANDALONE DIAGNOSTIC SUPERVISOR
FUNCTIONAL DIAGNOSTIC LOADER/MONITOR (XXDP+)

1.5 ASSUMPTIONS

ALL HARDWARE EXCEPT THE HARDWARE UNDER TEST IS ASSUMED TO WORK PROPERLY OR FALSE ERRORS CAN BE REPORTED.
THE TAPE BEING USED ON THE TSV05 TRANSPORT IS A KNOWN GOOD REEL OF TAPE.

2.0 OPERATING INSTRUCTIONS

THIS SECTION CONTAINS A BRIEF DESCRIPTION OF THE RUNTIME SERVICES. FOR DETAILED INFORMATION, REFER TO THE XXDP+ USER'S MANUAL (CHQUS).

2.1 COMMANDS

THERE ARE ELEVEN LEGAL COMMANDS FOR THE DIAGNOSTIC RUNTIME SERVICES (SUPERVISOR). THIS SECTION LISTS THE COMMANDS AND GIVES A VERY BRIEF DESCRIPTION OF THEM. THE XXDP+ USER'S MANUAL HAS MORE DETAILS.

COMMAND	EFFECT
-----	-----
START	START THE DIAGNOSTIC FROM AN INITIAL STATE
RESTART	START THE DIAGNOSTIC WITHOUT INITIALIZING
CONTINUE	CONTINUE AT TEST THAT WAS INTERRUPTED (AFTER +C)
PROCEED	CONTINUE FROM AN ERROR HALT
EXIT	RETURN TO XXDP+ MONITOR (XXDP+ OPERATION ONLY!)
ADD	ACTIVATE A UNIT FOR TESTING (ALL UNITS ARE CONSIDERED TO BE ACTIVE AT START TIME)
DROP	DEACTIVATE A UNIT
PRINT	PRINT STATISTICAL INFORMATION (IF IMPLEMENTED BY THE DIAGNOSTIC - SECTION 4.0)
DISPLAY	TYPE A LIST OF ALL DEVICE INFORMATION
FLAGS	TYPE THE STATE OF ALL FLAGS (SEE SECTION 2.3)
ZFLAGS	CLEAR ALL FLAGS (SEE SECTION 2.3)

A COMMAND CAN BE RECOGNIZED BY THE FIRST THREE CHARACTERS. SO YOU MAY, FOR EXAMPLE, TYPE "STA" INSTEAD OF "START".

2.1.1 OPERATOR COMMANDS

THE TSV05 DIAGNOSTIC IS A LSI-11 DIAGNOSTIC SUPERVISOR COMPATIBLE PROGRAM. ALL LOADING AND RUNTIME INSTRUCTIONS CAN BE REFERENCED IN THE CHQUS XXDP+ USERS GUIDE, DOCUMENT NUMBER AC-F348E-MC. THE USER ENTRY IS IN QUOTES.

BOOT THE DIAGNOSTIC MEDIA

```
.R VTSA??
DIAG. RUN-TIME SERVICES REV D. APR 79
CVTSA-A-0
****TSV05 LOGIC DIAGNOSTIC****
UNIT IS TSV05
>DR
```

2.2 SWITCHES

THERE ARE SEVERAL SWITCHES WHICH ARE USED TO MODIFY SUPERVISOR OPERATION. THESE SWITCHES ARE APPENDED TO THE LEGAL COMMANDS. ALL OF THE LEGAL SWITCHES ARE TABULATED BELOW WITH A BRIEF DESCRIPTION OF EACH. IN THE DESCRIPTIONS BELOW, A DECIMAL NUMBER IS DESIGNATED BY "DDDDD".

SWITCH	EFFECT
---	-----
/TESTS:LIST	EXECUTE ONLY THOSE TESTS SPECIFIED IN THE LIST. LIST IS A STRING OF TEST NUMBERS, FOR EXAMPLE - /TESTS:1:5:7-10. THIS LIST WILL CAUSE TESTS 1,5,7,8,9,10 TO BE RUN. ALL OTHER TESTS WILL NOT BE RUN.
/PASS:DDDDD	EXECUTE DDDDD PASSES (DDDDD = 1 TO 64000)
/FLAGS:FLGS	SET SPECIFIED FLAGS. FLAGS ARE DESCRIBED IN SECTION 2.3.
/EOP:DDDDD	REPORT END OF PASS MESSAGE AFTER EVERY DDDDD PASSES ONLY. (DDDDD = 1 TO 64000)
/UNITS:LIST	TEST/ADD/DROP ONLY THOSE UNITS SPECIFIED IN THE LIST. LIST EXAMPLE - /UNITS:0:5:10-12 USE UNITS 0,5,10,11,12 (UNIT NUMBERS = 0-63)

EXAMPLE OF SWITCH USAGE:

START/TESTS:1-5/PASS:1000/EOP:100

THE EFFECT OF THIS COMMAND WILL BE: 1) TESTS 1 THROUGH 5 WILL BE EXECUTED, 2) ALL UNITS WILL TESTED 1000 TIMES AND 3) THE END OF PASS MESSAGES WILL BE PRINTED AFTER EACH 100 PASSES ONLY. A SWITCH CAN BE RECOGNIZED BY THE FIRST THREE CHARACTERS. YOU MAY, FOR EXAMPLE, TYPE "/TES:1-5" INSTEAD OF "/TESTS:1-5".

BELOW IS A TABLE THAT SPECIFIES WHICH SWITCHES CAN BE USED BY EACH COMMAND.

	TESTS	PASS	FLAGS	EOP	UNITS
---	-----	-----	-----	-----	-----
START	X	X	X	X	X
RESTART	X	X	X	X	X
CONTINUE		X	X	X	
PROCEED			X		
DROP					X
ADD					X
PRINT					
DISPLAY					X
FLAGS					
ZFLAGS					
EXIT					

2.3 FLAGS

FLAGS ARE USED TO SET UP CERTAIN OPERATIONAL PARAMETERS SUCH AS LOOPING ON ERROR. ALL FLAGS ARE CLEARED AT STARTUP AND REMAIN CLEARED UNTIL EXPLICITLY SET USING THE FLAGS SWITCH. FLAGS ARE ALSO CLEARED AFTER A START COMMAND UNLESS SET USING THE FLAG SWITCH. THE ZFLAGS COMMAND MAY ALSO BE USED TO CLEAR ALL FLAGS. WITH THE EXCEPTION OF THE START AND ZFLAGS COMMANDS, NO COMMANDS AFFECT THE STATE OF THE FLAGS; THEY REMAIN SET OR CLEARED AS SPECIFIED BY THE LAST FLAG SWITCH.

FLAG	EFFECT
-	-----
HOE	HALT ON ERROR - CONTROL IS RETURNED TO RUNTIME SERVICES COMMAND MODE
LOE	LOOP ON ERROR
IER*	INHIBIT ALL ERROR REPORTS
IBR*	INHIBIT ALL ERROR REPORTS EXCEPT FIRST LEVEL (FIRST LEVEL CONTAINS ERROR TYPE, NUMBER, PC, TEST AND UNIT)
IXE*	INHIBIT EXTENDED ERROR REPORTS (THOSE CALLED BY PRINTX MACRO'S)
PRI	DIRECT MESSAGES TO LINE PRINTER
PNT	PRINT TEST NUMBER AS TEST EXECUTES
BOE	"BELL" ON ERROR
UAM	UNATTENDED MODE (NO MANUAL INTERVENTION)
ISR	INHIBIT STATISTICAL REPORTS (DOES NOT APPLY TO DIAGNOSTICS WHICH DO NOT SUPPORT STATISTICAL REPORTING)
IDR	INHIBIT PROGRAM DROPPING OF UNITS
ADR	EXECUTE AUTODROP CODE
LOT	LOOP ON TEST

*ERROR MESSAGES ARE DESCRIBED IN SECTION 3.1

SEE THE XXDP* USER'S MANUAL FOR MORE DETAILS ON FLAGS. YOU MAY SPECIFY MORE THAN ONE FLAG WITH THE FLAG SWITCH. FOR EXAMPLE, TO CAUSE THE PROGRAM TO LOOP ON ERROR, INHIBIT ERROR REPORTS AND TYPE A "BELL" ON ERROR, YOU MAY USE THE FOLLOWING STRING:

/FLAGS:LOE:IER:BOE

2.4 HARDWARE QUESTIONS

WHEN A DIAGNOSTIC IS STARTED, THE RUNTIME SERVICES WILL PROMPT THE USER FOR HARDWARE INFORMATION BY TYPING "CHANGE HW (L) ?" YOU MUST ANSWER "Y" AFTER A START COMMAND UNLESS THE HARDWARE INFORMATION HAS BEEN "PRELOADED" USING THE SETUP UTILITY (SEE CHAPTER 14 OF THE XXDP+ USER'S MANUAL). WHEN YOU ANSWER THIS QUESTION WITH A "Y", THE RUNTIME SERVICES WILL ASK FOR THE NUMBER OF UNITS (IN DECIMAL).

AFTER INITIAL STARTING OF THE PROGRAM (START COMMAND TO THE DIAGNOSTIC SUPERVISOR), THE PROGRAM WILL ISSUE THE "CHANGE HW?" QUESTION TO ASK IF THE HARDWARE PARAMETERS ARE TO BE CHANGED (BY THE OPERATOR).

ON A "N" (NO) RESPONSE TO THE "CHANGE HW?" QUESTION, THE DIAGNOSTIC WILL RUN USING THE DEFAULT VALUES FOR ALL QUESTIONS. THE DEFAULT ADDRESS AND VECTOR ARE:

TSBA/TSD8 = 172520, VECTOR = 224

ON A "Y" (YES) RESPONSE TO THE QUESTION, THE FOLLOWING QUESTIONS WILL THEN BE ASKED TO ALLOW THE OPERATOR TO SELECT THE UNITS TO BE TESTED. A VALUE, IF PRESENT, LOCATED TO THE LEFT OF THE QUESTION MARK IS THE DEFAULT VALUE THAT WILL BE TAKEN IF ONLY A CARRIAGE RETURN IS TYPED AS A RESPONSE. A "(D)" IN A QUESTION INDICATES THAT A DECIMAL NUMBER IS REQUIRED AS A RESPONSE. AN "(O)" INDICATES AN OCTAL NUMBER IS BEING SOLICITED. AN "(L)" INDICATES THAT A LOGICAL RESPONSE IS TO BE MADE: "Y" FOR YES, "N" FOR NO.

UNITS (D) ? <ENTER THE NUMBER OF M7196 CONTROLLERS
PRESENT TO BE TESTED>

UNIT 0

DEVICE ADDRESS (O) 172520 ? <ENTER THE ADDRESS OF THE
TSBA/TSD8 REGISTER>

VECTOR (O) 224 ? <ENTER ADDRESS OF INTERRUPT
VECTOR>

THE ADDRESS AND VECTOR QUESTIONS WILL BE ASKED FOR EACH OF THE NUMBER OF UNITS (CONTROLLERS) SPECIFIED IN THE "# UNITS?" QUESTION. LOGICAL UNIT NUMBERS ARE ASSIGNED IN ORDER, BEGINNING AT 0. UP TO FOUR UNITS CAN BE SELECTED FOR TESTING AS FOLLOWS:
UP TO 4 TSV05 CONTROLLERS PER LSI-11 AND UP TO 2 DRIVES PER CONTROLLER

2.5 SOFTWARE QUESTIONS

AFTER YOU HAVE ANSWERED THE HARDWARE QUESTIONS OR AFTER A RESTART OR CONTINUE COMMAND, THE RUNTIME SERVICES WILL ASK FOR SOFTWARE PARAMETERS. THESE PARAMETERS WILL GOVERN SOME DIAGNOSTIC SPECIFIC OPERATION MODES. YOU WILL BE PROMPTED BY "CHANGE SW (L) ?" IF YOU WISH TO CHANGE ANY PARAMETERS, ANSWER BY TYPING "Y". THE SOFTWARE QUESTIONS AND THE DEFAULT VALUES ARE DESCRIBED IN THE NEXT PARAGRAPH(S).

THE FOLLOWING QUESTIONS ARE ASKED ON A START, RESTART, OR CONTINUE. THEY ALLOW FLEXIBILITY IN THE WAY THE PROGRAM BEHAVES.

CHANGE SW (L) ? <TYPE Y TO CAUSE THE FOLLOWING
QUESTIONS TO BE ASKED>

INHIBIT ITERATIONS (L) N ? <TYPE "Y" TO PREVENT MULTIPLE
ITERATIONS OF CERTAIN TESTS.
THIS CAUSES EACH TEST PASS TO
RUN AS QUICKLY AS POSSIBLE.
ONLY QUICK-RUNNING LOGIC
TESTS USE MULTIPLE
ITERATIONS.>

2.6 EXTENDED P-TABLE DIALOGUE

WHEN YOU ANSWER THE HARDWARE QUESTIONS, YOU ARE BUILDING ENTRIES IN A TABLE THAT DESCRIBES THE DEVICES UNDER TEST. THE SIMPLEST WAY TO BUILD THIS TABLE IS TO ANSWER ALL QUESTIONS FOR EACH UNIT TO BE TESTED. IF YOU HAVE A MULTIPLEXED DEVICE SUCH AS A MASS STORAGE CONTROLLER WITH SEVERAL DRIVES OR A COMMUNICATION DEVICE WITH SEVERAL LINES, THIS BECOMES TEDIOUS SINCE MOST OF THE ANSWERS ARE REPETITIOUS.

TO ILLUSTRATE A MORE EFFICIENT METHOD, SUPPOSE YOU ARE TESTING A DEVICE, THE XY11. SUPPOSE THIS DEVICE CONSISTS OF A CONTROL MODULE WITH EIGHT UNITS (SUB-DEVICES) ATTACHED TO IT. THESE UNITS ARE DESCRIBED BY THE OCTAL NUMBERS 0 THROUGH 7. THERE IS ONE HARDWARE PARAMETER THAT CAN VARY AMONG UNITS CALLED THE Q-FACTOR. THIS Q-FACTOR MAY BE 0 OR 1. BELOW IS A SIMPLE WAY TO BUILD A TABLE FOR ONE XY11 WITH EIGHT UNITS.

UNITS (0) ? 8<CR>

UNIT 1
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 0<CR>
Q-FACTOR (0) 0 ? 1<CR>

UNIT 2
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 1<CR>
Q-FACTOR (0) 1 ? 0<CR>

UNIT 3
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 2<CR>
Q-FACTOR (0) 0 ? <CR>

UNIT 4
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 3<CR>
Q-FACTOR (0) 0 ? <CR>

UNIT 5
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 4<CR>
Q-FACTOR (0) 0 ? <CR>

UNIT 6
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 5<CR>
Q-FACTOR (0) 0 ? <CR>

UNIT 7
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 6<CR>
Q-FACTOR (0) 0 ? 1<CR>

UNIT 8
CSR ADDRESS (0) 160000<CR>
SUB-DEVICE # (0) ? 7<CR>
Q-FACTOR (0) 1 ? <CR>

NOTICE THAT THE DEFAULT VALUE FOR THE Q-FACTOR CHANGES WHEN A NON-DEFAULT RESPONSE IS GIVEN. BE CAREFUL WHEN SPECIFYING MULTIPLE UNITS!

AS YOU CAN SEE FROM THE ABOVE EXAMPLE, THE HARDWARE PARAMETERS DO NOT VARY SIGNIFICANTLY FROM UNIT TO UNIT. THE PROCEDURE SHOWN IS NOT VERY EFFICIENT.

THE RUNTIME SERVICES CAN TAKE MULTIPLE UNIT SPECIFICATIONS HOWEVER.
LET'S BUILD THE SAME TABLE USING THE MULTIPLE SPECIFICATION
FEATURE.

* UNITS (0) ? 8<CR>

UNIT 1
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 0,1<CR>
Q-FACTOR (0) 0 ? 1,0<CR>

UNIT 3
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 2-5<CR>
Q-FACTOR (0) 0 ? 0<CR>

UNIT 7
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 6,7<CR>
Q-FACTOR (0) 0 ? 1<CR>

AS YOU CAN SEE IN THE ABOVE DIALOGUE, THE RUNTIME SERVICES WILL BUILD AS MANY ENTRIES AS IT CAN WITH THE INFORMATION GIVEN IN ANY ONE PASS THROUGH THE QUESTIONS. IN THE FIRST PASS, TWO ENTRIES ARE BUILT SINCE TWO SUB-DEVICES AND Q-FACTORS WERE SPECIFIED. THE SERVICES ASSUME THAT THE CSR ADDRESS IS 160000 FOR BOTH SINCE IT WAS SPECIFIED ONLY ONCE. IN THE SECOND PASS, FOUR ENTRIES WERE BUILT. THIS IS BECAUSE FOUR SUB-DEVICES WERE SPECIFIED. THE "-" CONSTRUCT TELLS THE RUNTIME SERVICES TO INCREMENT THE DATA FROM THE FIRST NUMBER TO THE SECOND. IN THIS CASE, SUB-DEVICES 2, 3, 4 AND 5 WERE SPECIFIED. (IF THE SUB-DEVICE WERE SPECIFIED BY ADDRESSES, THE INCREMENT WOULD BE BY 2 SINCE ADDRESSES MUST BE ON AN EVEN BOUNDARY.) THE CSR ADDRESSES AND Q-FACTORS FOR THE FOUR ENTRIES ARE ASSUMED TO BE 160000 AND 0 RESPECTIVELY SINCE THEY WERE ONLY SPECIFIED ONCE. THE LAST TWO UNITS ARE SPECIFIED IN THE THIRD PASS.

THE WHOLE PROCESS COULD HAVE BEEN ACCOMPLISHED IN ONE PASS AS SHOWN BELOW.

* UNITS (0) ? 8<CR>

UNIT 1
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 0-7<CR>
Q-FACTOR (0) 0 ? 0,1,0,...,1,1<CR>

AS YOU CAN SEE FROM THIS EXAMPLE, NULL REPLIES (COMMAS ENCLOSING A NULL FIELD) TELL THE RUNTIME SERVICES TO REPEAT THE LAST REPLY.

2.7 QUICK START-UP PROCEDURE (XXDP+)

TO START-UP THIS PROGRAM:

1. BOOT XXDP+.
2. TYPE "R NAME", WHERE NAME IS THE NAME OF THE BIN OR BIC FILE FOR THIS PROGRAM.
3. TYPE "START".
4. ANSWER THE "CHANGE MW" QUESTION WITH "Y".
5. ANSWER ALL THE HARDWARE QUESTIONS.
6. ANSWER THE "CHANGE SW" QUESTION WITH "N".

WHEN YOU FOLLOW THIS PROCEDURE YOU WILL BE USING ONLY THE DEFAULTS FOR FLAGS AND SOFTWARE PARAMETERS. THESE DEFAULTS ARE DESCRIBED IN SECTIONS 2.3 AND 2.5.

3 0 ERROR INFORMATION

3 1 TYPES OF ERROR MESSAGES

THERE ARE THREE LEVELS OF ERROR MESSAGES THAT MAY BE ISSUED BY A DIAGNOSTIC: GENERAL, BASIC AND EXTENDED. GENERAL ERROR MESSAGES ARE ALWAYS PRINTED UNLESS THE "IER" FLAG IS SET (SECTION 2.3). THE GENERAL ERROR MESSAGE IS OF THE FORM:

NAME TYPE NUMBER ON UNIT NUMBER TST NUMBER PC:XXXXXX
ERROR MESSAGE

WHERE: NAME = DIAGNOSTIC NAME
TYPE = ERROR TYPE (SYS FATAL, DEV FATAL, HARD OR SOFT)
NUMBER = ERROR NUMBER
UNIT NUMBER = 0 - N (N IS LAST UNIT IN PTABLE)
TST NUMBER = TEST AND SUBTEST WHERE ERROR OCCURRED
PC:XXXXXX = ADDRESS OF ERROR MESSAGE CALL

BASIC ERROR MESSAGES ARE MESSAGES THAT CONTAIN SOME ADDITIONAL INFORMATION ABOUT THE ERROR. THESE ARE ALWAYS PRINTED UNLESS THE "IER" OR "IBR" FLAGS ARE SET (SECTION 2.3). THESE MESSAGES ARE PRINTED AFTER THE ASSOCIATED GENERAL MESSAGE.

EXTENDED ERROR MESSAGES CONTAIN SUPPLEMENTARY ERROR INFORMATION SUCH AS REGISTER CONTENTS OR GOOD/BAD DATA. THESE ARE ALWAYS PRINTED UNLESS THE "IER", "IBR" OR "IXE" FLAGS ARE SET (SECTION 2.3). THESE MESSAGES ARE PRINTED AFTER THE ASSOCIATED GENERAL ERROR MESSAGE AND ANY ASSOCIATED BASIC ERROR MESSAGES.

3.2 SPECIFIC ERROR MESSAGES

BELOW ARE SAMPLE ERROR MESSAGES. EACH ERROR MESSAGE REPRESENTS DIFFERENT TYPES OF ERRORS DETECTED BY THIS DIAGNOSTIC.

ERROR MESSAGE EXAMPLE 1

THIS ERROR IS INDICATIVE OF AN INCORRECT REGISTER OR STATUS WORD RETURNED TO THE DIAGNOSTIC. THE FIRST PART DEFINES THE TEST FUNCTION AND UNIT THAT FAILED. THE SECOND PART PROVIDES THE REGISTER BITS AND THEIR MNEMONICS FOR THE INCORRECT REGISTER OR STATUS WORDS. THE THIRD PART IS THE EXPECTED AND RECEIVED DATA.

TST: 016 FIFO EXERCISER TEST
CVTSA WRD ERR 01610 ON UNIT 00 TST 016 SUB 002 PC: 040624
FIFO STATUS (IN WORD 9) INCORRECT AFTER WRITE FIFO

TAPE BUS SIGNALS IN WORD #8: - DESIGNATOR <BIT #>
PARERR<15> IEOT <12> IFMK <9> IRDY<6> IRWD<2>
IRESV2<14> IIDENT<11> IHER <8> IONL<5> IFBY<1>
IRESV1<13> ICER <10> ISPEED<7> ILDP<4> IFPT<0>

TAPE BUS SIGNALS IN WORD #9:
DATMIS<7> ILW<6> OUTRDY<5> INRDY<4>

MESSAGE BUFFER ADDRESS = 047352

MESSAGE BUFFER CONTENTS:

WORD #0 EXPD:	100020	RECV:	100020	XOR:	000000
WORD #1 EXPD:	000012	RECV:	000012	XOR:	000000
WORD #2 EXPD:	000000	RECV:	000000	XOR:	000000
WORD #3 EXPD:	000010	RECV:	000010	XOR:	000000
WORD #4 EXPD:	000000	RECV:	000000	XOR:	000000
WORD #5 EXPD:	000000	RECV:	000000	XOR:	000000
WORD #6 EXPD:	000000	RECV:	000000	XOR:	000000
WORD #7 EXPD:	000000	RECV:	000000	XOR:	000000
WORD #8 EXPD:	070217	RECV:	070217	XOR:	000000
WORD #9 EXPD:	000074	RECV:	000034	XOR:	000040

ERROR MESSAGE EXAMPLE 2

THIS ERROR SHOWS A FATAL FUNCTION ERROR FROM THE TAPE DRIVE. IN THIS INSTANCE AN UNRECOVERABLE ERROR OCCURED WHICH INDICATES THAT THE CONTROLLER MAY BE DEFECTIVE

CVTSA WRD ERR 00159 ON UNIT 00 TST 001 SUB 005 PC: 026202
TSSR NOT CORRECT AFTER SPACE RECORDS COMMAND

TSSR = 100214

TSSR BITS SET: SC, SSR

TERMINATION CLASS CODE = UNRECOVERABLE ERROR

PACKET ADDRESS = 026420

PACKET WORD # = 140010

PACKET WORD # = 000010

PACKET WORD # = 000000

PACKET WORD # = 000024

ERROR MESSAGE EXAMPLE 3

THIS ERROR SHOWS THAT THE MOTION BIT DID NOT GET SET WHILE DOING A
REWIND WITH EXTENDED FEATURES MODE ENABLED.

CVTS W70 ERR 00121 ON UNIT 00 TST 001 SUB 002 P: 023306
MOT BIT (XST0) NOT SET DURING REWIND (EXTENDED FEATURES MODE)
EXP0 060312 RECV 000112 XOR 000200

4.0 PERFORMANCE AND PROGRESS REPORTS

AT THE END OF EACH PASS, THE PASS COUNT IS GIVEN ALONG WITH THE
TOTAL NUMBER OF ERRORS REPORTED SINCE THE DIAGNOSTIC WAS STARTED.
THE "EOP" SWITCH CAN BE USED TO CONTROL HOW OFTEN THE END
OF PASS MESSAGE IS PRINTED. SECTION 2.2 DESCRIBES SWITCHES.

SUCCESSFUL RUN EXAMPLE (LSI 11)

DR>STA/FLA-PNT MOE

UNITS (D) ? 1

UNIT 0

DEVICE ADDRESS (0) 172520 ? <CR>

VECTOR (0) 224 ? <CR>

CHANGE SW (L) ? N<CR>

THE ABOVE COMMAND WILL START THE DIAGNOSTIC. THE COMMAND HAS TWO
SWITCHES ON WHICH ARE "PRINT EACH TEST NBR AS EXECUTED" AND "HALT ON
ERROR".

TST: 001 INITIALIZE #1
TST: 002 WRAP DATA HIGH BYTE TEST
TST: 003 WRAP DATA LOW BYTE TEST
TST: 004 RAM TEST
TST: 005 INITIALIZE 2 TEST
TST: 006 COMMAND REJECT TEST
TST: 007 WRITE CHARACTERISTICS TEST
TST: 008 VOLUME CHECK
TST: 009 COMPLETION INTERRUPT TEST
TST: 010 BASIC PACKET PROTOCOL TEST
TST: 011 NON-TAPE-MOTION COMMANDS TEST

0 ERRORS

NOTE: THE DIAGNOSTIC WILL RUN CONTINUOUSLY UNLESS A PASS NUMBER LIMIT HAS BEEN
SPECIFIED WITH THE "/PASS:" SWITCH

PROGRAM RUN TIMES

THE AVERAGE RUN TIMES OF THE PROGRAM ARE LISTED BELOW. THESE FIGURES ARE TO BE USED AS A GUIDE. THE TIMING WAS DONE ON A LSI-11 PROCESSOR WITH A LA34 CONSOLE.

THE PROGRAM RUNS IN TWO MODES; NO ITERATIONS AND DEFAULT MODE. IN THE NO ITERATIONS MODE, EACH TEST IS RUN ONCE, WITH NO ITERATIONS. IN THE DEFAULT MODE EACH TEST IS REPEATED BY THE NUMBER OF TIMES INDICATED BY THE ITERATION COUNT. NO ITERATIONS MODE IS SELECTED BY ANSWERING THE INHIBIT ITERATIONS QUESTION WITH A "Y" (YES).

TEST NUMBER	N/I SECS.	ITER SECS	DEF SECS.
1	1	30	29
2	1	10	9
3	1	8	7
4	25	120	95
5	5	140	135
6	25	475	450
7	20	20	0
8	1	10	9
9	20	20	0
10	1	2	1
11	8	11	3

THE TIMES REQUIRED TO RUN TESTS 1 THROUGH 12 IN ONE COMMAND:

Q.V. 1 MIN 57 SECONDS
 DEFAULT 12 MINS

5.0 DEVICE INFORMATION TABLES

WHenever the program is started, via the STA(RT) command, the supervisor requests the following P-tables parameter changes:

CHANGE HW (L) ?

UNITS (D) ? <ENTER THE NUMBER OF M7196 CONTROLLERS
PRESENT TO BE TESTED>

UNIT 0

DEVICE ADDRESS (0) 172520 ? <ENTER THE ADDRESS OF THE
TSBA/TSDB REGISTER>

VECTOR (0) 224 ? <ENTER ADDRESS OF INTERRUPT
VECTOR>

THE ADDRESS AND VECTOR QUESTIONS WILL BE ASKED FOR EACH OF THE NUMBER OF UNITS (CONTROLLERS) SPECIFIED IN THE "# UNITS?" QUESTION. LOGICAL UNIT NUMBERS ARE ASSIGNED IN ORDER, BEGINNING AT 0. UP TO FOUR UNITS CAN BE SELECTED FOR TESTING.

IN ADDITION, ON A START, RESTART OR CONTINUE THE SUPERVISOR REQUESTS CHANGES TO THE SOFTWARE OPERATING PARAMETERS, AS FOLLOWS:

CHANGE SW (L) ?

INHIBIT ITERATIONS (L) N ?

6.0 TEST SUMMARIES

TEST 1: BUS RESET TEST

THIS TEST VERIFIES THAT THE M7196 MODULE'S DEVICE REGISTERS ARE ACCESSIBLE ON THE BUS (SUBTEST 1) AND THEN CHECKS THAT THE BUILT-IN INITIALIZATION SELF-TEST MICRODIAGNOSTIC DID NOT FIND ANY BASIC PROBLEMS IN THE MODULE. AREAS OF LOGIC TESTED BY THE SELF-TEST SEQUENCE ARE AS FOLLOWS: ROM AND PIPELINE REGISTER, SEQUENCER, INTERNAL BUSES, 2901 MICROPROCESSOR, AND, RAM. THIS TEST INITIALIZES THE CONTROLLER BY ISSUING THE BUS INIT SIGNAL VIA A RESET INSTRUCTION, OR BY WRITING INTO THE TSSR REGISTER, WAITS A PERIOD OF TIME (TO ALLOW THE CONTROLLER'S INITIALIZATION MICRODIAGNOSTIC SEQUENCE TO BE COMPLETED), AND THEN CHECKS THE CONTENTS OF THE TSSR REGISTER. SUCCESSFUL INITIALIZATION IS INDICATED BY SUBSYSTEM READY (SSR) AND NEED BUFFER ADDRESS (NBA) BITS BEING SET (1) AND ALL OTHER BITS (EXCEPT A17 AND A16 AND OFL, WHICH ARE IGNORED FOR THIS TEST) BEING CLEAR (0). IF THE CONTENTS OF TSSR ARE NOT AS EXPECTED, AN ERROR REPORT IS ISSUED LISTING THE EXPECTED DATA, ACTUAL DATA, AND THE DISCREPANCIES. THE ERROR REPORT ANALYZES THE TSSR CONTENTS AND DISCERNs AND REPORTS ONE OF THREE POSSIBILITIES:

1. TSSR CONTENTS ARE AMBIGUOUS (ANY OF BITS 11-14 ARE SET, OR STATES OF SSR AND SC BITS DO NOT CORRESPOND TO THE APPARENT ERROR CODE IN BITS 0-5); INDICATES THAT THE TSSR CONTENT CANNOT BE TRUSTED. INDICATES A CATASTROPHIC CONTROLLER MALFUNCTION. THIS IS A FATAL ERROR (EXECUTION IS ABORTED). FIELD ACTION WOULD BE TO REPLACE THE M7196. IF THE M7196 ITSELF IS BEING DEBUGGED, THE PROGRAM SHOULD BE RESTARTED WITH LOOP ON ERROR ENABLED IN ORDER TO PROBE FOR THE PROBLEM.
2. SSR = 0, SC = 0 AND THE ERROR CODE IN BITS 0-5 IS IN THE RANGE 17-13: THIS IS A FATAL ERROR. THE ERROR CODE IS DECODED AND THE APPROPRIATE DESCRIPTION GIVEN. INDICATES THAT A SERIOUS PROBLEM EXISTS.

TEST 2: WRAP DATA - HIGH BYTE

THIS TEST VERIFIES OPERATION OF:

1. PART OF THE LSI-11 BUS INTERFACE SECTION OF THE M7196 MODULE: PART OF THE INPUT FILE (TSDB HIGH BYTE), PART OF THE OUTPUT FILE (TSSR HIGH BYTE AND TSBA, BOTH BYTES), PART OF THE DCO05 TRANSCEIVER CIRCUITS (ADDRESS DECODER, BDAL DRIVERS, HIGH BYTE OF INTERNAL DAL BUS DRIVERS), AND BASIC PROGRAMMED I/O CONTROL SEQUENCES AND LOGIC;
2. PART OF 2901 MICROPROCESSOR ELEMENTS (Q-REGISTER, REGISTER 0, ROTATE AND NEGATE FUNCTIONS
3. Y AND SOURCE BUSES;
4. BASIC MICROPROGRAM SEQUENCES.

THE PROGRAM WRITES A TEST DATA BYTE INTO THE HIGH BYTE OF TSDB, WAITS FOR THE SSR BIT IN TSSR TO SET, THEN CHECKS THE CONTENTS OF BOTH TSBA AND TSSR. THE MODULE IS FUNCTIONING CORRECTLY IF DATA WRITTEN APPEARS IN BOTH BYTES OF TSBA AND THE FINAL CONTENT OF TSSR IS CORRECT (SAME AS AFTER INITIALIZATION EXCEPT FOR BITS 8 AND 9, WHICH SHOULD CONTAIN BITS 8 AND 9 OF THE DATA PATTERN WRITTEN. AN ERROR IS REPORTED AND A DESCRIPTIVE ANALYSIS GIVEN IF A DISCREPANCY IN TSBA OR TSSR IS DETECTED. THE ANALYSIS LISTS LIKELY FAULTY CANDIDATES FROM THE LOGIC ELEMENTS LISTED ABOVE. THE TEST IS REPEATED FOR ALL COMBINATIONS OF TEST DATA BYTES (0-377 OCTAL).

TEST 3: WRAP DATA - LOW BYTE

THIS TEST FURTHER VERIFIES OPERATION OF MANY OF THE SAME ELEMENTS TESTED IN TEST 2, AND ADDITIONALLY VERIFIES:

1. LOW BYTE OF THE TSDB INPUT FILE REGISTER.
2. LOW BYTE OF INTERNAL DAL BUS DRIVERS ON THE DC005 TRANSCEIVER CIRCUITS.
3. BASIC FUNCTIONING OF PARTS OF THE RAM.

THE PROGRAM WRITES A TEST DATA BYTE INTO THE LOW BYTE OF TSDB, WAITS FOR THE SSR BIT IN TSSR TO SET, THEN CHECKS THE CONTENTS OF BOTH TSBA AND TSSR. THE MODULE IS FUNCTIONING CORRECTLY IF DATA WRITTEN APPEARS IN BOTH BYTES OF TSBA AND THE FINAL CONTENT OF TSSR IS CORRECT (SAME AS AFTER INITIALIZATION EXCEPT FOR BITS 8 AND 9, WHICH SHOULD CONTAIN BITS 8 AND 9 OF THE DATA PATTERN WRITTEN. AN ERROR IS REPORTED AND A DESCRIPTIVE ANALYSIS GIVEN IF A DISCREPANCY IN TSBA OR TSSR IS DETECTED. THE ANALYSIS LISTS LIKELY FAULTY CANDIDATES FROM THE LOGIC ELEMENTS LISTED ABOVE. THE TEST IS REPEATED FOR ALL COMBINATIONS OF TEST DATA BYTES (0-377 OCTAL).

TEST 4: RAM TEST

THIS TEST VERIFIES THAT ALL LOCATIONS OF THE RAM ON THE M7196 CAN PROPERLY STORE AND READ BACK ALL DATA PATTERNS, AND THAT EACH RAM LOCATION IS UNIQUELY ADDRESSED (I.E., THAT ONE AND ONLY ONE LOCATION IS ACCESSED BY ANY PARTICULAR ADDRESS). THE BYPRODUCT OF THESE TESTS IS A VERIFICATION OF TWO REGISTERS IN THE 2901 AND THE CAPABILITY OF THE 2901 TO CORRECTLY PERFORM AN ADD.

TEST 5: SECOND INITIALIZATION TEST

THIS TEST VERIFIES THE SAME ELEMENTS AS DID INITIALIZATION TEST #1 AND ALSO CHECKS THAT CERTAIN PARTS OF RAM IS CLEARED TO ZERO AND THAT 2901 REGISTERS 10 AND 11 ARE ALSO CLEARED TO ZERO. THIS IS A CONFIDENCE CHECK OF A PART OF THE SELF-TEST SEQUENCE (I.E., THAT IT IS REALLY BEING EXECUTED). FOR EACH OF TWO SUBTESTS (ONE FOR INITIALIZING VIA A BUS INIT, THE OTHER FOR INITIALIZING BY WRITING INTO THE TSSR), THE FOLLOWING SEQUENCE IS PERFORMED:

1. EACH RAM LOCATION AND 2901 REGISTERS 10 AND 11 ARE SET TO ALL 1'S BY USING WRITES INTO THE TSDB REGISTER (LOW BYTE AND MAINTENANCE MODE WORD WRITES).
2. THE CONTROLLER IS INITIALIZED AND THE VARIOUS CHECKS ON THE TSSR DESCRIBED IN INITIALIZATION TEST #1 ARE PERFORMED.
3. #1'S (377 OCTAL) ARE WRITTEN INTO THE LOW BYTE OF TSDB, WHICH SHOULD CAUSE RAM LOCATION 0 TO BE WRITTEN TO ALL 1'S SINCE 2901 REGISTERS 10 AND 11, SPECIFYING THE RAM ADDRESS, SHOULD BE 0. RAM LOCATION 0 IS VERIFIED BY WRITING A WORD OF ZEROS INTO THE TSDB. THE RESULTING LOW BYTE OF TSBA SHOULD CONTAIN ALL 1'S.
4. THE ENTIRE RAM IS SCANNED. LOCATION 0 SHOULD CONTAIN ALL 1'S AND THE REMAINING LOCATIONS, EXCEPT FOR THE MESSAGE BUFFER IMAGE AREA, SHOULD CONTAIN 0. DISCREPANCIES ARE REPORTED. AN ERROR AT THIS POINT IS MOST LIKELY DUE TO A ROM, PIPELINE OR SEQUENCER PROBLEM OR A TIMING PROBLEM.

TEST 6: COMMAND REJECT

THIS TEST VERIFIES THAT ALL COMMANDS OTHER THAN WRITE CHARACTERISTICS ARE REJECTED DUE TO THE NEED BUFFER ADDRESS (NBA) BIT BEING SET IN TSSR, AND THAT THE TSBA AND TSSR REGISTERS ARE LEFT IN THE PROPER STATE AFTER EACH COMMAND IS REJECTED. THIS TEST CHECKS MICROPROCESSOR SEQUENCING, BASIC COMMAND DECODING AND DATA DMA HANDLING. THIS TEST CONTAINS TWO SUBTESTS: SUBTEST 1 SEQUENCES THROUGH ALL COMMAND WORDS (OTHER THAN WRITE CHARACTERISTICS) WITH THE INTERRUPT ENABLE (IE) BIT CLEAR AND VERIFIES THAT AN INTERRUPT IS NOT GENERATED BY THE REJECTED COMMAND; SUBTEST 2 PERFORMS SIMILARLY TO SUBTEST 1 BUT SETS THE IE BIT IN EACH COMMAND WORD AND VERIFIES THAT AN INTERRUPT IS GENERATED WHEN THE COMMAND IS REJECTED.

TEST 7: WRITE CHARACTERISTICS

THIS TEST VERIFIES BASIC OPERATION OF THE WRITE CHARACTERISTICS COMMAND. IT VERIFIES THAT THE COMMAND BLOCK AND CHARACTERISTICS DATA BLOCK ARE FETCHED PROPERLY FROM CPU MEMORY, THE NEED BUFFER ADDRESS (NBA) BIT IN TSSR IS HANDLED PROPERLY, AND THAT A PROPER MESSAGE PACKET IS STORED, WHERE APPROPRIATE. THIS TEST DOES NOT CHECK THAT THE VARIOUS FUNCTIONS ENABLED BY CHARACTERISTIC MODE DATA BITS OPERATE PROPERLY; THE FUNCTIONING OF THESE BITS IS VERIFIED IN SUBSEQUENT TESTS. ALL COMMANDS EXECUTED IN THIS TEST HAVE THE INTERRUPT ENABLE (IE) BIT CLEARED TO ZERO, SO NO INTERRUPTS SHOULD BE GENERATED. HOWEVER, THE PROGRAM RUNS AT PROCESSOR PRIORITY 0, WITH THE INTERRUPT SERVICE ROUTINE SET UP TO FLAG UNEXPECTED INTERRUPTS. IF AN INTERRUPT OCCURS, A PROBLEM EXISTS IN EITHER THE LSI-11 BUS INTERFACE SECTION OR IN THE ROM OR PIPELINE.

TEST 8: VOLUME CHECK

THIS TEST VERIFIES THAT THE VOLUME CHECK (VCK) BIT, A FLAG HELD WITHIN THE M7196 AND APPEARING IN XST0, IS SET BY INITIALIZE AND CLEARED BY EXECUTING A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT SET. IT IS ALSO VERIFIED THAT A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT CLEAR DOES NOT AFFECT THE STATE OF THE VOLUME CHECK BIT. THE ACTUAL FUNCTION OF VOLUME CHECK, THAT OF PREVENTING OR ALLOWING A TAPE MOTION COMMAND DEPENDING UPON WHETHER VOLUME CHECK IS SET OR CLEAR, IS NOT CHECKED BY THIS TEST; THIS FUNCTIONALITY IS CHECKED IN THE INDIVIDUAL TESTS OF TAPE MOTION COMMANDS.

TEST 9: COMPLETION INTERRUPT

THIS TEST VERIFIES THAT AN INTERRUPT IS GENERATED AT THE COMPLETION OF THE WRITE CHARACTERISTICS COMMAND IF THE INTERRUPT ENABLE (IE) BIT IN THE COMMAND HEADER WORD IS SET. THIS TEST CHECKS THE FUNCTIONING OF THE INTERRUPT LOGIC AND BASIC PROCESSING OF THE IE BIT.

THE SEQUENCES OF TEST 7 ARE REPEATED, EXCEPT THAT THE INTERRUPT SERVICE ROUTINE IS SET UP TO EXPECT INTERRUPTS AND EACH WRITE CHARACTERISTICS COMMAND IS ISSUED WITH THE IE BIT SET (1). IT IS VERIFIED, WHERE APPROPRIATE, THAT THE IE STATUS BIT IN XSTO OF ANY MESSAGE PACKET IS SET AND THAT A COMPLETION INTERRUPT IS GENERATED. FINALLY, A SEQUENCE OF TWO COMMANDS ARE ISSUED, THE FIRST WITH IE=1 AND THE SECOND WITH IE=0. IT IS VERIFIED THAT NO INTERRUPT IS GENERATED AFTER THE SECOND COMMAND AND THAT THE IE BIT IN XSTO IS 0.

TEST 10: BASIC PACKET PROTOCOL

THIS TEST VERIFIES BASIC OPERATION OF THE MESSAGE BUFFER RELEASE COMMAND, THE FUNCTION OF THE ACK BIT IN THE COMMAND HEADER WORD, AND THE REGISTER MODIFICATION REFUSED (RMR) LOGIC.

TEST 11: NON-TAPE MOTION COMMANDS

THIS TEST VERIFIES PROPER OPERATION OF THE INITIALIZE COMMAND. TWO SUBTESTS ARE USED. THE FIRST VERIFIES THAT THE COMMAND RUNS TO COMPLETION AND STORES A VALID MESSAGE PACKET. THE SECOND VERIFIES THAT NON-ZERO VALUES IN THE COMMAND MODE FIELD CAUSES COMMAND REJECT.

7.0 MAINTENANCE HISTORY

REVISION A - MARCH 1982

REVISION B - JUNE 1984

MINOR CHANGES FOR THE ORION CPU (11/??).
ELIMINATED THE MESSAGE DESCRIBING THE CPU TYPE.

```

2          .TITLE  TSV2  PROGRAM HEADER
3          .SBTTL  PROGRAM HEADER
4 000000    .PSECT  ABS
5
11         .MCALL  SVC
12 000000    SVC                      ; INITIALIZE SUPERVISOR MACROS
13         .ENABLE LC
14         .MLIST  BEX,CND
20 000000    .ENABL  ABS,AMA
21         .*2000
22 002000    BGNMOD  TSV2
23         TSV2::
24
25         ;**
26         ; THE PROGRAM HEADER IS THE INTERFACE BETWEEN
27         ; THE DIAGNOSTIC PROGRAM AND THE SUPERVISOR.
28         ;--
29
30         POINTER BGNSW,BGNSFT,BGNAU,BGNDU,BGNRPT
31 002000    HEADER  CVTSA,8,0,655.,0
          L$NAME::                      ;DIAGNOSTIC NAME
          .ASCII /C/
          .ASCII /V/
          .ASCII /T/
          .ASCII /S/
          .ASCII /A/
          .BYTE  0
          .BYTE  0
          .BYTE  0
          L$REV::                      ;REVISION LEVEL
          .ASCII /B/
          L$DEPO::                      ;0
          .ASCII /0/
          L$UNIT::                      ;NUMBER OF UNITS
          .WORD  0
          L$TIML::                      ;LONGEST TEST TIME
          .WORD  655.
          L$HPCP::                      ;PTR. TO H.W. PTABLE
          .WORD  L$HARD
          L$SPCP::                      ;PTR. TO S.W. PTABLE
          .WORD  L$SOFT
          L$HPTP::                      ;PTR. TO DEF. H.W. PTABLE
          .WORD  L$HW
          L$SPTP::                      ;PTR. TO S.W. PTABLE
          .WORD  L$SW
          L$LADP::                      ;DIAG. END ADDRESS
          .WORD  L$LAST
          L$STA::                      ;RESERVED FOR APT STATS
          .WORD  0
          L$CO::                      ;DIAGNOSTIC TYPE
          .WORD  0
          L$DTYP::                      ;APT EXPANSION
          .WORD  0
          L$DTP::                      ;PTR. TO DISPATCH TABLE

```

002040	002124		.WORD	L\$DISPATCH	
002042		L\$PRIO::			;DIAGNOSTIC RUN PRIORITY
002042	000000		.WORD	0	
002044		L\$ENVI::			;FLAGS DESCRIBE HOW IT WAS SETUP
002044	000000		.WORD	0	
002046		L\$EXP1::			;EXPANSION WORD
002046	000C00		.WORD	0	
002050		L\$MREV::			;SVC REV AND EDIT #
002050	003		.BYTE	C\$REVISION	
002051	003		.BYTE	C\$EDIT	
002052		L\$EF::			;DIAG. EVENT FLAGS
002052	00C000		.WORD	0	
002054	000000		.WORD	0	
002056		L\$SPC::			
002056	000000		.WORD	0	
002060		L\$DEVP::			; POINTER TO DEVICE TYPE LIST
002060	003400		.WORD	L\$DVTYP	
002062		L\$REPC::			;PTR. TO REPORT CODE
002062	022620		.WORD	L\$RPT	
002064		L\$EXP4::			
002064	000000		.WORD	0	
002066		L\$EXP5::			
002066	000000		.WORD	0	
002070		L\$AUT::			;PTR. TO ADD UNIT CODE
002070	022306		.WORD	L\$AU	
002072		L\$DUT::			;PTR. TO DROP UNIT CODE
002072	022404		.WORD	L\$DU	
002074		L\$LUN::			;LUN FOR EXERCISERS TO FILL
002074	000000		.WORD	0	
002076		L\$DESP::			;POINTER TO DIAG. DESCRIPTION
002076	003406		.WORD	L\$DESC	
002100		L\$LOAD::			;GENERATE SPECIAL AUTOLOAD EMT
002100	104035		EMT	E\$LOAD	
002102		L\$ETP::			;POINTER TO ERRIBL
002102	000000		.WORD	0	
002104		L\$ICP::			;PTR. TO .IT CODE
002104	021512		.WORD	L\$INIT	
002106		L\$CCP::			;PTR. TO CLEAN-UP CODE
002106	022572		.WORD	L\$CLEAN	
002110		L\$ACP::			;PTR. TO AUTO CODE
002110	022512		.WORD	L\$AUTO	
002112		L\$PRT::			;PTR. TO PROTECT TABLE
002112	021502		.WORD	L\$PROT	
002114		L\$TEST::			;TEST NUMBER
002114	000000		.WORD	0	
002116		L\$DLY::			;DELAY COUNT
002116	000000		.WORD	0	
002120		L\$HIME::			;PTR. TO HIGH MEM
002120	000000		.WORD	0	

TSV2 - PROGRAM HEADER MACRO M1113 14-JUN-84 15:15
PROGRAM HEADER

SEQ 0024

```

35
36          .SBTTL DISPATCH TABLE
37
38          ;**
39          ; THE DISPATCH TABLE CONTAINS THE STARTING ADDRESS OF EACH TEST.
40          ; IT IS USED BY THE SUPERVISOR TO DISPATCH TO EACH TEST.
41          ; -
42
43          DISPATCH 11
44          002122 000013          .WORD 11
45          002124          L$DISPATCH::
46          002124 023402          .WORD T1
47          002126 023622          .WORD T2
48          002130 024320          .WORD T3
49          002132 025012          .WORD T4
50          002134 026346          .WORD T5
51          002136 027452          .WORD T6
52          002140 030724          .WORD T7
53          002142 034312          .WORD T8
54          002144 035216          .WORD T9
55          002146 040342          .WORD T10
56          002150 043454          .WORD T11
57
58          .SBTTL DEFAULT HARDWARE P-TABLE
59
60          ;**
61          ; THE DEFAULT HARDWARE P-TABLE CONTAINS DEFAULT VALUES OF
62          ; THE TEST-DEVICE PARAMETERS. THE STRUCTURE OF THIS TABLE
63          ; IS IDENTICAL TO THE STRUCTURE OF THE RUN-TIME P-TABLE.
64          ; -
65          BGNHW DFPTBL          ;DEFAULT HARD-P-TABLE
66          002152 000003          .WORD L10000-L$HW/2
67          002154          L$HW::
68          002154          DFPTBL::
69
70          002154 172520          .WORD 172520          ; 1ST (OF 2) REGISTERS.
71          002156 000224          .WORD 224          ; INTERRUPT VECTOR
72          002160 000200          .WORD PRI04          ; INTERRUPT PRIORITY.
73          002162          ENDPHW
74          002162          L10000:

```

TSV2 PROGRAM HEADER MACRO M1113 14-JUN-84 15:15
SOFTWARE P TABLE

SEQ 0025

```

62          .SBTTL  SOFTWARE P TABLE
63
64          ;**
65          ; THE SOFTWARE P-TABLE CONTAINS THE VALUES OF THE PROGRAM
66          ; PARAMETERS THAT CAN BE CHANGED BY THE OPERATOR.
67          ;*
68 002162          BGNSW  SFPTBL
002162          .WORD  L10001 L$SW/2
002164          L$SW::
002164          SFPTBL::
69
70 002164          TRANSTST::      .WORD  0          ; ENABLE TEST OF TRANSPORT(S) IF =1
71 002166          NOITS::         .WORD  0          ; INHIBIT ITERATION OPTION.
72                                     ; ... 0 = ITERATE.
73                                     ; ...NZ = INHIBIT ITERATE.
74 002170          LERRMAX::        .WORD  15.         ; LOCAL (PER TEST) ERROR LIMIT
75 002172          GERRMAX::        .WORD  200.        ; GLOBAL (PER UNIT) ERROR LIMIT
76 002174          ENDSW
002174          L10001:
77
78 002174          ENDMOD
79
80
81
82
83
84

```


TSV3 - GLOBAL AREAS
SOFTWARE P-TABLE

MACRO M1113 14-JUN-84 15:15

SEQ 0026

7
8
13
19
20 002174
002174
21
22
23
24
25
26
27
28
32 002174

.TITLE TSV3 GLOBAL AREAS
.SBTTL GLOBAL EQUATES SECTION

BGNMOD TSV3
TSV3::

.SBTTL GLOBAL EQUATES SECTION

; THE GLOBAL EQUATES SECTION CONTAINS PROGRAM EQUATES THAT
; ARE USED IN MORE THAN ONE TEST.

EQUALS . GET STANDARD EQUATES.

; BIT DEFINITIONS

100000	BIT15== 100000
040000	BIT14== 40000
020000	BIT13== 20000
010000	BIT12== 10000
004000	BIT11== 4000
002000	BIT10== 2000
001000	BIT09== 1000
000400	BIT08== 400
000200	BIT07== 200
000100	BIT06== 100
000040	BIT05== 40
000020	BIT04== 20
000010	BIT03== 10
000004	BIT02== 4
000002	BIT01== 2
000001	BIT00== 1

001000	BIT9== BIT09
000400	BIT8== BIT08
000200	BIT7== BIT07
000100	BIT6== BIT06
000040	BIT5== BIT05
000020	BIT4== BIT04
000010	BIT3== BIT03
000004	BIT2== BIT02
000002	BIT1== BIT01
000001	BIT0== BIT00

; EVENT FLAG DEFINITIONS

; EF32:EF17 RESERVED FOR SUPERVISOR TO PROGRAM COMMUNICATION

000040	EF.START== 32.	; START COMMAND WAS ISSUED
000037	EF.RESTART== 31.	; RESTART COMMAND WAS ISSUED
000036	EF.CONTINUE== 30.	; CONTINUE COMMAND WAS ISSUED
000035	EF.NEW== 29.	; A NEW PASS HAS BEEN STARTED
000034	EF.PWR== 28.	; A POWER-FAIL/POWER-UP OCCURRED

; PRIORITY LEVEL DEFINITIONS

TSV3 - GLOBAL AREAS
GLOBAL EQUATES SECTION

MACRO M1113 14-JUN-84 15:15

SEQ 0027

```

000340      ;
000300      PRI07== 340
000240      PRI06== 300
000200      PRI05== 240
000140      PRI04== 200
000100      PRI03== 140
000040      PRI02== 100
000000      PRI01== 40
           PRI00== 0

           ;
           ;OPERATOR FLAG BITS
           ;
000004      EVL==      4
000010      LOT==     10
000020      ADR==     20
000040      IDU==     40
000100      ISR==    100
000200      UAM==    200
000400      BOE==    400
001000      PNT==   1000
002000      PRI==   2000
004000      IXE==   4000
010000      IBE==  10000
020000      IER==  20000
040000      LOE==  40000
100000      MOE== 100000

```

33
34 002174

```

           ;KT11
           ;.SBTTL MEMORY MANAGEMENT DEFINITIONS
           ;*KT11 VECTOR ADDRESS
000250      MMVEC= 250
           ;*KT11 STATUS REGISTER ADDRESSES
177572      SR0=   177572
177574      SR1=   177574
177576      SR2=   177576
172516      SR3=   172516
           .IF NB
           ;*USER "I" PAGE DESCRIPTOR REGISTERS
           UIPDR0= 177600
           UIPDR1= 177602
           UIPDR2= 177604
           UIPDR3= 177606
           UIPDR4= 177610
           UIPDR5= 177612
           UIPDR6= 177614
           UIPDR7= 177616
           .IF NB
           ;*USER "D" PAGE DESCRIPTOR REGISTERS
           UDPDR0= 177620
           UDPDR1= 177622
           UDPDR2= 177624
           UDPDR3= 177626
           UDPDR4= 177630
           UDPDR5= 177632
           UDPDR6= 177634
           UDPDR7= 177636
           .ENDC

```

;DEFINE MEMORY MANAGEMENT REGISTERS

TSV3 GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
MEMORY MANAGEMENT DEFINITIONS

SEQ 0028

```
;*USER "I" PAGE ADDRESS REGISTERS
UIPAR0= 177640
UIPAR1= 177642
UIPAR2= 177644
UIPAR3= 177646
UIPAR4= 177650
UIPAR5= 177652
UIPAR6= 177654
UIPAR7= 177656
. IF NB
;*USER "D" PAGE ADDRESS REGISTERS
UDPAR0= 177660
UDPAR1= 177662
UDPAR2= 177664
UDPAR3= 177666
UDPAR4= 177670
UDPAR5= 177672
UDPAR6= 177674
UDPAR7= 177676
. ENDC
. ENDC
. IF NB
;*SUPERVISOR "I" PAGE DESCRIPTOR REGISTERS
SIPDR0= 172200
SIPDR1= 172202
SIPDR2= 172204
SIPDR3= 172206
SIPDR4= 172210
SIPDR5= 172212
SIPDR6= 172214
SIPDR7= 172216
. IF NB
;*SUPERVISOR "D" PAGE DESCRIPTOR REGISTERS
SDPDR0= 172220
SDPDR1= 172222
SDPDR2= 172224
SDPDR3= 172226
SDPDR4= 172230
SDPDR5= 172232
SDPDR6= 172234
SDPDR7= 172236
. ENDC
;*SUPERVISOR "I" PAGE ADDRESS REGISTERS
SIPAR0= 172240
SIPAR1= 172242
SIPAR2= 172244
SIPAR3= 172246
SIPAR4= 172250
SIPAR5= 172252
SIPAR6= 172254
SIPAR7= 172256
. IF NB
;*SUPERVISOR "D" PAGE ADDRESS REGISTERS
SDPAR0= 172260
SDPAR1= 172262
SDPAR2= 172264
SDPAR3= 172266
```

TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
MEMORY MANAGEMENT DEFINITIONS

SEQ 0029

```
SDPAR4= 172270
SDPAR5= 172272
SDPAR6= 172274
SDPAR7= 172276
.ENDC
.ENDC
; *KERNEL "I" PAGE DESCRIPTOR REGISTERS
172300 KIPDR0= 172300
172302 KIPDR1= 172302
172304 KIPDR2= 172304
172306 KIPDR3= 172306
172310 KIPDR4= 172310
172312 KIPDR5= 172312
172314 KIPDR6= 172314
172316 KIPDR7= 172316
; IF NB
; *KERNEL "D" PAGE
DESCRIPTOR REGISTERS
KOPDR0= 172320
KOPDR1= 172322
KOPDR2= 172324
KOPDR3= 172326
KOPDR4= 172330
KOPDR5= 172332
KOPDR6= 172334
KOPDR7= 172336
.ENDC
; *KERNEL "I" PAGE ADDRESS REGISTERS
172340 KIPAR0= 172340
172342 KIPAR1= 172342
172344 KIPAR2= 172344
172346 KIPAR3= 172346
172350 KIPAR4= 172350
172352 KIPAR5= 172352
172354 KIPAR6= 172354
172356 KIPAR7= 172356
; IF NB
; *KERNEL "D" PAGE ADDRESS REGISTERS
KOPAR0= 172360
KOPAR1= 172362
KOPAR2= 172364
KOPAR3= 172366
KOPAR4= 172370
KOPAR5= 172372
KOPAR6= 172374
KOPAR7= 172376
.ENDC
```

TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
TSV05 REGISTER AND PACKET DEFINITIONS

SEQ 0030

```

39          .SBTTL  TSV05 REGISTER AND PACKET DEFINITIONS
40
41          ;
42          ; SOME GENERAL EQUATES.
43          ;
44
45          000004      ERRVEC==      4          ; POINTER TO ERROR VECTOR FOR BUS TIME OUT.
46          000060      TTIVEC==     60          ; INTERRUPT VECTOR FOR CONSOLE INPUT
47          177560      TTICSR==    177560       ; BUS ADDRESS OF CONSOLE INPUT
48          177562      TTIBFR==    177562       ; CONSOLE INPUT DATA BUFFER
49          177520      BDVPCR==    177520       ; BDV11 PAGE CONTROL REGISTER
50
51          ;*
52          ;BIT DEFINITIONS FOR TSSR REGISTER
53          ;-
54
55          100000      SC=      BIT15          ;SPECIAL CONDITION
56          040000      BIE=     BIT14          ;BUS INTERFACE ERROR
57          020000      SCE=     BIT13          ;SANITY CHECK ERROR
58          010000      RMR=     BIT12          ;MODIFICATION REFUSED
59          004000      NXM=     BIT11          ;NONEXISTANT MEMORY ERROR
60          002000      NBA=     BIT10          ;NEED BUFFER ADDRESS
61          001400      HIADDR= BIT9:BIT8       ;EXTENDED ADDRESS BITS
62          000200      SSR=     BIT7           ;SUB SYSTEM READY
63          000100      OFL=     BIT6           ;OFF LINE BIT
64          000060      FATERR= BIT4:BIT5       ;FATAL TERMINATION ERROR CODES
65          000016      TERCLS= BIT3:BIT2:BIT1  ;TERMINATION CODES
66
67          ;*
68          ;
69          ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 0
70          ;(XST0)
71          ;
72          ;-
73
74          100000      XSOTMK= BIT15          ;TAPE MARK DETECTED
75          040000      XSORLS= BIT14          ;RECORD LENGTH SHORT
76          020000      XSOLET= BIT13          ;LOGICAL END OF TAPE
77          010000      XSORLL= BIT12          ;RECORD LENGTH LONG
78          004000      XSOMLE= BIT11          ;WRITE LOCK ERROR
79          002000      XSONEF= BIT10          ;NON EXECUTABLE FUNCTION
80          001000      XS0ILC= BIT9           ;ILLEGAL COMMAND
81          000400      XS0ILA= BIT8           ;ILLEGAL ADDRESS
82          000200      XSOMOT= BIT7           ;TAPE IN MOTION
83          000100      XS0ONL= BIT6           ;TRANSPORT ON LINE
84          000040      XS0IE=  BIT5           ;INTERRUPT ENABLE
85          000020      XS0VCK= BIT4           ;VOLUME CHECK BIT
86          000010      XS0PED= BIT3           ;PHASE ENCODED DRIVE
87          000004      XS0MLK= BIT2           ;WRITE LOCKED
88          000002      XS0BOT= BIT1           ;BEGINNING OF TAPE
89          000001      XS0EOT= BIT0           ;END OF TAPE

```

TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
TSV05 REGISTER AND PACKET DEFINITIONS

SEQ 0031

```

91      ;*
92      ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 1
93      ;(XST1)
94      ;-
95      100000      X1.DLT = BIT15      ;DATA LATE
96      040000      X1.SPARE= BIT14      ;NOT USED
97      020000      X1.COR = BIT13      ;CORRECTABLE DATA ERROR
98      017375      X1.MBZ = BIT12·BIT11·BIT10·BIT9·BIT7·BIT6·BIT5·BIT4·BIT3·BIT2·BIT0 ;ALWAYS 0
99      000400      X1.RBP = BIT8      ;READ BUS PARITY ERROR
100     000002      X1.UNC = BIT1      ;UNCORRECTABLE DATA OR HARD ERROR
101
102     ;*
103     ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 2
104     ;(XST2)
105     ;-
106     100000      X2.OPM = BIT15      ;OPERATION IN PROGRESS (TAPE MOVING)
107     040000      X2.RCE = BIT14      ;RAM CHECKSUM ERROR
108     035400      X2.SPARE= BIT13·BIT12·BIT11·BIT9·BIT8 ;NOT USED BY TSV05 (ALWAYS=0)
109     002000      X2.WCF = BIT10      ;WRITE CLOCK FAILURE (FIFO NOT EMPTIED BY TRANSPORT)
110     000200      X2.EXTF = BIT7      ;IF WRITE CHAR CMD THEN = EXTENDED FEATURES ENABLED
111     000100      X2.BUFE = BIT6      ;IF WRITE CHAR CMD THEN = BUFFERING ENABLED
112     000077      X2.REV = 000077    ;IF WRITE CHAR CMD THEN = MICROCODE REVISION LEVEL
113     000007      X2.UNIT = BIT2·BIT1·BIT0 ;IF GET STATUS THEN = CURRENTLY SELECTED UNIT NO.
114
115     ;*
116     ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 3
117     ;(XST3)
118     ;-
119     177400      X3.MDE = 177400    ;MICRO-DIAGNOSTIC ERROR CODE
120     000200      X3.SPARE= BIT7      ;NOT USED BY TSV05
121     000100      X3.OPI = BIT6      ;OPERATION INCOMPLETE
122     000040      X3.REV = BIT5      ;REVERSE
123     000020      X3.TRF = BIT4      ;TRANSPORT RESPONSE FAILURE
124     000010      X3.DCK = BIT3      ;DENSITY CHECK
125     000006      X3.MBZ =BIT2·BIT1 ;NOT USED ALWAYS 0
126     000001      X3.RIB = BIT0      ;REVERSE INTO BOT
127
128     ;*
129     ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 4
130     ;(XST4)
131     ;-
132     100000      X4.HSP = BIT15      ;HIGH SPEED
133     040000      X4.RCE = BIT14      ;RETRY COUNT EXCEEDED
134     020000      X4.TSM = BIT13      ;TRANSPORT SPECIAL MODE
135     017400      X4.MBZ = BIT12·BIT11·BIT10·BIT9·BIT8 ;NOT USED ALWAYS 0
136     000377      X4.WRC = 000377    ;WRITE RETRY COUNT FIELD
137
138     ;*
139     ;
140     ;TSSR TERMINATION CODES (BIT 0-2)
141     ;
142     ;-
143
144     000006      TSREJ= 3·2      ;COMMAND REJECTED
145     000006      UNREC= 6      ;UNRECOVERABLE ERROR

```

TSV3 - GLOBAL AREAS MACRO M1113 14 JUN-84 15:15
TSV05 REGISTER AND PACKET DEFINITIONS

SEQ 0032

```

147      ;*
148      ;
149      ;DEVICE REGISTER OFFSETS
150      ;
151      ;
152
153      000000      TSBA== 0
154      000000      TSD8== 0      ;TSD8/TSBA REGISTER
155      000001      TSBAH== 1
156      000001      TSD8H== 1      ;TSD8/TSBA REGISTER HIGH BYTE
157      000002      TSSR== 2      ;TSSR REGISTER
158      000003      TSSRH== 3      ;TSSR REGISTER HIGH BYTE
159
160      ;*
161      ; TSD8 ADDRESS BIT DEFINITIONS
162      ;
163      000003      A1716 = BIT1+BIT0      ;ADDRESS BITS 17:16 ARE IN 1:0
164
165      ;*
166      ; COMMAND DEFINITIONS
167      ;
168      000017      P.GETSTAT      = 17      ;GET STATUS
169      000013      P.INIT        = 13      ;INITIALIZE
170      000012      P.CONTROL     = 12      ;CONTROL COMMANDS
171      000011      P.FORMAT      = 11      ;FORMAT
172      000010      P.POSITION    = 10      ;POSITION
173      000006      P.WRTSUB      = 6       ;SUBSYSTEM WRITE
174      000005      P.WRITE       = 5       ;WRITE
175      000004      P.WRTCHAR     = 4       ;WRITE CHARACTERISTICS
176      000001      P.READ        = 1       ;READ
177
178      ;*
179      ; COMMAND PACKET HEADER WORD BIT DEFINITIONS
180      ;
181      100000      P.ACK      = BIT15      ;BUFFER AVAIL FOR CONTROLLER
182      040000      P.CVC     = BIT14      ;CLEAR VOLUME CHECK
183      020000      P.OPP     = BIT13      ;REVERSE SEQUENCE OF DATA BITS
184      010000      P.SWB     = BIT12      ;SWAP BYTES IN MEMORY
185      007400      P.MODE    = BIT11:BIT10:BIT9:BIT8 ;EXTENDED COMMAND MODE FIELD
186      000200      P.IE      = BIT7       ;INTERRUPT ENABLE
187      000140      P.FMT     = BIT6:BIT5   ;PACKET HEADER TYPE (ALWAYS=0)
188      000037      P.CMD     = 37        ;MAJOR COMMAND FIELD
189
190      ;*
191      ; CONTROL COMMAND MODE CODES
192      ;
193      000000      PC.RELEASE   = 0*256.   ;RELEASE BUFFER
194      000400      PC.REWIND   = 1*256.   ;REWIND
195      001000      PC.NOOP     = 2*256.   ;NO-OP
196      002000      PC.IEREW    = 4*256.   ;REWIND IMMEDIATE INTERRUPT
197      002400      PC.ERASE    = 5*256.   ;SECURITY ERASE

```

TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
TSV05 REGISTER AND PACKET DEFINITIONS

SEQ 0033

```

198      ;*
199      ; CONTROLLER RAM DEFINITIONS
200      ; -
201      000167      RMCHBEG = 167      ; CHARACTERISTICS IO DATA BEGIN RAM ADDRESS
202      000200      RMCHEND = 200      ; CHARACTERISTICS IO DATA END RAM ADDRESS
203      000201      RMPKTBEG = 201      ; COMMAND PACKET BEGIN RAM ADDRESS
204      000210      RMPKTEND = 210      ; COMMAND PACKET END RAM ADDRESS
205      000215      RMMSGBEG = 215      ; MESSAGE BUFFER BEGIN RAM ADDRESS
206      000234      RMMSGEND = 234      ; MESSAGE BUFFER END RAM ADDRESS
207      ;*
208      ;
209      ; REGISTER DEFINITIONS IN THE MESSAGE BUFFER
210      ;
211      ; -
212
213      000006      XST0 = 6      ; EXTENDED STATUS REGISTER 0 (WORD 4)
214      000010      XST1 = 8      ; EXTENDED STATUS REGISTER 1 (WORD 5)
215      000012      XST2 = 10      ; EXTENDED STATUS REGISTER 2 (WORD 6)
216      000014      XST3 = 12      ; EXTENDED STATUS REGISTER 3 (WORD 7)
217      000016      XST4 = 14      ; EXTENDED STATUS REGISTER 4 (WORD 8)
218
219      ;*
220      ;
221      ; OFFSETS TO WORD LOCATIONS IN PACKET DEFINITIONS
222      ;
223      ; -
224
225      000002      PKLOW = 2      ; LOW ORDER CHARACTERISTIC DATA POINTER
226      000004      PKHI = 4      ; HIGH ORDER CHARACTERISTIC DATA POINTER
227      000006      PKBCNT = 6      ; NUMBER OF BYTES IN DATA PACKET
228
229      000010      EXBCNT = 10      ; NUMBER OF BYTES IN EXTENDED DATA PACKET
230
231      ;*
232      ; DATA PACKET OFFSETS FOR WRITE SUBSYSTEM COMMAND
233      ; -
234      000000      BSEL0 = 0      ; BYTE 0
235      000001      BSEL1 = 1      ; BYTE 1
236      000002      SEL2 = 2      ; WORD 2
237      000004      SELDATA = 4      ; WORD 3

```


TSV3 GLOBAL AREAS MACRO M1113 14 JUN 84 15:15
TSV05 REGISTER AND PACKET DEFINITIONS

SEQ 0034

```

239      ;*
240      ;BSEL0 SELECT CODES FOR WRITE SUBSYSTEM COMMAND
241      ;-
242      000000      PW.NOP          = 0          ;NO-OP
243      000001      PW.RDRAM        = 1          ;READ RAM
244      000002      PW.WTRAM        = 2          ;WRITE RAM
245      000003      PW.RFIFO        = 3          ;READ FIFO
246      000004      PW.WFIFO        = 4          ;WRITE FIFO
247      000005      PW.RDSTAT       = 5          ;READ STATUS
248      000006      PW.WCTL         = 6          ;WRITE TAPE CONTROL
249      000007      PW.WFMT         = 7          ;WRITE TAPE FORMAT
250      000010      PW.WMISC        = 10         ;WRITE MISCELLANEOUS
251      000011      PW.WNPR         = 11         ;WRITE NPR CONTROL
252      000020      PW.D22          = 20         ;DO MICROTEST 22
253      000021      PW.D11          = 21         ;DO MICROTEST 11
254      000022      PW.D13          = 22         ;DO MICROTEST 13
255      000023      PW.NO1311       = 23         ;DISABLE MICROTEST 11 AND 13
256      000024      PW.RDXT         = 24         ;READ EXT. TAPE STATUS (NOT SUPPORTED BY ALL TRANSPORTS)
257
258      ;*
259      ;BSEL1 CODES FOR WRITE TAPE CONTROL
260      ;-
261      000200      WC.IFAD          = BIT7        ;IFAD - FORMATTER ADDRESS
262      000100      WC.IOTAD         = BIT6        ;ITAD0 - TRANSPORT ADDRESS BIT 0
263      000040      WC.I1TAD         = BIT5        ;ITAD1 - TRANSPORT ADDRESS BIT 1
264      000020      WC.ISRESV        = BIT4        ;IRESV5 - RESERVED #5
265      000010      WC.IREW          = BIT3        ;IREW - REWIND
266      000004      WC.IRWU          = BIT2        ;IRWU - REWIND AND UNLOAD
267      000002      WC.IFEN          = BIT1        ;IFEN - FORMATTER ENABLE
268      000001      WC.IGO           = BIT0        ;GO
269
270      ;*
271      ;BSEL1 CODES FOR WRITE FORMAT
272      ;-
273      000200      WF.IHISP          = BIT7        ;IHISP - HIGH SPEED
274      000100      WF.IWRT          = BIT6        ;IWRT - WRITE
275      000040      WF.IREV          = BIT5        ;IREV - REVERSE
276      000020      WF.IWFM          = BIT4        ;IWFM - WRITE FILE MARK
277      000010      WF.IEDIT         = BIT3        ;IEDIT - EDIT
278      000004      WF.IERASE         = BIT2        ;IERASE - ERASE
279      000002      WF.I3RESV        = BIT1        ;IRESV3 - RESERVED #3
280      000001      WF.I4RESV        = BIT0        ;IRESV4 - RESERVED #4
281
282      ;*
283      ;BSEL1 CODES FOR WRITE MISCELLANEOUS SUBCOMMAND
284      ;-
285      000200      MS.EXT            = BIT7        ;INVERT SENSE OF EXTENDED FEATURES SWITCH
286      000020      MS.RSFIFO         = BIT4        ;RESET FIFO AND INPUT PARITY ERRORR
287      000010      MS.RSTAPE         = BIT3        ;RESET TAPE STATUS IN 2 FLIP-FLOPS
288      000006      MS.ATTN           = BIT2:BIT1   ;ATTENTION TRIGGER FIELD
289      000001      MS.RSD            = BIT0        ;RESET TIMER A,B THEN DELAY TIMES IN SEL2

```

TSV3 - GLOBAL AREAS MACRO M1113 14 JUN-84 15:15
TSV05 REGISTER AND PACKET DEFINITIONS

SEQ 0035

```

291      ;*
292      ; MS.ATTN SUBCODES
293      ; -
294      000000      MSA.NOP = 0*2      ;NO-OP (NOTHING TRIGGERED)
295      000002      MSA.VOL = 1*2      ;SIMULATE ON-LINE/OFF-LINE TRANSITION
296      000004      MSA.NRAM= 2*2      ;FORCE NON-FATAL RAM ERROR (FORCES ERRCODE 54)
297      000006      MSA.FRAME= 3*2      ;FORCE FATAL RAM ERROR (CAUSES SCE TO SET)
298      ;*
299      ; WRITE SUBSYSTEM WRITE NPR BSEL1 BIT DEFINITIONS
300      ;
301      000200      NP.IR      = BIT7      ;INTERUPT REQUEST (0-1 TRANSITION)
302      000100      NP.OUT     = BIT6      ;TAPE DATA DIRECTION OUT (0= IN)
303      000040      NP.LOOP    = BIT5      ;ENABLE TRANSPORT LOOPBACK
304      000020      NP.WRP     = BIT4      ;WRITE CORRECT PARITY (SET=0 TO WRITE WRONG)
305      ;*
306      ; READ STATUS MESSAGE BUFFER BIT DEFINITIONS
307      ; -
308
309      000200      S2.DIM      = BIT7      ;WORD #9 BYTE 2 DATA IN MISS
310      000100      S2.ILW     = BIT6      ;      ILW H
311      000040      S2.OUTRDY   = BIT5      ;      OUT RDY H
312      000020      S2.INRDY   = BIT4      ;      IN RDY H
313      000010      S2.ATIMR    = BIT3      ;      TIMER A FLAG H
314      000004      S2.BTIMR    = BIT2      ;      TIMER B FLAG H
315      000003      S2.UNDEF    = BIT1+BIT0 ; (UNDEFINED)
316      100000      S1.PARIN    = BIT15      ;WORD #8 BYTE 1 PARIN H
317      040000      S1.I2RESV   = BIT14      ;      IRESV2
318      020000      S1.I1RESV   = BIT13      ;      IRESV1
319      010000      S1.IEOT     = BIT12      ;      IEOT L
320      004000      S1.IIDENT   = BIT11      ;      IIDENT H
321      002000      S1.ICER     = BIT10      ;      ICER H
322      001000      S1.IFMK     = BIT9       ;      IFMK H
323      000400      S1.IHER     = BIT8       ;      IHER H
324      000200      S0.ISPEED   = BIT7      ;WORD #8 BYTE 0 ISPEED H
325      000100      S0.IRDY    = BIT6      ;      IRDY L
326      000040      S0.IONL     = BIT5      ;      IONL L
327      000020      S0.ILDP     = BIT4      ;      ILDP L
328      000010      S0.IDBY     = BIT3      ;      IDBY L
329      000004      S0.IRWD     = BIT2      ;      IRWD L
330      000002      S0.IFBY     = BIT1      ;      IFBY L
331      000001      S0.IFPT     = BIT0      ;      IFPT L

```


TSV3 GLOBAL AREAS MACRO M1113 14 JUN 84 15:15
SPECIAL MACROS AND OPDEFS.

SEQ 0036

```

333             .SBTTL SPECIAL MACROS AND OPDEFS.
334
335             ;+
336             ;SAVE GENERAL REGS 1 TO 5
337             ;-
338
339             .MACRO SAVREG
340             JSR     R5,REGSAV
341             .ENDM
342
343             ;+
344             ; MACRO TO FORCE AN ERROR
345             ;-
346             .MACRO FORCERROR TAG,NOTSSR
347             .NLIST
348             .IIF NDF LISTALL, .NLIST
349             .LIST
350             .IF B NOTSSR
351             MOV     TSSR(R5),R1      ;READ TSSR
352             .ENDC
353             MOV     FORCER,FORCER    ;IS FORCER SET? (LEAVE C BIT ALONE)
354             BNE     TAG              ;BR IF YES
355             .NLIST
356             .IIF NDF LISTALL, .LIST
357             .LIST
358             .ENDM
359
360             ;+
361             ; MACRO TO FORCE AN EXIT TO AVOID SECTION ITERATIONS
362             ; WILL EXIT TO A LABEL IF FORCER IS NEGATIVE
363             ; SO TO FORCE ERRORS AND EXIT ON 1 ERROR SET
364             ; FORCER TO 177777
365             ; TO FORCE ERRORS AND ITERATIONS SET FORCER TO 1.
366             ;-
367             .MACRO FORCEEXIT TAG
368             .NLIST
369             .IIF NDF LISTALL, .NLIST
370             .LIST
371             MOV     FORCER,FORCER    ;IS FORCER NEGATIVE?
372             BMI     TAG              ;BR IF YES
373             .NLIST
374             .IIF NDF LISTALL, .LIST
375             .LIST
376             .ENDM
377             ;+
378             ; MACRO TO INCREMENT ERROR COUNTS
379             ;-
380             .MACRO NEXT.ERRNO
381             .NLIST
382             ;;;.IIF NDF LISTALL, .NLIST
383             ERRNO=ERRNO+1
384             ;;;.IIF NDF LISTALL, .LIST
385             .LIST
386             .ENDM

```

TSV3 GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
SPECIAL MACROS AND OPDEFS.

SEQ 0037

```

388      ;
389      ;MACRO TO PERFORM XOR
390      ;
391      ;
392      .MACRO XOR      A,B
393      MOV      A,-(SP)
394      BIC      B,(SP)
395      BIC      A,B
396      BIS      (SP)+,B
397      .ENDM
398
399      000000      EN=0      ; INITIALIZE ERROR NUMBER
400      .SBTTL FORCER - FORCE ERROR FLAG
401
402      ;
403      ; THE FOLLOWING LOCATIONS MAY BE PATCHED BY THE USER
404      ; TO OBTAIN THE RESULTS DESCRIBED FOR EACH.
405      ;
406
407 002174 000000 FORCER::      0      ; FORCE TYPE ALL HARD ERRORS (THE ONES CALLED -
408      ; - BY THE MACRO "IFERROR"). AN ERROR NEED NOT -
409      ; - EXIST, JUST ASSUME AND TYPE THE MESSAGE.

```

TSV3 GLOBAL AREAS
GLOBAL DATA SECTION

MACRO M1113 14 JUN-84 15:15

SEQ 0038

.SBTTL GLOBAL DATA SECTION

```

411
412
413
414
415
416
417
418
419
420
421
422 002176 000000
423 002200 000000
424 002202 000000
425 002204 000000
426 002206 000224
427 002210 000200
428 002212 000000
429 002214 000000
430 002216 000000
431 002220 000000
432 002222 000000
433 002224 000000
434 002226 000000
435 002230 000000
436 002232 000000
437 002234 000000
438 002236 000000
439 002240
440 002300 000000
441 002302 000000
442 002304 000000
443 002306 000000
444 002310 000000
445 002312 000000
446 002314 000000
447 002316 000000
448 002320
449 002464
450 002630

;***
;THE GLOBAL DATA SECTION CONTAINS DATA THAT ARE USED
;IN MORE THAN ONE TEST.
;
;THE FOLLOWING DATA ARE SET FOR EACH UNIT AT INIT TIME.
;SINGLE UNIT DEFAULTS (LISTED) ARE IN THE DEFAULT P-TABLE.
;
EPRTSW::      .WORD 0      ;PRINT SWITCH
UNITN::       .WORD 0      ;UNIT # UNDER TEST.
QVP::         .WORD 0      ;QUICK VERIFY FLAG.
CSRADDR::     .WORD 0      ;ADDRESS OF CSR FOR CURRENT DEVICE
IVEC::        .WORD 224    ;INTERRUPT VECTOR
IPRI::        .WORD PRI04  ;INTERRUPT PRIORITY.
TSTCNT::      .WORD 0      ;NUMBER OF TESTS RUN IN THIS PASS
LOOPCNT::     .WORD 0      ;REMAINING ITERATION COUNT FOR TEST
DEVCNT::      .WORD 0      ;NUMBER OF DEVICE UNDER TEST
FATFLG::      .WORD 0      ;SET IF FATAL ERROR IS DETECTED IN TEST
INTRECV::     .WORD C      ;SET IF TAPE INTERRUPT WAS RECEIVED
EXTFEA::      .WORD 0      ;EXTENDED FEATURES SOFTWARE SW 0=OFF;1=ON
BENBSW::      .WORD 0      ;BUFFER ENABLE SWITCH SW 0=OFF;1=ON
EXPD::        .WORD 0      ;EXPECTED RAM DATA FOR PRAMPKT ROUTINE
RECV::        .WORD 0      ;RECEIVED RAM DATA FOR PRAMPKT ROUTINE
ERRHI::       .WORD 0      ;HIGH ADDRESS MEMORY ERROR
ERRLO::       .WORD 0      ;LOW ADDRESS MEMORY ERROR
RANDATA::     .BLKW 16.    ;DATA READ FROM RAM PACKET OR MESSAGE BUF AREA
RAMSIZ::      .WORD 0      ;RAM DATA SIZE FOR PRAMPKT ROUTINE
RCVHIADD::    .WORD 0      ;RECEIVED BUFFER HIGH ADDRESS
RCVLOADD::    .WORD 0      ;RECEIVED BUFFER LOW ADDRESS
COUNT::     .WORD 0      ;TEST COUNT PATTERN
DATA::        .WORD 0      ;TEST DATA
TSTFLAG::     .WORD 0      ;TEST FLAG WORD
TSTPTR::      .WORD 0      ;TSTBLK POINTER
PRMNO::       .WORD 0      ;PRINT ROUTINE TEMP
EXPMSG::      .BLKB 100.   ;EXPECTED MESSAGE BUFFER DATA
RECMMSG::     .BLKB 100.   ;RECEIVED MESSAGE BUFFER DATA
TMPBFR::      .BLKB 80.    ;TEMPORARY STORAGE FOR PRINT

```

TSV3 GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
TSTBLK TEST DATA TABLE

SEQ 0039

452
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455
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463
464
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497
498
499
500
501
502
503
504
505

002750
002750 000000
002752 177777
002754 000001
002756 000002
002760 000004
002762 000010
002764 000020
002766 000040
002770 000100
002772 000200
002774 000400
002776 001000
003000 002000
003002 004000
003004 010000
003006 020000
003010 040000
003012 100000
003014 177776
003016 177775
003020 177773
003022 177767
003024 177757
003026 177737
003030 177677
003032 177577
003034 177377
003036 176777
003040 175777
003042 173777
003044 167777
003046 157777
003050 137777
003052 077777
003054 125252
003056 052525
003060

.SBTTL TSTBLK TEST DATA TABLE

```

;
;
; THIS TABLE CONTAINS TEST DATA USED IN SEVERAL TESTS
;
; IN SEQUENCE THE DATA IS:
;
;     ALL ZEROS
;     ALL ONES
;     WALKING ONES
;     WALKING ZEROS
;     ALTERNATING ONES AND ZEROS
;
;

```

TSTBLK::

```

.WORD 0 ;ALL ZEROS
.WORD 177777 ;ALL ONES
.WORD BIT0 ;DATA FOR WALKING ONES
.WORD BIT1
.WORD BIT2
.WORD BIT3
.WORD BIT4
.WORD BIT5
.WORD BIT6
.WORD BIT7
.WORD BIT8
.WORD BIT9
.WORD BIT10
.WORD BIT11
.WORD BIT12
.WORD BIT13
.WORD BIT14
.WORD BIT15
.WORD +CBIT0 ;DATA FOR WALKING ZEROS
.WORD +CBIT1
.WORD +CBIT2
.WORD +CBIT3
.WORD +CBIT4
.WORD +CBIT5
.WORD +CBIT6
.WORD +CBIT7
.WORD +CBIT8
.WORD +CBIT9
.WORD +CBIT10
.WORD +CBIT11
.WORD +CBIT12
.WORD +CBIT13
.WORD +CBIT14
.WORD +CBIT15
.WORD 125252 ;ALTERNATING ONES, ZEROS
.WORD 052525 ;ALTERNATING ONES, ZERO OPPOSITE FROM ABOVE

```

TBEND**

```

507          .SBTTL  GLOBAL ENVIRONMENT STORAGE
508
509          ; STORAGE FOR DEVICE REGISTERS
510
511 003060 000000 100000 000000 DUMMY: 0,100000,0,0 ; DUMMY DEVICE REGISTERS...
512 003070 000000 000000 000000      0,0,0,0,0,0,0,0,0
513                                     ; ...FOR MULTI-UNIT CHECKOUT.
514
515 003110 000000 DUFLG:: .WORD 0 ; "DROPPED UNIT" FLAG.
516                                     ; INHIBITS CODE IN "CLEAN-UP".
517 003112 000000 NODEV:: .WORD 0 ; FLAG TO SAY NO DEVICE.
518
519 003114 000000 TEMP1:: .WORD 0 ; SOME TEMP LOCATIONS.
520 003116 000000 TEMP2:: .WORD 0
521 003120 000000 XXCOMM:: .WORD 0 ; XXDP+ COMM BLOCK POINTER.
522 003122 000000 FREE:: .WORD 0 ; 1ST FREE MEMORY ADDRESS...
523 003124 000000 FRESIZ:: .WORD 0 ; ...AND SIZE (IN WORDS).
524 003126 000000 FREEHI: .WORD 0 ; LAST WORD IN FREE SPACE
525 003130 000000 KTFLG:: .WORD 0 ; KT11, MEM AVAIL FLAG -
526                                     ; - .WORD 0 = <24K OR NO KT
527                                     ; - NZ = >24K AND KT.
528 003132 000000 KTENABLE:: .WORD 0 ; SET BY TEST ROUTINES TO FLAG >28K UNDER TEST
529 003134 000000 NXHFLG:: .WORD 0 ; SET IF WE CAN TEST CLEARED OTHERWISE
530 003136 000000 NXHLO:: .WORD 0 ; NXH LO ADDRESS BITS
531 003140 000000 NXHHI:: .WORD 0 ; NXH HI ADDRESS BITS FOR DAL'S 16-21
532 003142 000000 T23A:: .WORD 0 ; 11/23A FLAG
533 003144 000000 T23B:: .WORD 0 ; 11/23B FLAG
534 003146 000000 T38FLG:: .WORD 0 ; TEST 38 FLAG +0
535 003150 002000 PST32W:: .WORD 2000 ; 32W BLOCK ADDRESS FOR 32K START
536 003152 000000 SIFLAG:: .WORD 0 ;
537 003154 000000 BADDAT:: .WORD 0 ; ACTUAL DATA
538 003156 000000 GDDAT:: .WORD 0 ; EXPECTED DATA
539 003160 000000 LOOPFL:: .WORD 0
540 003162 CTAB:: .WORD 0 ; CONFIGURATION TABLES.
541 003162 000000 CTABM:: .WORD 0 ; CONFIG WORK.
542 003164 000000 .WORD 0
543 003166 000000 .WORD 0
544 003170 000000 .WORD 0
545 003172 177777 .WORD -1 ; END OF MEM TABLE.
546 003174
547          CTASE::
548          ; ERROR STATISTICS TABLE (1 WORD PER UNIT), 64 UNITS MAX:
549          ;
550          ; 0 - UNIT NOT TESTED
551          ; 100000 - UNIT ONLINE, NO ERRORS
552          ; 10XXXX - UNIT ONLINE, ENCOUNTERED XXXX ERRORS
553          ; 160000 - UNIT DROPPED, NON-EXISTENT DEVICE REGISTER
554          ; 160001 - UNIT DROPPED, NOT IDLE AT START
555          ; 14XXXX - UNIT DROPPED, ENCOUNTERED XXXX ERRORS
556 003174 ERTABL: .BLKW 64.
557 003374 000000 ERTABE: .WORD 0
558
559 003376 000000 SKIPT: .WORD 0 ; 1=SKIP SUBTEST 0=NO SKIP OF SUBTEST

```

```

561          .SBTTL  GLOBAL TEXT MESSAGES
562          ;**
563          ; THE GLOBAL TEXT SECTION CONTAINS FORMAT STATEMENTS,
564          ; MESSAGES, AND ASCII INFORMATION THAT ARE USED IN
565          ; MORE THAN ONE TEST.
566          ;--
567          ;*
568          ;NAMES OF DEVICES SUPPORTED
569          ;-
570 003400      DEVTP  <TSV05>
003400      L@DVTP::
003400      .ASCIZ  /TSV05/
124          .EVEN
123
126

571          ;*
573          ;TEST DESCRIPTION
574          ;-
575          ;
576 003406      DESCRIPT <**** TSV05 LOGIC DIAGNOSTIC - REPLACE M7196 IF ERROR ****>
003406      L@DESC::
003406      .ASCIZ  /**** TSV05 LOGIC DIAGNOSTIC - REPLACE M7196 IF ERROR ****/
052          .EVEN
052
052

597          ;*
598          ;BIT TO ASCII CONVERSION FOR TSSR REGISTER
599          ;-
600          ;
601 003500      003540 003543 003547 TSSRBIT::      .WORD  1#,2#,3#,4#,5#,6#,7#,8#
602 003520      003601 003605 003611      .WORD  9#,10#,11#,12#,13#,14#,15#,16#
603 003540      123    103    000  1#:      .ASCIZ  'SC'
604 003543      102    111    105  2#:      .ASCIZ  'BIE'
605 003547      123    103    105  3#:      .ASCIZ  'SCE'
606 003553      122    115    122  4#:      .ASCIZ  'RMR'
607 003557      116    130    115  5#:      .ASCIZ  'NXM'
608 003563      116    102    101  6#:      .ASCIZ  'NBA'
609 003567      102    111    124  7#:      .ASCIZ  'BIT9'
610 003574      102    111    124  8#:      .ASCIZ  'BIT8'
611 003601      123    123    122  9#:      .ASCIZ  'SSR'
612 003605      117    106    114 10#:      .ASCIZ  'OFL'
613 003611      102    111    124 11#:      .ASCIZ  'BIT5'
614 003616      102    111    124 12#:      .ASCIZ  'BIT4'
615 003623      102    111    124 13#:      .ASCIZ  'BIT3'
616 003630      102    111    124 14#:      .ASCIZ  'BIT2'
617 003635      102    111    124 15#:      .ASCIZ  'BIT1'
618 003642      102    111    124 16#:      .ASCIZ  'BIT0'
619          .EVEN
620 003650      124    123    123  SFIERR: .ASCIZ  'TSSR ERROR AFTER SOFT INIT'
621 003703      124    123    123  SFHERR: .ASCIZ  'TSSR ERROR AFTER BUS RESET'
622 003736      040    040    116  NXR:    .ASCIZ  / NON-EXISTANT DEVICE REGISTER/
623 003775      045    101    040  NXRX:   .ASCIZ  /#A ADDRESS: #06/
624 004016      045    101    040  TSSX:   .ASCIZ  /#A TSBA,TSSR EXP'D: #06#A,#06#N/
625 004056      045    101    040      .ASCIZ  /#A TSBA,TSSR REC'D: #06#A,#06/
626 004115      045    116    045  FUSI:   .ASCIZ  /#A/
627 004121      040    040    125  USI:    .ASCIZ  / UNEXPECTED INTERRUPT/
628 004150      040    040    111  NSI:    .ASCIZ  / INTERRUPT EXPECTED, NOT RECEIVED/
629 004213      045    116    045  FNOINTR: .ASCIZ  /#A/
630 004217      040    040    116  NOINTR: .ASCIZ  / NO INTERRUPT WAS GENERATED/
631 004254      040    040    111  IFALT:   .ASCIZ  / INTERRUPT FAULT/
632 004276      045    101    040  INTX:   .ASCIZ  /#A CPU PC: #06#A TSBA: #06/

```



```

633 004333      040      040      042 NOINIT: .ASCIZ / "BUS-INIT" DIDN'T INITIALIZE CONTROLLER/
634 004405      040      040      042 NSINIT: .ASCIZ / "SOFT-INIT" DIDN'T INITIALIZE THE DPU/
635 004455      040      040      042 BRINIT: .ASCIZ / "BUS-RESET" DIDN'T INITIALIZE THE DPU/
636 004525      000      000      000 NUL: .ASCIZ //
637 004526      045      116      000 NULCR: .ASCIZ /#N/
638 004531      045      101      040 EXPGOT: .ASCIZ /#A EXP'D: #06#A, REC'D: #06/
639 004565      045      116      045 EXPGT2: .ASCIZ /#N#A EXP'D: #06#A, #06#N#A REC'D: #0#A, #06/
640 004641      045      101      040 DUAD12: .ASCIZ /#A REG(W) WRITTEN TO: #06#A REG(R) READ, EXP'D: #06#A, REC'D: #06/
641 004743      122      101      115 PKTRAM: .ASCIZ 'RAM Contents Do Not Match Packet Sent'
642 005011      040      040      103 SCME: .ASCIZ / CONFIG DOESN'T MATCH MFG. MASTER/
643 005054      127      122      111 WRTMSG: .ASCIZ 'WRITE CHARACTERISTICS Failed'
644 005111      124      123      123 WRTERR: .ASCIZ 'TSSR Incorrect After WRITE Command, More Bits Set Than SSR'
645 005204      124      123      123 RDERR: .ASCIZ 'TSSR Incorrect After READ Command, More Bits Set Than SSR'
646 005276      106      101      124 SCHERR: .ASCIZ 'FATAL ERROR IN SUBTEST - CHECK TAPE,CABLES,TRANSPORT etc.'
647 005370      105      122      122 RETERR: .ASCIZ 'ERROR IN SUBTEST - WRITE DATA RETRY FIVE TIMES FAILED'
648 005456      045      116      045 NOMEM: .ASCIZ '#N#A ***** NO NXM ADDRESS--CANNOT TEST NXM TIMEOUT. *****N'
649 005552      045      116      045 M8186: .ASCIZ '#N#A ***** 11/23A SYSTEM *****N'
650 005643      045      116      045 M8189: .ASCIZ '#N#A ***** 11/23B SYSTEM *****N'
651
652
653
654
655
656
657
658
659 005734
005734
660 005734
005734 013746 003112
005740 012746 003775
005744 012746 000002
005750 010600
005752 104415
005754 062706 000006
661 005760 004737 005766
662 005764
005764
005764 104423
663
664
665
666
667 005766 005727
668 005770 000000
669 005772 001402
670 005774 004777 177770
671 006000
006000 012746 004526
006004 012746 000001
006010 010600
006012 104415
006014 062706 000004
672 006020 000207

```

```

.EVEN
.SBTTL GLOBAL ERROR REPORT SECTION

```

```

;..
; THE GLOBAL ERROR REPORT SECTION CONTAINS THE PRINTB AND PRINTX
; CALLS THAT ARE USED IN MORE THAN ONE TEST.
; ASCII TEXT STRINGS ARE FOUND IN THE GLOBAL TEXT SECTION.
;..

```

```

BGNMSG NXRRERR ;NON-EXISTANT DEVICE REGISTER.
NXRRERR:
PRINTX #NXRX,MODEV ;MODEV = NEXM ADDRESS.
MOV MODEV,-(SP)
MOV #NXRX,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C#PNTX
ADD #6,SP
JSR PC,EXTEND ; PRINT EXTENSION IF REQUIRED.
ENDMSG

```

```

L10002:
TRAP C#MSG

```

```

;
; THIS ROUTINE APPENDS A UNIQUE EXTENSION (IF REQUIRED)
; TO ANY OF THE ABOVE ERROR SIGNATURES.
;

```

```

EXTEND: TST (PC).
EXTA: 0 ; 0 = NO EXTENSION.
BEQ 11
JSR PC,BEXTA ; APPEND EXTENSION TEXT.
11: PRINTX #NULCR ; PRINT A BLANK LINE
MOV #NULCR,-(SP)
MOV #1,-(SP)
MOV SP,R0
TRAP C#PNTX
ADD #4,SP
RTS PC

```

TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
 PRITSSR - PRINT TSSR CONTENTS

SEQ 0043

```

674 .SBTTL PRITSSR - PRINT TSSR CONTENTS
675
676 ;*
677 ;
678 ;ROUTINE TO DISPLAY THE CONTENTS, AND BIT DEFINITIONS, OF
679 ;THE TSSR REGISTER. THIS ROUTINE IS NORMALLY CALLED ONLY
680 ;BY A MESSAGE PRINTING ROUTINE
681 ;
682 ;INPUTS:
683 ;
684 ; R1 CONTENTS OF TSSR
685 ;
686 ;SUBORDINATE ROUTINES:
687 ;
688 ; CHKAMB CHECK FOR AMBIGUOUS CONTENTS
689 ;
690 ;
691
692 PRITSSR: SAVREG ;SAVE GENERAL REGISTERS
693 MOV R1,R4 ;SAVE THE TSSR CONTENTS
694 PRINTB @TSSRFOR,R4 ;PRINT THE CONTENTS OF TSSR
695 MOV R4,-(SP)
MOV @TSSRFOR,-(SP)
MOV @2,-(SP)
MOV SP,R0
TRAP CIPNTB
ADD @6,SP
MOV R4,R0 ;GET TSSR BACK FOR CHKAMB
JSR PC,CHKAMB ;ARE CONTENTS AMBIGUOUS ?
BCS 5$ ;BRANCH IF NOT
PRINTX @AMBTSSR ;SHOW CONTENTS ARE AMBIGUOUS
MOV @AMBTSSR,-(SP)
MOV @1,-(SP)
MOV SP,R0
TRAP CIPNTX
ADD @4,SP
5$: MOV R4,R3 ;CONTENTS OF TSSR
BIC @HIADDR!FATERR!TERCLS,R3 ;CLEAR ALL MULTIPLE BIT FIELDS
BEQ 20$ ;NO BITS ARE SET
MOV @TMPBFR,R2 ;TEMPORARY ASCII BUFFER
MOV @TSSRBIT,R1 ;ASCII EQUIVALENT OF BITS
10$: TST R3 ;REMAINING BITS TO CONVERT
BEQ 15$ ;BRANCH WHEN ALL ARE DONE
CLC ;CLEAR CARRY FOR SHIFT
ROL R3 ;SHIFT NEXT BIT TO CARRY
BCC 13$ ;BRANCH IF BIT NOT SET
MOV (R1),R0 ;POINTER TO BIT DEFINITION
1$: MOVB (R0),-(R2) ;MOVE ASCII TO BUFFER
BNE 11$ ;MOVE ALL BITS
MOVB @',,-1(R2) ;INSERT A COMMA TO TERMINATE
13$: TST (R1) ;POINT TO NEXT DESCRIPTION
BR 10$ ;GET THE REMAINING BITS
15$: CLRB -(R2) ;TERMINATE THE LINE
PRINTX @TSSDEF,@TMPBFR ;PRINT THE BIT DEFINITIONS
MOV @TMPBFR,-(SP)
MOV @TSSDEF,-(SP)

```


TSV3 - GLOBAL AREAS MACRO M1113 14 JUN-84 15:15
 PRITSSR - PRINT TSSR CONTENTS

SEQ 0044

006166	012746	000002	MOV	#2,-(SP)	
006172	010600		MOV	SP,R0	
006174	104415		TRAP	C#PNTX	
006176	062706	000006	ADD	#6,SP	
718					
719	006202	010403	204: MOV	R4,R3	;GET THE TSSR CONTENTS
720	006204	042703	BIC	#1CTERCLS,R3	;CLEAR ALL BUT TERMINATION
721	006210	016303	MOV	TCOCOD(R3),R3	;GET THE TERMINATION CODE MEANING
722	006214		PRINTX	#TCOASC,R3	;PRINT THE TERMINATION CODE
	006214	010346	MOV	R3,-(SP)	
	006216	012746	MOV	#TCOASC,-(SP)	
	006222	012746	MOV	#2,-(SP)	
	006226	010600	MOV	SP,R0	
	006230	104415	TRAP	C#PNTX	
	006232	062706	ADD	#6,SP	
723	006236	010403	MOV	R4,R3	;TSSR CONTENTS AGAIN
724	006240	042703	BIC	#1CFATERR,R3	;CLEAR ALL BUT FATAL TERMINATION
725	006244	001416	BEQ	254	;DON'T PRINT IF ZERO
726	006246	006203	ASR	R3	
727	006250	006203	ASR	R3	
728	006252	006203	ASR	R3	;ALINE TERMINATION CODE FOR INDEX
729	006254	016303	MOV	TSFCOD(R3),R3	;GET THE FATAL TERMINATION CODE
730	006260		PRINTX	#TFCASC,R3	;PRINT THE FATAL TERMINATION CODE
	006260	010346	MOV	R3,-(SP)	
	006262	012746	MOV	#TFCASC,-(SP)	
	006266	012746	MOV	#2,-(SP)	
	006272	010600	MOV	SP,R0	
	006274	104415	TRAP	C#PNTX	
	006276	062706	ADD	#6,SP	
731	006302	042704	254: BIC	#1CHIADDR,R4	;CLEAR ALL BUT EXTENDED ADDRESS
732	006306	001411	BEQ	304	;DON'T PRINT IF ZERO
733	006310		PRINTX	#TEXASC,R4	;PRINT THE EXTENDED ADDRESS BITS
	006310	010446	MOV	R4,-(SP)	
	006312	012746	MOV	#TEXASC,-(SP)	
	006316	012746	MOV	#2,-(SP)	
	006322	010600	MOV	SP,R0	
	006324	104415	TRAP	C#PNTX	
	006326	062706	ADD	#6,SP	
734	006332	013703	304: MOV	EPRTSW,R3	;PRINT MEASGE BUFFER ADDRESS
735	006336		PRINTX	R3	;PRINT PROPER MESSAGE
	006336	010346	MOV	R3,-(SP)	
	006340	012746	MOV	#1,-(SP)	
	006344	010600	MOV	SP,R0	
	006346	104415	TRAP	C#PNTX	
	006350	062706	ADD	#4,SP	
736	006354	000207	RTS	PC	;RETURN TO CALLER

TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
 PRITSSR - PRINT TSSR CONTENTS

SEQ 0045

```

739 006356      EPRT2:
740 006356      045      116      045 EPRT1: .ASCIZ 'ANSA *****REPLACE M7196*****'
755 006413      045      116      045 TSSRFOR: .ASCIZ 'ANSA TSSR = #06'
756 006433      045      116      045 TEXASC: .ASCIZ 'ANSA Extended Address Bits = #06'
757 006474      045      116      045 TCOASC: .ASCIZ 'ANSA Termination Class Code = #T'
758 006535      045      116      045 TFCASC: .ASCIZ 'ANSA Fatal Termination Class Code = #T'
759 006604      045      116      045 TSSDEF: .ASCIZ 'ANSA TSSR Bits Set: #T'
760 006633      045      116      045 AMBTSSR: .ASCIZ 'ANSA TSSR Contents Are Ambiguous'
761              .EVEN
762 006674      006714      006737      006765 TCOCOD: .WORD 1#,2#,3#,4#,5#,6#,7#,8#
763 006714      116      157      162 1#: .ASCIZ 'Normal Termination'
764 006737      124      145      162 2#: .ASCIZ 'Termination Condition'
765 006765      124      141      160 3#: .ASCIZ 'Tape Status Alert'
766 007007      106      165      156 4#: .ASCIZ 'Function Reject'
767 007027      122      145      143 5#: .ASCIZ 'Recoverable Error - Tape Position One Record Down'
768 007111      122      145      143 6#: .ASCIZ 'Recoverable Error - Tape Was Not Moved'
769 007160      125      156      162 7#: .ASCIZ 'Unrecoverable Error'
770 007204      106      141      164 8#: .ASCIZ 'Fatal Controller Error'
771              .EVEN
772
773 007234      007244      007300      007311 TSFCOD: .WORD 1#,2#,3#,4#
774 007244      111      156      164 1#: .ASCIZ 'Internal Diagnostic Failure'
775 007300      122      145      163 2#: .ASCIZ 'Reserved'
776 007311      102      165      163 3#: .ASCIZ 'Bus Interface or Sanity Check Error'
777 007355      122      145      163 4#: .ASCIZ 'Reserved'
778              .EVEN

```

TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
 PRIPKT - PRINT THE ADDRESS/CONTENTS OF COMMAND PACKET

SEQ 0046

```

780 .SBTTL PRIPKT - PRINT THE ADDRESS/CONTENTS OF COMMAND PACKET
781
782
783 ;*
784 ;THIS ROUTINE PRINTS THE ADDRESS AND CONTENTS OF A COMMAND PACKET.
785 ;THIS ROUTINE IS NORMALLY ONLY CALLED FROM A PRINT ROUTINE.
786 ;
787 ;INPUT:
788 ;
789 ; R0      NUMBER OF WORDS IN PACKET
790 ; R3      HIGH ORDER COMMAND PACKET ADDRESS
791 ; R4      ADDRESS OF COMMAND PACKET
792 ;
793 ; NOTF:   R3 IS IGNORED IF THE KENABLE FLAG IS CLEAR.
794 ;
795 PRIPKT::
796 SAVREG                                ;SAVE THE REGISTERS
797 MOV R0,R5                            ;SAVE NO. OF WORDS IN PACKET
798 TST KENABLE                          ;ABOVE 28K UNDER TEST?
799 BNE 10$                             ;BR IF YES
800 CLR R3                              ;SET HIGH ORDER ADDRESS TO 0
801 10$: MOV R3,R1                       ;COPY HIGH ORDER ADDRESS
802 MOV R4,R0                           ;GET LOWER ADDRESS
803 ROL R0                              ;SHIFT BIT 15 INTO C BIT
804 ROL R1                              ;AND INTO HIGH ORDER.
805 PRINTB #PKTADD,R1,R4                ;PRINT PACKET ADDRESS
806 MOV R4,-(SP)
807 MOV R1,-(SP)
808 MOV #PKTADD,-(SP)
809 MOV #3,-(SP)
810 MOV SP,R0
811 TRAP C#PNTB
812 ADD #10,SP
813 15$: MOV R3,R0                       ;GET HIGH ORDER ADDRESS
814 BEQ 20$                             ;BR IF NOT ABOVE 28K.
815 MOV R4,R1                           ;GET LOW ORDER ADDRESS
816 JSR PC,SETMAP                       ;SETUP PAR6 MAPPING FOR 18 BIT ADDRESS
817 MOV R0,R4                           ;GET RETURNED PAR6 ADDRESS BIAS
818 20$: CLR R1                          ;SAVE WORD NUMBER
819 25$: MOV (R4)+,R2                    ;GET PACKET CONTENTS
820 PRINTB #PKTFRM,R1,R2                ;PRINT THE DATA
821 MOV R2,-(SP)
822 MOV R1,-(SP)
823 MOV #PKTFRM,-(SP)
824 MOV #3,-(SP)
825 MOV SP,R0
826 TRAP C#PNTB
827 ADD #10,SP
828 INC R1                              ;NEXT WORD NUMBER
829 CMP R1,R5                          ;DONE ALL PACKET WORDS?
830 BLT 25$                            ;LOOP TILL ALL DONE
831 RTS PC                             ;RETURN
832
833 045 PKTFRM: .ASCIZ 'N$A Packet Word #01$A = #06
834 045 PKTADD: .ASCIZ 'N$A Packet Address = #01$05'
835 .EVEN

```

```

823 .SBTTL PRIBXOR - PRINT EXPD, RECV AND XOR BYTE
824
825 ;*
826 ;PRINT EXPECTED DATA, RECEIVED DATA, AND XOR OF THE DATA BYTE
827 ;THIS ROUTINE IS NORMALLY CALLED ONLY FOR PRINT ROUTINES.
828
829 ;INPUTS:
830
831 ; R1 RECEIVED DATA
832 ; R2 EXPECTED DATA
833
834 ;OUTPUT:
835
836 ; R0 XOR OF EXPECTED/RECEIVED DATA
837
838 PRIBXOR:
839 SAVREG ;SAVE THE REGISTERS
840 MOV R2,R3 ;EXPECTED DATA
841 XOR R1,R3 ;FORM THE EXCLUSIVE OR
842 MOV #C<377>,R0 ;BYTE MASK
843 BIC R0,R1 ;SAVE LOW BYTE RECV
844 BIC R0,R2 ;SAVE LOW BYTE EXPD
845 BIC R0,R3 ;SAVE LOW BYTE XOR
846 PRINTB #XORFOR,R2,R1,R3 ;PRINT THE MESSAGE
847 MOV R3,-(SP)
848 MOV R1,-(SP)
849 MOV R2,-(SP)
850 MOV #XORFOR,-(SP)
851 MOV #4,-(SP)
852 MOV SP,R0
853 TRAP C#PNTB
854 ADD #12,SP
855 MOV R3,R0 ;R0 HAS XOR ON RETURN
856 RTS PC ;RETURN TO CALLER
857
858 007610 010203 177400
859 007614 012700 040001
860 007616 040002 040003
861 007626 010346 007672
862 007642 010146 000004
863 007644 010246
864 007646 012746 000012
865 007652 012746
866 007656 010600
867 007660 104414
868 007662 062706
869 007666 010300
870 007670 000207
871 007672 045 116 045 XORFOR: .ASCIZ '#N#A EXPD: #03#A RECV: #03#A XOR: #03'
872 .EVEN
873 .SBTTL PRIBXOR - PRINT EXPD, RECV AND XOR
874
875 ;*
876 ;PRINT EXPECTED DATA, RECEIVED DATA, AND XOR OF THE TWO
877 ;THIS ROUTINE IS NORMALLY CALLED ONLY FOR PRINT ROUTINES.
878
879 ;INPUTS:
880
881 ; R1 RECEIVED DATA
882 ; R2 EXPECTED DATA
883
884 ;OUTPUT:
885
886 ; R0 XOR OF EXPECTED/RECEIVED DATA
887
888 PRIBXOR:
889 SAVREG ;SAVE THE REGISTERS
890 MOV R2,R3 ;EXPECTED DATA
891 XOR R1,R3 ;FORM THE EXCLUSIVE OR
892 PRINTB #XORFOR,R2,R1,R3 ;PRINT THE MESSAGE

```

TSV3 - GLOBAL AREAS MACRO M1113 14-JUN 84 15:15
 PRI XOR - PRINT EXPD, RECV AND XOR

SEQ 0048

	007756	010346		MOV	R3, -(SP)	
	007760	010146		MOV	R1, -(SP)	
	007762	010246		MOV	.2, -(SP)	
	007764	012746	010010	MOV	#XORFOR, -(SP)	
	007770	012746	000004	MOV	#4, -(SP)	
	007774	010600		MOV	SP, R0	
	007776	104414		TRAP	C:PNTB	
	010000	062706	000012	ADD	#12, SP	
872	010004	010300		MOV	R3, R0	;R0 HAS XOR ON RETURN
873	010006	000207		RTS	PC	;RETURN TO CALLER
874						
875	010010	045	116	045	XORFOR: .ASCIZ	'#N#A EXPD: #06#A RECV: #06#A XOR: #06'
876					.EVEN	

TSV3 GLOBAL AREAS MACRO M1113 14 JUN 84 15:15
 PRIEQU - PRINT BIT NUMBERS AS ASCII EQUIVALENT

SEQ 0049

```

878 .SBTTL PRIEQU - PRINT BIT NUMBERS AS ASCII EQUIVALENT
879
880 ;*
881 ;
882 ;ROUTINE TO CONVERT BIT VALUES TO ASCII AND PRINT THE STRING
883 ;THIS ROUTINE IS NORMALLY CALLED FROM A PRINT ROUTINE
884 ;
885 ;INPUTS:
886 ;
887 ; R0 OCTAL VALUE TO CONVERT
888 ; R1 TABLE OF POINTERS TO ASCII EQUIVALENT
889 ;
890 ;-
891
892 010056 PRIEQU: SAVREG ;SAVE THE REGISTERS
893 010056 RTS PC ;RETURN TO CALLER
894 010062 000207
895
896 .SBTTL PRIRAM - PRINT RAM ADDRESS
897
898 ;*
899 ;
900 ;PRINT CONTROLLER RAM ADDRESS.
901 ;THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.
902 ;
903 ;INPUTS:
904 ;
905 ; R4 RAM ADDRESS
906 ;-
907 010064 PRIRAM: SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
908 010064 PRINTB #RAMFOR,R4 ;PRINT RAM ADDRESS IN ERROR
909 010070 MOV R4,-(SP)
910 010070 010446 MOV #RAMFOR,-(SP)
911 010072 012746 010114 MOV #2,-(SP)
912 010076 012746 000002 MOV SP,PC
913 010102 010600 TRAP C#PNTB
914 010104 104414 ADD #6,SP
915 010106 062706 000006 RTS PC ;RETURN
916
917 010114 045 116 045 RAMFOR: .ASCIZ 'N/A CONTROLLER RAM ADDRESS = #06'
918 .EVEN

```


TSV3 GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
 PRIADD - PRINT MEMORY ERROR ADDRESS

SEQ 0050

```

915 .SBTTL PRIADD - PRINT MEMORY ERROR ADDRESS
916 ;*
917 ;
918 ;PRINT MEMORY ADDRESS
919 ;THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.
920 ;
921 ; IMPLICIT INPUTS
922 ;
923 ; ERRHI - HIGH ORDER ADDRESS
924 ; ERRLO - LOW ORDER ADDRESS
925 ;
926 ;
927 PRIADD: SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
928 010156 MOV ERRHI,R0 ;GET HIGH ADDRESS
929 010162 013700 002234 MOV ERRLO,R1 ;GET LOW ADDRESS
930 010166 013701 002236 MOV R1,R2 ;COPY LOW ADDRESS
931 010172 010102 ROL R1 ;SHIFT BIT 15 TO C BIT
932 010174 006101 ROL R0 ;SHIFT INTO HIGH ORDER
933 010176 006100 PRINTB #PRIA0,R0,R2 ;PRINT MEMORY ADDRESS IN ERROR
934 010200 MOV R2,-(SP)
010200 010246 MOV R0,-(SP)
010202 010046 MOV #PRIA0,-(SP)
010204 012746 010226 MOV #3,-(SP)
010210 012746 000003 MOV SP,R0
010214 010600 TRAP C#PNTB
010216 104414 ADD #10,SP
010220 062706 000010 RTS PC ;RETURN
935 010224 000207
936
937 010226 045 116 045 PRIA0: .ASCIZ 'MEMORY ERROR ADDRESS = #01#05'
938 .EVEN
939
940 .SBTTL PRITADD - PRINT MEMORY TEST ADDRESS
941 ;*
942 ;
943 ;PRINT MEMORY ADDRESS
944 ;THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.
945 ;
946 ; IMPLICIT INPUTS
947 ;
948 ; ERRHI - HIGH ORDER ADDRESS
949 ; ERRLO - LOW ORDER ADDRESS
950 ;
951 ;
952 PRITADD: SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
953 010272 MOV ERRHI,R2 ;GET HIGH ADDRESS
954 010276 013702 002234 MOV ERRLO,R1 ;GET LOW ADDRESS
955 010302 013701 002236 MOV R1,R2 ;COPY LOW ADDRESS
956 ;MOV R1,R2 ;COPY LOW ADDRESS
957 ;ROL R1 ;SHIFT BIT 15 TO C BIT
958 ;ROL R0 ;SHIFT INTO HIGH ORDER
959 010306 PRIMB #PRITO,R1 ;PRINT MEMORY ADDRESS LOW IN ERROR
010306 010146 MOV R1,-(SP)
010310 012746 010354 MOV #PRITO,-(SP)
010314 012746 000002 MOV #2,-(SP)
010320 010600 MOV SP,R0
010322 104414 TRAP C#PNTB

```

TSV3 - GLOBAL AREAS MACRO M1113 14 JUN-84 15:15
 PRITADD - PRINT MEMORY TEST ADDRESS

SEQ 0051

	010324	062706	000006		ADD	#6, SP	
960	010330				PRINTB	@PRIT1, R2	; PRINT MEMORY ADDRESS HIGH IN ERROR
	010330	010246			MOV	R2, -(SP)	
	010332	012746	010417		MOV	@PRIT1, -(SP)	
	010336	012746	000002		MOV	#2, -(SP)	
	010342	010600			MOV	SP, R0	
	010344	104414			TRAP	C#PNTB	
	010346	062706	000006		ADD	#6, SP	
961	010352	000207			RTS	PC	; RETURN
962							
963	010354	045	116	045	PRIT0:	.ASCIZ	'#N#A MEMORY TEST ADDRESS LOW - #06'
964	010417	045	116	045	PRIT1:	.ASCIZ	'#N#A MEMORY TEST ADDRESS HIGH - #06'
965						.EVEN	

TSV3 GLOBAL AREAS MACRO M1113 14 JUN-84 15:15
SPACE SPACE RECORDS (FORWARD AND REVERSE) COMMAND

SEQ 0052

```

967 .SBTTL SPACE SPACE RECORDS (FORWARD AND REVERSE) COMMAND
968
969
970
971 ;ROUTINE TO ISSUE A SPACE RECORDS
972 ;COMMAND (FORWARD OR REVERSE)
973
974 ;INPUT:
975
976 R3 NUMBER OF RECORDS TO BE SPACED OVER
977 BIT15 CONTROLS DIRECTION
978 BIT15 = 0 IS FORWARD
979 BIT15 = 1 IS REVERSE
980 R5 FIRST DEVICE UNIBUS ADDRESS
981
982 ;REQUIRES A WRITE CHARACTERISTICS DONE PREVIOUSLY
983
984 ;OUTPUT:
985
986 CARRY SET SPACE RECORDS COMMAND OK
987 CLR SPACE RECORDS FAILED
988
989
990 R0 THE CONTENTS OF R4 IS MOVED TO R0
991
992
993 ;IMPLICIT OUTPUT:
994
995 TAPE HAS BEEN MOVED
996
997 ;SIDE EFFECTS:
998
999
1000
1001
1002 SPACE:
1003 SAVREG
1004 MOV #500.,SDELAY ;SAVE THE GENERAL REGISTERS
1005 MOV #140010,801 ;SET UP DELAY
1006 TST R3 ;SET UP COMMAND, SPACE FORWARD
1007 BMI S1 ;CHECK FOR DIRECTION
1008 MOV R3,901 ;BR, IF REVERSE INDICATED
1009 BR 101 ;LOAD UP NUMBER OF RECORDS TO SPACE
1010 BIC #BIT15,R3 ;GO DO COMMAND
1011 MOV R3,901 ;CLEAR DIRECTION BIT
1012 BIS #BIT8,801 ;LOAD UP NUMBER OF RECORDS TO SPACE
1013 MOV #801,R4 ;SET REVERSE BIT IN COMMAND PACKET
1014 MOV R4,TSD8(R5) ;SET UP R4 WITH PACKET ADDRESS
1015 JSR PC,WAITF ;SEND OUT COMMAND
1016 BCS 201 ;WAIT FOR SSR
1017 DELAY 250 ;BR, IF SSR IS SET AND OK
1018 MOV #250,(PC) ;DELAY ABOUT .25 SECONDS
1019 .WORD 0
1020 MOV L#DLY,(PC)
1021 .WORD 0
1022 DEC -6(PC)
1023 BNE . 4

```

TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
 SPACE - SPACE RECORDS (FORWARD AND REVERSE) COMMAND

SEQ 0053

010574	005367	177756	DEC	-22(PC)	
010600	001367		BNE	.-20	
1018 010602	005337	010660	DEC	SDELAY	;BUMP DELAY COUNTER DOWN
1019 010606	001356		BNE	15;	;BR, IF MORE DELAY
1020 010610	000411		BR	60;	;BR IF TROUBLE CARRY = CLEAR
1021 010612	016501	000002	201: MOV	TSSR(R5),R1	;READ TSSR
1022 010616	012702	000200	MOV	#SSR,R2	;SET UP EXPECTED
1023 010622	020201		251: CMP	R2,R1	;ARE THEY OK
1024 010624	001401		BEG	40;	;BR, IF EQUAL = OK
1025 010626	000402		BR	60;	;TROUBLE EXIT
1026 010630	000261		401: SEC		;SET CARRY NO TROUBLE
1027 010632	000401		BR	70;	;EXIT
1028 010634	000241		601: CLC		;CARRY CLEAR = ERROR
1029 010636			701:		
1030 010636	010400		MOV	R4,R0	;PASS PACKET ADDRESS
1031 010640	000207		RTS	PC	;RETURN
1032			:		
1033			:		
1034			:		
1035			:		
1036			:		
1038	010650		:		
1040			:		
1041			:		
1042 010650	000000		:		
1043			:		
1044 010652	000000		:		
1045 010654	000000		:		
1046 010656	000000		:		
1047 010660	000000		:		
1048			:		
1049			:		

;PACKET FOR SPACE COMMAND
 .-<..10>E177770
 ;COMMAND WORD
 801: .WORD
 ;NUMBER OF RECORDS TO BE SPACED OVER WORD
 901: .WORD
 .WORD
 .WORD
 SDELAY: .WORD 0 ;DELAY COUNTER
 .EVEN
 .SBTTL WRCHR - WRITE CHARACTERISTICS COMMAND

1051				;ROUTINE TO ISSUE A WRITE CHARACTERISTICS
1052				;COMMAND SO THAT OTHER COMMANDS WILL BE ACCEPTED
1053				
1054				;INPUT:
1055				
1056				R4 ADDRESS OF PACKET FROM TEST
1057				R5 FIRST DEVICE UNIBUS ADDRESS
1058				REQUIRES A CALL TO SOFINIT BE DONE PREVIOUSLY
1059				
1060				;OUTPUT:
1061				R0 TSSR CONTENTS
1062				CARRY SET - WRITE CHARACTERISTICS COMMAND OK
1063				CLR - WRITE CHARACTERISTICS FAILED
1064				
1065				;IMPLICIT OUTPUT:
1066				
1067				MESSAGE BUFFER AND OTHER BUFFERS ALL SET UP
1068				SOFTWARE SWITCHES SET AS FOLLOWS:
1069				EXTFEA = EXTENDED FEATURES PRESENT
1070				BENBSW = BUFFER ENABLE SWITCH ON OR OFF
1071				
1072				;SIDE EFFECTS:
1073				;-
1074	010662			WRTCHR::
1075	010662			SAVREG ;SAVE THE GENERAL REGISTERS
1076	010666	005037	002226	CLR BENBSW ;CLEAR BUFFER ENABLE SWITCH
1077	010672	005037	002224	CLR EXTFEA ;CLEAR EXTENDED FEATURES SW SWITCH
1078	010676	010465	000000	104: MOV R4,TSD8(R5) ;SEND OUT COMMAND
1079	010702	004737	016336	JSR PC,CHKTSSR ;WAIT FOR SSR
1080	010706	103401		BCS 204 ;BR, IF SSR IS SET AND OK
1081	010710	000435		BR 604 ;BR IF TROUBLE CARRY = CLEAR
1082	010712	016501	000002	204: MOV TSSR(R5),R1 ;READ TSSP
1083	010716	012702	000200	MOV #SSR,R2 ;SET UP EXPECTED
1084	010722	032701	000100	BIT #OFL,R1 ;WAS OFF LINE SET IN TSSR
1085	010726	001402		BEQ 254 ;BR, IF NO OFL SET
1086	010730	052702	000100	BIS #OFL,R2 ;MAKE THEM LOOK ALIKE
1087	010734	020201		254: CMP R2,R1 ;ARE THEY OK
1088	010736	001401		BEQ 404 ;BR, IF EQUAL = OK
1089	010740	000421		BR 604 ;TROUBLE EXIT
1090	010742	062704	000010	404: ADD #8.,R4 ;POINT TO WRT CHARA DATA PACKET
1091	010746	011403		MOV (R4),R3 ;GET ADDRESS OF MESSAGE BUFFER
1092	010750	032763	000200 000012	BIT #X2.EXTF,XST2(R3) ;EXTENDED FEATURES BIT SET?
1093	010756	001402		BEQ 454 ;BR IF NO
1094	010760	005237	002224	INC EXTFEA ;SET EXTENDED FEATURES SW SWITCH
1095	010764			454:
1096	010764	032763	000100 000012	BIT #X2.BUFE,XST2(R3) ;BUFFER ENABLE SWITCH SET
1097	010772	001402		BEQ 504 ;BR, IF SWITCH NOT SET
1098	010774	005237	002226	INC BENBSW ;SET SOFTWARE SWITCH FOR ENABLED
1099	011000			504:
1100	011000	000261		SEC ;SET CARRY NO TROUBLE
1101	011002	000401		BR 704 ;EXIT
1102	011004	000241		604: CLC ;CARRY CLEAR = ERROR
1103	011006	016500	000002	704: MOV TSSR(R5),R0 ;RETURN TSSR CONTENTS
1104	011012	000207		RTS PC ;RETURN

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TSV3 GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
 REWIND - POSITION TAPE (REWIND) COMMAND

SEQ 0055

```

1106 .SBTTL REWIND - POSITION TAPE (REWIND) COMMAND
1107 ;
1108 ;
1109 ; THIS ROUTINE WILL REWIND THE SELECTED TAPE.
1110 ;
1111 ; CAUTION: THE ROUTINE DOES NOT WAIT FOR BOT
1112 ; TO ARRIVE. ALSO THE CALLER MUST CHECK FOR
1113 ; SSR TO SET IN THE TSSR
1114 ;
1115 ;
1116 ; CALLING SEQUENCE:
1117 ;
1118 ; DO A SOFT INIT
1119 ; DO A WRITE CHARACTERISTICS
1120 ; JSR PC,REWIND
1121 ;
1122 ; INPUT:
1123 ;
1124 ; R5 FIRST DEVICE UNIBUS ADDRESS
1125 ;
1126 ;
1127 ; OUTPUT
1128 ;
1129 ; R0 THE CONTENTS OF R4 IS PASSED TO R0
1130 ;
1131 ;
1132 ;
1133 REWIND::
1134 ; SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
1135 MOV #RMPACK,R4 ;GET PACKET ADDRESS
1136 MOV R4,TSDB(R5) ;SEND PACKET ADDRESS TO EXECUTE
1137 MOV #360.,R3 ;ENOUGH TIME FOR 2400' REEL TO REWIND
1138 JSR PC,WAITF ;WAIT FOR SSR TO SET
1139 BCS 20$ ;LEAVE WHEN SSR IS SET
1140 DELAY 250. ;WAIT FOR .25 SECONDS
1141 MOV #250.,(PC).
1142 .WORD 0
1143 MOV L$DLY,(PC).
1144 .WORD 0
1145 DEC -6(PC)
1146 BNE .-4
1147 DEC -22(PC)
1148 BNE .-20
1149 DEC R3 ;BUMP COUNTER DOWN
1150 BNE 10$ ;KEEP GOING
1151 CLC ;CLEAR CARRY TO SET ERROR
1152 MOV R4,R0 ;PASS THE PACKET ADDRESS
1153 RTS PC ;RETURN
1154
1155 RMPACK: .=<.10>E177770
1156 .WORD 102010 ;POSTION COMMAND (REWIND)
1157 .WORD 0 ;NOT USED

```

TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
CKRAM - COMPARE RAM TO I/O PACKET

SEQ 0056

```

1154 .SBTTL CKRAM - COMPARE RAM TO I/O PACKET
1155 ;
1156 ;
1157 ;ROUTINE TO READ THE FIRST 8 BYTES FROM RAM
1158 ;MEMORY AND COMPARE THIS DATA TO A COMMAND PACKET.
1159 ;
1160 ;INPUT:
1161 ;
1162 ;      R4      ADDRESS OF THE COMMAND PACKET
1163 ;      R5      FIRST DEVICE UNIBUS ADDRESS
1164 ;
1165 ;OUTPUT:
1166 ;
1167 ;      CARRY    SET    RAM MATCHES PACKET
1168 ;              CLR    RAM DOES NOT MATCH PACKET
1169 ;
1170 ;IMPLICIT OUTPUT:
1171 ;
1172 ;      THE TABLE RAMDATA IS FILLED WITH THE
1173 ;      DATA HELD IN RAM.
1174 ;      RAMSIZ IS SET TO 8. FOR PRAMPKT ROUTINE
1175 ;
1176 ;SIDE EFFECTS:
1177 ;
1178 ;      THE SUBSYSTEM IS LEFT IN MAINTENANCE MODE
1179 ;
1180 ;-
1181
1182 CKRAM::
1183     SA' REG
1184     MOV     @RAMDATA,R1
1185     MOV     @RMPKTBEGR2
1186     CLR     R3
1187     JSR     PC,CHKTSSR
1188     MOVB    #0,TSDB(R5)
1189     JSR     PC,CHKTSSR
1190     MOV     R2,TSDB(R5)
1191     JSR     PC,CHKTSSR
1192     MOVB    TSBA(R5),(R1)
1193     CMPB    (R1)*,(R4)*
1194     BEQ     20$
1195     INC     R3
1196     INC     R2
1197     CMP     R2,@RMPKTEND
1198     BLE     10$
1199     TST     R3
1200     BEQ     30$
1201     CLC
1202     BR      50$
1203     SEC
1204     MOV     #8.,RAMSIZ
1205     RTS     PC

```

1154 011114 012701 002240
1155 011114 012702 000201
1156 011124 005003
1157 011132 004737 016336
1158 011136 112765 000000 000000
1159 011144 004737 016336
1160 011150 010265 000000
1161 011154 004737 016336
1162 011160 116511 000000
1163 011164 122124
1164 011166 001401
1165 011170 005203
1166 011172 005202
1167 011174 020227 000210
1168 011200 003761
1169 011202 005703
1170 011204 001402
1171 011206 000241
1172 011210 000401
1173 011212 000261
1174 011214 012737 000010 002300
1175 011222 000207

10\$: JSR PC,CHKTSSR
20\$: INC R2
30\$: BEQ 30\$
50\$: AND EXIT

1183 ;SAVE THE GENERAL REGISTERS
1184 ;ADDRESS TO SAVE THE RAM DATA
1185 ;BYTE ADDRESS OF FIRST RAM DATA
1186 ;CLEAR THE ERROR FLAG
1187 ;WAIT FOR SSR
1188 ;SET MAINTENANCE MODE
1189 ;WAIT FOR SSR TO SET
1190 ;SELECT NEXT RAM ADDRESS
1191 ;WAIT FOR SSR TO SET
1192 ;READ THE RAM DATA
1193 ;COMPARE TO EXPECTED
1194 ;BRANCH IF OK
1195 ;SET ERROR FLAG
1196 ;ADDRESS OF NEXT RAM LOCATION
1197 ;REACHED END YET ?
1198 ;BRANCH TILL ALL READ
1199 ;WAS AN ERROR FOUND ?
1200 ;BRANCH IF NOT
1201 ;CLEAR CARRY TO SHOW ERROR
1202 ;AND EXIT
1203 ;SHOW GOOD COMPARE
1204 ;SETUP RAMSIZ FOR PRAMPKT ROUTINE
1205 ;RETURN

```

1207 .SBTTL CKRAM2 - COMPARE RAM TO I/O CHARACTERISTICS DATA
1208 ;*
1209 ;
1210 ;ROUTINE TO READ THE FIRST 8 OR 10 BYTES FROM RAM
1211 ;MEMORY AND COMPARE THIS DATA TO A CHARACTERISTICS DATA BLOCK.
1212 ;
1213 ;INPUT:
1214 ;
1215 ;      R4      ADDRESS OF THE CHARACTERISTICS DATA
1216 ;      R5      FIRST DEVICE UNIBUS ADDRESS
1217 ;
1218 ;OUTPUT:
1219 ;
1220 ;      CARRY   SET - RAM MATCHES PACKET
1221 ;             CLR - RAM DOES NOT MATCH PACKET
1222 ;
1223 ;IMPLICIT OUTPUT:
1224 ;
1225 ;      THE TABLE RAMDATA IS FILLED WITH THE
1226 ;      DATA HELD IN RAM.
1227 ;      RAMSIZ IS SET TO 8. OR 10. FOR PRAMPKT ROUTINE
1228 ;
1229 ;SIDE EFFECTS:
1230 ;
1231 ;      THE SUBSYSTEM IS LEFT IN MAINTENANCE MODE
1232 ;
1233 CKRAM2::
1234 SAVREG                                ;SAVE THE GENERAL REGISTERS
1235 MOV      @RAMDATA,R1                  ;ADDRESS TO SAVE THE RAM DATA
1236 MOV      @RMCHBEG,R2                  ;BYTE ADDRESS OF FIRST RAM DATA
1237 CLR      R3                          ;CLEAR THE ERROR FLAG
1238 JSR      PC,CHKTSSR                   ;WAIT FOR SSR
1239 MOVB     @0,TSDB(R5)                  ;SET MAINTENANCE MODE
1240 JSR      PC,CHKTSSR                   ;WAIT FOR SSR TO SET
1241 MOV      R2,TSDB(R5)                  ;SELECT NEXT RAM ADDRESS
1242 JSR      PC,CHKTSSR                   ;WAIT FOR SSR TO SET
1243 MOVB     TSBA(R5),(R1)                 ;READ THE RAM DATA
1244 CMPB     (R1)+,(R4)+                  ;COMPARE TO EXPECTED
1245 BEQ      204                          ;BRANCH IF OK
1246 INC      R3                          ;SET ERROR FLAG
1247 INC      R2                          ;ADDRESS OF NEXT RAM LOCATION
1248 MOV      @8,RAMSIZ                    ;ASSUME EXTFEA NOT SET
1249 TST      EXTFEA                       ;IS THE SOFTWARE EXTENDED FEATURES SET
1250 BEQ      254                          ;BR, IF NOT SET
1251 MOV      @10,RAMSIZ                   ;SET RAMSIZ FOR EXTEND FEATURES
1252 CMP      R2,@RMCHEND                  ;AT END OF EXTENDED BUFFER
1253 BLE      104                          ;BR, IF NOT AT END YET
1254 BR       274                          ;AT END BRANCH
1255 CMP      R2,@RMCHEND-2                ;REACHED END YET ?
1256 BLE      104                          ;BRANCH TILL ALL READ
1257 TST      R3                          ;WAS AN ERROR FOUND ?
1258 BEQ      304                          ;BRANCH IF NOT
1259 CLC                                         ;CLEAR CARRY TO SHOW ERROR
1260 BR       504                          ;AND EXIT
1261 SEC                                         ;SHOW GOOD COMPARE
1262 RTS      PC                          ;RETURN

```


TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
CKMSG - COMPARE WRITE CHAR. MESSAGE BUFFERS

SEQ 0058

```

1264 .SBTTL CKMSG - COMPARE WRITE CHAR. MESSAGE BUFFERS
1265 ;
1266 ;
1267 ; ROUTINE TO COMPARE A WRITE CHARACTERISTICS EXPD AND RECV
1268 ; BUFFER. THE EXPECTED AND RECEIVED BUFFERS ARE STORED FOR
1269 ; ERROR PRINT ROUTINES.
1270 ;
1271 ; INPUT:
1272 ;
1273 ; R0 RECV MESSAGE BUFFER HIGH ORDER ADDRESS
1274 ; R1 RECV MESSAGE BUFFER LOW ORDER ADDRESS
1275 ; R2 EXPD MESSAGE BUFFER ADDRESS
1276 ; OUTPUT:
1277 ;
1278 ; CARRY SET - MESSAGE BUFFERS MATCH
1279 ; CLR -MESSAGE BUFFERS DON'T MATCH
1280 ;
1281 ; IMPLICIT OUTPUT:
1282 ;
1283 ; EXPMSG BUFFER IS SET TO EXPD DATA
1284 ; RECMG BUFFER IS SET TO RECV DATA
1285 ; RCVHIADD SET TO HIGH ORDER ADDRESS OF RECV
1286 ; RCVLOADD SET TO LOW ORDER ADDRESS OF RECV
1287 ;
1288 ;
1289 011360 CKMSG::
1290 011360 SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
1291 011364 010037 002302 MOV R0,RCVHIADD ;SAVE RECV HIGH ADDRESS
1292 011370 010137 002304 MOV R1,RCVLOADD ;SAVE RECV LOW ADDRESS
1293 011374 005737 003132 TST KTENABLE ;TESTING ABOVE 28K?
1294 011400 001403 BEQ 10$ ;BR IF NO
1295 011402 004737 017316 JSR PC,SETHAP ;RETURN ADDRESS BIASED TO PAR6 IN R0
1296 011406 010001 MOV R0,R1 ;GET RETURNED ADDRESS BIASED TO PAR6
1297 011410 005004 10$: CLR R4 ;WORD IN BUFFER
1298 011412 005003 CLR R3 ;CLEAR ERROR SEEN FLAG
1299 011414 010205 MOV R2,R5 ;GET EXPD BUFFER ADDRESS
1300 011416 011264 002320 15$: MOV (R2),EXPMSG(R4) ;SAVE EXPD FOR ERROR REPORT
1301 011422 011164 002464 MOV (R1),RCMSG(R4) ;SAVE RECV FOR ERROR REPORT
1302 011426 022221 CMP (R2), (R1) ;EXPD EQUAL RECV?
1303 011430 001401 BEQ 25$ ;BR IF YES
1304 011432 005203 INC R3 ;SET ERROR SEEN FLAG
1305 011434 062704 000002 25$: ADD #2,R4 ;POINT TO NEXT WORD ADDRESS
1306 011440 020427 000014 CMP R4,#14 ;DONE FIRST 7 WORDS?
1307 011444 003764 BLE 15$ ;BR IF NO
1308 011446 032765 000200 000012 BIT #X2.EXTF,XST2(R5) ;IS EXTENDED FEATURES SET IN EXPD?
1309 011454 001403 BEQ 50$ ;BR IF NO
1310 011456 020427 000016 CMP R4,#16 ;DONE EXTENDED FEATURES WORD?
1311 011462 003755 BLE 15$ ;BR IF NO
1312 011464 005703 50$: TST R3 ;ANY ERRORS SEEN?
1313 011466 001402 BEQ 55$ ;BR IF NO
1314 011470 000241 CLC ;SET FAILURE
1315 011472 000401 BR 60$ ;
1316 011474 000261 55$: SEC ;SET SUCCESS
1317 011476 000207 60$: RTS PC ;RETURN

```

TSV3 GLOBAL AREAS MACRO M1113 14-JUN 84 15:15
CKMSG2 COMPARE EXPD RECV MESSAGE BUFFERS

SEQ 0059

```

1319 .SBTTL CKMSG2 - COMPARE EXPD RECV MESSAGE BUFFERS
1320 ;*
1321 ;ROUTINE TO COMPARE AN EXPECTED AND RECEIVED MESSAGE
1322 ;BUFFER. THE EXPECTED AND RECEIVED BUFFERS ARE STORED FOR
1323 ;ERROR PRINT ROUTINES.
1324 ;
1325 ;INPUT:
1326 ;      R0      RECV MESSAGE BUFFER HIGH ORDER ADDRESS
1327 ;      R1      RECV MESSAGE BUFFER LOW ORDER ADDRESS
1328 ;      R2      EXPD MESSAGE BUFFER ADDRESS
1329 ;      R3      NUMBER OF BYTES TO COMPARE
1330 ;
1331 ;OUTPUT:
1332 ;      CARRY    SET - MESSAGE BUFFERS MATCH
1333 ;              CLR - MESSAGE BUFFERS DON'T MATCH
1334 ;
1335 ;IMPLICIT OUTPUT:
1336 ;      EXPMSG    BUFFER IS SET TO EXPD DATA
1337 ;      RECMSG    BUFFER IS SET TO RECV DATA
1338 ;      RCVHIADD  SET TO HIGH ORDER ADDRESS OF RECV
1339 ;      RCVLOADD  SET TO LOW ORDER ADDRESS OF RECV
1340 ;
1341 CKMSG2::
1342     SAVREG                                ;SAVE R1-R5 UNTIL NEXT RETURN
1343     CMP      R3,#RECMMSG-EXPMSG,800      ;R3 IS COUNT ABOVE MAX ALLOWED?
1344     BLE      50:                          ;BR IF NO
1345     MOV      #RECMMSG-EXPMSG,R3,800
1346     PRINTF   #DEBUGMSG,800
1347     MOV      #DEBUGMSG,-(SP)
1348     MOV      #1,-(SP)
1349     MOV      SP,R0
1350     TRAP     C$PRINTF
1351     ADD      #4,SP
1352     MOV      R0,RCVHIADD                  ;SAVE RECV HIGH ADDRESS
1353     MOV      R1,RCVLOADD                  ;SAVE RECV LOW ADDRESS
1354     TST      K$ENABLE                     ;TESTING ABOVE 28K?
1355     BEQ      10:                          ;BR IF NO
1356     JSR      PC,SETMAP                    ;RETURN ADDRESS BIASED TO PAR6 IN R0
1357     MOV      R0,R1                      ;GET RETURNED ADDRESS BIASED TO PAR6
1358     CLR      R4                          ;WORD IN BUFFER
1359     CLR      R5                          ;CLEAR ERROR SEEN FLAG
1360     MOV      (R2),EXPMSG(R4)              ;SAVE EXPD FOR ERROR REPORT
1361     MOV      (R1),RECMMSG(R4)            ;SAVE RECV FOR ERROR REPORT
1362     CMP      (R2)+,(R1)+                 ;EXPD EQUAL RECV?
1363     BEQ      25:                          ;BR IF YES
1364     INC      R5                          ;SET ERROR SEEN FLAG
1365     ADD      #1,R4                       ;POINT TO NEXT BYTE
1366     CMP      R4,R3                      ;DONE ALL BYTES?
1367     BCE      50:                          ;BR IF YES
1368     BR       15:                          ;DO NEXT BYTE
1369     TST      R5                          ;ANY ERRORS SEEN?
1370     BEQ      55:                          ;BR IF NO
1371     CLC                                           ;SET FAILURE
1372     BR       60:
1373     SEC                                           ;SET SUCCESS
1374     RTS      PC                             ;RETURN

```


TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
CKMSG2 - COMPARE EXPD RECV MESSAGE BUFFERS

SEQ 0060

```

1371 011632      120      122      117 DEBUGMSG: .ASCIZ 'PROGRAM INTERNAL ERROR -CKMSG2 MESSAGE BUFFER EXCEEDED- ;@@D
1372 011722      045      116      045 FERCM:  .ASCII /NMA ***/
1373 011733      040      040      124 ERCM:   .ASCIZ / TSSR ERROR CODE REC'D - /
1374 011766      056      056      056 SIMSG:  .ASCIZ /.... AFTER DOING SOFT INIT/
1375 012021      124      105      123 TINERR: .ASCIZ /TEST: .../
1376                                     .EVEN
1377                                     ;*
1378                                     ;
1379                                     ;PRINT ROUTINE TO FATAL SOFT INIT ERRORS
1380                                     ;
1381                                     ;INPUT:
1382                                     ;
1383                                     ;      R1      CONTENTS OF TSSR AT ERROR
1384                                     ;
1385                                     ;SIDE EFFECTS:
1386                                     ;
1387                                     ;      EXECUTES DROP UNIT TO CEASE TESTING
1388                                     ;
1389                                     ;-
1390
1391 012034          BGNMSG  SFMSG
1392 012034          SFMSG::
1393 012034 004737 006022      JSR      PC,PRITSSR      ;PRINT CONTENTS OF TSSR REGISTER
1394 012040 004737 017202      JSR      PC,CKDROP      ;DROP UNIT, IF ALLOWED
1395 012044          ENDMSG
1396 012044          L10003:
1397 012044          TRAP      C#MSG
1398
1399                                     ;*
1400                                     ;PRINT ROUTINE TO PRINT THE CONTENTS OF
1401                                     ;TSSR AND A COMMAND PACKET OTHER THAN GET STATUS COMMAND PACKET.
1402                                     ;
1403                                     ;INPUTS:
1404                                     ;
1405                                     ;      R1      TSSR CONTENTS
1406                                     ;      R4      ADDRESS OF COMMAND PACKET
1407                                     ;-
1408
1409 012046          BGNMSG  PKTSSR
1410 012046          PKTSSR::
1411 012046 004737 006022      JSR      PC,PRITSSR      ;PRINT THE CONTENTS OF TSSR REGISTER
1412 012052 012700 000004      MOV      #4,R0          ;NO. OF WORDS IN PACKET
1413 012056 004737 007366      JSR      PC,PRIPKT      ;PRINT THE CONTENTS OF COMMAND PACKET
1414 012062          ENDMSG
1415 012062          L10004:
1416 012062          TRAP      C#MSG

```

```

1413      ;*
1414      ;PRINT ROUTINE TO PRINT THE CONTENTS OF
1415      ;TSSR AND A GET STATUS COMMAND PACKET.
1416      ;
1417      ;INPUTS:
1418      ;
1419      ;      R1      TSSR CONTENTS
1420      ;      R4      ADDRESS OF COMMAND PACKET
1421      ;
1422      ;      BGNMSG  PKTGETS
1423      ;      PKTGETS:
1424      ;      JSR      PC,PRITSSR      ;PRINT THE CONTENTS OF TSSR REGISTER
1425      ;      MOV      #2,R0          ;NO. OF WORDS IN GET STATUS PACKET
1426      ;      JSR      PC,PRIPKT      ;PRINT THE CONTENTS OF COMMAND PACKET
1427      ;      ENDMMSG
1428      ;
1429      ;      L10005:
1430      ;      TRAP      C$MSG
1431      ;
1432      ;*
1433      ;PRINT TSSR ERRORS FOR INITIALIZATION TESTS
1434      ;
1435      ;INPUTS:
1436      ;      R1      TSSR CONTENTS
1437      ;      R4      ADDRESS OF COMMAND PACKET
1438      ;
1439      ;      BGNMSG  SFFMSG
1440      ;      SFFMSG:
1441      ;      JSR      PC,PRITSSR      ;PRINT CONTENTS OF TSSR REGISTER
1442      ;      ENDMMSG
1443      ;
1444      ;      L10006:
1445      ;      TRAP      C$MSG
1446      ;      .SBTTL  PKTMES - PRINT TSSR AND MESSAGE BUFFER
1447      ;
1448      ;*
1449      ;PRINT ROUTINE TO PRINT THE CONTENTS OF TSSR AND MESSAGE
1450      ;BUFFER FOR ERROR REPORTS
1451      ;
1452      ;INPUTS:
1453      ;      R1      CONTENTS OF TSSR
1454      ;      R2      LOW ORDER MESSAGE BUFFER
1455      ;      R3      HIGH ORDER MESSAGE BUFFER ADDRESS
1456      ;      NOTE: R3 IS IGNORED IF KTENABLE FLAG IS CLEAR
1457      ;
1458      ;      BGNMSG  PKTMES
1459      ;      PKTMES:
1460      ;      JSR      PC,PRITSSR      ;PRINT CONTENTS OF TSSR
1461      ;      MOV      R2,R0          ;LOW ORDER ADDRESS
1462      ;      MOV      R3,R1          ;HIGH ORDER ADDRESS
1463      ;      JSR      PC,PRMESS      ;PRINT THE MESSAGE BUFFER
1464      ;      ENDMMSG
1465      ;
1466      ;      L10007:
1467      ;      TRAP      C$MSG

```

```

1456 .SBTTL ADDSSR - PRINT TEST ADDRESS AND TSSR
1457 ;*
1458 ;PRINT ROUTINE TO PRINT THE CONTENTS OF
1459 ;TSSR AND A MEMORY TEST ADDRESS
1460 ;
1461 ;INPUTS:
1462 ;
1463 ; R5 FIRST DEVICE UNIBUS ADDRESS
1464 ; ERRHI HIGH ORDER MEMORY TEST ADDRESS
1465 ; ERRLO LOW ORDER MEMORY TEST ADDRESS
1466 ;
1467
1468 012126 BGNMSG ADDSSR
      012126 ADDSSR::
1469 012126 004737 010272 JSR PC,PRITADD ;PRINT MEMORY TEST ADDRESS
1470 012132 016501 000002 MOV TSSR(R5),R1 ;GET CURRENT TSSR
1471 012136 004737 006022 JSR PC,PRITSSR ;PRINT THE CONTENTS OF TSSR REGISTER
1472 012142 ENDMMSG
      012142 L10010:
      012142 104423 TRAP C#MSG

1473 .SBTTL MSGEXP - PRINT WRITE CHAR. EXPD-RCV MESSAGE BUFFERS
1474 ;*
1475 ;PRINT ROUTINE TO PRINT WRITE CHARACTERISTIC MESSAGE BUFFER
1476 ;
1477 ;IMPLICIT INPUTS:
1478 ;
1479 ; EXPMSG - EXPECTED MESSAGE BUFFER
1480 ; RECMMSG - RECEIVED MESSAGE BUFFER
1481 ; RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
1482 ; RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
1483 ;
1484 ;
1485
1486 012144 BGNMSG MSGEXP
      012144 MSGEXP::
1487 012144 012700 000007 MOV #7,R0 ;ASSUME NO EXT FEATURES
1488 012150 005737 002224 TST EXTFEA ;EXT FEATURES SET?
1489 012154 001402 BEQ 5$ ;BR IF NO
1490 012156 012700 000010 MOV #8.,R0 ;EXT FEATURE BUFFER IS 8 WORDS
1491 012162 004737 014552 JSR PC,PRMSGEXP ;PRINT EXPD/RCV MESSAGE BUFFERS
1492 012166 ENDMMSG
      012166 L10011:
      012166 104423 TRAP C#MSG
  
```

TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
 FIFEXP - PRINT FIFO EXP/RECV DATA

SEQ 0063

```

1494 .SBTTL FIFEXP - PRINT FIFO EXP/RECV DATA
1495
1496 ;
1497 ;PRINT ROUTINE TO PRINT FIFO EXP/RECV DATA
1498 ;
1499 ; R1 - BYTE COUNT
1500 ;
1501 ;IMPLICIT INPUTS:
1502 ;
1503 ; EXPMSG - EXPECTED MESSAGE BUFFER (CONTAINS FIFO DATA ONLY)
1504 ; RECMMSG - RECEIVED MESSAGE BUFFER (CONTAINS FIFO DATA ONLY)
1505 ;
1506 BGNMSG FIFEXP
1507 FIFEXP:
1508 PRINTX #FIF1MSG,R1 ;PRINT BYTES TRANSFERRED
1509 MOV R1,-(SP)
1510 MOV #FIF1MSG,-(SP)
1511 MOV #2,-(SP)
1512 MOV SP,R0
1513 TRAP C#PNTX
1514 ADD #6,SP
1515 PRINTX #FIF2MSG ;PRINT HEADER MSG
1516 MOV #FIF2MSG,-(SP)
1517 MOV #1,-(SP)
1518 MOV SP,R0
1519 TRAP C#PNTX
1520 ADD #4,SP
1521 MOV R1,R0 ;GET BYTE COUNT
1522 JSR PC,PRBYTEXP ;PRINT FIFO BYTES IN ERROR
1523 ENDMSG
1524
1525 L10012:
1526 TRAP C#MSG
1527 .ASCIZ 'NUMBER OF BYTES TRANSFERRED * #02
1528 .ASCIZ 'FIFO DATA BYTES IN ERROR:'
1529 .EVEN

```

TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
MSGSTAT - PRINT STATUS HEADER AND MESSAGE BUFFERS

SEQ 0064

```

1516 .SBTTL MSGSTAT - PRINT STATUS HEADER AND MESSAGE BUFFERS
1517
1518
1519 ;PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RECV
1520
1521 ;
1522 ;IMPLICIT INPUTS:
1523
1524 ; EXPMSG - EXPECTED MESSAGE BUFFER
1525 ; RECMMSG - RECEIVED MESSAGE BUFFER
1526 ; RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
1527 ; RCVLOAD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
1528
1529 012350 BGNMSG MSGSTAT
1530 012350 MSGSTAT:
1531 012350 012701 012412 MOV #STATCOD,R1 ;ASCII ADDRESS TABLE
1532 012354 012100 10$: MOV (R1),R0 ;DONE ALL MSG LINES?
1533 012356 001410 BEQ 20$ ;BR IF YES
1534 012360 PRINTX R0 ;PRINT STATUS BIT NAMES
1535 012360 010046 MOV R0,-(SP)
1536 012362 012746 000001 MOV #1,-(SP)
1537 012366 010600 MOV SP,R0
1538 012370 104415 TRAP C$PNTX
1539 012372 062706 000004 ADD #4,SP
1540 012376 000766 BR 10$ ;DO ANOTHER MSG LINE
1541 012400 012700 000012 20$: MOV #10,R0 ;NUMBER OF WORDS IN A READ STATUS BUFFER
1542 012404 004737 014552 JSR PC,PRMSGEXP ;PRINT EXPD/RECV MESSAGE BUFFERS
1543 012410 ENDMMSG
1544 012410 L10013:
1545 012410 104423 TRAP C$MSG
1546
1547 1538
1539 012412 012430 012472 012563 STATCOD: .WORD 1$,2$,3$,4$,5$,6$,0
1540 012430 045 116 045 1$:ASCIZ 'ANSA Tape Bus Signals in Word #8:'
1541 012472 045 116 045 2$:ASCIZ 'ANSA PARERR<15> IEOT <12> IFMK <9> IRDY<6> IRWD<2>'
1542 012563 045 116 045 3$:ASCIZ 'ANSA IRESV2<14> IIDENT<11> IHER <8> IONL<5> IFBY<1>'
1543 012654 045 116 045 4$:ASCIZ 'ANSA IRESV1<13> ICER <10> ISPEED<7> ILDP<4> IFPT<0>'
1544 012745 045 116 045 5$:ASCIZ 'ANSA Tape Bus Signals in Word #9:'
1545 013007 045 116 045 6$:ASCIZ 'ANSA DATMIS<7> ILW<6> OUTRDY<5> INRDY<4>'
1546 .EVEN
1547

```

TSV3 GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
MSGLOOP PRINT LOOPBACK HEADER AND MESSAGE BUFFERS

SEQ 0065

```

1549 .SBTTL MSGLOOP PRINT LOOPBACK HEADER AND MESSAGE BUFFERS
1550
1551
1552 ;PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RCV
1553
1554 ;IMPLICIT INPUTS:
1555
1556 EXPMSG - EXPECTED MESSAGE BUFFER
1557 RECMMSG - RECEIVED MESSAGE BUFFER
1558 RCVHIADD - RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
1559 RCVLOADD - RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
1560
1561 BGNMSG MSGLOOP
1562 MSGLOOP:
1563     MOV     @LOOPCOD,R1      ;ASCII ADDRESS TABLE
1564     MOV     (R1),R0          ;DONE ALL MSG LINES?
1565     BEQ     201              ;BR IF YES
1566     PRINTX  R0               ;PRINT STATUS BIT NAMES
1567     MOV     R0,-(SP)
1568     MOV     @1,-(SP)
1569     MOV     SP,R0
1570     TRAP    C@PRINTX
1571     ADD     @4,SP
1572     BR      101              ;DO ANOTHER MSG LINE
1573     MOV     @10,R0           ;NUMBER OF WORDS IN A READ STATUS BUFFER
1574     JSR     PC,PRMSGEXP      ;PRINT EXPD/RCV MESSAGE BUFFERS
1575     ENDMMSG
1576
1577 L10C14:
1578     TRAP    C@MSG
1579
1580 LOOPCOD: .WORD 11,21,31,41,51,61,71,0
1581 11::ASCIZ 'ENSA Tape Bus Loopback Signals in Word 08:'
1582 21::ASCIZ 'ENSA PARERR<15> IRESV2<14> IRESV1<13>'
1583 31::ASCIZ 'ENSA IHISP=>IEOT<12> IWRT=>IIDENT<11> IREV =>ICER <10>'
1584 41::ASCIZ 'ENSA IWMH =>IFMK<09> IEDIT=>IHER <08> IFAD =>ISPEED<07>'
1585 51::ASCIZ 'ENSA ITADO=>IRDY<06> ITAD1=>IONL <05> IERASE=>ILDOP <04>'
1586 61::ASCIZ 'ENSA IREW =>IDBY<03> IRWU =>IRWD <02> IFEN =>IFBY <01>'
1587 71::ASCIZ 'ENSA IGO =>IFPT<00>'
1588 .EVEN

```


TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
 MSGSUB - PRINT WRITE SUBSYSTEM MESSAGE BUFFER

SEQ 0066

```

1581 .SBTTL MSGSUB - PRINT WRITE SUBSYSTEM MESSAGE BUFFER
1582 ;
1583 ;
1584 ;PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RCV
1585 ;
1586 ;
1587 ;IMPLICIT INPUTS:
1588 ;
1589 ;   EXPMSG - EXPECTED MESSAGE BUFFER
1590 ;   RECMSG - RECEIVED MESSAGE BUFFER
1591 ;   RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
1592 ;   RCVLOADC- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
1593 ;
1594 013742 BGNMSG MSGSUB
1595 013742 MSGSUB::
1596 013742 012700 000012 MOV #10.,R0 ;SIZE OF WRITE SUBSYSTEM BUFFER
1597 013746 004737 014552 JSR PC,PRMSGEXP ;PRINT EXPD/RCV MESSAGE BUFFERS
1598 013752 ENDMSG
1599 013752 L10015:
1600 013752 104423 TRAP C#MSG
1601 ;
1602 .SBTTL MEMADD - PRINT MEMORY ADDRESS DATA ERROR
1603 ;
1604 ;PRINT ROUTINE TO PRINT MEMORY ADDRESS DATA COMPARE ERROR
1605 ;
1606 ;IMPLICIT INPUTS:
1607 ;
1608 ;   ERRHI - MEMORY ERROR HIGH ORDER ADDRESS
1609 ;   ERRLO - MEMORY ERROR LOW ORDER ADDRESS
1610 ;   EXP - EXPECTED DATA
1611 ;   RECV - RECEIVED DATA
1612 ;
1613 013754 BGNMSG MEMADD
1614 013754 MEMADD::
1615 013754 004737 010156 JSR PC,PRIADD ;PRINT MEMORY ADDRESS IN ERROR
1616 013760 013701 002230 MOV EXPD,R1 ;GET EXPD DATA
1617 013764 013702 002232 MOV RECV,R2 ;GET RECEIVED DATA
1618 013770 004737 007740 JSR PC,PRIXOR ;PRINT EXPD/RCV
1619 013774 ENDMSG
1620 013774 L10016:
1621 013774 104423 TRAP C#MSG

```

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TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
PRAMPKT - PRINT RAM AND PACKET DATA

SEQ 0067

```

1618 .SBTTL PRAMPKT PRINT RAM AND PACKET DATA
1619 ;
1620 ;PRINT ROUTINE TO DISPLAY RAM/PACKET DATA
1621 ;WHEN THE RAM DATA DOES NOT MATCH.
1622 ;
1623 ;INPUTS:
1624 ;
1625 ; R4 POINTER TO COMMAND PACKET
1626 ;IMPLICIT INPUTS:
1627 ; RAMDATA DATA AS READ FROM THE RAM
1628 ; RAMSIZ NUMBER OF BYTES IN PACKET
1629 ; IF RAMSIZ=0 THEN DEFAULT TO 8.
1630 ;
1631 ;IMPLICIT OUTPUTS:
1632 ; RAMSIZ SET TO 0
1633 ;
1634 PRAMPKT:
1635 SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
1636 MOV #RAMDATA,R1 ;DATA FROM THE RAM
1637 CLR R2 ;INIT BYTE NUMBER
1638 54: CMPB (R1),.(R4). ;COMPARE EXPECTED, RECEIVED
1639 BNE 74 ;BR IF NO MATCH
1640 FORCERROR 74,NOTSSR
1641 BR 104 ;SSD
1642 74: MOVB -1(R1),R5 ;GET RECV RAM DATA
1643 MOVB -1(R4),R3 ;GET EXPD PACKET DATA
1644 XOR R5,R3 ;XOR EXPD/RECV
1645 BIC #177400,R3 ;LOW BYTE ONLY
1646 MOVB -1(R1),RECV ;GET RECEIVED RAM DATA
1647 MOVB -1(R4),EXPD ;GET EXPECTED RAM DATA
1648 PRINTB #RAMASC,R2,RECV,EXPD,R3
1649 MOVB 010346,R3,.(SP)
1650 MOVB 013746,EXPD,.(SP)
1651 MOVB 013746,RECV,.(SP)
1652 MOVB 010246,R2,.(SP)
1653 MOVB 012746,#RAMASC,.(SP)
1654 MOVB 012746,#5,.(SP)
1655 MOVB 010600,SP,R0
1656 014114 TRAP C#PNTB
1657 014116 ADD #14,SP
1658 104: INC R2 ;UPDATE BYTE COUNT
1659 TST RAMSIZ ;DEFAULT TO 8.?
1660 BEQ 154 ;BR IF YES
1661 CMP R2,RAMSIZ ;DONE ALL BYTES?
1662 BLE 54 ;BR IF NO
1663 BR 254 ;
1664 154: CMP R2,#8. ;DONE DEFAULT NUMBER OF BYTES?
1665 BLT 54 ;BR IF NO
1666 254: CLR RAMSIZ ;SET DEFAULT RAMSIZ
1667 RTS PC ;RETURN
1668
1669 045 116 045 RAMASC: .ASCIZ '##A BYTE: #D2#A RAM: #03#A Packet: #03#A XOR:#03'
1670 .EVEN
1671

```


TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
 PRMESS - PRINT CONTENTS OF MESSAGE BUFFER

SEQ 0068

```

1663 .SBTTL PRMESS - PRINT CONTENTS OF MESSAGE BUFFER
1664 ;
1665 ; THIS ROUTINE PRINTS THE CONTENTS OF
1666 ; THE 7 OR 8 WORD MESSAGE BUFFER RETURNED BY THE TSV 05.
1667 ;
1668 ; INPUT:
1669 ; R0 LOW ORDER ADDRESS OF MESSAGE BUFFER
1670 ; R1 HIGH ORDER ADDRESS OF MESSAGE BUFFER
1671 ; NOTE: R1 IS IGNORED IF KTENABLE FLAG IS CLEAR
1672 ; THIS ROUTINE IS NORMALLY CALLED FROM A PRINT ROUTINE
1673 ;
1674 PRMESS: SAVREG ;SAVE THE REGISTERS
1675 MOV R0,R5 ;SAVE LOW ORDER ADDRESS
1676 TST KTENABLE ;ADDRESS ABOVE 28K?
1677 BNE 104 ;BR IF YES
1678 CLR R1 ;SET HIGH ORDER ADDRESS TO 0
1679 104: MOV R1,R3 ;SAVE HIGH ORDER ADDRESS
1680 ROL R0 ;SHIFT BIT15 TO C BIT
1681 ROL R1 ;SHIFT TO HIGH ORDER FOR PRINTOUT
1682 PRINTX @PROASC,R1,R5 ;PRINT MESSAGE BUFFER ADDRESS
      MOV R5,-(SP)
      MOV R1,-(SP)
      MOV @PROASC,-(SP)
      MOV @3,-(SP)
      MOV SP,R0
      TRAP C:PNTX
      ADD @10,SP
1683 PRINTX @PRIASC ;PRINT HEADER FOR CONTENTS
      MOV @PRIASC,-(SP)
      MOV @1,-(SP)
      MOV SP,R0
      TRAP C:PNTX
      ADD @4,SP
1684 CLR R4 ;NUMBER OF THE NEXT WORD
1685 MOV R5,R1 ;COPY LOW ORDER ADDRESS
1686 MOV R3,R0 ;COPY HIGH ORDER ADDRESS
1687 BEQ 204 ;BR IF NOT ABOVE 28K
1688 JSR PC,SETMAP ;SETUP PAR ADDRESS IN R0
1689 MOV R0,R5 ;GET PAR FORMAT ADDRESS ABOVE 28K
1690 204: PRINTX @PRASC,R4,(R5) ;PRINT THE CONTENTS OF MEMORY BUFFER
      MOV (R5),-(SP)
      MOV R4,-(SP)
      MOV @PRASC,-(SP)
      MOV @3,-(SP)
      MOV SP,R0
      TRAP C:PNTX
      ADD @10,SP
1691 INC R4 ;NUMBER OF THE NEXT
1692 CMP R4,@7 ;DONE ALL YET ?
1693 BGT 504 ;BRANCH IF ALL DONE
1694 BLT 204 ;PRINT FIRST 7 WORDS
1695 BIT @X2.EXTF,XST2(R3) ;EXTENDED FEATUTES ON ?
1696 BNE 204 ;PRINT EXTENDED STATUS WORD
1697 504: RTS PC ;RETURN
1698 PROASC: .ASCIZ '##A Message Buffer Address = #01#05'
1699 PRIASC: .ASCIZ '##A Message Buffer Contents:'
1700 PRASC: .ASCIZ '##A Word#D1#A: #0'

```

TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
 PRMESS - PRINT CONTENTS OF MESSAGE BUFFER

SEQ 0069

```

1702 .EVEN
1703 .SBTTL PRMSGEXP - PRINT EXPD/RCV MESSAGE BUFFERS
1704 ;*
1705 ;ROUTINE TO PRINT EXPECTED AND RECEIVED MESSAGE BUFFERS
1706 ; RO - NUMBER OF WORDS IN BUFFER
1707 ;IMPLICIT INPUTS:
1708 ; EXPMSG - EXPECTED MESSAGE BUFFER
1709 ; RECMMSG - RECEIVED MESSAGE BUFFER
1710 ; RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
1711 ; RCVLOAD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
1712 ;
1713 PRMSGEXP::
1714 SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
1715 MOV RO,R5 ;SAVE NUMBER OF WORDS
1716 MOV RCVLOAD,RO ;GET RECV LOW ADDRESS
1717 MOV RO,R4 ;COPY LOW ADDRESS
1718 MOV RCVHIADD,R1 ;GET RECV HIGH ADDRESS
1719 ROL RO ;SHIFT BIT15 TO C BIT
1720 ROL R1 ;SHIFT TO HIGH ORDER FOR PRINTOUT
1721 PRINTX @PRMSG0,R1,R4 ;PRINT MESSAGE BUFFER ADDRESS
1722 014576 010446 MOV R4,-(SP)
1723 014576 010146 MOV R1,-(SP)
1724 014600 010146 MOV @PRMSG0,-(SP)
1725 014602 012746 014732 MOV @3,-(SP)
1726 014606 012746 000003 MOV SP,RO
1727 014612 010600 TRAP C:PNTX
1728 014614 104415 ADD #10,SP
1729 014616 062706 000010 PRINTX @PRMSG1 ;PRINT HEADER FOR CONTENTS
1730 014622 012746 014777 MOV @PRMSG1,-(SP)
1731 014626 012746 000001 MOV #1,-(SP)
1732 014632 010600 MOV SP,RO
1733 014634 104415 TRAP C:PNTX
1734 014636 062706 000004 ADD #4,SP
1735 014642 005004 CLR R4 ;NUMBER OF THE CURRENT WORD
1736 014644 012701 002320 MOV @EXPMSG,R1 ;GET EXPD BUFFER ADDRESS
1737 014650 012702 002464 MOV @RCMMSG,R2 ;GET RECV BUFFER ADDRESS
1738 014654 011100 204: MOV (R1),R0 ;GET EXPD
1739 014656 011203 MOV (R2),R3 ;GET RECV
1740 014660 XOR R0,R3 ;XOR EXPD/RCV
1741 014670 PRINTX @PRMSG2,R4,(R1),,(R2),,R3
1742 014670 010346 MOV R3,-(SP)
1743 014672 012246 MOV (R2),,-(SP)
1744 014674 012146 MOV (R1),,-(SP)
1745 014676 010446 MOV R4,-(SP)
1746 014700 012746 015035 MOV @PRMSG2,-(SP)
1747 014704 012746 000005 MOV #5,-(SP)
1748 014710 010600 MOV SP,RO
1749 014712 104415 TRAP C:PNTX
1750 014714 062706 000014 ADD #14,SP
1751 014720 005204 INC R4 ;NUMBER OF THE NEXT
1752 014722 020405 CMP R4,R5 ;DONE ALL YET?
1753 014724 002001 BGE 504 ;BR IF YES
1754 014726 000752 BR 204 ;DO ANOTHER
1755 014730 000207 504: RTS PC ;RETURN
1756 014732 045 116 045 PRMSG0: .ASCIIZ '##A Message Buffer Address = #01#05'
1757 014777 045 116 045 PRMSG1: .ASCIIZ '##A Message Buffer Contents:'
1758 015035 045 116 045 PRMSG2: .ASCIIZ '##A WORD #D2#A EXPD: #06#A RECV: #06#A XOR: #06'

```

```

1739 .EVEN
1740 .SBTTL PRBYTEXP - PRINT ERROR BYTES IN EXP/REC MESSAGE BUFFER
1741
1742 ;
1743 ;ROUTINE TO PRINT ERROR BYTES IN MESSAGE BUFFERS
1744 ; ONLY THE FIRST 8 ERRORS ENCOUNTERED ARE PRINTED DUE TO SCREEN SPACE
1745 ;
1746 ; RO - NUMBER OF BYTES IN BUFFER
1747 ;
1748 ;IMPLICIT INPUTS:
1749 ;
1750 ; EXPMSG - EXPECTED MESSAGE BUFFER
1751 ; RECMMSG - RECEIVED MESSAGE BUFFER
1752 ;
1753 PRBYTEXP::
1754 SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
1755 MOV RO,R5 ;SAVE NUMBER OF BYTES
1756 CLR PRMNO ;INIT ERROR COUNT
1757 CLR R4 ;NUMBER OF THE CURRENT BYTE
1758 MOV #EXPMSG,R1 ;GET EXPD BUFFER ADDRESS
1759 MOV #RECMMSG,R2 ;GET RECV BUFFER ADDRESS
1760 20$: MOVB (R1),R0 ;GET EXPD BYTE
1761 BIC #C<377>,R0 ;CLEAR UPPER BYTE
1762 MOVB R0,PRBEXP ;SAVE FOR ERROR REPORT
1763 MOVB (R2),R3 ;GET RECV BYTE
1764 BIC #C<377>,R3 ;CLEAR UPPER BYTE
1765 MOVB R3,PRBREC ;FOR ERROR REPORT
1766 XOR R0,R3 ;XOR EXPD/RECV
1767 CMPB (R1),R2 ;EXPD = RECV?
1768 BEQ 30$ ;BR IF YES
1769 INC PRMNO ;UPDATE ERROR COUNT
1770 000010 CMP PRMNO,#8 ;PRINTED 8?
1771 BHI 30$ ;BR IF YES
1772 27$: PRINTX #PRBMSG,R4,PRBEXP,PRBREC,R3
1773 MOV R3,-(SP)
1774 MOV PRBREC,-(SP)
1775 MOV PRBEXP,-(SP)
1776 MOV R4,-(SP)
1777 MOV #PRBMSG,-(SP)
1778 MOV #5,-(SP)
1779 MOV SP,R0
1780 TRAP C#PNTX
1781 ADD #14,SP
1782 FORCEEXIT 50$ ;880
1783 BR 35$ ;880
1784 30$:
1785 FORCERROR 27$,NOTSSR ;880
1786 35$:
1787 INC R4 ;NUMBER OF THE NEXT
1788 CMP R4,R5 ;DONE ALL YET?
1789 BGE 50$ ;BR IF YES
1790 BR 20$ ;DO ANOTHER
1791 50$: PRINTX #PRBTOT,PRMNO ;PRINT TOTAL ERROR COUNT
1792 MOV PRMNO,-(SP)
1793 MOV #PRBTOT,-(SP)
1794 MOV #2,-(SP)
1795 MOV SP,R0

```

TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
 PRBYTEXP - PRINT ERROR BYTES IN EXP/REC MESSAGE BUFFER

SEQ 0071

```

015326 104415
015330 062706 000006
1783 015334 000207 TRAP C#PNTX
1784 ADD #6,SP
1785 015336 045 116 045 PRBMSG: .ASCIZ '##A BYTE #D2#A EXPD: #03#A RECV: #03#A XOR: #03'
1786 015423 045 116 045 PRBTOT: .ASCIZ '##A NUMBER OF BYTES IN ERROR = #D2'
1787 .EVEN
1788 015470 000000 PRBEXP: .WORD 0 ;EXPD
1789 015472 000000 PRBREC: .WORD 0 ;RECV
1790 .SBTTL EXPREC - PRINT EXPD/RECV WORD DATA
1791 ;
1792 ;
1793 ;PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
1794 ;
1795 ;INPUTS:
1796 ;
1797 ; R1 RECEIVED DATA
1798 ; R2 EXPECTED DATA
1799 ;
1800 ;
1801 ;
1802 015474 BGNMSG EXPREC
1803 015474 004737 007740 EXPREC:: JSR PC,PRIXOR ;PRINT THE DATA
1804 015500 ENDMSG
1805 015500 104423 L10017: TRAP C#MSG
1806 .SBTTL EXPBREC - PRINT EXPD/RECV BYTE DATA
1807 ;
1808 ;
1809 ;PRINT ROUTINE TO DISPLAY BYTE EXPD/RECV DATA
1810 ;
1811 ;INPUTS:
1812 ;
1813 ; R1 RECEIVED DATA BYTE
1814 ; R2 EXPECTED DATA BYTE
1815 ;
1816 ;
1817 ;
1818 015502 BGNMSG EXPBREC
1819 015502 004737 007610 EXPBREC:: JSR PC,PRIBXOR ;PRINT THE DATA
1820 015506 ENDMSG
1821 015506 104423 L10020: TRAP C#MSG
1822 .SBTTL RAMERR - PRINT RAM AND PACKET DATA
1823 ;
1824 ;
1825 ;PRINT ROUTINE TO DISPLAY RAM/PACKET DATA
1826 ;
1827 ;INPUTS:
1828 ;
1829 ; R4 POINTER TO COMMAND PACKET
1830 ;
1831 ;

```

```

1832      ;IMPLICIT INPUTS:
1833      ;
1834      ;      RAMDATA      DATA AS READ FROM THE RAM
1835      ;      RAMSIZ      NUMBER OF BYTES IN PACKET
1836      ;                  IF RAMSIZ=0 THEN DEFAULT TO 8.
1837      ;
1838      ;IMPLICIT OUTPUTS:
1839      ;
1840      ;      RAMSIZ  SET TO 0
1841      ;
1842      ;
1843      015510      BGNMSG  RAMERR
1844      015510      RAMERR:: JSR      PC,PRAMPKT      ;PRINT RAM/PACKET DATA
1845      015514      004737 013776      ENDMMSG
1846      015514      104423      L10021: TRAP      C#MSG
1847      ;
1848      ;      .SBTTL  RAMTADD - PRINT TEST ADDRESS, RAM AND PACKET DATA
1849      ;
1850      ;PRINT ROUTINE TO DISPLAY RAM/PACKET DATA
1851      ;
1852      ;INPUTS:
1853      ;
1854      ;      R4      POINTER TO COMMAND PACKET
1855      ;
1856      ;IMPLICIT INPUTS:
1857      ;
1858      ;      RAMDATA      DATA AS READ FROM THE RAM
1859      ;      RAMSIZ      NUMBER OF BYTES IN PACKET
1860      ;                  IF RAMSIZ=0 THEN DEFAULT TO 8.
1861      ;      ERRHI      HIGH ORDER TEST ADDRESS
1862      ;      ERRLO      LOW ORDER TEST ADDRESS
1863      ;
1864      ;IMPLICIT OUTPUTS:
1865      ;
1866      ;      RAMSIZ  SET TO 0
1867      ;
1868      ;
1869      015516      BGNMSG  RAMTADD
1870      015516      RAMTADD:: JSR      PC,PRITADD      ;PRINT TEST ADDRESS
1871      015522      004737 010272      JSR      PC,PRAMPKT      ;PRINT RAM/PACKET DATA
1872      015526      004737 013776      ENDMMSG
1873      015526      104423      L10022: TRAP      C#MSG
1874      ;
1875      ;      .SBTTL  RAMEXP - PRINT RAM EXPD/RECV DATA
1876      ;
1877      ;PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
1878      ;
1879      ;INPUTS:
1880      ;
1881      ;      R1      RECEIVED DATA
1882      ;      R2      EXPECTED DATA

```

TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
 RAMEXP - PRINT RAM EXPD/RECV DATA

SEQ 0073

```

1883      ;      R4      CONTROLLER RAM ADDRESS
1884      ;
1885
1886 015530      BGNMSG  RAMEXP
1887 015530      RAMEXP:
1888 015530      BIC      #C<377>,R1      ;SAVE EXPD RAM DATA BYTE
1889 015534      BIC      #C<377>,R2      ;SAVE EXPD RAM DATA BYTE
1890 015540      JSR      PC,PRIRAM      ;PRINT THE RAM ADDRESS
1891 015544      JSR      PC,PRIXOR      ;PRINT THE DATA
1892 015550      ENDMMSG
1893 015550      L10023:
1894 015550      TRAP      C#MSG
1895      .SBTTL  TIMEXP - PRINT TIMER A,B AND EXP/REC
1896      ;*
1897      ;PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
1898      ;AND TIMER A,B HEADER MESSAGE
1899      ;
1900      ;INPUTS:
1901      ;
1902      ;      R1      RECEIVED DATA
1903      ;      R2      EXPECTED DATA
1904      ;
1905      BGNMSG  TIMEXP
1906 015552      TIMEXP:
1907 015552      PRINTX   #TIMSGO      ;PRINT HEADER
1908 015552      MOV      #TIMSGO,-(SP)
1909 015552      MOV      #1,-(SP)
1910 015556      MOV      SP,R0
1911 015562      TRAP      C#PNTX
1912 015564      ADD      #4,SP
1913 015566      JSR      PC,PRIXOR      ;PRINT THE DATA
1914 015572      ENDMMSG
1915 015576      L10024:
1916 015576      TRAP      C#MSG
1917
1918 015600      045      116      045  TIMSGO: .ASCIZ 'TIMER A STATUS IS IN BIT 3'
1919      .EVEN      ;TIMER B STATUS IS IN BIT 2'
1920      .SBTTL  BADSSR - PRINT TSSR ERRORS ON DATA TRANSFERS
1921      ;*
1922      ;PRINT ROUTINE FOR TSSR ERRORS ON DATA TRANSFERS
1923      ;
1924      ;INPUTS:
1925      ;
1926      ;      R1      CONTENTS OF TSSR
1927      ;      R2      DATA WRITTEN (8 BITS)
1928      ;
1929      BGNMSG  BADSSR
1930 015700      BADSSR:
1931 015700      MOV      R2,-(SP)      ;SAVE DATA TRANSFERRED
1932 015702      BIC      #177400,R2      ;GET JUST ONE BYTE

```


TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
 BADSSR - PRINT TSSR ERRORS ON DATA TRANSFERS

SEQ 0074

1928	015706				PRINTB	#XFERASC,R2	
	015706	010246			MOV	R2,-(SP)	
	015710	012746	015740		MOV	#XFERASC,-(SP)	
	015714	012746	000002		MOV	#2,-(SP)	
	015720	010600			MOV	SP,R0	
	015722	104414			TRAP	C#PNTB	
	015724	062706	000006		ADD	#6,SP	
1929	015730	012602			MOV	(SP)+,R2	;RESTORE R2
1930	015732	004737	006022		JSR	PC,PRITSSR	;DECODE TSSR CONTENTS
1931	015736				ENDMSG		
	015736			L10025:			
	015736	104423			TRAP	C#MSG	
1932	015740	045	116	045	XFERASC:	.ASCIZ	'#N#A Data Transferred = #03'

```

1934 .SBTTL GLOBAL SUBROUTINES SECTION
1935
1936 ;*
1937 ; THE GLOBAL SUBROUTINES SECTION CONTAINS THE SUBROUTINES
1938 ; THAT ARE USED IN MORE THAN ONE TEST.
1939 ;
1940 .SBTTL SOFINIT - SOFT INITIALIZE OF CONTROLLER
1941
1942 ;*
1943 ;
1944 ; ROUTINE TO DO A SOFT INITIALIZE OF THE CONTROLLER
1945 ; BY WRITING INTO THE TSSR REGISTER. AFTER THE INIT,
1946 ; THE TSSR REGISTER IS TESTED FOR ERRORS. ANY ERRORS
1947 ; DETECTED SHOULD BE TREATED AS DEVICE FATAL ERRORS.
1948 ;
1949 ; INPUTS:
1950 ;
1951 ; R5 ADDRESS OF FIRST REGISTER
1952 ;
1953 ; OUTPUTS:
1954 ;
1955 ; R0 CONTENTS OF TSSR, IF ERROR
1956 ; CARRY SET IF INIT WAS OKAY
1957 ; CLEAR IF FATAL ERROR
1958 ;
1959 ; CALLING SEQUENCE:
1960 ;
1961 ; MOV #ADDRESS,R5
1962 ; JSR PC,SOFINIT
1963 ; BCS CONTINUE
1964 ; ERROF ;REPORT FATAL ERROR
1965 ;
1966 ; -
1967
1968 015774 SOFINIT::
1969 015774 SAVREG ; SAVE THE REGISTERS
1970 016000 012765 000000 000002 MOV #0,TSSR(R5) ; DO THE INIT.
1971 016006 004737 016250 JSR PC,WAITF ; WAIT FOR SSR
1972 016012 016500 000002 MOV TSSR(R5),R0 ; GET THE TSSR REGISTER
1973 016016 010004 MOV R0,R4 ; TSSR CONTENTS
1974 016020 042704 176277 BIC #1C<HIADDR!OFL>,R4
1975 016024 052704 002200 BIS #SSR!NBA,R4 ; R4 HAS EXPECTED CONTENTS
1976 016030 020400 CMP R4,R0 ; ONLY EXPECTED BITS SET ?
1977 016032 001402 BEQ 54 ; BRANCH IF OKAY
1978 016034 000241 CLC ; CLEAR THE CARRY FOR ERROR
1979 016036 000401 BR 104 ; GO TO EXIT
1980 016040 000261 54: SEC ; SET THE CARRY BIT
1981 016042 000207 104: RTS PC ; RETURN TO CALLER

```



```

1983      .SBTTL  CHKAMB  - CHECK TSSR FOR AMBIGUITY
1984
1985      ;*
1986      ;
1987      ;THIS ROUTINE TESTS THE CONTENTS OF THE TSSR REGISTER
1988      ;FOR AMBIGUITY
1989      ;
1990      ;INPUT:
1991      ;
1992      ;      RO      CONTENTS OF TSSR
1993      ;
1994      ;OUTPUT:
1995      ;
1996      ;      RO      CONTENTS OF TSSR
1997      ;
1998      ;      CARRY   SET - NO AMBIGUITY
1999      ;              CLR - AMBIGUOUS CONTENTS
2000      ;
2001      ;-
2002
2003      CHKAMB:
2004      SAVREG      ;SAVE THE GENERAL REGISTERS
2005      MOV         RO,R4      ;CONTENTS OF TSSR
2006      BIT         #SC,RO     ;IS BIT 15 SET ?
2007      BNE         5$        ;BRANCH IF YES
2008      BIT         #+C<NBA!OFL!SSR!HIADDR>,RO ;ANY OTHER BITS SET ?
2009      BNE         40$        ;MUST BE AN ERROR
2010      BR          45$        ;RETURN WITH SUCCESS
2011      5$: BIT      #SSR,RO    ;IS READY BIT SET ?
2012      BNE         10$        ;BRANCH IF READY BIT IS SET.
2013      BIT         #BIT5,RO   ;IS FATAL ERROR BIT SET ?
2014      BEQ         40$        ;ERROR IF NOT
2015      BIC         #+CTERCLS,R4 ;CLEAR ALL BUT TERMINATION CODE
2016      CMP         R4,#16     ;ALL THREE BITS MUST BE SET
2017      BNE         40$        ;ERROR IF NOT SET
2018      BR          45$        ;OK IF ALL ARE SET
2019      10$: BIT     #BIT5,RO   ;IS FATAL ERROR BIT SET ?
2020      BEQ         45$        ;ERROR IF BIT IS SET WITH SSR
2021      BIT         #BIT2!BIT1,RO ;IS THIS A FUNCTION REJECT
2022      BNE         45$        ;BR. IF TSSR IS OK
2023      40$: CLC              ;AMBIGUOUS CONTENTS
2024      BR          50$
2025      45$: SEC              ;SHOW SUCCESS - NO AMBIGUITY
2026      50$: RTS             ;RETURN TO CALLER

```

```

2028      .SBTTL ENAIN,DSBINT - ENABLE/DISABLE INTERRUPTS
2029      ;
2030      ; DEFAULT DISPLAY INTERRUPT HANDLERS.
2031      ; IF DISPLAY TIME-OUT, REPORT DEV FATAL, AND ABORT PASS.
2032      ; OTHERWISE, SAVE DPU REGISTERS AND DISMISS.
2033      ;
2034      ;
2035      ; BIT DEFINITIONS FOR "INTMASK" AND "INTFLAG" BYTES:
2036      ;
2037      IOKCKIN=BIT7      ; DON'T CHECK FOR BAD INTERRUPTS -- TEST WILL.
2038      IOKSTP=BIT0      ; EXPECT "STOP" INTERRUPT.
2039      ;
2040      ; INTERRUPT MASK -- SAYS EXPECTING INTERRUPTS
2041      INTMASK: .BYTE 0
2042      ; INTERRUPT FLAG -- SAYS WE GOT ONE (IF POSITIVE)
2043      INTFLAG: .BYTE 0
2044      ;
2045      ; SAVED INTERRUPT VECTOR:
2046      INTVEC: .WORD 0
2047      ; SAVE CPU PC
2048      INTCPC: .WORD 0
2049      ;
2050      ; SUBROUTINE TO ENABLE INTERRUPTS:
2051      ENAIN: MOV     RO, -(SP)      ; SAVE RO
2052             MOV     IVEC, RO      ; GET POINTER TO VECTORS
2053             MOV     @INTR, (RO)+  ; SET UP INTERRUPT VECTOR
2054             MOV     @PRI07, (RO)+
2055             MOV     (SP)+, RO      ; RESTORE RO
2056             MOV     (SP), -(SP)
2057             MOV     @0, 2(SP)     ; SET CPU TO LEVEL 0
2058             RTI
2059      ;
2060      ; SUBROUTINE TO DISABLE INTERRUPTS (RAISE PRIORITY TO LEVEL 7)
2061      DSBINT: MOV     (SP), -(SP)
2062             MOV     @PRI07, 2(SP)
2063             RTI
2064      .SBTTL INTR - INTERRUPT HANDLERS
2065      ;
2066      BGNSRV INTR      ; DEFINE INTERRUPT ENTRY
2067      INTR:: MOV     @1, INTRECV   ; SET FLAG TO SHOW INTERRUPT RECEIVED
2068             CLRB    INTFLAG      ; CLEAR FLAG TO SAY WE GOT INTERRUPT
2069             BITB    @IOKSTP, INTMASK ; EXPECTING STOP INTERRUPT?
2070             BNE     1$           ; BR IF YES
2071             BISB    @IOKSTP, INTFLAG ; NO. SET THE ERROR FLAG.
2072      ;
2073      ; SAVE REGISTERS, MSG BUFFER, ETC.
2074      1$:
2075      ENDSRV
2076      L10026:
2077      RTI

```

NE

TSV3 GLOBAL AREAS MACRO M1113 14-JUN 84 15:15
WAITF WAIT FOR SUBSYSTEM READY

SEQ 0078

```

2077 .SBTTL WAITF WAIT FOR SUBSYSTEM READY
2078 ;
2079 ; SUBROUTINE TO WAIT FOR THE SUBSYSTEM READY FLAG
2080 ;
2081 ; INPUTS:
2082 ;
2083 ; R5 ADDRESS OF FIRST DEVICE REGISTER
2084 ;
2085 ; OUTPUTS:
2086 ;
2087 ; R0 CONTENTS OF LAST TSSR READ
2088 ; CARRY SET READY BIT SET
2089 ; CLR TIMEOUT WAITING FOR READY
2090 ;
2091 016250 000401 WAITF:: BR 1$ ;NOP WHEN SUPER FIXED
2092 016252 016252 104422 BREAK ; DO A SUPVSR BREAK FIRST.
2093 016254 012746 011000 TRAP C$BRK
2094 016260 016500 000002 1$ MOV #11000,-(SP) ;25-APRIL-83 REV B 1100 MSEC TIMER
2095 016264 105700 2$ MOV TSSR(R5),R0 ;READ THE TSSR REGISTER
2096 TSTB R0 ;TEST FOR READY BIT SET
2097 016266 100420 BMI 3$ ; EX.T ON STOP FLAG.
2098 016270 DELAY 1 ; WAIT 100 USEC
2099 016270 012727 000001 MOV #1,(PC).
2100 016274 000000 .WORD 0
2101 016276 013727 002116 MOV L$DLY,(PC).
2102 016302 000000 .WORD 0
2103 016304 005367 177772 DEC -6(PC)
2104 016310 001375 BNE . 4
2105 016312 005367 177756 DEC -22(PC)
2106 016316 001367 BNE . 20
2107 016320 005316 DEC (SP) ;REDUCE DELAY COUNT
2108 016322 001356 BNE 2$ ;RETRY UNTIL TIMER EXPIRES
2109 016324 000241 CLC ; C = 0, CONTROLLER STILL RUNNING...
2110 016326 000401 BR 4$ ;...OR HUNG-UP AFTER 300 MSEC.
2111 016330 000261 3$ SEC ; C = 1, CONTROLLER IS STOPPED.
2112 016332 005326 4$ DEC (SP). ;RESTORE STACK WITHOUT CHANGING CARRY BIT
2113 016334 000207 RTS PC

```

TSV3 - GLOBAL AREAS MACRO M1113 14 JAN-84 15:15
 CHKTSSR CHECK TSSR FOR READY

SEQ 0079

```

2107 .SBTTL CHKTSSR - CHECK TSSR FOR READY
2108 ;
2109 ; THIS ROUTINE WAITS FOR READY IN THE TSSR
2110 ; AND TESTS FOR AMBIGUOUS BIT SETTINGS IN TSSR.
2111 ;
2112 ; INPUT:
2113 ; R5 ADDRESS OF CSA REGISTERS
2114 ;
2115 ; OUTPUT:
2116 ; R0 CONTENTS OF TSSR
2117 ; CARRY SET - OKAY
2118 ; CLR - NOT READY AMBIGUOUS, OR SC SET
2119 ;
2120 CHKTSSR:
2121 JSR PC, WAITF ; WAIT FOR READY
2122 BCC 201 ; BRANCH IF TIME OUT
2123 JSR PC, CHKAMB ; TSSR AMBIGUOUS?
2124 BCC 101 ; BR IF YES
2125 BIT #SC, R0 ; SPECIAL CONDITION SET?
2126 BEQ 151 ; BR IF NO
2127 BIT #<SCE!BIE!RMR!NXM>, R0 ; ANY ERROR BITS SET?
2128 BEQ 151 ; BR IF NO
2129 101: CLC ; SET FAILURE
2130 BR 201 ;
2131 151: SEC ; SET SUCCESS
2132 201: RTS PC ; RETURN TO CALLER
2133 .SBTTL NXNM - CHECK FOR NONEXISTENT MEMORY
2134 ;
2135 ; ROUTINE TO TEST FOR A NEXM IN THE RANGE (R1) THRU (R2).
2136 ; ON RETURN, IF "C" = 1, (R1) = NEXM ADDRESS.
2137 ; "C" = 0, ALL ADDRESSES OK.
2138 ;
2139 ; CALL: MOV ADR1, R1
2140 ; MOV ADR2, R2
2141 ; JSR PC, NXNM
2142 ; RETURN ; TEST "C" AND PROCEED.
2143 NXNM: MOV #21, B04 ; SET BUSERR VECTOR.
2144 MOV #PRI04, B06 ;
2145 CLR R3 ; FLAG.
2146 11: TST (R1) ; TEST THE ADDRESS(ES).
2147 ; IF ANY TRAP, CONTINUE AT 21.
2148 CMP R1, R2 ; OTHERWISE, CONTINUE HERE.
2149 BEQ 31 ; BR IF FINISHED (NO NEXM'S).
2150 ADD #2, R1 ; SET NEXT ADDRESS...
2151 BR 11 ; ...AND CONTINUE.
2152 21: COM R3 ; GOT ONE, SET FLAG...
2153 MOV #31, (SP) ;
2154 RTI ; ...AND DISMISS INTERRUPT...
2155 31: CLRVEC #4 ; ...AND GIVE BACK THE VECTOR.
2156 MOV #4, R0 ;
2157 TRAP C1CVEC ;
2158 TST R3 ; DID WE CATCH ONE ??
2159 BEQ .+4 ; NO, "C" = 0, SKIP NEXT.
2158 SEC ; YES, "C" = 1, (R1) = NEXM ADDR.
2159 RTS PC ;

```

2161
2162
2163
2164
2165
2166
2167
2168
2169 016456
2170 016456 005737 002166
2171 016462 001006
2172 016464 005737 002202
2173 016470 100403
2174 016472 005337 002214
2175 016476 001002
2176 016500 000241
2177 016502 000401
2178 016504 000261
2179 016506 000207
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2200
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2202
2203
2204
2205
2206
2207 016510
2208 016510 010046
2209 016512 005037 003152
2210 016516 005037 016756
2211 016522 005037 005770
2212 016526 105037 016144
2213 016532 013700 002200
2214 016536 006300
2215 016540 005737 003112
2216 016544 001430
2217 016546 100010

```
.SBTTL TSTLOOP - CHECK ITERATION COUNT
;
; SUBROUTINE TO EXECUTE TEST ITERATIONS.
; EXIT WITH "C" SET IF LOOPS ALLOWED AND LOOP COUNT NON ZERO.
; LOOP COUNTER IS SET BY "BEGIN.TEST" MACRO.
;
; CALL: LOOPTO ARG
;
TSTLOOP::
    TST     NOITS          ; ITERATIONS INHIBITED?
    BNE     1$             ; YES.
    TST     QVP            ; NO.
    BMI     1$             ; LOOPS DISALLOWED IN QUICK PASS.
    DEC     LOOPCNT        ; BUMP LOOP COUNTER.
    BNE     2$
1$: CLC
    BR      3$
2$: SEC
    RTS     PC             ; LOOP ENABLED.
3$:

.SBTTL TSTSETUP - PRINT TEST NAME AND INIT ERROR COUNTS
;
; PRINT THE NUMBER AND NAME OF EACH TEST AS WE GO ALONG.
; INCREMENT "TESTK" TO INDICATE THE NUMBER OF TESTS
; IN THE CURRENT RUN SEQUENCE.
; CLEAR THE ERROR COUNTER AND SIGNATURE EXTENSION FLAGS.
;
; INPUT:
;
;     R0      POINTER TO TEST ID ASCIZ STRING
;
; OUTPUT:
;
;     R5      ADDRESS OF FIRST DEVICE REGISTER
;
; IMPLICIT OUTPUTS:
;
;     TSTCNT  UPDATED TO COUNT TESTS PERFORMED SINCE START OR RESTART
;
; SIDE EFFECTS:
;
;     INTERRUPT LEVEL IS RASIED TO LEVEL OF
;     THE DEVICE UNDER TEST
;
; -
TSTSETUP::
    MOV     R0, -(SP)      ; SAVE THE TEST ID MESSAGE
    CLR     SIFLAG         ; CLEAR "SOFT INIT" FLAG
    CLR     ERRK           ; CLEAR LOCAL ERROR COUNTER.
    CLR     EXTA           ; CLEAR ERROR EXTENSION FLAG.
    CLR     INTMASK        ; CLEAR INTERRUPT MASK (CHECK ERROR)
    MOV     UNITN, R0      ; GET THE UNIT NUMBER.
    ASL     R0              ; ... AND MAKE IT A WORD OFFSET.
    TST     NODDEV         ; DID STARTUP FIND THE DEVICE?
    BEQ     4$             ; BR IF YES
    BPL     3$             ; BR IF NOT IDLE
```

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TSTSETUP - PRINT TEST NAME AND INIT ERROR COUNTS

SEQ 0081

```

2218 016550 052760 160000 003174      BIS      #160000,ERTABL(R0) ; FLAG ERROR IN THE ERROR TABLE
2219 016556      ERROF      1,NXR,NXRERR ; NO DEVICE HERE      PRINT IT
      016556      104455      TRAP      CIEROF
      016560      000001      .WORD      1
      016562      003736      .WORD      NXR
      016564      005734      .WORD      NXRERR
2220 016566      000407      BR          2$
2221 016570      052760 160001 003174 3$: BIS      #160001,ERTABL(R0) ; FLAG ERROR IN THE ERROR TABLE
2222 016576      ERROF      2,NOINIT ; DEVICE NOT IDLE
      016576      104455      TRAP      CIEROF
      016600      000002      .WORD      2
      016602      004333      .WORD      NOINIT
      016604      000000      .WORD      0
2223 016606      012737 177777 003110 2$: MOV      #-1,DUFLG ; DROP THE UNIT
2224 016614      DODU      UNITN
      016614      013700 002200      MOV      UNITN,R0
      016620      104451      TRAP      CIDODU
2225 016622      DOCLN      ; ABORT THE PASS
      016622      104444      TRAP      CIDCLN
2226 016624      000423      BR          5$
2227
2228 016626      4$:      RFLAGS      R0 ; GET THE OPERATOR FLAGS.
      016626      104421      TRAP      CIRFLA
2229 016630      032700 001000      BIT      #PNT,R0 ; PRINT THE TEST NUMBERS?
2230 016634      001412      BEQ      1$ ; BR IF NO
2231 016636      011600      MOV      (SP),R0 ; GET THE ID MESSAGE
2232 016640      PRINTF      #TNAM,R0 ; DISPLAY THE TEST ID
      016640      010046      MOV      R0,-(SP)
      016642      012746 016704      MOV      #TNAM,-(SP)
      016646      012746 000002      MOV      #2,-(SP)
      016652      010600      MOV      SP,R0
      016654      104417      TRAP      CIPNTF
      016656      062706 000006      ADD      #6,SP
2233 016662      005237 002212      1$: INC      TSTCNT ; BUMP TEST COUNTER.
2234 016666      SETPRI      IPRI ; PRIORITY THAT OF DEVICE
      016666      013700 002210      MOV      IPRI,R0
      016672      104441      TRAP      CISPRI
2235 016674      005726      5$: TST      (SP) ; FIX UP THE STACK
2236 016676      013705 002204      MOV      CSRADDR,R5 ; ADDRESS OF TSV REGISTERS ON UNIBUS
2237 016702      000207      RTS      PC
2238 016704      045      123      045 TNAM: .ASCIZ 'SSTA Test'
2239      .EVEN
2240      .SBTTL TSTEND - PRINT ERRORS RECEIVED
2241
2242 ; AT END OF EACH TEST, PRINT THE NUMBER OF ERRORS RECEIVED
2243 ; IF NORMAL ERROR REPORTING IS DISABLED (FLA:IER).
2244
2245 TSTEND: RFLAGS      R0
      016720      104421      TRAP      CIRFLA
2246 016722      030027 020000      BIT      R0,#IER
2247 016726      001412      BEQ      1$ ; BR IF "IER" NOT SET.
2248 016730      PRINTF      #ESUM,ERRK ; PRINT ERROR COUNT.
      016730      013746 016756      MOV      ERRK,-(SP)
      016734      012746 016760      MOV      #ESUM,-(SP)
      016740      012746 000002      MOV      #2,-(SP)
      016744      010600      MOV      SP,R0
      016746      104417      TRAP      CIPNTF

```


E7

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TSTEND - PRINT ERRORS RECEIVED

SEQ 0002

2249	016750	062706	000006			ADD	#6,SP	
2250	016754	000207			18:	RTS	PC	
2251	016756	000000			ERRK:	0		; LOCAL ERROR COUNT.
2252	016760	045	101	040	ESUM:	.ASCIZ	/#A #D#A ERRORS/	
2253	016777	105	122	122	EMAXDU:	.ASCIZ	/ERROR LIMIT REACHED - DROPPING UNIT/	
2254						.EVEN		

```

2256                                     .SBTTL INCERK - INCREMENT LOCAL ERROR COUNT
2257
2258 ; ROUTINES TO INCREMENT LOCAL ERROR COUNT AND CHECK FOR LIMIT:
2259 ;
2260 017044 005237 016756 INCERK: INC ERRK ; INCREMENT LOCAL ERROR COUNT
2261 017050 010046      MOV RO,-(SP) ; SAVE RO
2262 017052 013700      MOV UNITN,RO ; GET UNIT NUMBER,
2263 017056 006300      ASL RO ; ... AND MAKE IT A WORD OFFSET.
2264 017060 062700      ADD #ERTABL,RO ; RO GETS ADDRESS OF ERROR TABLE ENTRY.
2265 017064 005210      INC (RO) ; INCREMENT THE DEVICE ERROR COUNT
2266 017066 032710      BIT #7777,(RO) ; DID WE OVERFLOW THE FIELD?
2267 017072 001001      BNE 1$ ; BR IF NO.
2268 017074 005310      DEC (RO) ; YES -- BACK IT UP TO 7777.
2269 017076 012600 1$: MOV (SP)+,RO ; RESTORE RO
2270 017100 000207      RTS PC ; RETURN TO CALLER.
2271
2272 017102 010046 CKEMAX: MOV RO,-(SP) ; SAVE RO
2273 017104 013700      MOV UNITN,RO ; GET UNIT NUMBER
2274 017110 006300      ASL RO ; ... AND MAKE IT A WORD OFFSET
2275 017112 016000      MOV ERTABL(RO),RO ; GET ERROR TABLE ENTRY
2276 017116 042700      BIC #170000,RO ; EXTRACT ERROR COUNT FIELD
2277 017122 020037      CMP RO,GERRMAX ; IS GLOBAL LIMIT EXCEEDED FOR THIS UNIT?
2278 017126 103004      BHIS 1$ ; BR IF YES
2279 017130 023737 016756 002170 CMP ERRK,LERRMAX ; IS LOCAL LIMIT EXCEEDED FOR THIS TEST?
2280 017136 103417      BLO 2$ ; BR IF NO
2281 017140      RFLAGS RO ; GET OPERATOR FLAGS
2282 017142 032700      TRAP C#RFLA ; IS DROPPING INHIBITED?
2283 017146 001013      BIT #IDU,RO ; BR IF YES.
2284 017150 012737 177777 003110 MOV #-1,DUFLG ; NO -- DROP THE UNIT
2285 017156      ERDIF 4,EMAXDU
2286 017166      TRAP C#ERDF
2287 017174      .WORD 4
2288 017176      .WORD EMAXDU
2289 017200      .WORD 0
2290 017202      DODU UNITN
2291 017204      MOV UNITN,RO
2292 017206      TRAP C#DODU
2293 017208      DOCLN
2294 017210      TRAP C#DCLN
2295 017212      MOV (SP)+,RO ; RESTORE RO
2296 017214      RTS PC ; RETURN TO CALLER

```


TSV3 GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
CKDROP - CHECK IF UNIT SHOULD BE DROPPED

SEQ 0084

```

2291 .SBTTL CKDROP - CHECK IF UNIT SHOULD BE DROPPED
2292 ;
2293 ; CHECK IF UNIT SHOULD BE DROPPED
2294 ;
2295 017202 010046 CKDROP: MOV RO, -(SP)
2296 017204 FORCERROR 1$, NOTSSR
2297 017214 RFLAGS RO
2298 017216 104421 TRAP C$RFLA
2299 017222 032700 000040 BIT $IDU, RO
2300 017224 001010 BNE 1$
2301 017226 011600 MOV (SP), RO
2302 017234 012737 177777 003110 MOV #-1, DUFLG
2303 017234 013700 002200 DODU UNITN
2304 017242 104451 MOV UNITN, RO
2305 017244 012600 TRAP C$DODU
2306 017246 000207 DOCLN ; ABORT THE PASS
2307 TRAP C$DOCLN
2308 1$: MOV (SP)+, RO
2309 RTS PC
2310
2311 .SBTTL CONFIG - DETERMINE CONFIGURATION OF SYSTEM
2312 ;
2313 ; SUBROUTINE - DETERMINE CONFIGURATION OF TSVOS SYSTEM.
2314 ;
2315 CONFIG: JSR PC, SOFINIT
2316 RTS PC
2317 .SBTTL KTON, KTOFF - ENABLE/DISABLE MEMORY MANAGEMENT
2318 ;
2319 ; SUBROUTINE - ENABLE MEM MGT.
2320 ;
2321 KTON: TST KTFLG ; GOT KT?
2322 BEQ 1$ ; NO.
2323 MOV $1, SRO ; YES. ENABLE KT11.
2324 1$: RTS PC
2325 ;
2326 ; SUBROUTINE - DISABLE MEM MGT.
2327 ;
2328 KTOFF: TST KTFLG ; GOT KT11?
2329 BEQ 1$ ; NO.
2330 NOP
2331 NOP
2332 MOV $0, SRO ; DISABLE KT.
2333 1$: RTS PC

```

```

2334 .SBTTL SETMAP    SETUP PAR6 MAPPING
2335
2336 ;*
2337 ;
2338 ; THIS ROUTINE SETS UP KERNEL PAR6 TP HANDLE
2339 ; AN 18 BIT ADDRESS. THE OFFSET INTO THE PAGE
2340 ; IS RETURNED BIASED TO PAR6.
2341 ;
2342 ; INPUTS:
2343 ;
2344 ;        R0        HIGH ORDER ADDRESS BITS
2345 ;        R1        LOW ORDER ADDRESS BITS
2346 ;
2347 ; OUTPUTS:
2348 ;
2349 ;        R0        OFFSET INTO BLOCK WITH PAR6 BIAS (I.E. THE ADDRESS)
2350 ;        CARRY     SET IF SUCCESS
2351 ;                   CLR IF ERROR
2352 ;
2353 ; SETMAP:
2354        SAVREG                                ;SAVE R1-R4 UNTIL NEXT RETURN
2355        TST        KTFLG                     ;SYSTEM HAVE ABOVE 28K?
2356        BEQ        10$                       ;BR IF NO
2357        MOV        R1,R2                     ;SAVE LOW ORDER BITS
2358        .REPT       6
2359        ASR        R0                        ;CONVERT WORD ADDRESS TO 32W BLOCKS
2360        ROR        R1                       ;MAKE IT DOUBLE PRECISION
2361        .ENDR
2362        BIC        #177,R1                   ;ALINE FOR LOWER 4K BOUNDARY
2363        CMP        R1,KTFLG                  ;HIGHER THAN EXISTING MEMORY?
2364        BHIS       10$                       ;BR IF YES
2365        MOV        R1,#KIPAR6               ;SETUP MAPPING REGISTER PAR6
2366        BIC        #160000,R2               ;SETUP DISPLACEMENT IN PAGE
2367        ADD        #140000,R2               ;ADD IN PAR6 BIAS
2368        MOV        R2,R0                     ;RETURN IN R0
2369        SEC                                  ;SET SUCCESS
2370        BR        15$                       ;
2371        CLC                                  ;SET FAILURE
2372        RTS        PC                       ;RETURN
2373        .SBTTL    FILLMEM - FILL MEMORY WITH BACKGROUND PATTERN
2374 ;*
2375 ; FILL MEMORY WITH A BACKGROUND PATTERN
2376 ;
2377 ; INPUTS:
2378 ;
2379 ;        R0 = BACKGROUND PATTERN
2380 ;        FREE    = FIRST LOCATION AVAILABLE TO DIAGNOSTIC
2381 ;        KTFLG   = SET TO HIGHEST MEMORY LOCATION IF > 28K.
2382 ;
2383 ; OUTPUTS:
2384 ;
2385 ;        NONE
2386 ;
2387 ;
2388 ; FILLMEM:
2389        SAVREG                               ;SAVE R1-R5 UNTIL NEXT RETURN
2390        JSR        PC,KTOFF                  ;DISABLE KT.

```

2391	017432	010003		MOV	R0,R3	;COPY TEST PATTERN
2392	017434	013701	003122	MOV	FREE,R1	;GET FIRST FREE LOCATION
2393	017440	013702	003124	MOV	FRESIZ,R2	;SIZE OF FREE SPACE BELOW 28K.
2394	017444	010321		104: MOV	R3,(R1)+	;STORE A BACKGROUND WORD
2395	017446	005302		DEC	R2	;DONE ALL MEMORY IN FREE SPACE?
2396	017450	003375		BGT	104	;BR IF NO
2397	017452	005737	003130	TST	KTFLG	; GOT KT?
2398	017456	001477		BEQ	554	; NO. GET OUT.
2399	017460	004737	017256	JSR	PC,KTON	; YES. ENABLE KT.
2400	017464	005000		CLR	R0	;HIGH ORDER ADDRESS START
2401	017466	013701	003150	MOV	PST32W,R1	;GET >28K START ADDRESS (IN 32W BLOCKS)
2402		000006		.REPT	6	
2403				CLC		;CLEAR C BIT
2404				ROL	R1	;CONVERT BLOCKS TO WORDS
2405				ROL	R0	;MAKE IT DOUBLE PRECISION
2406				.ENDR		
2407	017536	004737	017316	JSR	PC,SETHAP	;SETUP PAR6 MAPPING REGISTER
2408	017542	010320		304: MOV	R3,(R0)+	;STORE TEST PATTERN IN >28K ADDRESS
2409	017544	020027	160000	CMP	R0,#160000	;END OF PAR6 MAPPING AREA?
2410	017550	103774		BLO	304	;BR IF NO
2411	017552	162700	020000	SUB	#20000,R0	;BACKUP INTO PAR6 MAPPING BEGIN
2412	017556	062737	000200	172354	ADD	#200,#KIPAR6
2413	017564	023737	172354	003130	CMP	#KIPAR6,KTFLG
2414	017572	001427		BEQ	504	;END OF MEMORY?
2415	017574	005737	003142	TST	T23A	;BR IF YES
2416	017600	001407		BEQ	354	;11/23A?
2417	017602	013704	177572	MOV	SRO,R4	;NO KEEP GOING
2418	017606	042704	177761	BIC	#177761,R4	;GET SRO CONTENTS
2419	017612	022704	000016	CMP	#16,R4	;CLEAR ALL BUT PAGE NUMBER
2420	017616	001415		BEQ	504	;SEE IF PAGE 7
2421	017620	005737	003144	354: TST	T23B	;EXIT IF THERE
2422	017624	001410		BEQ	454	;11/23B?
2423	017626	023727	172354	007600	CMP	#KIPAR6,#7600
2424	017634	103001		BHIS	404	;NO KEEP GOING
2425	017636	000403		BR	454	;YES
2426	017640	012737	000020	172516	404: MOV	#20,SR3
2427	017646	000137	017542	454: JMP	304	;SET 22 BIT RELOCATION
2428	017652	004737	017274	504: JSR	PC,KTOFF	;KEEP GOING ON ETC.
2429	017656	000207		554: RTS	PC	; DISABLE KT.

TSV3 GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
CMPMEM - COMPARE MEMORY TO BACKGROUND PATTERN

SEQ 0087

```

2431          .SBTTL CMPMEM    COMPARE MEMORY TO BACKGROUND PATTERN
2432          ;
2433          ; COMPARE MEMORY WITH A BACKGROUND PATTERN
2434          ;
2435          ; INPUTS:
2436          ;
2437          ;     RO = BACKGROUND PATTERN
2438          ;     FREE  = FIRST LOCATION AVAILABLE TO DIAGNOSTIC
2439          ;     KTFLG  = SET TO HIGHEST MEMORY LOCATION IF > 28K.
2440          ;
2441          ; OUTPUTS:
2442          ;
2443          ;     CARRY  - SET IF NO ERROR
2444          ;     CARRY  - CLR IF ERROR
2445          ;
2446          ; IMPLICIT OUTPUTS:
2447          ;
2448          ;     ERRHI  - ERROR HIGH ADDRESS
2449          ;     ERRLO  - ERROR LOW ADDRESS
2450          ;     EXPD   - EXPECTED DATA
2451          ;     RECV   - RECEIVED DATA
2452          ;
2453          ; CMPMEM:
2454          ;
2455          ;     SAVREG
2456          ;     MOV     RO,R3          ;SAVE R1-R5 UNTIL NEXT RETURN
2457          ;     JSR     PC,KTOFF      ;COPY TEST PATTERN
2458          ;     MOV     FREE,R1       ;DISABLE KT.
2459          ;     MOV     FRESIZ,R2     ;GET FIRST FREE LOCATION
2460          ;     CMP     R3,(R1)       ;SIZE OF FREE SPACE BELOW 28K.
2461          ;     BEQ     15$          ;FREE SPACE LOCATION EQUAL TO EXPD?
2462          ;     MOV     R1,ERRLO      ;BR IF YES
2463          ;     CLR     ERRHI         ;SAVE ADDRESS IN ERROR
2464          ;     MOV     R3,EXPD       ;NO HIGH ADDRESS
2465          ;     MOV     (R1),RECV     ;SAVE EXPD FOR ERROR REPORT
2466          ;     BR      50$          ;SAVE RECV FOR ERPOP REPORT
2467          ;
2468          ;     15$:  TST     (R1)+   ;
2469          ;     DEC     R2            ;POINT TO NEXT ADDRESS
2470          ;     BGT     10$          ;DONE ALL MEMORY IN FREE SPACE?
2471          ;     TST     KTFLG        ;BR IF NO
2472          ;     BEQ     55$          ; GOT KT?
2473          ;     JSR     PC,KTON       ; NO. GET OUT.
2474          ;     CLR     RO           ; YES. ENABLE KT.
2475          ;     MOV     PST32W,R1     ;HIGH ORDER ADDRESS START
2476          ;     .REPT    6           ;GET >28K START ADDRESS (IN 32W BLOCKS)
2477          ;     ROL     R1            ;
2478          ;     ROL     RO           ;CONVERT BLOCKS TO WORDS
2479          ;     .ENDR              ;MAKE IT DOUBLE PRECISION
2480          ;
2481          ;     BIC     #177,R1      ;ALINE 4K BOUNDARY
2482          ;     MOV     RO,-(SP)      ;SAVE HIGH ORDER
2483          ;     MOV     R1,-(SP)      ;SAVE LOW ORDER
2484          ;     JSR     PC,SETMAP     ;SETUP PAR6 MAPPING REGISTER
2485          ;     MOV     RO,R4        ;COPY ADDRESS BIASED TO PAR6
2486          ;     MOV     (SP)+,R1     ;RESTORE LOW ORDER IN NON PAR6 FORMAT
2487          ;     MOV     (SP)+,RO     ;RESTORE HIGH ORDER IN NON PAR6 FORMAT
2488          ;     CMP     R3,(R4)      ;ABOVE 28K LOCATION EQUAL EXPD?
2489          ;     BEQ     32$          ;BR IF YES
2490          ;     MOV     RO,ERRHI      ;SAVE HIGH ORDER IN ERROR

```

```

2488 020040 010137 002236      MOV    R1,ERRLO      ;SAVE LOW ORDER IN ERROR
2489 020044 010337 002230      MOV    R3,EXPD      ;SAVE EXPD FOR ERROR REPORT
2490 020050 011437 002232      MOV    (R4),RECV    ;SAVE RECV FOR ERROR REPORT
2491 020054 000421              BR      50$              ;
2492 020056 062701 002002      32$:  ADD    #2,R1      ;UPDATE NON PAR6 ADDRESS
2493 020062 005500              ADC     R0              ;MAKE IT DOUBLE PRECISION ADD
2494 020064 062704 000002      ADD    #2,R4      ;UPDATE PAR FORMAT ADDRESS
2495 020070 020427 160000      CMP     R4,#160000    ;END OF PAR6 MAPPING AREA?
2496 020074 103755              BLO     30$              ;BR IF NO
2497 020076 162704 020000      SUB     #20000,R4    ;BACKUP INTO PAR6 MAPPING BEGIN
2498 020102 062737 000200      ADD     #200,#KIPAR6 ;POINT TO NEXT 4K BLOCK >28K.
2499 020110 023737 172354      CMP     #KIPAR6,KTFLG ;END OF MEMORY?
2500 020116 101744              BLOS    30$              ;BR IF NO
2501 020120 004737 017274      50$:  JSR     PC,KTOFF    ;TURN OFF MEMORY MAPPING
2502 020124 000241              CLC              ;SET FAILURE
2503 020126 000403              BR      60$              ;
2504 020130 004737 017274      55$:  JSR     PC,KTOFF    ;TURN OFF MEMORY MAPPING
2505 020134 000261              SEC              ;SET SUCCESS
2506 020136 000207      60$:  RTS      PC
2507                          .SBTTL  REGSAV  - SAVE R1-R5 ON STACK
2508                          ;*
2509                          ;
2510                          ;ROUTINE TO
2511                          ;SAVE R1 THROUGH R5 ON THE STACK
2512                          ;
2513                          ;CALLING SEQUENCE:
2514                          ;
2515                          ;      JSR      R5,REGSAV
2516                          ;
2517                          ;THIS IS A COOROUTINE WHICH TRANSFER CONTROL BACK TO
2518                          ;THE CALLING ROUTINE. AT THE END OF THE CALLING ROUTINE,
2519                          ;THE RTS PC RETURNS CONTROL TO THIS ROUTINE TO RESTORE
2520                          ;REGISTERS.
2521                          ;
2522                          ;THIS ROUTINE SHOULD ONLY BE CALLED FROM ROUTINES WHICH ARE
2523                          ;CALLED VIA A JSR PC INSTRUCTION
2524                          ;
2525                          ;-
2526
2527 020140      REGSAV:
2528 020140 010446      MOV     R4,-(SP)
2529 020142 010346      MOV     R3,-(SP)
2530 020144 010246      MOV     R2,-(SP)
2531 020146 010146      MOV     R1,-(SP)
2532 020150 010546      MOV     R5,-(SP)
2533 020152 016605 000012      MOV     10.(SP),R5
2534 020156 004736      JSR     PC,#(SP)+
2535 020160 012601      MOV     (SP)+,R1
2536 020162 012602      MOV     (SP)+,R2
2537 020164 012603      MOV     (SP)+,R3
2538 020166 012604      MOV     (SP)+,R4
2539 020170 012605      MOV     (SP)+,R5
2540 020172 000207      RTS      PC

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TSV3 GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
 GETPAT - GET 8 BIT PATTERN FROM OPERATOR

SEQ 0089

```

2542 .SBTTL GETPAT - GET 8 BIT PATTERN FROM OPERATOR
2543 ;
2544 ;ROUTINE TO REQUEST AN 8 BIT DATA PATTERN FROM THE OPERATOR
2545 ;
2546 ;INPUTS:      NONE.
2547 ;
2548 ;OUTPUTS:
2549 ;      RO      OCTAL NUMBER FROM THE OPERATOR
2550 ;
2551 ;CALLING SEQUENCE:
2552 ;      JSR      PC,GETPAT
2553 ;
2554 GETPAT::
2555     SAVREG      ;SAVE THE GENERAL REGISTERS
2556     1$: GMANID DATASC,PATDAT,0,377,0,377,NO
           TRAP    C$GMAN
           BR      10000$
           .WORD   PATDAT
           .WORD   T$CODE
           .WORD   DATASC
           .WORD   377
           .WORD   T$LOLIM
           .WORD   T$HILIM
2557     10000$: BNCOMplete      1$      ;RETRY IF ERROR
           BCC     1$
2558     MOV      PATDAT,RO      ;DATA PATTERN FROM OPERATOR
2559     RTS       PC            ;RETURN TO CALLER
2560
2561 ;
2562 ;LOCAL DATA AREA
2563 ;
2564
2565     PATDAT: .WORD 0            ;TEMPORARY STORAGE FOR DATA
2566     DATASC: .ASCIZ 'ENTER DATA PATTERN'
2567     .EVEN

```


TSV3 - GLOBAL AREAS MACRO M1.13 14-JUN-84 15:15
GETSEL ISSUE MENU AND GET OPERATOR RESPONSE

SEQ 0090

```

2569 .SBTTL GETSEL - ISSUE MENU AND GET OPERATOR RESPONSE
2570 ;
2571 ;ROUTINE TO ISSUE A MENU AND GET THE OPERATOR'S RESPONSE.
2572 ;
2573 ;INPUTS:
2574 ;      R0      ADDRESS OF ASCIZ STRING OF MENU
2575 ;      R1      MAXIMUM ALLOWABLE OPERATOR RESPONSE
2576 ;
2577 ;OUTPUTS:
2578 ;      R0      NUMBER OF THE OPERATOR'S SELECTION
2579 ;
2580 GETSEL::
2581     SAVREG                ;SAVE GENERAL REGISTERS
2582     MOV      R0,R2        ;SAVE THE MENU ADDRESS
2583     MOV      R2,R3        ;START OF MENU STRING
2584     TST      (R3)         ;END OF ASCII ?
2585     BEQ      3$          ;BRANCH IF ALL LINES DISPLAYED
2586     PRINTF   @SELASC,(R3) ;DISPLAY THE MENU
2587     MOV      (R3),-(SP)
2588     MOV      @SELASC,-(SP)
2589     MOV      @2,-(SP)
2590     MOV      SP,R0
2591     TRAP     C$PNTF
2592     ADD      @6,SP
2593     BR       2$
2594     GMANID   MENASC,MENRES,D,-1,0,-1,NO
2595     TRAP     C$GMAN
2596     BR       10001$
2597     .WORD    MENRES
2598     .WORD    T$CODE
2599     .WORD    MENASC
2600     .WORD    -1
2601     .WORD    T$LOLIM
2602     .WORD    T$HILIM
2603     10001$: BNCOMPLETE    1$      ;RETRY IF ERROR
2604     BCC      1$
2605     MOV      MENRES,R0      ;GET THE OPERATOR'S REPLY
2606     CMP      R0,R1         ;COMPARE TO MAXIMUM ALLOWED
2607     BLOS     5$            ;BRANCH IF OK
2608     PRINTF   @MENERR       ;DISPLAY ERROR MESSAGE
2609     MOV      @MENERR,-(SP)
2610     MOV      @1,-(SP)
2611     MOV      SP,R0
2612     TRAP     C$PNTF
2613     ADD      @4,SP
2614     BR       1$            ;RETRY
2615     RTS      PC            ;RETURN TO CALLER
2616     5$:      MENERR: .ASCIZ  'MNSA *** Menu Selection Too Large ***'
2617     SELASC:  .ASCIZ  'MNST'
2618     MENASC:  .ASCIZ  'Enter Menu Selection:'
2619     .EVEN
2620     MENRES:  .WORD    0

```

TSV3 GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
 CHKMAN CHECK MANUAL INTERVENTION LEGALITY

SEQ 0091

```

2602          .SBTTL  CHKMAN  - CHECK MANUAL INTERVENTION LEGALITY
2603          ;*
2604          ;
2605          ;ROUTINE TO TEST FOR MANUAL INTERVENTION LEGALITY.
2606          ;
2607          ;INPUT:
2608          ;
2609          ;      NONE.
2610          ;
2611          ;OUTPUT:
2612          ;
2613          ;      CARRY  0      MANUAL INTERVENTION NOT ALLOWED
2614          ;      CARRY  1      MANUAL INTERVENTION IS OK
2615          ;
2616          ;SIDE EFFECTS:
2617          ;
2618          ;      A MESSAGE IS DISPLAYED WARNING THAT TEST IS
2619          ;      NOT EXECUTED IF MANUAL INTERVENTION IS NOT
2620          ;      ALLOWED.
2621          ;
2622          ;
2623          ;
2624          CHKMAN::
2625          SAVREG          ;SAVE THE REGISTERS
2626          MANUAL          ;SEE IF MANUAL INTERVENTION OK
2627          TRAP  C$MANI
2628          BCOMPLETE 1$    ;BRANCH IF ALLOWED
2629          BCS  1$
2630          PRINTF  #NOMAN    ;PRINT THE WARNING MESSAGE
2631          MOV  #NOMAN, -(SP)
2632          MOV  #1, -(SP)
2633          MOV  SP, R0
2634          TRAP  C$PNTF
2635          ADD  #4, SP
2636          CLC          ;CLEAR CARRY FOR ERROR
2637          RTS  PC      ;RETURN
2638          1$:
2639          NOMAN: .ASCIZ  'N/A *** Manual Intervention not Allowed  Test Aborted ***'
2640          .even

```


TSV3 GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
 ENVIRN SETUP FREE DIAGNOSTIC SPACE

SEQ 0092

```

2635 .SBTTL ENVIRN SETUP FREE DIAGNOSTIC SPACE
2636 ;
2637 ; SUBROUTINE TO SET-UP VARIOUS ENVIRONMENTAL PARAMETERS.
2638 ;
2639 ENVIRN: MEMORY R0
2640 020630 104431 TRAP C0MEM
2641 020632 010037 003122 MOV R0,FREE ; GET 1ST FREE ADDRESS...
2642 020636 062737 000002 003122 ADD #2,FREE
2643 020644 011037 003124 (R0),FRESIZ ; ...AND WORD COUNT.
2644 020650 162737 000004 003124 SUB #4,FRESIZ
2645 020656 013702 002012 MOV L$UNIT,R2 ; GET NUMBER OF UNITS
2646 020662 162737 000007 003124 101: SUB #7,FRESIZ ; TAKE AWAY 7 WORDS PER UNIT
2647 020670 005302 DEC R2
2648 020672 001373 BNE 101
2649 020674 013700 003122 MOV FREE,R0 ; GET FIRST FREE ADDRESS
2650 020700 063700 003124 ADD FRESIZ,R0 ; POINT TO LAST FREE ADDRESS
2651 020704 162700 000002 SUB #2,R0 ; BACKUP 1 WORD
2652 020710 010037 003126 MOV R0,FREEM ; STORE LAST FREE ADDRESS
2653 020714 000240 NOP ; *****
2654 020716 012701 177520 MOV #BDVPCR,R1 ; GET BDV11 PCR ADDRESS
2655 020722 010102 MOV R1,R2 ; COPY TO R2
2656 020724 062702 000002 ADD #2,R2 ; SET THE RANGE
2657 020730 004737 016376 JSR PC,XNOM ; SEE IF WE HAVE ONE
2658 020734 103001 BCC 151 ; OK TO SET FLAGS
2659 020736 000423 BR 401 ; RETURN WITH FLAGS CLEAR
2660 020740 013701 177520 151: MOV BDVPCR,R1 ; SAVE PCR CONTENTS
2661 020744 062701 000001 ADD #1,R1 ; ADD ONE TO IT
2662 020750 012702 177520 MOV #BDVPCR,R2 ; GET BDV11 PCR ADDRESS
2663 020754 005212 INC (R2) ; TRY TO WRITE TO IT
2664 020756 013703 177520 MOV BDVPCR,R3 ; GET RESULTS
2665 020762 020103 CMP R1,R3 ; DID IT CHANGE?
2666 020764 001006 BNE 201 ; NO, MUST BE 11/238
2667 020766 005237 003142 INC T23A ; SET THE FLAG
2668 020772 C42737 170000 002120 BIC #170000,L$HIME ; SUPERVISOR COULD BE WRONG
2669 ; NOP ; BR 401 FOR RELEASE
2670 021000 000402 PRINTF #M8186 ; TELL THE SYSTEM TYPE
2671 021002 005237 003144 201: BR 401 ; RETURN
2672 ; NOP ; SET THE FLAG
2673 ; PRINTF #M8189 ; BR 401 FOR RELEASE
2674 021006 401: ; TELL THE SYSTEM TYPE
2675 021006 000207 RTS PC ; RETURN

```

TSV3 - GLOBAL AREAS MACRO M1113 14-JUN-84 15:15
KTINIT - SETUP KT11 MEMORY MANAGEMENT REGISTERS

SEQ 0093

```

2677          .SBTTL  KTINIT  SETUP KT11 MEMORY MANAGEMENT REGISTERS
2678          ;*
2679          ;
2680          ;ROUTINE TO INIT KT 11
2681          ;
2682          ;-
2683
2684          KTINIT:
2685 021010      005037  003130      CLR      KTFLG      ; INIT >28K MEMORY FLAG
2686 021014      005037  003132      CLR      KTENABLE   ; INIT TEST >28K FLAG
2687 021020      023727  002120  001577  CMP      L#HIME,#1577 ; GOT ENOUGH MEMORY (>28K)?
2688 021026      101444          BLOS     9#           ; NO.
2689 021030      013700  000004      MOV      @ERRVEC,R0   ; SAVE OLD ERR VEC PTR.
2690 021034      012737  021126  000004      MOV      #2#,@ERRVEC ; SET ERR VEC PTR.
2691 021042      005737  177572      TST      @SRO       ; GOT KT11?
2692 021046      000240          NOP                   ; (TRAP IF NO).
2693 021050      013737  002120  003130      MOV      L#HIME,KTFLG ; YES. SET KT FLAG.
2694 021056      042737  000177  003130      BIC      #177,KTFLG ;
2695 021064      010037  000004      MOV      R0,@ERRVEC   ; RESTORE OLD ERR VEC PTR.
2696 021070      005000          CLR      R0            ; R0 = AR DATA.
2697 021072      012701  172340      MOV      @KIPAR0,R1   ; R1 = KI REGS PTR.
2698 021076      012761  077406  177740  1# : MOV      #77406,-40(R1) ; SET DESCRIPTOR REG.
2699 021104      010021          MOV      R0,(R1)        ; SET KIPAR REG.
2700 021106      062700  000200      ADD      #200,R0     ; BUMP AR DATA BY "4K".
2701 021112      020027  002000      CMP      R0,#2000    ; AT "I/O"?
2702 021116      001367          BNE     1#           ; NO.
2703 021120      012741  177600      MOV      #177600,-(R1) ; YES. SET KIPAR7 FOR I/O.
2704 021124      000405          BR      9#           ;
2705
2706 021126      012716  021134      2# : MOV      #6#,(SP)   ; SET UP RETURN
2707 021132      000002          RTI                   ; RTI TO NEXT LOCATION
2708
2709 021134      010037  000004      6# : MOV      R0,@ERRVEC   ; RESTORE OLD ERR VEC PTR.
2710
2711 021140      000207          9# : RTS      PC

```

```

2713      ;
2714      ; SUBROUTINE TO SET EXTENDED FEATURES SWITCH
2715      ;
2716      ; Requires that SOFINIT and WRTCHR have been done previous to call.
2717      ;
2718      ;
2719      ; INPUTS:
2720      ; R5      CURRENT UNIT NUMBER
2721      ; OUTPUTS:
2722      ; The Extended Features Switch is set.
2723      ;
2724      ;
2725      ;
2726      INVERT::
2727
2728      021142 005737 002224      TST      EXTFEA      ; IS SWITCH SET?
2729      021146 001020      BNE      11      ; YES,EXIT STAGE RIGHT!(or the next one outa town!)
2730      021150 012737 100206 021214      MOV      @100206,CHDPKT      ; WRT SUB-SYS MEM CMD
2731      021156 012737 021224 021216      MOV      @WSMBK,CHDPKT+2      ; MSG BUF ADDR
2732      021164 012737 000006 021222      MOV      @6,CHDPKT+6      ; BYTE COUNT
2733      021172 012737 100010 021224      MOV      @100010,WSMBK      ; INVERT THE SWITCH
2734      021200 012704 021214      MOV      @CHDPKT,R4      ; SET CHDPKT INTO R4
2735      021204 004737 010662      JSR      PC,WRTCHR      ; DO IT
2736      021210 000207      11:      RTS      PC      ; RETURN
2737
2738      ; COMMAND PACKET.
2739
2740      021214      .      *      <..+3>E177774      ;MUST BE ON MOD 4 BOUNDRY.
2741
2742      021214 000000      CHDPKT:: 0      ;1ST WORD IS TS05 COMMAND.
2743      021216 000000      0      ;2ND WORD IS THE BUFFER LOW ADDRESS.
2744      021220 000000      0      ;3RD WORD IS THE BUFFER HIGH ADDRESS.
2745      021222 000000      0      ;4TH WORD IS THE BYTE/RECORD/FILE COUNT.
2746
2747      ; WRITE SUB-SYSTEM MEMORY CHARACTERISTIC BLOCK.
2748
2749      021224 000000      WSMBK:: 0      ;1ST WORD:: SEL 0
2750      021226 000000      0      ;2ND WORD:: SEL 2
2751      021230 000000      0      ;3RD WORD:: SEL 4
2752      .EVEN
2753
2754      ; SUBROUTINE TO CHECK WETHER OR NOT WE'LL TEST NXM
2755      ;
2756      ;
2757      ; INPUTS:
2758      ; OUTPUTS:
2759      ; The NXMFLG is set if we can test.
2760      ; The NXMLO and NXMHI addresses are setup.
2761      ;
2762      ;
2763      021232      MEMCK::
2764
2765      021232      SAVREG      ;SAVE THE REGISTERS
2766      021236 005037 003134      CLR      NXMFLG      ;CLEAR THE FLAG
2767      021242 005037 003136      CLR      NXMLO      ;CLEAR THE TEST ADDRESS LO
2768      021246 005037 003140      CLR      NXMHI      ;CLEAR THE TEST ADDRESS HI
2769      021252 005737 003144      TST      T238      ;IS IT A 11/238?

```

Address	Hex	Dec	Label	Op	Op2	Op3	Op4	Op5	Op6	Op7	Op8	Op9	Op10	Op11	Op12	Op13	Op14	Op15	Op16	Op17	Op18	Op19	Op20	Op21	Op22	Op23	Op24	Op25	Op26	Op27	Op28	Op29	Op30	Op31	Op32	Op33	Op34	Op35	Op36	Op37	Op38	Op39	Op40	Op41	Op42	Op43	Op44	Op45	Op46	Op47	Op48	Op49	Op50	Op51	Op52	Op53	Op54	Op55	Op56	Op57	Op58	Op59	Op60	Op61	Op62	Op63	Op64	Op65	Op66	Op67	Op68	Op69	Op70	Op71	Op72	Op73	Op74	Op75	Op76	Op77	Op78	Op79	Op80	Op81	Op82	Op83	Op84	Op85	Op86	Op87	Op88	Op89	Op90	Op91	Op92	Op93	Op94	Op95	Op96	Op97	Op98	Op99	Op100	Op101	Op102	Op103	Op104	Op105	Op106	Op107	Op108	Op109	Op110	Op111	Op112	Op113	Op114	Op115	Op116	Op117	Op118	Op119	Op120	Op121	Op122	Op123	Op124	Op125	Op126	Op127	Op128	Op129	Op130	Op131	Op132	Op133	Op134	Op135	Op136	Op137	Op138	Op139	Op140	Op141	Op142	Op143	Op144	Op145	Op146	Op147	Op148	Op149	Op150	Op151	Op152	Op153	Op154	Op155	Op156	Op157	Op158	Op159	Op160	Op161	Op162	Op163	Op164	Op165	Op166	Op167	Op168	Op169	Op170	Op171	Op172	Op173	Op174	Op175	Op176	Op177	Op178	Op179	Op180	Op181	Op182	Op183	Op184	Op185	Op186	Op187	Op188	Op189	Op190	Op191	Op192	Op193	Op194	Op195	Op196	Op197	Op198	Op199	Op200	Op201	Op202	Op203	Op204	Op205	Op206	Op207	Op208	Op209	Op210	Op211	Op212	Op213	Op214	Op215	Op216	Op217	Op218	Op219	Op220	Op221	Op222	Op223	Op224	Op225	Op226	Op227	Op228	Op229	Op230	Op231	Op232	Op233	Op234	Op235	Op236	Op237	Op238	Op239	Op240	Op241	Op242	Op243	Op244	Op245	Op246	Op247	Op248	Op249	Op250	Op251	Op252	Op253	Op254	Op255	Op256	Op257	Op258	Op259	Op260	Op261	Op262	Op263	Op264	Op265	Op266	Op267	Op268	Op269	Op270	Op271	Op272	Op273	Op274	Op275	Op276	Op277	Op278	Op279	Op280	Op281	Op282	Op283	Op284	Op285	Op286	Op287	Op288	Op289	Op290	Op291	Op292	Op293	Op294	Op295	Op296	Op297	Op298	Op299	Op300	Op301	Op302	Op303	Op304	Op305	Op306	Op307	Op308	Op309	Op310	Op311	Op312	Op313	Op314	Op315	Op316	Op317	Op318	Op319	Op320	Op321	Op322	Op323	Op324	Op325	Op326	Op327	Op328	Op329	Op330	Op331	Op332	Op333	Op334	Op335	Op336	Op337	Op338	Op339	Op340	Op341	Op342	Op343	Op344	Op345	Op346	Op347	Op348	Op349	Op350	Op351	Op352	Op353	Op354	Op355	Op356	Op357	Op358	Op359	Op360	Op361	Op362	Op363	Op364	Op365	Op366	Op367	Op368	Op369	Op370	Op371	Op372	Op373	Op374	Op375	Op376	Op377	Op378	Op379	Op380	Op381	Op382	Op383	Op384	Op385	Op386	Op387	Op388	Op389	Op390	Op391	Op392	Op393	Op394	Op395	Op396	Op397	Op398	Op399	Op400	Op401	Op402	Op403	Op404	Op405	Op406	Op407	Op408	Op409	Op410	Op411	Op412	Op413	Op414	Op415	Op416	Op41
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TSV4 - MISCELLANEOUS SECTIONS MACRO M1113 14 JUN 84 15:15
KTINIT - SETUP KT11 MEMORY MANAGEMENT REGISTERS

SEQ 0096

```
7
8
9 021502          .TITLE  TSV4  MISCELLANEOUS SECTIONS
  021502          BGNMOD  TSV4
                TSV4::
10
16
17
18
19          .SBTTL  PROTECTION TABLE
20 021502          BGNPROT
  021502          L$PROT::
21 021502 177777 177777 177777 .WORD  -1, -1, 1, 1          ;NO DEVICE PROTECTION REQUIRED.
22 021512          ENDPROT
```

.SBTTL INITIALIZE SECTION

```

24
25
26
27
28
29
30
31
32
33
34
35
36
37 021512
    021512
38 021512 005037 002224
39 021516 005037 003134
40 021522 012737 006356 002176
41 021530 005037 003152
42 021534 005037 003132
43 021540 005037 002300
44 021544
    021544 012700 000036
    021550 104447
45 021552
    021552 103023
46 021554 023737 002200 002012
47 021562 103070
48 021564 005737 003110
49 021570 100472
50 021572 013701 002200
51 021576 006301
52 021600 005761 003174
53 021604 001516
54 021606 032761 040000 003174
55 021614 001060
56 021616
    021616 104432
    021620 000416
57 021622
    021622 012700 000035
    021626 104447
58 021630
    021630 103052
59 021632
    021632 012700 000040
    021636 104447
60 021640
    021640 103404
61 021642
    021642 012700 000037
    021646 104447
62 021650
    021650 103031
63 021652
64 021652
    021652 104433

```

```

    BGNINIT
L$INIT::
40$: CLR EXTFEA
    CLR NXMFLG
    MOV #EPRT1,EPRTSW
    CLR SIFLAG
    CLR KTENABLE
    CLR RAMSIZ
    READEF #EF.CONTINUE
    MOV #EF.CONTINUE,R0
    TRAP C$REFG
    BNCOMPLETE 1$
    BCC 1$
    CMP UNITN,L$UNIT
    BHS 4$
    TST DUFLG
    BMI NXTU
    MOV UNITN,R1
    ASL R1
    TST ERTABL(R1)
    BEQ SETU
    BIT #BIT14,ERTABL(R1)
    BNE NXTU
    EXIT INIT
    TRAP C$EXIT
    .WORD L10030-.
1$: READEF #EF.NEW
    MOV #EF.NEW,R0
    TRAP C$REFG
    BNCOMPLETE NXTU
    BCC NXTU
    READEF #EF.START
    MOV #EF.START,R0
    TRAP C$REFG
    BCOMPLETE 2$
    BCS 2$
    READEF #EF.RESTART
    MOV #EF.RESTART,R0
    TRAP C$REFG
    BNCOMPLETE 31$
    BCC 31$
2$: BRESET
    TRAP C$RESET

```

```

;THE INITIALIZE SECTION CONTAINS THE CODING THAT IS PERFORMED
;AT THE BEGINNING OF EACH PASS.
;
;IF "START" OR "RESTART", SET QUICK-PASS FLAG AND BUS-INIT.
;IF "CONTINUE", NOTHING IS REQUIRED.
;
;--
;
;INSERT TEMPORARY JUMP TO ODT
;
;SET UP PRIMARY MESSAGE FOR REPLACEMENT
;CLEAR "SOFT INIT" FLAG
;CLEAR TEST ABOVE 28K FLAG
;CLEAR RAM SIZE FOR RAMERR ROUTINE
;
;UNIT IN RANGE?
;BR IF NO.
;DROPPED UNIT?
;BR IF YES
;DROPPED?
;DO NOTHING IF "CONTINUE".
;TAKE NEXT UNIT IF NOT NEW PASS.
;1ST PASS, BUS-INIT...
;BUS RESET.

```


65	021654	005037	002212		CLR	TSTCNT		;NUMBER OF TESTS RUN IN PASS
66	021660	005037	002220		CLR	FATFLG		;CLEAR FATAL ERROR COUNT
67	021664	005037	003142		CLR	T23A		;CLEAR 11/23A FLAG
68	021670	005037	003144		CLR	T23B		;CLEAR 11/23B FLAG
69					MOV	#340,-(SP)		
70					MOV	#204,-(SP)		;RETURN TO DEBUGGER
71					JMP	0.00T		;ENTER THE DEBUGGER
72	021674	005037	003376		CLR	SKIPT		;CLEAR THE SUBTEST "SKIPPER"
73	021700			204:				
74	021700	012737	177777	002202	MOV	#-1,QVP		;...QUICK VERIFY...
75	021706	004737	020630		JSR	PC,ENVIRN		;SET ENVIRONMENT.
76	021712	004737	021010		JSR	PC,KTINIT		;INITIALIZE KT MEMORY MANAGEMENT
77	021716	012700	003174		MOV	#ERTABL,RO		
78	021722	005020		304:	CLR	(RO)+		;CLEAR THE ERROR TABLE
79	021724	020027	003374		CMP	RO,#ERTABE		
80	021730	103774			BLO	304		
81	021732	000404			BR	44		
82	021734	005037	002202	314:	CLR	QVP		
83	021740	000137	022010		JMP	PASRPT		;GO REPORT THE STATUS
84								
85	021744			44:				
86	021744	012737	177777	002200	NEWPAS: MOV	#-1,UNITN		;INIT UNIT NUMBER...
87	021752	005037	002216		CLR	DEVcnt		;CLEAR COUNT OF DEVICES RUNNING
88	021756			NXTU:	BREAK			
	021756	104422			TRAP	C#BRK		
89	021760	005237	002200		INC	UNITN		;...AND SET NEXT UNIT NUMBER.
90	021764	023737	002200	002012	CMP	UNITN,L#UNIT		
91	021772	103423			BLO	SETU		
92	021774	012737	177777	003110	MOV	#-1,DUFLG		
93	022002	000401			BR	114		
94	022004				DOCLN			;ABORT, NO MORE UNITS.
	022004	104444			TRAP	C#DCLN		
95	022006	000240		114:	NOP			
96	022010			PASRPT:				
97	022010	023727	002012	000001	CMP	L#UNIT,#1		;HOW MANY UNITS SELECTED?
98	022016	101752			BLOS	NEWPAS		;BR IF ONLY 1
99	022020	005737	002216		TST	DEVcnt		;ARE ANY STILL RUNNING?
100	022024	001747			BEQ	NEWPAS		;BR IF NO
101	022026				RFLAGS	RO		
	022026	104421			TRAP	C#RFLA		
102	022030	032700	000100		BIT	#ISR,RO		;SHOULD WE PRINT STATISTICS
103	022034	001343			BNE	NEWPAS		;BR IF NO
104								
105	022036				DORPT			
	022036	104424			TRAP	C#DRPT		
106	022040	000741			BR	NEWPAS		
107	022042			104:				
108								
109	022042			SETU:	GPHARD	UNITN,RO		;GET UNIT N P TABLE POINTER.
	022042	013700	002200		MOV	UNITN,RO		
	022046	104442			TRAP	C#GPHRD		
110	022050				BNCOMPLETE	NXTU		;BR IF UNIT NOT AVAILABLE.
	022050	103342			BCC	NXTU		
111	022052	005037	003110		CLR	DUFLG		;CLEAR "DROPPED" FLAG.
112	022056	005237	002216		INC	DEVcnt		
113	022062	012001			MOV	(RO)+,R1		;GET 1ST REGISTER ADDRESS.
114	022064	010137	002204		MOV	R1,CSRADDR		;ADDRESS OF REGISTERS OF UNIT UNDER TEST

```

115
116 022070 012001      MOV      (R0),R1      ;GET VECTOR ADDRESS.
117                    ;MOV      (R0),R2      ;GET INTERRUPT PRIORITY
118                    ;MOV      R2,IPRI      ;SET INTERRUPT PRIORITY.
119 022072 010137 002206 MOV      R1,IVEC      ;SET INTERRUPT VECTOR POINTER...
120 022076 012721 016216 MOV      @INTR,(R1)+    ;...VECTOR...
121 022102 013721 002210 MOV      IPRI,(R1)+    ;...AND PRIORITY.
122
123 022106              1$:
124                    ;      TST      QVP      ;1ST PASS ??
125                    ;      BEQ      5$      ;NO, SKIP THE PASS 1 STUFF.
126
127                    ;
128                    ;1ST PASS, CHECK THAT DEVICE ADDRESSES ARE VALID, AND
129                    ;THAT THE DISPLAY STATUS IS PROPERLY INITIALIZED.
130                    ;
131 022106 013701 002200      MOV      UNITN,R1
132 022112 006301      ASL      R1
133 022114 052761 100000 003174 BIS      @BIT15,ERTABL(R1)      ;SAY DEVICE RUNNING
134 022122 005037 005770      CLR      EXTA      ;CLEAR ERROR EXTENSION FLAG.
135 022126 023727 002012 000001 CMP      L$UNIT,#1      ;ARE WE TESTING MULTIPLE UNITS?
136 022134 101416      BLOS      10$      ;BR IF NO.
137 022136      RFLAGS      R0      ;YES -- GET OPERATOR FLAGS.
138 022136 104421      TRAP      C$RFLA
139 022140 032700 001000      BIT      @PNT,R0      ;SHOULD WE PRINT UNIT #?
140 022144 001412      BEQ      10$      ;BR IF NOT.
141 022146      PRINTF      @PUNIT,UNITN      ;PRINT THE UNIT #
142 022146 013746 002200      MOV      UNITN,-(SP)
143 022152 012746 022240      MOV      @PUNIT,-(SP)
144 022156 012746 000002      MOV      @2,-(SP)
145 022162 010600      MOV      SP,R0
146 022164 104417      TRAP      C$PNTF
147 022166 062706 000006      ADD      @6,SP
148 022172              10$:
149 022172 005037 003112      CLR      NODEV
150 022176 013701 002204      MOV      CSRADDR,R1      ;ADDRESS OF FIRST REGISTER
151 022202 010102      MOV      R1,R2      ;START OF REGISTERS
152 022204 062702 000002      ADD      @TSSR,R2      ;ADDRESS OF TSSR REGISTER
153 022210 004737 016376      JSR      PC,XNXM      ;TEST BOTH CONTROLLER REGISTERS...
154 022214 103005      BCC      2$      ;...AND BR IF ALL OK.
155 022216 010137 003112      MOV      R1,NODEV      ;FLAG DEVICE AS NON-EXISTENT
156 022222 012737 177777 003110 MOV      @-1,DUFLG      ;DROP THIS UNIT.
157 022230              2$:
158                    ;
159                    ;FINALLY, SET CPU PRIORITY AND WE'RE DONE.
160                    ;
161 022230              5$:      SETPRI      @PRI00      ;ENABLE INTERRUPTS.
162 022230 012700 000000      MOV      @PRI00,R0
163 022234 104441      TRAP      C$SPRI
164 022236      ENDINIT
165 022236              L10030:
166 022236 104411      TRAP      C$INIT
167 022240      045      116      045 PUNIT: .ASCIZ /NNNA***** TESTING UNIT #D2A *****/
168 .EVEN

```



```

160                      .SBTTL  ADD AND DROP UNITS SECTIONS
161
162                      ;**
163                      ; THE ADD-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
164                      ; TO BE (A) ADDED TO THE TEST LIST FOR THE FIRST TIME,
165                      ; OR (B) RE-INSERTED IF IT HAD BEEN PREVIOUSLY DROPPED.
166                      ;--
167 022306                BGNAU
168 022306                L$AU::
169 022306 010001          MOV     R0,R1                ; GET UNIT TO BE ADDED (R0)
170 022310 006301          ASL     R1                  ; MAKE IT A WORD INDEX
171 022312 052761 100000 003174  BIS     #100000,ERTABL(R1) ; SET THE "ACTIVE" BIT
172 022320 042761 040000 003174  BIC     #40000,ERTABL(R1) ; CLEAR THE "DROPPED" BIT
173 022326                PRINTF  #1$,R0
174 022326 010046          MOV     R0,-(SP)
175 022330 012746 022354    MOV     #1$,-(SP)
176 022334 012746 000002    MOV     #2$,-(SP)
177 022340 010600          MOV     SP,R0
178 022342 104417          TRAP    C$PNTF
179 022344 062706 000006    ADD     #6,SP
180 022350                EXIT     AU
181 022350 000167          .WORD    J$JMP
182 022352 000026          .WORD    L10031-2-
183 022354 045 116 045 1$: .ASCIZ  /#N$A UNIT #D$A ADDED/
184                      .EVEN
185
186                      ENDAU                ; UNUSED.
187 022402                L10031:
188 022402 104452          TRAP    C$AU
189
190                      ;**
191                      ; THE DROP-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
192                      ; TO BE REMOVED FROM THE TEST LIST.
193                      ;
194                      ; SUPVSR DOES THE "DROPPING". THIS IS JUST TO TELL THE MAN,
195                      ; "DROPPED" UNITS ARE RE-SELECTED ON OPERATOR "STA" OR "ADD"
196                      ; COMMAND, OTHERWISE REMAIN INACTIVE. THE "DISPLAY" COMMAND
197                      ; WILL PRINT ALL DROPPED UNITS, AND THE P-TABLES OF THOSE
198                      ; WHICH ARE STILL ACTIVE.
199                      ; UPON ENTRY, R0 CONTAINS THE UNIT TO BE DROPPED.
200
201 022404                BGNDU
202 022404                L$DU::
203 022404 012737 177777 003110  MOV     #-1,DUFLG
204 022412 010001          MOV     R0,R1
205 022414 006301          ASL     R1
206 022416 052761 140000 003174  BIS     #140000,ERTABL(R1) ; SAY DROPPED
207 022424 000240 000240 000240 240,240,240 ; ??????????
208 022432                PRINTF  #1$,R0
209 022432 010046          MOV     R0,-(SP)
210 022434 012746 022460    MOV     #1$,-(SP)
211 022440 012746 000002    MOV     #2$,-(SP)
212 022444 010600          MOV     SP,R0
213 022446 104417          TRAP    C$PNTF
214 022450 062706 000006    ADD     #6,SP
215 022454                EXIT     DU
216 022454 000167          .WORD    J$JMP
217 022456 000030          .WORD    L10032-2

```

TSV4 - MISCELLANEOUS SECTIONS
ADD AND DROP UNITS SECTIONS

MACRO M1113 14 JUN-84 15:15

SEQ 0101

```

197 022460      045      116      045 1$: .ASCIZ /%N% A UNIT %D% A DROPPED/
198                                     .EVEN
199 022510                                     ENDDU
    022510      L10032:
    022510      104453      TRAP      C$DU
200                                     ;**
201                                     ; AUTO-DROP CODE SECTION.
202                                     ;
203 022512      BGNAUTO
    022512      L$AUTO::
204 022512      013705      002204      MOV      CSRADDR,R5      ;POINT TO DEVICE REGISTER
205 022516      012703      000550      MOV      #360.,R3      ;ENOUGH TIME FOR 2400' REEL TO REWIND
206 022522      004737      016250      10$: JSR      PC,WAITF      ;WAIT FOR SSR TO SET
207 022526      103420      BCS      20$      ;LEAVE WHEN SSR IS SET
208 022530      DELAY      250.      ;WAIT FOR .25 SECONDS
    022530      012727      000372      MOV      #250.,(PC)+
    022534      000000      .WORD      0
    022536      013727      002116      MOV      L$DLY,(PC)+
    022542      000000      .WORD      0
    022544      005367      177772      DEC      -6(PC)
    022550      001375      BNE      .-4
    022552      005367      177756      DEC      -22(PC)
    022556      001367      BNE      .-20
209 022560      005303      DEC      R3      ;BUMP COUNTER DOWN
210 022562      001357      BNE      10$      ;KEEP GOING
211 022564      004737      017202      JSR      PC,CKDROP      ;TRY AND DROP UNIT
212 022570      20$:
213 022570      ENDAUTO      ; UNUSED.
    022570      L10033:
    022570      104461      TRAP      C$AUTO

```

				.SBTTL	CLEAN-UP AND REPORT CODING SECTIONS	
215				;	**	
216				:	THE CLEANUP CODING SECTION CONTAINS THE CODING THAT IS	
217				:	EXECUTED AT THE END OF EACH PASS (OR SUB-PASS).	
218				:	USE TO RETURN DEVICE UNDER TEST TO A NEUTRAL STATE.	
219				:		
220				:		
221				:		
222	022572			BGNCLN		
222	022572			L\$CLEAN::		
223	022572	013705	002204	MOV	CSRADDR,R5	;POINT TO DEVICE REGISTER
224	022576	005737	003110	TST	DUFLG	; "DROPPED" FLAG IS SET ON...
225	022602	100405		BMI	1\$	...AND GROSS CONTROLLER FAULT...
226						...DON'T TRY TO XCT CLEANUP CODE.
227						
228	022604	012765	000000	MOV	#0,TSSR(R5)	;DO SOFT INIT
229	022612	004737	016250	JSR	PC,WAITF	
230	022616			1\$:		
231	022616			2\$:	ENDCLN	
232	022616			L10034:		
233	022616	104412		TRAP	C\$CLEAN	
234						
235				:	**	
236	022620			:	THE REPORT CODING SECTION CONTAINS THE	
237	022620			:	"PRINTS" CALLS THAT GENERATE STATISTICAL REPORTS.	
238	022620			:	--	
239	022620			BGNRPT		
240	022620			L\$RPT::		
241	022620	012746	023062	PRINTS	#DEVSUM	
242	022624	012746	000001	MOV	#DEVSUM,-(SP)	
243	022630	010600		MOV	#1,-(SP)	
244	022632	104416		MOV	SP,R0	
245	022634	062706	000004	TRAP	C\$PNTS	
246	022640	010246		ADD	#4,SP	
247	022642	010346		MOV	R2,-(SP)	
248	022644	010446		MOV	R3,-(SP)	
249	022646	012704	003174	MOV	R4,-(SP)	
250	022652	005003		MOV	#ERTABL,R4	; GET START OF ERROR TABLE.
251	022654	011402		CLR	R3	; CLEAR UNIT NUMBER
252	022656	001467		1\$:	MOV (R4),R2	; GET ERROR TABLE ENTRY & TEST IT.
253	022660	100066		BEQ	4\$	; ZERO IF UNIT NOT RUN
254	022662	032702	040000	BPL	4\$	
255	022666	001015		BIT	#BIT14,R2	; WAS UNIT DROPPED?
256	022670	042702	170000	BNE	2\$	; BR IF YES
257	022674			BIC	#C7777,R2	; GET ERROR COUNT FIELD
258	022674	010246		PRINTS	#DEVONL,R3,R2	; PRINT
259	022676	010346		MOV	R2,-(SP)	
260	022700	012746	023117	MOV	R3,-(SP)	
261	022704	012746	000003	MOV	#DEVONL,-(SP)	
262	022710	010600		MOV	#3,-(SP)	
263	022712	104416		MOV	SP,R0	
264	022714	062706	000010	TRAP	C\$PNTS	
265	022720	000446		ADD	#10,SP	
266	022722	020227	160000	BR	4\$	
267	022726	001012		2\$:	CMP R2,#160000	; WAS UNIT NON-EXISTENT?
268	022730			BNE	3\$	; BR IF NO
269	022730	010346		PRINTS	#DEVNXR,R3	
270	022732	012746	023167	MOV	R3,-(SP)	
271				MOV	#DEVNXR,-(SP)	

```

022736 012746 000002      MOV      #2,-(SP)
022742 010600      MOV      SP,R0
022744 104416      TRAP     C:PNTS
022746 062706 000006      ADD      #6,SP
254 022752 000431      BR      4$
255 022754 020227 160001  3$:      CMP      R2,#160001      ; WAS UNIT NOT READY AT STARTUP?
256 022760 001012      BNE      30$      ; BR IF NO.
257 022762      PRINTS    #DEVNRD,R3
022762 010346      MOV      R3,-(SP)
022764 012746 023251      MOV      #DEVNRD,-(SP)
022770 012746 000002      MOV      #2,-(SP)
022774 010600      MOV      SP,R0
022776 104416      TRAP     C:PNTS
023000 062706 000006      ADD      #6,SP
258 023004 000414      BR      4$
259 023006 042702 170000  30$:      BIC      #C7777,R2
260 023012      PRINTS    #DEVDR0,R3,R2
023012 010246      MOV      R2,-(SP)
023014 010346      MOV      R3,-(SP)
023016 012746 023332      MOV      #DEVDR0,-(SP)
023022 012746 000003      MOV      #3,-(SP)
023026 010600      MOV      SP,R0
023030 104416      TRAP     C:PNTS
023032 062706 000010      ADD      #10,SP
261 023036 062704 000002  4$:      ADD      #2,R4
262 023042 005203      INC      R3
263 023044 020427 003374      CMP      R4,#ERTABE
264 023050 103701      BLO      1$
265 023052 012604      MOV      (SP)+,R4
266 023054 012603      MOV      (SP)+,R3
267 023056 012602      MOV      (SP)+,R2
268 023060      ENDRPT      ; UNUSED.
023060      L10035:
023060 104425      TRAP     C:RPT
269
270 023062      045      116      045 DEVSUM: .ASCIIZ /#N#ADEVICE STATUS SUMMARY:#N/
271 023117      045      101      040 DEVONL: .ASCIIZ /#A UNIT #D3#A ONLINE, ERRORS = #D#N/
272 023167      045      101      040 DEVNXR: .ASCIIZ /#A UNIT #D3#A DROPPED, NON-EXISTENT REGISTER#N/
273 023251      045      101      040 DEVNRD: .ASCIIZ /#A UNIT #D3#A DROPPED, NOT READY AT STARTUP#N/
274 023332      045      101      040 DEVDR0: .ASCIIZ /#A UNIT #D3#A DROPPED, ERRORS = #D#N/
275      .EVEN
276
277 023402      ENDMOD
278

```

```

1      .TITLE  TSV5A  HARDWARE TESTS
2
9
10 023402      BGNMOD  TSV5
    023402      TSV5::

16
24
25      .SBTTL  TEST  1: BUS RESET TEST
26
27
28      :      THIS TEST VERIFIES THAT THE M7196 MODULE'S DEVICE REGISTERS ARE
29      :      ACCESSIBLE ON THE BUS (SUBTEST 1) AND THEN CHECKS THAT THE
30      :      BUILT-IN INITIALIZATION SELF-TEST MICRODIAGNOSTIC DID NOT FIND
31      :      ANY BASIC PROBLEMS IN THE MODULE.  AREAS OF LOGIC TESTED BY THE
32      :      SELF-TEST SEQUENCE ARE AS FOLLOWS:  ROM AND PIPELINE REGISTER,
33      :      SEQUENCER, INTERNAL BUSES, 2901 MICROPROCESSOR, AND, RAM.  THIS
34      :      TEST INITIALIZES THE CONTROLLER BY ISSUING THE BUS INIT SIGNAL
35      :      VIA A RESET INSTRUCTION, OR BY WRITING INTO THE TSSR REGISTER,
36      :      WAITS A PERIOD OF TIME (TO ALLOW THE CONTROLLER'S INITIALIZATION
37      :      MICRODIAGNOSTIC SEQUENCE TO BE COMPLETED), AND THEN CHECKS THE
38      :      CONTENTS OF THE TSSR REGISTER.  SUCCESSFUL INITIALIZATION IS
39      :      INDICATED BY SUBSYSTEM READY (SSR) AND NEED BUFFER ADDRESS (NBA)
40      :      BITS BEING SET (1) AND ALL OTHER BITS (EXCEPT A17 AND A16 AND
41      :      OFL, WHICH ARE IGNORED FOR THIS TEST) BEING CLEAR (0).  IF THE
42      :      CONTENTS OF TSSR ARE NOT AS EXPECTED, AN ERROR REPORT IS ISSUED
43      :      LISTING THE EXPECTED DATA, ACTUAL DATA, AND THE DISCREPANCIES.
44      :      THE ERROR REPORT ANALYZES THE TSSR CONTENTS AND DISCERNs AND
45      :      REPORTS ONE OF THREE POSSIBILITIES:
46
47
48      :      1.  TSSR CONTENTS ARE AMBIGUOUS (ANY OF BITS 11-14 ARE SET,
49      :      OR STATES OF SSR AND SC BITS DO NOT CORRESPOND TO THE
50      :      APPARENT ERROR CODE IN BITS 0-5):  INDICATES THAT THE
51      :      TSSR CONTENT CANNOT BE TRUSTED.  INDICATES A
52      :      CATASTROPHIC CONTROLLER MALFUNCTION.  THIS IS A FATAL
53      :      ERROR (EXECUTION IS ABORTED).  FIELD ACTION WOULD BE TO
54      :      REPLACE THE M7196.  IF THE M7196 ITSELF IS BEING
55      :      DEBUGGED, THE PROGRAM SHOULD BE RESTARTED WITH LOOP ON
56      :      ERROR ENABLED IN ORDER TO PROBE FOR THE PROBLEM.
57
58      :      2.  SSR = 0, SC = 0 AND THE ERROR CODE IN BITS 0-5 IS IN
59      :      THE RANGE 17-13:  THIS IS A FATAL ERROR.  THE ERROR
60      :      CODE IS DECODED AND THE APPROPRIATE DESCRIPTION GIVEN.
61      :      INDICATES THAT A SERIOUS PROBLEM EXISTS.
62
63
64 023402      BGNTST
    023402
69 023402      012700  023600      MOV      #TST1ID,R0      T1::
70 023406      004737  016510      JSR      PC,TSTSETUP      ;ASCII MESSAGE TO IDENTIFY TEST
71 023412      012737  000024  002214  MOV      #20.,LOOPCNT      ;DO INITIAL TEST SETUP
72 023420      T1LOOP:      ;PERFORM 20 ITERATIONS
73 023420      005003      CLR      R3      ;USE R3 AS FATAL ERROR FLAG
74
75 023422      BGNSUB      ;////////// BEGIN SUBTEST ////////////
    023422      T1.1:
    023422      104402      TRAP      C#BSUB

```

TSVSA HARDWARE TESTS MACRO M1113 14 JUN-84 15:15
TEST 1: BUS RESET TEST

SEQ 0105

76							
77	023424			BRESET		ISSUE A BUS RESET	
	023424	104433					TRAP C18RESF
78	023426	004737	016250	JSR	PC, WAITF	WAIT FOR READY	
79	023432	016501	000002	MOV	TSSR(R5), R1	GET THE CONTENTS OF TSSR	
80	023436	010102		MOV	R1, R2	CONTENTS OF TSSR	
81	023440	042702	176277	BIC	01C<HIADDR!OFL>, R2	THESE BITS MAY BE SET	
82	023444	052702	002200	BIS	05SR!NBA, R2	READY AND NEW DATA SHOULD BE SET	
83	023450	020102		CMF	R1, R2	COMPARE EXPECTED TO RECEIVED	
84	023452	001405		BEQ	101	BRANCH IF COMPARE	
88	023454			ERRDF	ERRNO, SFHERR, SFFMSG	REPORT A FATAL ERROR	
	023454	104455					TRAP C18ERDF
	023456	000145					.WORD 101
	023460	003703					.WORD SFHERR
	023462	012102					.WORD SFFMSG
89	023464	005203		INC	R3	SET THE FATAL ERROR FLAG	
90	023466		101:				
91	023466			ENDSUB		END SUBTEST	
	023466					L10037:	
	023466	104403					TRAP C18ESUB
92							
93	023470	005703		TST	R3	DID WE HAVE FATAL ERROR ?	
94	023472	001402		BEQ	201	BRANCH IF NOT	
95	023474	004737	017202	JSR	PC, CKDROP	GO DROP THIS UNIT, IF ALLOWED	
96	023500	005003		CLR	R3	RESET FATAL ERROR FLAG	
97			201:				
98							
99	023502			BGN SUB		BEGIN SUBTEST	
	023502					T1.2:	
	023502	104402					TRAP C18SUB
100							
101	023504	005065	000002	CLR	TSSR(R5)	WRITE TO ISSUE A SOFT RESET	
102	023510	004737	016250	JSR	PC, WAITF	WAIT FOR READY TO SET	
103	023514	016501	000002	MOV	TSSR(R5), R1	GET REGISTER TSSR DATA	
104	023520	010102		MOV	R1, R2	CONTENTS OF TSSR	
105	023522	042702	176277	BIC	01C<HIADDR!OFL>, R2	THESE BITS MAY BE SET	
106	023526	052702	002200	BIS	05SR!NBA, R2	READY AND NEW DATA SHOULD BE SET	
107	023532	020102		CMF	R1, R2	COMPARE EXPECTED TO RECEIVED	
108	023534	001405		BEQ	101	BRANCH IF COMPARE	
112	023536			ERRDF	ERRNO, SFIERR, SFFMSG	REPORT A FATAL ERROR	
	023536	104455					TRAP C18ERDF
	023540	000146					.WORD 102
	023542	003650					.WORD SFIERR
	023544	012102					.WORD SFFMSG
113	023546	005203		INC	R3	SET THE ERROR FLAG	
114	023550		101:				
115	023550			ENDSUB		END SUBTEST	
	023550					L10040:	
	023550	104403					TRAP C18ESUB
116							
117							
118	023552	005703		TST	R3	FATAL ERROR DETECTED ?	
119	023554	001402		BEQ	201	BRANCH IF NOT	
120	023556	004737	017202	JSR	PC, CKDROP	SEE IF TIME TO DROP UNIT	
121	023562	004737	016456	JSR	PC, TSTLOOP	SHOULD WE DO ITERATIONS ?	
122	023566	103002		BCC	401	BRANCH IF NOT	
123	023570	000137	023420	JMP	T1LOOP	LOOP UNTIL COUNT EXPIRED	


```

124 023574 401: EXIT TST ;ALL DONE THIS TEST
023574 104432 TRAP CEXIT
023576 000022 .WORD L10036
125
126 ;
127 ;LOCAL TEXT MESSAGES FOR TEST
128 ;
129
130 023600 111 156 151 TST1ID: .ASCIZ 'Initialization'
131 .EVEN
132 023620 ENDTST
023620 L10036:
023620 104401 TRAP CEXITST
133
134 .SBTTL TEST 2: WRAP DATA - HIGH BYTE
135
136
137 ;
138 ;
139 ;
140 ;
141 ;
142 ;
143 ;
144 ;
145 ;
146 ;
147 ;
148 ;
149 ;
150 ;
151 ;
152 ;
153 ;
154 ;
155 ;
156 ;
157 ;
158 ;
159 ;
160 ;
161 ;
162 ;
163 ;
164 ;
165 ;
166 ;
167 ;
168 ;
169 023622 BGNTST
023622
174 023622 012700 024270 MOV #TST2ID,R0 ;ASCII MESSAGE TO IDENTIFY TEST
175 023626 004737 016510 JSR PC,TSTSETUP ;DO INITIAL TEST SETUP
176 023632 012737 000024 002214 MOV #20,LOOPCNT ;PERFORM 20 ITERATIONS
177 023640 005004 T2LOOP: CLR R4 ;STARTING DATA PATTERN
178 023642 012703 177777 MOV #-1,R3 ;DO INIT ON FIRST TIME THROUGH
179 023646 005703 54: TST R3 ;DO WE NEED SOFT INIT

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180	023650	001412		BEQ	10:	;BRANCH IF NOT	
181	023652	005003		LR	R3	;DON'T NEED INIT NEXT TIME THRU	
182	023654	004737	015774	JSR	PC,SOFINIT	;DO SOFT INIT OF CONTROLLER	
183	023660	103406		BCS	10:	;BR IF SOFT INIT = OK	
187	023662	010001		MOV	R0,R1	;SAVE CONTENTS OF TSSR	
188	023664			ERRDF	ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT	
	023664	104455					TRAP C\$ERDF
	023666	000311					.WORD 201
	023670	003650					.WORD SFIERR
	023672	012034					.WORD SFIMSG
189	023674	005203		INC	R3	;FORCE SOFT INIT ON NEXT PASS	
190	023676	005037	002220	CLR	FATFLG	;CLEAR FATAL ERROR FLAG	
191							
192	023702			BGNSEG		; >>>>>>>>> BEGIN SEGMENT >>>>>>>>	
	023702	104404					TRAP C\$BSEG
193							
194	023704	110465	000001	MOVB	R4,TSD8H(R5)	;SET MAINT MODE + WRITE DATA	
195	023710	004737	016250	JSR	PC,WAITF	;WAIT FOR SSR TO SET	
196	023714	103411		BCS	15:	;BR IF CARRY SET (GOOD RETURN)	
197	023716	010001		MOV	R0,R1	;SAVE CONTENTS OF TSSR	
198	023720	010402		MOV	R4,R2	;DATA THAT WAS WRITTEN	
202	023722			ERRDF	ERRNO,T2SSR,EXPREC	;DEVICE FATAL SSR FAILED TO SET	
	023722	104455					TRAP C\$ERDF
	023724	000312					.WORD 202
	023726	024216					.WORD T2SSR
	023730	015474					.WORD EXPREC
203	023732	005203		INC	R3	;FORCE SOFT INIT ON NEXT PASS	
204	023734	005237	002220	INC	FATFLG	;SET FATAL ERROR FLAG	
205	023740			CKLOOP		;LOOP ON ERROR, IF FLAG SET	
	023740	104406					TRAP C\$CLP1
206	023742	005737	002220	TST	FATFLG	;WAS FATAL ERROR RECEIVED ?	
207	023746	001402		BEQ	20:	;BRANCH IF NOT	
208	023750	004737	017202	JSR	PC,CKDROP	;SEE IF TIME TO DROP UNIT	
209	023754	010402		MOV	R4,R2	;DATA PATTERN WRITTEN	
210	023756	042702	177774	BIC	#+C<BIT0!BIT1>,R2	;CLEAR ALL BUT LOW 2 BITS	
211	023762	000302		SWAB	R2	;BITS 8 AND 9 HAVE LOW DATA BITS	
212	023764	052702	002200	BIS	#SSR!NBA,R2	;THESE BITS MUST BE SET ALSO	
213	023770	016501	000002	MOV	TSSR(R5),R1	;GET THE CONTENTS OF TSSR	
214	023774	032701	000100	BIT	#OFL,R1	;IS OFF-LINE BIT SET ?	
215	024000	001402		BEQ	25:	;BRANCH IF NOT OFF-LINE	
216	024002	052702	000100	BIS	#OFL,R2	;SET OFF-LINE IN EXPECTED DATA	
217	024006	020201		CMP	R2,R1	;DOES EXPECTED MATCH RECEIVED ?	
218	024010	001405		BEQ	30:	;OKAY IF MATCH	
222	024012			ERRMRD	ERRNO,T2TSSR,EXPREC	;TSSR WASN'T CORRECT	
	024012	104456					TRAP C\$ERRMRD
	024014	000313					.WORD 203
	024016	024151					.WORD T2TSSR
	024020	015474					.WORD EXPREC
223	024022	005203		INC	R3	;FORCE SOFT INIT ON NEXT PASS	
224	024024			CKLOOP		;LOOP ON ERROR ?	
	024024	104406					TRAP C\$CLP1
225	024026	016501	000000	MOV	TSBA(R5),R1	;GET TSBA REGISTER CONTENTS	
226	024032	005002		CLR	R2		
227	024034	150402		BISB	R4,R2	;DATA PATTERN WRITTEN	
228	024036	000302		SWAB	R2	;MOVE INTO TOP BYTE	
229	024040	150402		BISB	R4,R2	;BOTH HALVES SHOULD BE SAME	
230	024042	020102		CMP	R1,R2	;COMPARE EXPECTED TO RECEIVED	

Line	Address	Hex	Label	Op	Op2	Op3	Op4	Op5	Op6	Op7	Op8	Op9	Op10	Op11	Op12	Op13	Op14	Op15	Op16	Op17	Op18	Op19	Op20	Op21	Op22	Op23	Op24	Op25	Op26	Op27	Op28	Op29	Op30	Op31	Op32	Op33	Op34	Op35	Op36	Op37	Op38	Op39	Op40	Op41	Op42	Op43	Op44	Op45	Op46	Op47	Op48	Op49	Op50	Op51	Op52	Op53	Op54	Op55	Op56	Op57	Op58	Op59	Op60	Op61	Op62	Op63	Op64	Op65	Op66	Op67	Op68	Op69	Op70	Op71	Op72	Op73	Op74	Op75	Op76	Op77	Op78	Op79	Op80	Op81	Op82	Op83	Op84	Op85	Op86	Op87	Op88	Op89	Op90	Op91	Op92	Op93	Op94	Op95	Op96	Op97	Op98	Op99	Op100	Op101	Op102	Op103	Op104	Op105	Op106	Op107	Op108	Op109	Op110	Op111	Op112	Op113	Op114	Op115	Op116	Op117	Op118	Op119	Op120	Op121	Op122	Op123	Op124	Op125	Op126	Op127	Op128	Op129	Op130	Op131	Op132	Op133	Op134	Op135	Op136	Op137	Op138	Op139	Op140	Op141	Op142	Op143	Op144	Op145	Op146	Op147	Op148	Op149	Op150	Op151	Op152	Op153	Op154	Op155	Op156	Op157	Op158	Op159	Op160	Op161	Op162	Op163	Op164	Op165	Op166	Op167	Op168	Op169	Op170	Op171	Op172	Op173	Op174	Op175	Op176	Op177	Op178	Op179	Op180	Op181	Op182	Op183	Op184	Op185	Op186	Op187	Op188	Op189	Op190	Op191	Op192	Op193	Op194	Op195	Op196	Op197	Op198	Op199	Op200	Op201	Op202	Op203	Op204	Op205	Op206	Op207	Op208	Op209	Op210	Op211	Op212	Op213	Op214	Op215	Op216	Op217	Op218	Op219	Op220	Op221	Op222	Op223	Op224	Op225	Op226	Op227	Op228	Op229	Op230	Op231	Op232	Op233	Op234	Op235	Op236	Op237	Op238	Op239	Op240	Op241	Op242	Op243	Op244	Op245	Op246	Op247	Op248	Op249	Op250	Op251	Op252	Op253	Op254	Op255	Op256	Op257	Op258	Op259	Op260	Op261	Op262	Op263	Op264	Op265	Op266	Op267	Op268	Op269	Op270	Op271	Op272	Op273	Op274	Op275	Op276	Op277	Op278	Op279	Op280	Op281	Op282	Op283	Op284	Op285	Op286	Op287	Op288	Op289	Op290	Op291	Op292	Op293	Op294	Op295	Op296	Op297	Op298	Op299	Op300	Op301	Op302	Op303	Op304	Op305	Op306	Op307	Op308	Op309	Op310	Op311	Op312	Op313	Op314	Op315	Op316	Op317	Op318	Op319	Op320	Op321	Op322	Op323	Op324	Op325	Op326	Op327	Op328	Op329	Op330	Op331	Op332	Op333	Op334	Op335	Op336	Op337	Op338	Op339	Op340	Op341	Op342	Op343	Op344	Op345	Op346	Op347	Op348	Op349	Op350	Op351	Op352	Op353	Op354	Op355	Op356	Op357	Op358	Op359	Op360	Op361	Op362	Op363	Op364	Op365	Op366	Op367	Op368	Op369	Op370	Op371	Op372	Op373	Op374	Op375	Op376	Op377	Op378	Op379	Op380	Op381	Op382	Op383	Op384	Op385	Op386	Op387	Op388	Op389	Op390	Op391	Op392	Op393	Op394	Op395	Op396	Op397	Op398	Op399	Op400	Op401	Op402	Op403	Op404	Op405	Op406	Op407	Op408	Op409	Op410	Op411	Op412	Op413	Op414	Op415	Op416	Op417	Op418	Op419	Op420	Op421	Op422	Op423	Op424	Op425	Op426	Op427	Op428	Op429	Op430	Op431	Op432	Op433	Op434	Op435	Op436	Op437	Op438	Op439	Op440	Op441	Op442	Op443	Op444	Op445	Op446	Op447	Op448	Op449	Op450	Op451	Op452	Op453	Op454	Op455	Op456	Op457	Op458	Op459	Op460	Op461	Op462	Op463	
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TSVSA - HARDWARE TESTS MACRO M1113 14-JUN-84 15:15
TEST 3: WRAP DATA - LOW BYTE

SEQ 0110

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024512 000457
024514 024646
024516 015474
339 024520 005203
340 024522 104406
024522 016501 000000
341 024524 005002
342 024530 150402
343 024532 000302
344 024534 150402
345 024536 020102
346 024540 001405
347 024542 104456
351 024544 000460
024546 024602
024550 015474
352 024554 005203
353
354 024556
024556 104405
024556 105204
355 024560 001270
356 024562 004737 016456
357 024564 103002
358 024570 000137 024336
359 024572 104432
360 024576 000210
361 024576 104432
024576 104432
024600 000210
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367 024602 124 123 102
368 024646 124 123 123
369 024712 116 157 040
370 024763 127 162 141
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372 025010
025010
025010 104401
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304: INC R3
CKLOOP
;FORCE INIT ON NEXT PASS
;LOOP ON ERROR ?
TRAP C1CLP1
;GET TSBA REGISTER CONTENTS
;
;DATA PATTERN WRITTEN
;MOVE INTO TOP BYTE
;BOTH HALVES SHOULD BE SAME
;COMPARE EXPECTED TO RECEIVED
;BR IF ERROR NOT EQUAL
;PRINT THE ERROR & EXPD/RECV
TRAP C1ERHRO
;WORD 304
;WORD T3TSBA
;WORD EXPREC

354: INC R3
;FORCE INIT ON NEXT PASS
;END SEGMENT
100004: TRAP C1ESEG

304: INCB R4
BNE 54
JSR PC,TSTLOOP
BCC 404
JMP T3LOOP
404: EXIT TST
TRAP C1EXIT
;WORD L10042-

;LOCAL TEXT MESSAGES FOR TEST
;
T3TSBA: .ASCIZ 'TSBA Incorrect After TSDB Low Write'
T3TSSR: .ASCIZ 'TSSR Incorrect After TSDB Low Write'
T3SSR: .ASCIZ 'No Sub-System Ready After TSDB Low Write'
T3T3ID: .ASCIZ 'Wrap Data - Low Byte'
.EVEN
ENDTST

L10042: TRAP C1ETST

.SBTTL TEST 4: RAM TEST

;
; THIS TEST VERIFIES THAT ALL LOCATIONS OF THE RAM ON THE M7196
; CAN PROPERLY STORE AND READ BACK ALL DATA PATTERNS, AND THAT
; EACH RAM LOCATION IS UNIQUELY ADDRESSED (I.E., THAT ONE AND ONLY
; ONE LOCATION IS ACCESSED BY ANY PARTICULAR ADDRESS). THESE
; TESTS ARE PERFORMED BY THREE SUBTESTS, DESCRIBED BELOW. A
; BYPRODUCT OF THESE TESTS IS A VERIFICATION OF TWO REGISTERS IN
; THE 2901 AND THE CAPABILITY OF THE 2901 TO CORRECTLY PERFORM AN
; ADD.
;

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421 025012
    025012
422
423 025012
    025012
    025012 104402
424
429 025014 012700 026322
430 025020 004737 016510
431 025024 012737 000005 002214
432 025032
433 025032 004737 015774
434 025036 103405
438 025040 010001
439 025042
    025042 104455
    025044 000621
    025046 003650
    025050 012034
440 025052 005004
441 025054 004737 016336

:
:
: TEST 4 , SUBTEST 1:
:
: THIS SUBTEST VERIFIES EACH RAM LOCATION BY FIRST PLACING THE
: M7196 INTO MAINTENANCE MODE BY WRITING INTO THE LOW BYTE OF TSDB
: AND THEN PERFORMING THE FOLLOWING SEQUENCE FOR EACH ADDRESS
: 0-7777 (OCTAL):
:
: 1. THE ADDRESS TO BE TESTED IS LOADED INTO THE TSDB (VIA A
:    WORD WRITE).
:
: 2. THE ADDRESSED RAM LOCATION IS WRITTEN, THEN READ INTO
:    THE LOW BYTE OF TSBA, BY WRITING A DATA BYTE INTO THE
:    LOW BYTE OF TSDB.
:
: 3. THE LOW BYTE OF TSBA IS CHECKED TO SEE IF IT CONTAINS
:    THE DATA PATTERN ORIGINALLY WRITTEN; A DISCREPANCY IS
:    REPORTED AS AN ERROR.
:
: 4. THE ADDRESS OF THE LOCATION BEING TESTED IS AGAIN
:    WRITTEN INTO TSDB (WORD WRITE), TO CAUSE THE LOCATION
:    UNDER TEST TO AGAIN BE READ INTO THE LOW BYTE OF TSBA.
:    THE LOW BYTE OF TSBA IS AGAIN CHECKED AND DISCREPANCIES
:    REPORTED.
:
: 5. THE HIGH BYTE OF TSBA IS CHECKED; IT SHOULD CONTAIN
:    THE SUM OF THE HIGH AND LOW BYTES LAST WRITTEN INTO
:    TSDB AS A WORD. A DISCREPANCY IS REPORTED AS A 2901
:    PROBLEM.
:
: 6. THE CONTENT OF TSSR IS CHECKED; SETTING OF THE SC BIT
:    IS IGNORED. OTHER DISCREPANCIES IN TSSR ARE REPORTED.
:
: BGNTST
:
: T4::
:
: BGNSUB
:
: ////////////////////////////////// BEGIN SUBTEST //////////////////////////////////
: T4.1:
:
: TRAP C#BSUB
:
: MOV #TST4ID,R0 ;ASCII MESSAGE TO IDENTIFY TEST
: JSR PC,TSTSETUP ;DO INITIAL TEST SETUP
: MOV #5,LOOPCNT ;PERFORM 5 ITERATIONS
:
: T4LOOP:
: JSR PC,SOFINIT ;DO INITIALIZE ON CONTROLLER
: BCS 20# ;BR IF INIT WAS OK
: MOV R0,R1 ;CONTENTS OF TSSR REGISTER
: ERDIF ERRNO,SFIERR,SFIMSG ;FATAL ERROR TSSR WAS NOT OK
:
: TRAP C#ERDIF
: .WORD 401
: .WORD SFIERR
: .WORD SFIMSG
:
: 20#: CLR R4 ;SET RAM ADDRESS AT ZERO
: JSP PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS

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Line	Address	Offset	Value	Instruction	Comment	Trap	Value
442	025060	105065	000000	CLRB	TSDB(R5)		
443	025064						
444	025064			BGNSEG			
	025064	104404					
445							
446	025066	110402		MOVB	R4,R2		
447	025070	004737	016336	JSR	PC,CHKTSSR		
448	025074	010465	000000	MOV	R4,TSDB(R5)		
449	025100	004737	016336	JSR	PC,CHKTSSR		
450	025104	110265	000000	MOVB	R2,TSDB(R5)		
451	025110	004737	016336	JSR	PC,CHKTSSR		
452	025114	016501	000000	MOV	TSBA(R5),R1		
453	025120	120102		CMPB	R1,R2		
454	025122	001404		BEQ	30\$		
458	025124			ERRHRD	ERRNO,TSBAM2,EXFREC		
	025124	104456				TRAP	C\$ERHRD
	025126	000622				.WORD	402
	025130	026160				.WORD	TSBAM2
	025132	015474				.WORD	EXPREC
459	025134						
460	025134			ENDSEG			
	025134						
	025134	104405				TRAP	C\$ESEG
461							
462	025136	005204		INC	R4		
463	025140	020427	010000	CMP	R4,#10000		
464	025144	001347		BNE	25\$		
465	025146			ESCAPE	SUB		
	025146	104410				TRAP	C\$ESCAPE
	025150	000102				.WORD	L10044-
466							
467	025152	005002		CLR	R2		
468	025154	005304		DEC	R4		
469	025156	110402		MOVB	R4,R2		
470	025160	004737	016336	JSR	PC,CHKTSSR		
471	025164	010465	000000	MOV	R4,TSDB(R5)		
472	025170	004737	016336	JSR	PC,CHKTSSR		
473	025174	016501	000000	MOV	TSBA(R5),R1		
474	025200	120102		CMPB	R1,R2		
475	025202	001404		BEQ	45\$		
479	025204			EFRRD	ERRNO,TSBAM2,EXPREC		
	025204	104456				TRAP	C\$ERHRD
	025206	000623				.WORD	403
	025210	026160				.WORD	TSBAM2
	025212	015474				.WORD	EXPREC
480	025214						
	025214	104406				TRAP	C\$CLP1
481	025216	116501	000001	MOVB	TSBA(R5),R1		
482	025222	010402		MOV	R4,R2		
483	025224	000302		SWAB	R2		
484	025226	060402		ADD	R4,R2		
485	025230	120102		CMPB	R1,R2		
486	025232	001404		BEQ	50\$		
490	025234			ERRHRD	ERRNO,M2901,EXPREC		
	025234	104456				TRAP	C\$ERHRD
	025236	000624				.WORD	404
	025240	026070				.WORD	M2901

	025242	015474			.WORD	EXPREC
491	025244		50\$:	CKLOOP	;SCOPE LOOP	
	025244	104406				TRAP C#CLP1
492	025246	005304		DEC R4	;DROP DATA COUNTER (PATTERN)	
493	025250	002342		BGE 40\$	;NOT AT LOC. ZERO YET	
494						
495	025252			ENDSUB	;\\\\\\\\\\\\\\\\\\\\\\ END SUBTEST \\\\\\\\\\\\\\\\\\\\\\	
	025252				L10044:	
	025252	104403				TRAP C#ESUB
496						
497						
498	025254			BGNSUB	;////////// BEGIN SUBTEST //////////	
	025254				T4.2:	
	025254	104402				TRAP C#BSUB
499			:	TEST 4. SUBTEST 2		
500			:			
501			:			
502			:	THIS SUBTEST WRITES RAM WITH ALL ZEROS		
503			:	THEN WALKS AN ALL ONES WORD DOWN THROUGH MEMORY		
504			:			
505	025256	004737	015774	JSR PC,SOFINIT	;DO INITIALIZE ON CONTROLLER	
506	025262	103405		BCS 20\$	;BR IF INIT WAS OK	
510	025264	010001		MOV R0,R1	;CONTENTS OF TSSR REGISTER	
511	025266			ERRDF ERRNO,SFIERR,SFIMSG	;FATAL ERROR TSSR WAS NOT OK	
	025266	104455				TRAP C#ERDF
	025270	000625				.WORD 405
	025272	003650				.WORD SFIERR
	025274	012034				.WORD SFIMSG
512	025276	005002	20\$:	CLR R2	;TEST DATA = 0	
513	025300	005004		CLR R4	;STARTING RAM ADDRESS = 0	
514	C25302	004737	016336	JSR PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
515	025306	105065	000000	CLRB TSDB(R5)	;SET INTO MAINTENANCE MODE	
516	025312		25\$:			
517	025312			BGNSEG	;>>>>>>>> BEGIN SEGMENT >>>>>>>>	
	025312	104404				TRAP C#BSEG
518						
519	025314	004737	016336	JSR PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
520	025320	010465	000000	MOV R4,TSDB(R5)	;LOAD ADDRESS INTO TSDB	
521	025324	004737	016336	JSR PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
522	025330	110265	000000	MOVB R2,TSDB(R5)	;LOADS DATA INTO RAM LOCATION	
523	025334	004737	016336	JSR PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
524	025340	016501	000000	MOV TSBA(R5),R1	;READS WRAP DATA	
525	025344	120102		CMPB R1,R2	;DOES WRITTEN(WRAP) = READ ?	
526	025346	001404		BEQ 30\$	;BR IF OK, THEY ARE EQUAL	
530	C25350			ERRHRD ERRNO,TSBAM2,EXPREC	;DATA NOT WRAPPED CORRECTLY	
	025350	104456				TRAP C#ERHRD
	025352	000626				.WORD 406
	025354	026160				.WORD TSBAM2
	025356	015474				.WORD EXPREC
531	025360		30\$:			
532	025360			ENDSEG	<<<<<<<<<<<< END SEGMENT <<<<<<<<<<<<	
	025360				10000\$:	
	025360	104405				TRAP C#ESEG
533						
534	025362	005204		INC R4	;NEXT ADDRESS	
535	025364	020427	010000	CMP R4 #10000	;END OF RAM MEMORY CHECK	
536	025370	001350		BNE 25\$	;BR, MORE RAM TO GO	

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537
538 025372 005304
539 025374 005002
540 025376 004737 016336
541 025402 010465 000000
542 025406 004737 016336
543 025412 016501 000000
544 025416 005002
545 025420 120102
546 025422 001404
550 025424
    025424 104456
    025426 000627
    025430 026242
    025432 015474
551 025434 012702 000377
552 025440 010465 000000
553 025444 004737 016336
554 025450 110265 000000
555 025454 004737 016336
556 025460 016501 000000
557 025464 120102
558 025466 001404
562 025470
    025470 104456
    025472 000630
    025474 026150
    025476 015474
563 025500
    025500 104406
564 025502 004737 016336
565 025506 010465 000000
566 025512 004737 016336
567 025516 116501 000001
568 025522 010403
569 025524 000303
570 025526 060403
571 025530 120103
572 025532 001404
576 025534
    025534 104456
    025536 000631
    025540 026070
    025542 015474
577 025544
    025544 104406
578 025546 005304
579 025550 002312
580
581 025552
    025552
    025552 104403
582
583
584 025554
    025554
    025554 104402

354: DEC R4
    CLR R2
404: JSR PC,CHKTSSR
    MOV R4,TSDB(R5)
    JSR PC,CHKTSSR
    MOV TSBA(R5),R1
    CLR R2
    CMPB R1,R2
    BEQ 434
    ERRHRD ERRNO,TSBAM3,EXPREC

;SET BACK TO 7777
;SET TO ALL ZERO
;WAIT FOR READY (SSR) TO SET
;LOAD UP THE ADDRESS FOR RAM
;WAIT FOR READY (SSR) TO SET
;READ THE RAM CONTENTS BACK
;LOOKING FOR 000000 (EXPECTED)
;BOTH SHOULD BE 00000000 BINARY
;BR, IF DATA IS GOOD
;CHARACTERISTICS DATA NOT CORRECT
    TRAP C$ERHRD
    .WORD 407
    .WORD TSBAM3
    .WORD EXPREC

434: MOV #000377,R2
    MOV R4,TSDB(R5)
    JSR PC,CHKTSSR
    MOVB R2,TSDB(R5)
    JSR PC,CHKTSSR
    MOV TSBA(R5),R1
    CMPB R1,R2
    BEQ 454
    ERRHRD ERRNO,TSBAM2,EXPREC

;SET ALL ONES WORD
;LOAD UP RAM ADDRESS POINTER
;WAIT FOR READY, NON-AMBIGUOUS
;WRITE DATA INTO RAM
;WAIT FOR READY, NON-AMBIGUOUS
;READ RAM CONTENTS BACK
;CHECK WITH DATA WRITTEN
;BR IF OK, DATA IN = DATA OUT
;WRITTEN DATA NOT = TO READ
    TRAP C$ERHRD
    .WORD 408
    .WORD TSBAM2
    .WORD EXPREC

454: CKLOOP

;SCOPE LOOP
    TRAP C$CLP1

    JSR PC,CHKTSSR
    MOV R4,TSDB(R5)
    JSR PC,CHKTSSR
    MOVB TSBAH(R5),R1
    MOV R4,R3
    SWAB R3
    ADD R4,R3
    CMPB R1,R3
    BEQ 504
    ERRHRD ERRNO,M2901,EXPREC

;WAIT FOR READY, NON-AMBIGUOUS
;WORD WRITE TO SET UP ADDRESS
;WAIT FOR READY, NON-AMBIGUOUS
;HIGH BYTE READ OF TSBA
;DATA PATTERN WRITTEN
;HIGH TO LOW
;TOTAL OF BYTES IN LOW BYTE
;SUM OF BYTES WRITTEN TO TSDB = TSBAH
;BR IF OK, THEY SHOULD BE
;2901 PROBLEM ADDER
    TRAP C$ERHRD
    .WORD 409
    .WORD M2901
    .WORD EXPREC

504: CKLOOP

;SCOPE LOOP
    TRAP C$CLP1

    DEC R4
    BGE 404

;DROP RAM ADDRESS POINTER
;NOT AT LOC. ZERO YET

ENDSUB

;////////// END SUBTEST //////////
    L10045:
    TRAP C$ESUB

BGNSUB

;////////// BEGIN SUBTEST //////////
    T4.3:
    TRAP C$BSUB

```

585		:	TEST 4, SUBTEST 3	
586		:		
587		:		
588		:	THIS SUBTEST WRITES RAM WITH ALL ONES	
589		:	THEN WALKS A ZERO WORD DOWN THROUGH MEMORY	
590		:		
591	025556	004737	015774	JSR PC,SOFINIT ;DO INITIALIZE ON CONTROLLER
592	025562	103405		BCS 20\$;BR IF INIT WAS OK
596	025564	010001		MOV R0,R1 ;CONTENTS OF TSSR REGISTER
597	025566			ERRDF ERRNO,SFIERR,SFIMSG ;FATAL ERROR TSSR WAS NOT OK
	025566	104455		
	025570	000632		
	025572	003650		
	025574	012034		
598	025576	012702	177777	20\$: MOV #177777,R2 ;SET DATA AT ALL ONES
599	025602	005004		CLR R4 ;SET RAM ADDRESS AT ZERO
600	025604	004737	016336	JSR PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
601	025610	105065	000000	CLRB TSDB(R5) ;SET INTO MAINTENANCE MODE
602	025614			
603	025614			25\$: BGNSEG ;>>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>>
	025614	104404		
604				
605	025616	004737	016336	JSR PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
606	025622	010465	000000	MOV R4,TSDB(R5) ;LOAD ADDRESS INTO TSDB
607	025626	004737	016336	JSR PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
608	025632	110265	000000	MOVB R2,TSDB(R5) ;LOADS DATA INTO RAM LOCATION
609	025636	004737	016336	JSR PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
610	025642	016501	000000	MOV TSBA(R5),R1 ;READS WRAP DATA
611	025646	120102		CMPB R1,R2 ;DOES WRITTEN(WRAP) = READ ?
612	025650	001404		BEQ 30\$;OR IF OK, THEY ARE EQUAL
616	025652			ERRHRD ERRNO,TSBAM2,EXPREC ;DATA NOT WRAPPED CORRECTLY
	025652	104456		
	025654	000633		
	025656	026160		
	025660	015474		
617	025662			
618	025662			30\$: ENDSEG ;<<<<<<<<<<<<<<< END SEGMENT <<<<<<<<<<<<<<<<<<<<<
	025662			
	025662	104405		10000\$: TRAP C\$ESEG
61^				
620	025664	005204		INC R4 ;NEXT ADDRESS
621	025666	020427	010000	CMP R4,#10000 ;END OF RAM MEMORY CHECK
622	025672	001350		BNE 25\$;BR, MORE RAM TO GO
623				
624	025674			ESCAPE SUB ;NO CHECK IF WRITTEN INCORRECTLY
	025674	104410		
	025676	000152		
625	025700	005304		
626	025702	004737	016336	35\$: DEC R4 ;SET BACK TO 7777
627	025706	012702	000377	40\$: JSR PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
628	025712	005001		MOV #000377,R2 ;SET UP EXPECTED DATA REGISTER
629	025714	010465	000000	CLR R1 ;CLEAN OUT REGISTER
630	025720	004737	016336	MOV R4,TSDB(R5) ;SELECT ADDRESS IN RAM
631	025724	016501	000000	JSR PC,CHKTSSR ;WAIT FOR READY (SSR)
632	025730	120102		MOV TSBA(R5),R1 ;PICK UP RAM CONTENTS
633	025732	001404		CMPB R1,R2 ;IS MEMORY STILL ALL ONES
637	025734			BEQ 43\$;BR, IF OK (ALL ONES)
				ERRHRD ERRNO,TSBAM3,EXPREC ;MEMORY CHANGED AFTER ALL ONES WRITE

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TEST 4: RAM TEST

SEQ 0116

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025734 104456                                TRAP      C$ERRRD
025736 000634                                .WORD      412
025740 026242                                .WORD      TSBAM3
025742 015474                                .WORD      EXPREC
638 025744 005002          43$: CLR      R2                ;SET UP NEW EXPECTED
639 025746 010465 000000      MOV      R4,TSDB(R5)          ;LOAD UP RAM ADDRESS POINTER
640 025752 004737 016336      JSR      PC,CHKTSSR           ;WAIT FOR READY, NON-AMBIGUOUS
641 025756 110265 000000      MOVB     R2,TSDB(R5)          ;WRITE DATA INTO RAM
642 025762 004737 016336      JSR      PC,CHKTSSR           ;WAIT FOR READY, NON-AMBIGUOUS
643 025766 016501 000000      MOV      TSBA(R5),R1        ;READ RAM CONTENTS BACK
644 025772 120102          CMPB     R1,R2                ;CHECK WITH DATA WRITTEN
645 025774 001404          BEQ      45$                    ;BR IF OK, DATA IN = DATA OUT
649 025776          ERRHRD   ERRNO,TSBAM2,EXPREC           ;WRITTEN DATA NOT = TO READ
                                TRAP      C$ERRRD
                                .WORD      413
                                .WORD      TSBAM2
                                .WORD      EXPREC
025776 104456
026000 000635
026002 026160
026004 015474
650 026006          45$:  CKLOOP          ;SCOPE LOOP
                                TRAP      C$CLP1
026006 104406                                ;WAIT FOR READY, NON-AMBIGUOUS
651 026010 004737 016336      JSR      PC,CHKTSSR           ;HIGH BYTE READ OF TSBA
652 026014 116501 000001      MOVB     TSBAH(R5),R1        ;DATA PATTERN WRITTEN
653 026020 010203      MOV      R2,R3                ;HIGH TO LOW
654 026022 000303      SWAB     R3                    ;TOTAL OF BYTES IN LOW BYTE
655 026024 060203      ADD      R2,R3                    ;SUM OF BYTES WRITTEN TO TSDB = TSBAH
656 026026 120103      CMPB     R1,R3                    ;BR IF OK, THEY SHOULD BE
657 026030 001404      BEQ      50$                    ;2901 PROBLEM ADDER
661 026032          ERRHRD   ERRNO,M2901,EXPREC
                                TRAP      C$ERRRD
                                .WORD      414
                                .WORD      M2901
                                .WORD      EXPREC
026032 104456
026034 000636
026036 026070
026040 015474
662 026042          50$:  CKLOOP          ;SCOPE LOOP
                                TRAP      C$CLP1
026042 104406                                ;DROP RAM ADDRESS POINTER
663 026044 005304      DEC      R4                    ;NOT AT LOC. ZERO YET
664 026046 001315      BNE      40$
665
666 026050          ENDSUB          ;////////////////// END SUBTEST ////////////////////
                                L10046:
026050
026050 104403                                TRAP      C$ESUB
667
668 026052 004737 016456      JSR      PC,TSTLOOP          ;DO WE NEED TO ITERATE TEST ?
669 026056 103002      BCC      63$                    ;BRANCH IF NOT
670 026060 000137 025032      JMP      T4LOOP          ;EXECUTE AGAIN
671 026064          63$:  EXIT      TST                ;ALL DONE THIS TEST
                                TRAP      C$EXIT
                                .WORD      L10043-
026064 104432
026066 000256
672
673          ;*
674          ;LOCAL TEXT MESSAGES FOR TEST
675          ;-
676 026070          040      124      123  M2901: .ASCIZ ' TSBA High Byte Not Sum of Last TSDB Write (2901 Error)'
677 026160          040      127      162  TSBAM2: .ASCIZ ' Write to TSDB Not Equal to Read of TSBA Low Byte'
678 026242          127      162      151  TSBAM3: .ASCIZ 'Write To RAM Location Modified Another Location'
679 026322          122      101      115  TST4ID: .ASCIZ 'RAM Verification'
680
681 026344          .EVEN
026344          ENDTST
                                L10043:

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TEST 4: RAM TEST

SEQ 0117

026344 104401

TRAP C\$ETST

.SBTTL TEST 5: SECOND INITIALIZATION TEST

THIS TEST VERIFIES THE SAME ELEMENTS AS DID INITIALIZATION TEST #1 AND ALSO CHECKS THAT CERTAIN PARTS OF RAM IS CLEARED TO ZERO AND THAT 2901 REGISTERS 10 AND 11 ARE ALSO CLEARED TO ZERO. THIS IS A CONFIDENCE CHECK OF A PART OF THE SELF-TEST SEQUENCE (I.E., THAT IT IS REALLY BEING EXECUTED). FOR EACH OF TWO SUBTESTS (ONE FOR INITIALIZING VIA A BUS INIT, THE OTHER FOR INITIALIZING BY WRITING INTO THE TSSR), THE FOLLOWING SEQUENCE IS PERFORMED:

1. EACH RAM LOCATION AND 2901 REGISTERS 10 AND 11 ARE SET TO ALL 1'S BY USING WRITES INTO THE TSDB REGISTER (LOW BYTE AND MAINTENANCE MODE WORD WRITES).
2. THE CONTROLLER IS INITIALIZED AND THE VARIOUS CHECKS ON THE TSSR DESCRIBED IN INITIALIZATION TEST #1 ARE PERFORMED.
3. #1'S (377 OCTAL) ARE WRITTEN INTO THE LOW BYTE OF TSDB, WHICH SHOULD CAUSE RAM LOCATION 0 TO BE WRITTEN TO ALL 1'S SINCE 2901 REGISTERS 10 AND 11, SPECIFYING THE RAM ADDRESS, SHOULD BE 0. RAM LOCATION 0 IS VERIFIED BY WRITING A WORD OF ZEROS INTO THE TSDB. THE RESULTING LOW BYTE OF TSBA SHOULD CONTAIN ALL 1'S.
4. THE ENTIRE RAM IS SCANNED. LOCATION 0 SHOULD CONTAIN ALL 1'S AND THE REMAINING LOCATIONS, EXCEPT FOR THE MESSAGE BUFFER IMAGE AREA, SHOULD CONTAIN 0. DISCREPANCIES ARE REPORTED. AN ERROR AT THIS POINT IS MOST LIKELY DUE TO A ROM, PIPELINE OR SEQUENCER PROBLEM OR A TIMING PROBLEM.

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720	026346					BGNTST	
	026346						T5::
725	026346	012700	027320			MOV #TST5ID,R0	;ASCII MESSAGE TO IDENTIFY TEST
726	026352	004737	016510			JSR PC,TSTSETUP	;DO INITIAL TEST SETUP
727	026356	012737	000024	002214		MOV #20.,LOOPCNT	;PERFORM 20 ITERATIONS
728	026364				T5LOOP:		
729	026364	005037	002220			CLR FATFLG	;CLEAR THE FATAL ERROR FLAG
730							
731	026370					BGNSUB	;////////// BEGIN SUBTEST //////////
	026370						T5.1:
	026370	104402					TRAP C\$BSUB
732							
733	026372	004737	015774			JSR PC,SOFINIT	;DO A SOFT TO START
734	026376	103404				BCS 104	;BRANCH IF O.K.
738	026400					ERRDF ERRNO,SFIERR,SFIMSG	;REPORT ERROR AND DROP DRIVE
	026400	104455					TRAP C\$ERDF
	026402	000765					.WORD 501
	026404	003650					.WORD SFIERR

PC	Address	Hex	Label	Instruction	Comment	Trap	Reset
739	026406	012034					SFIMSG
740	026410	012702	100:	MOV # 1,R2	; ALL ONE DATA PATTERN		
741	026414	005004		CLR R4	; STARTING RAM ADDRESS		
742	026416	004737		JSR PC,CHKTSSR	; WAIT FOR READY, NON-AMBIGUOUS		
743	026422	105055	150:	CLRB TSOB(R5)	; SET MAINTENANCE MODE		
744	026426	004737		JSR PC,CHKTSSR	; WAIT FOR READY, NON-AMBIGUOUS		
745	026432	010465		MOV R4,TSOB(R5)	; SET THE NEXT RAM ADDRESS		
746	026436	004737		JSR PC,CHKTSSR	; WAIT FOR READY, NON-AMBIGUOUS		
747	026442	110265		MOVB R2,TSOB(R5)	; LOAD TEST DATA		
748	026446	005204		INC R4	; NEXT ADDRESS TO TEST		
749	026450	020427	007777	CMP R4,#7777	; COMPARE TO LAST ADDRESS		
750	026454	003762		BLE 150	; BRANCH TILL ALL DATA WRITTEN		
751	026456	104433		BRESET	; ISSUE A BUS RESET	TRAP	C0RESET
752	026460	004737		JSR PC,CHKTSSR	; WAIT FOR READY, NON-AMBIGUOUS		
753	026464	016501		MOV TSSR(R5),R1	; GET THE CONTENTS OF TSSR		
754	026470	010102		MOV R1,R2	; CONTENTS OF TSSR		
755	026472	042702	176277	BIC #1C<HIADDR!OFL>,R2	; THESE BITS MAY BE SET		
756	026476	052702	002200	BIS #SSR!MBA,R2	; READY AND NEW DATA SHOULD BE SET		
757	026502	020102		CMP R1,R2	; COMPARE EXPECTED TO RECEIVED		
758	026504	001406		BEQ 200	; BRANCH IF COMPARE		
759	026506	104455		ERRDF	; REPORT A FATAL ERROR	TRAP	C0ERDF
760	026510	000766				.WORD	502
761	026512	003703				.WORD	SFHERR
762	026514	012102				.WORD	SFFMSG
763	026516	005237	200:	INC FATFLG	; SET FATAL ERROR FLAG		
764	026522	104406		CKLOOP	; LOOP ON ERROR IF FLAG SET		
765	026524	104410		ESCAPE	; EXIT IF FATAL ERROR DETECTED	TRAP	C0CLP1
766	026526	000170				TRAP	C0ESCAPE
767	026530	004737		JSR PC,CHKTSSR	; WAIT FOR SSR TO SET	.WORD	L10050-
768	026534	105065		CLRB TSOB(R5)	; PUT BACK INTO MAINTENANCE MODE		
769	026540	004737		JSR PC,CHKTSSR	; WAIT FOR READY, NON-AMBIGUOUS		
770	026544	005065		CLR TSOB(R5)	; SET ADDRESS BACK TO 0000		
771	026550	012702		MOV #377,R2			
772	026554	004737		JSR PC,CHKTSSR	; WAIT FOR READY, NON-AMBIGUOUS		
773	026560	110265		MOVB R2,TSOB(R5)	; SHOULD POINT TO RAM 0		
774	026564	004737		JSR PC,CHKTSSR	; WAIT FOR READY, NON-AMBIGUOUS		
775	026570	005065		CLR TSOB(R5)	; SELECT LOCATION 0		
776	026574	004737		JSR PC,CHKTSSR	; WAIT FOR READY, NON-AMBIGUOUS		
777	026600	116501		MOVB TSBA(R5),R1	; READ RAM LOCATION SPECIFIED		
778	026604	120102		CMPB R1,R2	; LOCATION SHOULD BE 377 OCTAL		
779	026606	001406		BEQ 250	; BR IF OK		
780	026610	104455		ERRDF	; WASN'T POINTING TO CORRECT LOC.	TRAP	C0ERDF
781	026612	000766				.WORD	502
782	026614	027406				.WORD	TSADDR
783	026616	015474				.WORD	EXPREC
784	026620	005237	250:	INC FATFLG	; SET THE FATAL ERROR FLAG		
785	026624	104406		CKLOOP	; SCOPE LOOP		
786	026626	104410		ESCAPE	; NO MORE CHECKS IF FATAL ERROR	TRAP	C0CLP1
787	026630	000066				TRAP	C0ESCAPE
788	026632	012704		MOV #310,R4	; START WITH LOC 310	.WORD	L10050-

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TEST 5: SECOND INITIALIZATION TEST

SEQ 0119

783	026636	005002		CLR	R2	;MEMORY EXPECTED SHOULD BE 000000	
784	026640	004737	016336	JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
785	026644	010465	000000	301: MOV	R4,TSDB(R5)	;SELECT LOCATION SPECIFIED	
786	026650	004737	016336	JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
787	026654	116501	000000	MOVB	TSBA(R5),R1	;READ LOC CONTENTS	
788	026660	120102		CMPB	R1,R2	;CHECK MEMORY FOR 000000	
789	026662	001406		BEQ	401	;BRANCH IF DATA OKAY	
790	026664			ERRDF	ERRNO,TSMEM,SFFMSG	;MEMORY NOT ZERO AFTER INIT.	
	026664	104455				TRAP	C1ERRDF
	026666	000766				.WORD	502
	026670	027350				.WORD	TSMEM
	026672	012102				.WORD	SFFMSG
791	026674	005237	002220	401: INC	FA'FLG	;SET THE FATAL ERROR FLAG	
792	026700			CKLOOP			
	026700	104406					
793	026702			ESCAPE	SUB	;EXIT ON FATAL ERROR	
	026702	104410				TRAP	C1CLP1
	026704	000012				TRAP	C1ESCAPE
	026706	005204				.WORD	L10050-
794	026706	005204		INC	R4	;LOOK AT NEXT RAM LOC.	
795	026710	020427	000400	CMP	R4,#400	;AT TOP OF RAM ADDRESS SPACE	
796	026714	001353		BNE	301	;BRANCH TILL ALL MEMORY TESTED	
797							
798	026716			ENDSUB		;////////// END SUBTEST //////////	
	026716					L10050:	
	026716	104403				TRAP	C1ESUB
799							
800	026720	005737	002220	TST	FATFLG	;IS FATAL ERROR FLAG SET ?	
801	026724	001404		BEQ	501	;BRANCH IF NOT	
802	026726	004737	017202	JSR	PC,CKDROP	;NO LOOP, TRY TO DROP DEVICE	
803	026732	005037	002220	CLR	FATFLG	;CLEAR THE FATAL ERROR FLAG	
804	026736			501: BGNSUB		;////////// BEGIN SUBTEST //////////	
805						TS.2:	
806	026736					TRAP	C1BSUB
	026736						
	026736	104402					
807							
808	026740	004737	015774	JSR	PC,SOFINIT	;DO A SOFT TO START	
809	026744	103404		BCS	101	;BRANCH IF O.K.	
813	026746			ERRDF	ERRNO,SFIERR,SFIMSG	;REPORT ERROR AND DROP DRIVE	
	026746	104455				TRAP	C1ERRDF
	026750	000767				.WORD	503
	026752	003650				.WORD	SFIERR
	026754	012034				.WORD	SFIMSG
814	026756	012702	177777	101: MOV	#-1,R2	;ALL ONE DATA PATTERN	
815	026762	005004		CLR	R4	;STARTING RAM ADDRESS	
816	026764	004737	016336	JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
817	026770	105065	000000	151: CLRB	TSDB(R5)	;SET MAINTENANCE MODE	
818	026774	004737	016336	JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
819	027000	010465	000000	MOV	R4,TSDB(R5)	;SET THE NEXT RAM ADDRESS	
820	027004	004737	016336	JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
821	027010	110265	000000	MOVB	R2,TSDB(R5)	;LOAD TEST DATA	
822	027014	005204		INC	R4	;NEXT ADDRESS TO TEST	
823	027016	020427	007777	CMP	R4,#7777	;COMPARE TO LAST ADDRESS	
824	027022	003762		BLE	151	;BRANCH TILL ALL DATA WRITTEN	
825	027024	005065	000002	CLR	TSSR(R5)	;ISSUE A SOFT RESET	
826	027030	004737	016336	JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
827	027034	016501	000002	MOV	TSSR(R5),R1	;GET THE CONTENTS OF TSSR	

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TEST 5: SECOND INITIALIZATION TEST

SEQ 0120

828	027040	010102		MOV	R1,R2		;CONTENTS OF TSSR		
829	027042	042702	176277	BIC	#C<HIADDR!OFL>,R2		;THESE BITS MAY BE SET		
830	027046	052702	002200	BJS	#SSR!NBA,R2		;READY AND NEW DATA SHOULD BE SET		
831	027052	020102		CMP	R1,R2		;COMPARE EXPECTED TO RECEIVED		
832	027054	001406		BEQ	20:		;BRANCH IF COMPARE		
836	027056			ERRDF	ERRNO,SFHERR,SFFMSG		;REPORT A FATAL ERROR		
	027056	104455						TRAP	C!ERDF
	027060	000770						.WORD	504
	027062	003703						.WORD	SFHERR
	027064	012102						.WORD	SFFMSG
837	027066	005237	002220	INC	FATFLG		;SET FATAL ERROR FLAG		
838	027072			20:	CKLOOP		;LOOP ON ERROR IF FLAG SET		
	027072	104406						TRAP	C!CLP1
839	027074			ESCAPE	SUB		;EXIT IF FATAL ERROR DETECTED		
	027074	104410						TRAP	C!ESCAPE
	027076	000170						.WORD	L10051-
840	027100	004737	016336	JSR	PC,CHKTSSR		;WAIT FOR SSR TO SET		
841	027104	105065	000000	CLRB	TSDB(R5)		;PUT BACK INTO MAINTENANCE MODE		
842	027110	004737	016336	JSR	PC,CHKTSSR		;WAIT FOR READY, NON-AMBIGUOUS		
843	027114	005065	000000	CLR	TSDB(R5)		;SET ADDRESS BACK TO 0000		
844	027120	012702	000377	MOV	#377,R2				
845	027124	004737	016336	JSR	PC,CHKTSSR		;WAIT FOR READY, NON-AMBIGUOUS		
846	027130	110265	000000	MOVB	R2,TSDB(R5)		;SHOULD POINT TO RAM 0		
847	027134	004737	016336	JSR	PC,CHKTSSR		;WAIT FOR READY, NON-AMBIGUOUS		
848	027140	005065	000000	CLR	TSDB(R5)		;SELECT LOCATION 0		
849	027144	004737	016336	JSR	PC,CHKTSSR		;WAIT FOR READY, NON-AMBIGUOUS		
850	027150	116501	000000	MOVB	TSBA(R5),R1		;READ RAM LOCATION SPECIFIED		
851	027154	120102		CMPB	R1,R2		;LOCATION SHOULD BE 377 OCTAL		
852	027156	001406		BEQ	25:		;BR IF OK		
853	027160			ERRDF	ERRNO,TSADDR,EXPREC		;WASN'T POINTING TO CORRECT LOC.		
	027160	104455						TRAP	C!ERDF
	027162	000770						.WORD	504
	027164	027406						.WORD	TSADDR
	027166	015474						.WORD	EXPREC
854	027170	005237	002220	INC	FATFLG		;SET THE FATAL ERROR FLAG		
855	027174			25:	CKLOOP		;SCOPE LOOP		
	027174	104406						TRAP	C!CLP1
856	027176			ESCAPE	SUB		;NO MORE CHECKS IF FATAL ERROR		
	027176	104410						TRAP	C!ESCAPE
	027200	000066						.WORD	L10051 .
857	027202	012704	000310	MOV	#310,R4		;START WITH LOC 310		
858	027206	005002		CLR	R2		;MEMORY EXPECTED SHOULD BE 000000		
859	027210	004737	016336	JSR	PC,CHKTSSR		;WAIT FOR READY, NON-AMBIGUOUS		
860	027214	010465	000000	30:	MOV	R4,TSDB(R5)	;SELECT LOCATION SPECIFIED		
861	027220	004737	016336	JSR	PC,CHKTSSR		;WAIT FOR READY, NON-AMBIGUOUS		
862	027224	116501	000000	MOVB	TSBA(R5),R1		;READ LOC CONTENTS		
863	027230	120102		CMPB	R1,R2		;CHECK MEMORY FOR 000000		
864	027232	001406		BEQ	40:		;BRANCH IF DATA OKAY		
865	027234			ERRDF	ERRNO,TSMEM,SFFMSG		;MEMORY NOT ZERO AFTER INIT.		
	027234	104455						TRAP	C!ERDF
	027236	000770						.WORD	504
	027240	027350						.WORD	TSMEM
	027242	012102						.WORD	SFFMSG
866	027244	005237	002220	INC	FATFLG		;SET THE FATAL ERROR FLAG		
867	027250			40:	CKLOOP				
	027250	104406						TRAP	C!CLP1
868	027252			ESCAPE	SUB		;EXIT ON FATAL ERROR		

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TEST 5: SECOND INITIALIZATION TEST

SEQ 0121

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      027252 104410                                TRAP  C#ESCAPE
      027254 000012                                .WORD  L10051 .
869 027256 005204                                INC    R4
870 027260 020427 000400                        CMP    R4,#400
871 027264 001353                                BNE    30$
872
873 027266                                ENDSUB                                ;//////////////// END SUBTEST //////////////////
      027266                                L10051:
      027266 104403                                TRAP  C#ESUB
874
875 027270 005737 002220                        TST    FATFLG                                ;IS FATAL ERROR FLAG SET ?
876 027274 001402                                BEQ    50$                                ;BRANCH IF NOT
877 027276 004737 017202                        JSR    PC,CKDROP                                ;NO LOOP, TRY TO DROP DEVICE
878 027302 004737 016456 50$:                  JSR    PC,TSTLOOP                                ;SHOULD WE DO ITERATIONS ?
879 027306 103002                                BCC    60$                                ;BRANCH IF NOT
880 027310 000137 026364                        JMP    T5LOOP                                ;LOOP UNTIL COUNT EXPIRED
881 027314                                60$: EXIT    TST                                ;ALL DONE THIS TEST
      027314 104432                                TRAP  C#EXIT
      027316 000132                                .WORD  L10047-.
882
883
884                                ;*
885                                ;LOCAL TEXT MESSAGES FOR TEST
886                                ;*
887 027320 105 170 164 TSTSID: .ASCIZ 'Extended Initialization'
888 027350 111 156 143 TSMEM: .ASCIZ 'Incorrect RAM Data After Init'
889 027406 111 156 143 TSADDR: .ASCIZ 'Incorrect RAM Address After Init'
890                                .EVEN
891 027450                                ENDTST
      027450                                L10047:
      027450 104401                                TRAP  C#ETST
892
893
894                                .SBTTL TEST 6: COMMAND REJECT
895
896
897                                ;
898                                ; THIS TEST VERIFIES THAT ALL COMMANDS OTHER THAN WRITE
899                                ; CHARACTERISTICS ARE REJECTED DUE TO THE NEED BUFFER ADDRESS
900                                ; (NBA) BIT BEING SET IN TSSR, AND THAT THE TSBA AND TSSR
901                                ; REGISTERS ARE LEFT IN THE PROPER STATE AFTER EACH COMMAND IS
902                                ; REJECTED. THIS TEST CHECKS MICROPROCESSOR SEQUENCING, BASIC
903                                ; COMMAND DECODING AND DATA DMA HANDLING. THIS TEST CONTAINS TWO
904                                ; SUBTESTS: SUBTEST 1 SEQUENCES THROUGH ALL COMMAND WORDS (OTHER
905                                ; THAN WRITE CHARACTERISTICS) WITH THE INTERRUPT ENABLE (IE) BIT
906                                ; CLEAR AND VERIFIES THAT AN INTERRUPT IS NOT GENERATED BY THE
907                                ; REJECTED COMMAND; SUBTEST 2 PERFORMS SIMILARLY TO SUBTEST 1 BUT
908                                ; SETS THE IE BIT IN EACH COMMAND WORD AND VERIFIES THAT AN
909                                ; INTERRUPT IS GENERATED WHEN THE COMMAND IS REJECTED. SUBTEST 1
910                                ; SETS UP THE INTERRUPT SERVICE ROUTINE TO FLAG UNEXPECTED
911                                ; INTERRUPTS. THE COMMAND WORD IN THE COMMAND BUFFER IS
912                                ; INITIALIZED TO 100000 (OCTAL) AND THE REMAINING THREE WORDS IN
913                                ; THE COMMAND BUFFER ARE SET TO KNOWN UNIQUE PATTERNS. THEN THE
914                                ; FOLLOWING SEQUENCE IS PERFORMED:
915                                ;
916                                ; 1. INITIALIZE THE CONTROLLER BY WRITING INTO THE TSSR;
917                                ; PROPER INITIAL CONDITIONS ARE VERIFIED.

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TEST 6: COMMAND REJECT

SEQ 0123

976	027524			ERRDF	ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT	
	027524	104455				TRAP	C#ERDF
	027526	001131				.WORD	601
	027530	003650				.WORD	SFIERR
	027532	012034				.WORD	SFIMSG
977	027534	005037	002220	104:	CLR	FATFLG	;CLEAR FATAL ERROR FLAG
978	027540	005037	002222		CLR	INTRECV	;CLEAR INTERRUPT RECEIVED FLAG
979	027544	004737	016336		JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS
980	027550	042714	000200		BIC	#BIT7,(R4)	;DISABLE INTERRUPTS
981	027554	010465	000000		MOV	R4,TSDB(R5)	;SET THE PACKET ADDRESS
982	027560	004737	016250		JSR	PC,WAITF	;WAIT FOR SSR TO SET
983	027564	103407			BCS	154	;BR IF CARRY SET (GOOD RETURN)
984	027566	010001			MOV	R0,R1	;SAVE CONTENTS OF TSSR
988	027570				ERRDF	ERRNO,T6SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET
	027570	104455				TRAP	C#ERDF
	027572	001132				.WORD	602
	027574	030415				.WORD	T6SSR
	027576	012046				.WORD	PKTSSR
989	027600	005237	002220		INC	FATFLG	;SET FATAL ERROR FLAG
990	027604			154:	CKI OOP		;LOOP ON ERROR, IF FLAG SET
	027604	104406				TRAP	C#CLP1
991	027606				ESCAPE	SUB	;BY-PASS SUBTEST IF FATAL ERROR
	027606	104410				TRAP	C#ESCAPE
	027610	000170				.WORD	L10053
992	027612	005737	002222		TST	INTRECV	;DID AN INTERRUPT OCCUR ?
993	027616	001404			BEQ	224	;BRANCH IF NOT
997	027620				ERRHRD	ERRNO,T6INT,PKTSSR	
	027620	104456				TRAP	C#ERRHRD
	027622	001133				.WORD	603
	027624	030473				.WORD	T6INT
	027626	012046				.WORD	PKTSSR
998	027630	012702	102206	224:	MOV	#SC!NBA!SSR!TSREJ,R2	;EXPECTED CONTENTS OF TSSR
999	027634	004737	016336		JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS
1000	027640	016501	000002		MOV	TSSR(R5),R1	;GET THE CONTENTS OF TSSR
1001	027644	032701	000100		BIT	#OFL,R1	;IS OFF-LINE BIT SET ?
1002	027650	001402			BEQ	254	;BRANCH IF NOT OFF-LINE
1003	027652	052702	000100		BIS	#OFL,R2	;SET OFF-LINE IN EXPECTED DATA
1004	027656	020201		254:	CMP	R2,R1	;DOES EXPECTED MATCH RECEIVED ?
1005	027660	001404			BEQ	304	;OKAY IF MATCH
1009	027662				ERRHRD	ERRNO,T6NBA,PKTSSR	;NBA NOT SET TO REJECT
	027662	104456				TRAP	C#ERRHRD
	027664	001134				.WORD	604
	027666	030370				.WORD	T6NBA
	027670	012046				.WORD	PKTSSR
1010	027672			304:	CKLOOP		;LOOP ON ERROR ?
	027672	104406				TRAP	C#CLP1
1011	027674	004737	016336		JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS
1012	027700	016501	000000		MOV	TSBA(R5),R1	;GET TSBA REGISTER CONTENTS
1013	027704	010402			MOV	R4,R2	;START OF THE PACKET
1014	027706	062702	000010		ADD	#10,R2	;EXPECT TSBA TO PACKET + 10
1015	027712	020102			CMP	R1,R2	;COMPARE EXPECTED TO RECEIVED
1016	027714	001404			BEQ	354	;ERROR IF NOT EQUAL
1020	027716				ERRHRD	ERRNO,T6TSBA,EXPREC	;PRINT THE ERROR & EXPD/RECV
	027716	104456				TRAP	C#ERRHRD
	027720	001135				.WORD	605
	027722	030631				.WORD	T6TSBA
	027724	015474				.WORD	EXPREC

1021									
1022									
1023	027726	004737	011114	35:	JSR	PC,CKRAM	;SEE IF DATA IN RAM IS CORRECT		
1024	027732	103404			BCS	40:	;BRANCH IF PACKET IN RAM IS CORRECT		
1028	027734				ERRHRD	ERRNO,PKTRAM,RAMERR	;REPORT THE RAM ERROR(S)		
	027734	104456						TRAP	C#ERHRD
	027736	001136						.WORD	606
	027740	004743						.WORD	PKTRAM
	027742	015510						.WORD	RAMERR
1029	027744			40:	ENDSEG		;***** END SEGMENT *****		
	027744						10000:		
	027744	104405						TRAP	C#ESEG
1030	027746	011300			MOV	(R3),R0	;PACKET COMMAND WORD		
1031	027750	042700	177740		BIC	#177740,R0	;GET BITS 0-4		
1032	027754	020027	000004		CMP	R0,#4	;DON'T TEST WRITE CHARACTERISTICS		
1033	027760	001002			BNE	45:	;BRANCH IF OK		
1034	027762	062703	000002		ADD	#2,R3	;GET NEXT WORD FROM DATA TABLE		
1035	027766	020327	003060	45:	CMP	R3,#TBLEND	;REACHED END OF TABLE ?		
1036	027772	103002			BHIS	50:	;BRANCH IF END OF TABLE		
1037	027774	000137	027510		JMP	5:	;CONTINUE TEST WITH NEW DATA		
1038									
1039	030000			50:	ENDSUB		;////////// END SUBTEST //////////		
	030000						L10053:		
	030000	104403						TRAP	C#ESUB
1040									
1041	030002	005737	002220		TST	FATFLG	;ANY FATAL ERRORS ?		
1042	030006	001402			BEQ	60:	;BRANCH IF NOT		
1043	030010	004737	017202		JSR	PC,CKDROP	;TRY TO DROP THE UNIT		
1044									
1045	030014			60:	BGNSUB		;////////// BEGIN SUBTEST //////////		
	030014						T6.2:		
	030014	104402						TRAP	C#BSUB
1046									
1047	030016				SETPRI	#PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS		
	030016	012700	000000					MOV	#PRI00,R0
	030022	104441						TRAP	C#SPRI
1048	030024	012704	030360		MOV	#T6PACKET,R4	;GET THE ADDRESS OF COMMAND PACKET		
1049	030030	012703	002750		MOV	#TSTBLK,R3	;START OF TEST DATA		
1050	030034	012314		5:	MOV	(R3)+,(R4)	;PLACE NEXT DATA WORD IN PACKET		
1051	030036				BGNSEG		;***** BEGIN SEGMENT *****		
	030036	104404						TRAP	C#BSEG
1052	030040	004737	015774		JSR	PC,SOFINIT	;DO SOFT INIT OF CONTROLLER		
1053	030044	103405			BCS	10:	;BR IF SOFT INIT = OK		
1057	030046	010001			MOV	R0,R1	;SAVE CONTENTS OF TSSR		
1058	030050				ERRDF	ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT		
	030050	104455						TRAP	C#ERDF
	030052	001137						.WORD	607
	030054	003650						.WORD	SFIERR
	030056	012034						.WORD	SFIMSG
1059	030060	005037	002220	10:	CLR	FATFLG	;CLEAR FATAL ERROR FLAG		
1060	030064	005037	002222		CLR	INTRECV	;CLEAR INTERRUPT RECEIVED FLAG		
1061	030070	004737	016336		JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS		
1062	030074	052714	000200		BIS	#BIT7,(R4)	;ENABLE INTERRUPTS		
1063	030100	010465	000000		MOV	R4,TSD8(R5)	;SET THE PACKET ADDRESS		
1064	030104	004737	016250		JSR	PC,WAITF	;WAIT FOR SSR TO SET		
1065	030110	103407			BCS	15:	;BR IF CARRY SET (GOOD RETURN)		
1066	030112	010							

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TEST 6: COMMAND REJECT

SEQ 0125

1070	030114			ERRDF	ERRNO,T6SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET		
	030114	104455					TRAP	C#ERDF
	030116	001140					.WORD	608
	030120	030415					.WORD	T6SSR
	030122	012046					.WORD	PKTSSR
1071	030124	005237	002220	154:	INC	FATFLG	;SET FATAL ERROR FLAG	
1072	030130				CKLOOP		;LOOP ON ERROR, IF FLAG SET	
	030130	104406					TRAP	C#CLP1
1073	030132				ESCAPE	SUB	;BY-PASS SUBTEST IF FATAL ERROR	
	030132	104410					TRAP	C#ESCAPE
	030134	000170					.WORD	L10054-
1074	030136	005737	002222		TST	INTRECV	;DID AN INTERRUPT OCCUR ?	
1075	030142	001004			BNE	224	;BRANCH IF YES	
1079	030144				ERRMRD	ERRNO,T6NINT,PKTSSR	;REPORT ERROR IF NO INTERRUPT	
	030144	104456					TRAP	C#ERMRD
	030146	001141					.WORD	609
	030150	030551					.WORD	T6NINT
	030152	012046					.WORD	PKTSSR
1080	030154	012702	102206	224:	MOV	#SC!NBA!SSR!TSREJ,R2	;EXPECTED CONTENTS OF TSSR	
1081	030160	004737	016336		JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
1082	030164	016501	000002		MOV	TSSR(R5),R1	;GET THE CONTENTS OF TSSR	
1083	030170	032701	000100		BIT	#OFL,R1	;IS OFF-LINE BIT SET ?	
1084	030174	001402			BEQ	254	;BRANCH IF NOT OFF-LINE	
1085	030176	052702	000100		BIS	#OFL,R2	;SET OFF-LINE IN EXPECTED DATA	
1086	030202	020201		254:	CHP	R2,R1	;DOES EXPECTED MATCH RECEIVED ?	
1087	030204	001404			BEQ	304	;OKAY IF MATCH	
1091	030206				ERRMRD	ERRNO,T6NBA,PKTSSR	;NBA NOT SET TO REJECT	
	030206	104456					TRAP	C#ERMRD
	030210	001142					.WORD	610
	030212	030370					.WORD	T6NBA
	030214	012046					.WORD	PKTSSR
1092	030216			304:	CKLOOP		;LOOP ON ERROR ?	
	030216	104406					TRAP	C#CLP1
1093	030220	004737	016336		JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
1094	030224	016501	000000		MOV	TSBA(R5),R1	;GET TSBA REGISTER CONTENTS	
1095	030230	010402			MOV	R4,R2	;START OF THE PACKET	
1096	030232	062702	000010		ADD	#10,R2	;EXPECT TSBA TO PACKET + 10	
1097	030236	020102			CHP	R1,R2	;COMPARE EXPECTED TO RECEIVED	
1098	030240	001404			BEQ	354	;ERROR IF NOT EQUAL	
1102	030242				ERRMRD	ERRNO,T6TSBA,EXPREC	;PRINT THE ERROR & EXPD/RECV	
	030242	104456					TRAP	C#ERMRD
	030244	001143					.WORD	611
	030246	030631					.WORD	T6TSBA
	030250	015474					.WORD	EXPREC
1103								
1104								
1105	030252	004737	011114	354:	JSR	PC,CKRAM	;SEE IF DATA IN RAM IS CORRECT	
1106	030256	103404			BCS	404	;BRANCH IF PACKET IN RAM IS CORRECT	
1110	030260				ERRMRD	ERRNO,PKTRAM,RAMERR	;REPORT THE RAM ERROR(S)	
	030260	104456					TRAP	C#EPHRD
	030262	001144					.WORD	612
	030264	004743					.WORD	PKTRAM
	030266	015510					.WORD	RAMERR
1111	030270			404:	ENDSEG		;***** END SEGMENT *****	
	030270						100004:	
	030270	104405					TRAP	C#ESEG
1112	030272	011300			MOV	(R3),R0	;NEXT PACKET COMMAND WORD	

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TEST 6: COMMAND REJECT

SEQ 0126

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1113 030274 042700 177740      BIC      #177740,R0      ;GET BITS 0-4
1114 030300 020027 000004      CMP      R0,#4        ;DON'T TEST WRITE CHARACTERISTICS
1115 030304 001002              BNE      45$          ;BRANCH IF NOT WRITE CHARACTERISTICS
1116 030306 062703 000002      ADD      #2,R3        ;BY-PASS WRITE CHARACTERISTICS
1117 030312 020327 003060      45$:    CMP      R3,#TBLEND    ;HAVE WE COMPLETED DATA TABLE ?
1118 030316 103002              BHS      50$          ;BRANCH IF ALL TESTED
1119 030320 000137 030034      JMP      5$          ;TEST WITH NEXT DATA
1120
1121 030324              50$:    ENDSUB              ;////////// END SUBTEST ////////////
                                           L10054:
                                           TRAP      C$ESUB
1122 030326 005737 002220      TST      FATFLG        ;ANY FATAL ERRORS ?
1123 030332 001402              BEQ      60$          ;BRANCH IF NOT
1124 030334 004737 017202      JSR      PC,CKDROP      ;TRY TO DROP THE UNIT
1125 030340 004737 016456      60$:    JSR      PC,TSTLOOP    ;SHOULD WE DO ITERATIONS ?
1126 030344 103002              BCC      62$          ;BRANCH IF NOT
1127 030346 000137 027470      JMP      T6LOOP      ;LOOP UNTIL COUNT EXPIRED
1128 030352              62$:    EXIT      TST          ;ALL DONE THIS TEST
                                           TRAP      C$EXIT
                                           .WORD     L10052-.
1129
1130
1131      ;*
1132      ;LOCAL STORAGE FOR THIS TEST
1133      ;*
1135 030356              .BLKB     10-<.-TSV2&7>
1137 030360      16PACKET:      .WORD     0              ;COMMAND PACKET FOR TEST
1138 030360 000000              .WORD     052525         ;WILL CONTAIN VARIABLE COMMANDS
1139 030362 052525              .WORD     125252
1140 030364 125252              .WORD     052525
1141 030366 052525
1142
1143
1144      ;*
1145      ;LOCAL TEXT MESSAGES FOR TEST
1146      ;*
1147
1148 030370      103      157      155 T6NBA: .ASCIZ 'Command Not Rejected'
1149 030415      103      157      156 T6SSR: .ASCIZ 'Contents of TSSR Incorrect After Write Packet'
1150 030473      125      156      145 T6INT: .ASCIZ 'Unexpected Interrupt Received On Write Packet'
1151 030551      105      170      160 T6NINT: .ASCIZ 'Expected Interrupt Not Received On Write Packet'
1152 030631      111      156      143 T6TSBA: .ASCIZ 'Incorrect TSBA Address After Packet Write'
1153 030703      103      157      155 TST6ID: .ASCIZ 'Command Reject'
1154
1155 030722              .EVEN
1156 030722              ENDTST
1157
1158              L10052:
1159              TRAP      C$ETST
1160
1161      .SBTTL TEST 7: WRITE CHARACTERISTICS
1162
1163      ; THIS TEST VERIFIES BASIC OPERATION OF THE WRITE CHARACTERISTICS
1164      ; COMMAND. IT VERIFIES THAT THE COMMAND BLOCK AND CHARACTERISTICS
1165      ; DATA BLOCK ARE FETCHED PROPERLY FROM CPU MEMORY, THE NEED BUFFER
1166      ; ADDRESS (NBA) BIT IN TSSR IS HANDLED PROPERLY, AND THAT A PROPER
1167      ; MESSAGE PACKET IS STORED, WHERE APPROPRIATE. THIS TEST DOES NOT
1168      ; CHECK THAT THE VARIOUS FUNCTIONS ENABLED BY CHARACTERISTIC MODE

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1265

Line	Address	Offset	Label	Instruction	Comment	Trap	Value
1315	031374	012046	154:	CKLOOP	;LOOP ON ERROR, IF FLAG SET	TRAP	PKTSSR
	031376	104406					C\$CLP1
1316	031400	104410		ESCAPE SEG	;BY-PASS CHECKS IF FATAL ERROR	TRAP	C\$ESCAPE
	031402	000116					100004
1317	031404	005737	002222	TST INTRECV	;DID AN INTERRUPT OCCUR ?		
1318	031410	001404		BEQ 224	;BRANCH IF NOT		
1322	031412			ERRHRD ERRNO,T7INT,PKTSSR			
	031412	104456				TRAP	C\$ERHRD
	031414	001305				.WORD	709
	031416	034041				.WORD	T7INT
	031420	012046				.WORD	PKTSSR
1323	031422	016501	000002	224: MOV TSSR(R5),R1	;GET THE CONTENTS OF TSSR		
1324	031426	012702	102206	MOV #SC!SSR!TSREJ!NBA,R2	;EXPECTED CONTENTS OF TSSR		
1325	031432	032701	000100	BIT #OFL,R1	;IS OFF-LINE BIT SET ?		
1326	031436	001402		BEQ 254	;BRANCH IF NOT OFF-LINE		
1327	031440	052702	000100	BIS #OFL,R2	;SET OFF-LINE IN EXPECTED DATA		
1328	031444	020201		254: CMP R2,R1	;DOES EXPECTED MATCH RECEIVED ?		
1329	031446	001414		BEQ 304	;OKAY IF MATCH		
1330	031450	010100		MOV R1,R0	;DATA FROM TSSR		
1331	031452			XOR R2,R0	;FIND BITS IN ERROR		
1332	031462	020027	002000	CMP R0,#NBA	;IS NBA ONLY BIT IN ERROR ?		
1333	031466	001404		BEQ 304	;DON'T PRINT ERROR IF NBA ONLY BAD BIT		
1337	031470			ERRHRD ERRNO,T72REJ,PKTSSR	;COMMAND NOT REJECTED		
	031470	104456				TRAP	C\$ERHRD
	031472	001306				.WORD	710
	031474	033273				.WORD	T72REJ
	031476	012046				.WORD	PKTSSR
1338	031500		304:	CKLOOP	;LOOP ON ERROR ?		
	031500	104406				TRAP	C\$CLP1
1339	031502	032701	002000	BIT #NBA,R1	;IS NBA BIT SET ?		
1340	031506	001004		BNE 354	;OKAY IF NBA SET		
1344	031510			ERRHRD ERRNO,T72NBA,PKTSSR	;NBA NOT SET		
	031510	104456				TRAP	C\$ERHRD
	031512	001307				.WORD	711
	031514	033142				.WORD	T72NBA
	031516	012046				.WORD	PKTSSR
1345	031520		354:	ENDSEG	;<<<<<<<<<<<<<<<< END SEGMENT <<<<<<<<<<<<<<<<<<		
1346	031520						
	031520	104405				100004:	
						TRAP	C\$ESEG
1347	031522	062703	000004	ADD #4,R3	;POINT TO NEXT DATA PAIR		
1349	031526	020327	033142	CMP R3,#T72DONE	;COMPARE TO END OF TEST DATA		
1350	031532	103002		BHIS 574	;BRANCH IF ALL DATA TESTED		
1351	031534	000137	031304	JMP 54	;BRANCH TILL BACK TO ZERO		
1352							
1353	031540		574:	ENDSUB	;\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\ END SUBTEST \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		
	031540						
	031540	104403				L10057:	
						TRAP	C\$ESUB
1354							
1355							
1356							
1357							
1358							
1359							

Address	Offset	Hex Data	Label	Instruction	Comment
1360					IF ISSUED WITH AN INVALID DATA BLOCK BYTE COUNT
1361					
1362					
1363					
1364	031542		BGNSUB		////////// BEGIN SUBTEST //////////
	031542				T7.3:
	031542	104402			TRAP C0BSUB
1365					
1366	031544		SETPRI	#PRI00	LOWER PRIORITY TO ALLOW INTERRUPTS
	031544	012700			MOV #PRI00,R0
	031550	104441			TRAP C0SPRI
1367	031552	012703			
1368	031556	012704	51:	MOV #1,R3	STARTING BYTE COUNT
1369	031562	004737		MOV #T7PACKET,R4	GET THE ADDRESS OF COMMAND PACKET
1370				JSR PC,T7REST	RESTORE PACKET TO STARTING VALUES
1371	031566		BGNSEG		***** BEGIN SEGMENT *****
	031566	104404			TRAP C0BSEG
1372					
1373	031570	004737		JSR PC,SOFINIT	DO SOFT INIT OF CONTROLLER
1374	031574	103405		BCS 101	BR IF SOFT INIT = OK
1378	031576	010001		MOV R0,R1	SAVE CONTENTS OF TSSR
1379	031600			ERRDF ERRNO,SFIERR,SFIMSG	DEVICE FATAL ERROR DURING INIT
	031600	104455			TRAP C0ERDF
	031602	001310			.WORD 712
	031604	003650			.WORD SFIERR
	031606	012034			.WORD SFIMSG
1380	031610	005037	101:	CLR INTRECV	CLEAR INTERRUPT RECEIVED FLAG
1381	031614	010364		MOV R3,PKBCNT(R4)	INSERT THE BYTE COUNT FOR TEST
1382	031620	010465		MOV R4,TSDB(R5)	SET THE PACKET ADDRESS
1383	031624	004737		JSR PC,WAITF	WAIT FOR SSR TO SET
1384	031630	103405		BCS 151	BR IF CARRY SET (GOOD RETURN)
1385	031632	010001		MOV R0,R1	SAVE CONTENTS OF TSSR
1389	031634			ERRDF ERRNO,T7SSR,PKTSSR	DEVICE FATAL SSR FAILED TO SET
	031634	104455			TRAP C0ERDF
	031636	001311			.WORD 713
	031640	033661			.WORD T7SSR
	031642	012046			.WORD PKTSSR
1390	031644		151:	CKLOOP	LOOP ON ERROR, IF FLAG SET
	031644	104406			TRAP C0CLP1
1391	031646			ESCAPE SEG	BY-PASS SUBTEST IF FATAL ERROR
	031646	104410			TRAP C0ESCAPE
	031650	000116			.WORD 100001
1392	031652	005737		TST INTRECV	DID AN INTERRUPT OCCUR ?
1393	031656	001404		BEQ 221	BRANCH IF NOT
1397	031660			ERRHRD ERRNO,T7INT,PKTSSR	
	031660	104456			TRAP C0ERHRD
	031662	001312			.WORD 714
	031664	034041			.WORD T7INT
	031666	012046			.WORD PKTSSR
1398	031670	016501	221:	MOV TSSR(R5),R1	GET THE CONTENTS OF TSSR
1399	031674	012702		MOV #SC!SSR!TSREJ!NBA,R2	EXPECTED CONTENTS OF TSSR
1400	031700	032701		BIT #OFL,R1	IS OFF-LINE BIT SET ?
1401	031704	001402		BEQ 251	BRANCH IF NOT OFF-LINE
1402	031706	052702		BIS #OFL,R2	SET OFF-LINE IN EXPECTED DATA
1403	031712	020201	251:	CMP R2,R1	DOES EXPECTED MATCH RECEIVED ?
1404	031714	001414		BEQ 301	OKAY IF MATCH
1405	031716	010100		MOV R1,R0	DATA FROM TSSR

PC	Address	Offset	Label	Instruction	Comment	Trap	Trap Code
1406	031720			XOR R2,R0	;FIND BITS IN ERROR		
1407	031730	020027	002000	CMP R0,#NBA	;IS NBA ONLY BIT IN ERROR ?		
1408	031734	001404		BEG 30:	;DON'T PRINT ERROR IF NBA ONLY BAD BIT		
1412	031736			ERRMRD ERRNO,T73REJ,PKTSSR	;COMMAND NOT REJECTED		
	031736	104456				TRAP	C1ERMRO
	031740	001313				.WORD	715
	031742	033372				.WORD	T73REJ
	031744	012046				.WORD	PKTSSR
1413	031746		30:	CKLOOP	;LOOP ON ERROR ?		
	031746	104406				TRAP	C1CLP1
1414	031750	032701	002000	BIT #NBA,R1	;IS NBA BIT SET ?		
1415	031754	001004		BNE 35:	;OKAY IF NBA SET		
1419	031756			ERRMRD ERRNO,T72NBA,PKTSSR	;NBA NOT SET		
	031756	104456				TRAP	C1ERMRO
	031760	001314				.WORD	716
	031762	033142				.WORD	T72NBA
	031764	012046				.WORD	PKTSSR
1420	031766		35:				
1421	031766			ENDSEG	;***** END SEGMENT *****		
	031766				10000:		
	031766	104405				TRAP	C1ESEG
1422							
1423	031770	005203		INC R3	;NEXT BYTE COUNT		
1424	031772	020327	000006	CMP R3,#6	;TESTED ALL INVALID ?		
1425	031776	002002		BGE 57:	;BRANCH IF TEST DONE		
1426	032000	000137	031556	JMP 5:	;BRANCH TILL BACK TO ZERO		
1427							
1428	032004		57:	ENDSUB	;***** END SUBTEST *****		
	032004				L10060:		
	032004	104403				TRAP	C1ESUB
1429							
1430							
1431							
1432							
1433							
1434							
1435							
1436							
1437							
1438							
1439	032006			BGNSUB	;***** BEGIN SUBTEST *****		
	032006				T7.4:		
	032006	104402				TRAP	C18SUB
1440							
1441	032010			SETPRI #PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS		
	032010	012700	000000			MOV	#PRI00,R0
	032014	104441				TRAP	C1SPRI
1442	032016	012703	033126	MOV #T72DATA,R3	;START OF TEST DATA FOR SUBTEST		
1443	032022	012704	033060	MOV #T7PACKET,R4	;GET THE ADDRESS OF COMMAND PACKET		
1444	032026	004737	034242	JSR PC,T7REST	;RESTORE PACKET TO STARTING VALUES		
1445							
1446							
1447	032032	004737	015774	JSR PC,SOFINIT	;DO SOFT INIT OF CONTROLLER		
1448	032036	103405		BCS 10:	;BR IF SOFT INIT = OK		
1452	032040	010001		MOV R0,R1	;SAVE CONTENTS OF TSSR		
1453	032042			ERRDF ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT		
	032042	104455				TRAP	C1ERDF

Line	Address	Hex	Label	Op	Op2	Op3	Op4	Op5	Op6	Op7	Op8	Op9	Op10	Op11	Op12	Op13	Op14	Op15	Op16	Op17	Op18	Op19	Op20	Op21	Op22	Op23	Op24	Op25	Op26	Op27	Op28	Op29	Op30	Op31	Op32	Op33	Op34	Op35	Op36	Op37	Op38	Op39	Op40	Op41	Op42	Op43	Op44	Op45	Op46	Op47	Op48	Op49	Op50	Op51	Op52	Op53	Op54	Op55	Op56	Op57	Op58	Op59	Op60	Op61	Op62	Op63	Op64	Op65	Op66	Op67	Op68	Op69	Op70	Op71	Op72	Op73	Op74	Op75	Op76	Op77	Op78	Op79	Op80	Op81	Op82	Op83	Op84	Op85	Op86	Op87	Op88	Op89	Op90	Op91	Op92	Op93	Op94	Op95	Op96	Op97	Op98	Op99	Op100	Op101	Op102	Op103	Op104	Op105	Op106	Op107	Op108	Op109	Op110	Op111	Op112	Op113	Op114	Op115	Op116	Op117	Op118	Op119	Op120	Op121	Op122	Op123	Op124	Op125	Op126	Op127	Op128	Op129	Op130	Op131	Op132	Op133	Op134	Op135	Op136	Op137	Op138	Op139	Op140	Op141	Op142	Op143	Op144	Op145	Op146	Op147	Op148	Op149	Op150	Op151	Op152	Op153	Op154	Op155	Op156	Op157	Op158	Op159	Op160	Op161	Op162	Op163	Op164	Op165	Op166	Op167	Op168	Op169	Op170	Op171	Op172	Op173	Op174	Op175	Op176	Op177	Op178	Op179	Op180	Op181	Op182	Op183	Op184	Op185	Op186	Op187	Op188	Op189	Op190	Op191	Op192	Op193	Op194	Op195	Op196	Op197	Op198	Op199	Op200	Op201	Op202	Op203	Op204	Op205	Op206	Op207	Op208	Op209	Op210	Op211	Op212	Op213	Op214	Op215	Op216	Op217	Op218	Op219	Op220	Op221	Op222	Op223	Op224	Op225	Op226	Op227	Op228	Op229	Op230	Op231	Op232	Op233	Op234	Op235	Op236	Op237	Op238	Op239	Op240	Op241	Op242	Op243	Op244	Op245	Op246	Op247	Op248	Op249	Op250	Op251	Op252	Op253	Op254	Op255	Op256	Op257	Op258	Op259	Op260	Op261	Op262	Op263	Op264	Op265	Op266	Op267	Op268	Op269	Op270	Op271	Op272	Op273	Op274	Op275	Op276	Op277	Op278	Op279	Op280	Op281	Op282	Op283	Op284	Op285	Op286	Op287	Op288	Op289	Op290	Op291	Op292	Op293	Op294	Op295	Op296	Op297	Op298	Op299	Op300	Op301	Op302	Op303	Op304	Op305	Op306	Op307	Op308	Op309	Op310	Op311	Op312	Op313	Op314	Op315	Op316	Op317	Op318	Op319	Op320	Op321	Op322	Op323	Op324	Op325	Op326	Op327	Op328	Op329	Op330	Op331	Op332	Op333	Op334	Op335	Op336	Op337	Op338	Op339	Op340	Op341	Op342	Op343	Op344	Op345	Op346	Op347	Op348	Op349	Op350	Op351	Op352	Op353	Op354	Op355	Op356	Op357	Op358	Op359	Op360	Op361	Op362	Op363	Op364	Op365	Op366	Op367	Op368	Op369	Op370	Op371	Op372	Op373	Op374	Op375	Op376	Op377	Op378	Op379	Op380	Op381	Op382	Op383	Op384	Op385	Op386	Op387	Op388	Op389	Op390	Op391	Op392	Op393	Op394	Op395	Op396	Op397	Op398	Op399	Op400	Op401	Op402	Op403	Op404	Op405	Op406	Op407	Op408	Op409	Op410	Op411	Op412	Op413	Op414	Op415	Op416	Op41
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1498					;*
1499					;
1500					;TEST 7, SUBTEST 5
1501					;
1502					;CHECK THAT WRITE CHARACTERISTICS COMMAND IS REJECTED IF THE
1503					;MESSAGE BUFFER LENGTH SPECIFIES AN INVALID COUNT (LESS THAN 14)
1504					;
1505					;-
1506					
1507	032234			BGNSUB	;/!!!!!!!!!!!!!! BEGIN SUBTEST !!!!!!!!!!!!!!!
	032234				T7.5:
	032234	104402			TRAP C#BSUB
1508					
1509	032236			SETPRI #PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS
	032236	012700	0000C0		MOV #PRI00,R0
	032242	104441			TRAP C#SPRI
1510	032244	012703	000001		;STARTING BUFFER LENGTH
1511	032250	012704	033060	54:	;GET THE ADDRESS OF COMMAND PACKET
1512	032254	004737	034242		;RESTORE PACKET TO STARTING VALUES
1513					
1514	032260			BGNSEG	;>>>>>>>>> BEGIN SEGMENT >>>>>>>>>
	032260	104404			TRAP C#BSEG
1515					
1516	032262	004737	015774		;DO SOFT INIT OF CONTROLLER
1517	032266	103405			;BR IF SOFT INIT = OK
1521	032270	010001			;SAVE CONTENTS OF TSSR
1522	032272				;DEVICE FATAL ERROR DURING INIT
	032272	104455			TRAP C#ERDF
	032274	001322			.WORD 722
	032276	003650			.WORD SFIERR
	032300	012034			.WORD SFIMSG
1523	032302	005037	002222	104:	;CLEAR INTERRUPT RECEIVED FLAG
1524	032306	010337	033074		;INSERT THE BAD MESSAGE LENGTH
1525	032312	010465	000000		;SET THE PACKET ADDRESS
1526	032316	004737	016250		;WAIT FOR SSR TO SET
1527	032322	103405			;BR IF CARRY SET (GOOD RETURN)
1528	032324	010001			;SAVE CONTENTS OF TSSR
1532	032326				;DEVICE FATAL SSR FAILED TO SET
	032326	104455			TRAP C#ERDF
	032330	001323			.WORD 723
	032332	033661			.WORD T7SSR
	032334	012046			.WORD PKTSSR
1533	032336			154:	;LOOP ON ERROR, IF FLAG SET
	032336	104406			TRAP C#CLP1
1534	032340			ESCAPE SEG	;BY-PASS SUBTEST IF FATAL ERROR
	032340	104410			TRAP C#ESCAPE
	032342	000116			.WORD 100001-
1535	032344	005737	002222		;DID AN INTERRUPT OCCUR ?
1536	032350	001404			;BRANCH IF NOT
1540	032352				
	032352	104456			TRAP C#ERHRD
	032354	001324			.WORD 724
	032356	034041			.WORD T7INT
	032360	012046			.WORD PKTSSR
1541	032362	016501	000002	224:	;GET THE CONTENTS OF TSSR
1542	032366	012702	102206		;EXPECTED CONTENTS OF TSSR
1543	032372	032701	000100		;IS OFF-LINE BIT SET ?

Line	Address	Offset	Label	Operation	Register/Value	Comment	Trap	Value
1544	032376	001402		BEQ	254	;BRANCH IF NOT OFF-LINE		
1545	032400	052702	000100	BIS	#0FL,R2	;SET OFF-LINE IN EXPECTED DATA		
1546	032404	020201		CMP	R2,R1	;DOES EXPECTED MATCH RECEIVED ?		
1547	032406	001414		BEQ	304	;OKAY IF MATCH		
1548	032410	010100		MOV	R1,R0	;DATA FROM TSSR		
1549	032412			XOR	R2,R0	;FIND BITS IN ERROR		
1550	032422	020027	002000	CMP	R0,#NBA	;IS NBA ONLY BIT IN ERROR ?		
1551	032426	001404		BEQ	304	;DON'T PRINT ERROR IF NBA ONLY BAD BIT		
1555	032430			ERRHRD	ERRNO,T75REJ,PKTSSR	;COMMAND NOT REJECTED		
	032430	104456					TRAP	C#ERHRD
	032432	001325					.WORD	725
	032434	033563					.WORD	T75REJ
	032436	012046					.WORD	PKTSSR
1556	032440			304:	CKLOOP	;LOOP ON ERROR ?		
	032440	104406					TRAP	C#CLP1
1557	032442	032701	002000	BIT	#NBA,R1	;IS NBA BIT SET ?		
1558	032446	001004		BNE	354	;OKAY IF NBA SET		
1562	032450			ERRHRD	ERRNO,T72NBA,PKTSSR	;NBA NOT SET		
	032450	104456					TRAP	C#ERHRD
	032452	001326					.WORD	726
	032454	033142					.WORD	T72NBA
	032456	012046					.WORD	PKTSSR
1563	032460			354:				
1564	032460				ENDSEG	;***** END SEGMENT *****		
	032460					100004:		
	032460	104405					TRAP	C#ESEG
1565								
1566	032462	005203		INC	R3	;NEXT BUFFER LENGTH		
1567	032464	020327	000016	CMP	R3,#14.	;HAVE ALL BAD VALUES BEEN TESTED ?		
1568	032470	002002		BGE	574	;BRANCH IF ALL TESTED		
1569	032472	000137	032250	JMP	54	;BRANCH TILL BACK TO ZERO		
1570								
1571	032476			574:	ENDSUB	;////////// END SUBTEST //////////		
	032476					L10062:		
	032476	104403					TRAP	C#ESUB
1572								
1573								
1574								
1575								
1576								
1577								
1578								
1579								
1580								
1581								
1582								
1583								
1584								
1585	032500				BGNSUB	;////////// BEGIN SUBTEST //////////		
	032500					T7.6:		
	032500	104402					TRAP	C#BSUB
1586	032502	005737	002224	TST	EXTFEA	;IS EXTENDED FEATURES SOFT. SW SET?		
1587	032506	001002		BNE	44	;BR, IF SOFTWARE SWITCH IS SET (ON)		
1588	032510			EXIT	TST	;NO EXTENDED FEATURES EXIT THIS TEST		
	032510	104432					TRAP	C#EXIT
	032512	001576					.WORD	L10055-
1589	032514	004737	034242	44:	JSR	PC,T7REST		
						;SET PACKET TO START-UP VALUES		

Address	Offset	Hex Data	Hex Data	Label	Instruction	Comment	Trap	Trap Comment
1590	032520				SETPRI	#PRI00		LOWER PRIORITY TO ALLOW INTERRUPTS
1591	032520	012700	000000				MOV	#PRI00,R0
	032524	104441					TRAP	C#SPRI
1592	032526	012703	002764		MOV	#TSTBLK+12.,R3		START OF TEST DATA
1593	032532	012704	033060		MOV	#T7PACKET,R4		GET THE ADDRESS OF COMMAND PACKET
1594	032536	012737	000200	033100	MOV	#200,T7SP		SPECIAL BIT SET FOR EXTFEA RAM RD
1595	032544	012764	000012	000006	MOV	#10.,PKBCNT(R4)		START WITH EXTENDED FEATURES VALUE
1596	032552			5#:				
1597	032552				BGNSEG			***** BEGIN SEGMENT *****
	032552	104404					TRAP	C#BSEG
1598								
1599	032554	004737	015774		JSR	PC,SOFINIT		DO SOFT INIT OF CONTROLLER
1600	032560	103405			BCS	10#		BR IF SOFT INIT = OK
1604	032562	010001			MOV	R0,R1		SAVE CONTENTS OF TSSR
1605	032564				ERRDF	ERRNO,SFIERR,SFIMSG		DEVICE FATAL ERROR DURING INIT
	032564	104455					TRAP	C#ERDF
	032566	001327					.WORD	727
	032570	003650					.WORD	SFIERR
	032572	012034					.WORD	SFIMSG
1606	032574	005037	002220	10#:	CLR	FATFLG		CLEAR FATAL ERROR FLAG
1607	032600	005037	002222		CLR	INTRECV		CLEAR INTERRUPT RECEIVED FLAG
1608	032604	010465	000000		MOV	R4,TSD8(R5)		SET THE PACKET ADDRESS
1609	032610	004737	016336		JSR	PC,CHKTSSR		WAIT FOR SSR TO SET
1610	032614	103407			BCS	15#		BR IF CARRY SET (GOOD RETURN)
1611	032616	010001			MOV	R0,R1		SAVE CONTENTS OF TSSR
1615	032620				ERRDF	ERRNO,T7SSR,PKTSSR		DEVICE FATAL SSR FAILED TO SET
	032620	104455					TRAP	C#ERDF
	032622	001330					.WORD	728
	032624	033661					.WORD	T7SSR
	032626	012046					.WORD	PKTSSR
1616	032630	005237	002220		INC	FATFLG		SET FATAL ERROR FLAG
1617	032634			15#:	CKLOOP			LOOP ON ERROR, IF FLAG SET
	032634	104406					TRAP	C#CLP1
1618	032636				ESCAPE	SEG		BY-PASS SUBTEST IF FATAL ERROR
	032636	104410					TRAP	C#ESCAPE
	032640	000156					.WORD	10000#-
1619	032642	005737	002222		TST	INTRECV		DID AN INTERRUPT OCCUR ?
1620	032646	001404			BEQ	22#		BRANCH IF NOT
1624	032650				ERRHRD	ERRNO,T7INT,PKTSSR		
	032650	104456					TRAP	C#ERHRD
	032652	001331					.WORD	729
	032654	034041					.WORD	T7INT
	032656	012046					.WORD	PKTSSR
1625	032660	016501	000002	22#:	MOV	TSSR(R5),R1		GET THE CONTENTS OF TSSR
1626	032664	012702	000200		MOV	#SSR,R2		EXPECTED CONTENTS OF TSSR
1627	032670	032701	000100		BIT	#OFL,R1		IS OFF-LINE BIT SET ?
1628	032674	001402			BEQ	25#		BRANCH IF NOT OFF-LINE
1629	032676	052702	000100		BIS	#OFL,R2		SET OFF-LINE IN EXPECTED DATA
1630	032702	020201		25#:	CMP	R2,R1		DOES EXPECTED MATCH RECEIVED ?
1631	032704	001404			BEQ	30#		OKAY IF MATCH
1635	032706				ERRHRD	ERRNO,T7NBA,PKTSSR		NBA NOT ZERO
	032706	104456					TRAP	C#ERHRD
	032710	001332					.WORD	730
	032712	033220					.WORD	T7NBA
	032714	012046					.WORD	PKTSSR
1636	032716			30#:	CKLOOP			LOOP ON ERROR ?

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TEST 7: WRITE CHARACTERISTICS

SEQ 0138

```

1687 033066 000010 .WORD 8. ;STARTING VALUE OF BLOCK SIZE
1688
1689 033070 T7DATA: ;CHARACTERISTICS DATA BLOCK
1690 033070 033106 .WORD T7BFR ;ADDRESS OF MESSAGE BUFFER
1691 033072 000000 .WORD 0
1692 033074 000016 .WORD 14. ;LENGTH OF MESSAGE BUFFER
1693 033076 000000 .WORD 0
1694 033100 000000 T7SP: .WORD 0 ;EXTFEA EXTRA WORD
1695
1696 033102 000000 000000 .WORD 0,0 ;SPACE
1697 033106 T7BFR: .BLKW 8. ;MESSAGE BUFFER
1698
1699 ;*
1700 ;
1701 ;TEST DATA FOR SUBTEST TWO
1702 ;
1703 ;DATA HAS FORMAT:
1704 ;
1705 ; 1ST WORD OFFSET TO TEST WORD IN PACKET
1706 ; 2ND WORD BITS TO SET FOR TEST
1707 ;
1708 ;-
1709
1710 033126 T72DATA:
1711 033126 000000 037140 .WORD 0,BIT5!BIT6!BIT9!BIT10!BIT11!BIT12!BIT13
1712 033132 000002 000001 .WORD 2,BIT0
1713 033136 000004 100100 .WORD 4,BIT6!BIT15
1714 033142 T72DONE=.
1715
1716
1717 ;*
1718 ;LOCAL TEXT MESSAGES FOR TEST
1719 ;-
1720
1721 033142 116 102 101 T72NBA: .ASCIZ 'NBA Not Set On Rejected WRITE CHARACTERISTICS'
1722 033220 127 122 111 T7NBA: .ASCIZ 'WRITE CHARACTERISTICS Command Not Accepted'
1723 033273 127 122 111 T72REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Non-Zero Unused Fields'
1724 033372 127 122 111 T73REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Data Count'
1725 033465 127 122 111 T74REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Block Address'
1726 033563 127 122 111 T75REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Buffer Length'
1727 033661 103 157 156 T7SSR: .ASCIZ 'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
1728 033750 105 170 160 T7NINT: .ASCIZ 'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
1729 034041 125 156 145 T7INT: .ASCIZ 'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
1730 034130 111 156 143 T7TSBA: .ASCIZ 'Incorrect TSBA Address After WRITE CHARACTERISTICS'
1731 034213 127 162 151 T7TID: .ASCIZ 'Write Characteristics'
1732 .EVEN
1733
1734
1735 ;*
1736 ;
1737 ;ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
1738 ;
1739 ;-
1740
1741 034242 T7REST:
1742 034242 SAVREG ;SAVE THE REGISTERS
1743 034246 012701 033060 MOV #T7PACKET,R1 ;START OF THE PACKET

```

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TEST 7: WRITE CHARACTERISTICS

SEQ 0139

1744	034252	012721	100004	MOV	#100004,(R1)+	;WRITE CHARACTERISTICS WITH ACK
1745	034256	012721	033070	MOV	#T7DATA,(R1)+	;ADDRESS OF CHAR DATA BLOCK
1746	034262	005021		CLR	(R1)+	;EXTENDED ADDRESS
1747	034264	012721	000010	MOV	#8,(R1)+	;SIZE OF DATA BLOCK IN BYTES
1748	034270	012721	033106	MOV	#T7BFR,(R1)+	;ADDRESS OF MESSAGE BUFFER
1749	034274	005021		CLR	(R1)+	
1750	034276	012721	000020	MOV	#16,(R1)+	;LENGTH OF MESSAGE BUFFER
1751	034302	005021		CLR	(R1)+	
1752	034304	005011		CLR	(R1)	
1753	034306	000207		RTS	PC	;RETURN
1754	034310			ENDTST		
	034310					
	034310	104401				

L10055: TRAP C\$ETST

.SBTTL TEST 8: VOLUME CHECK

THIS TEST VERIFIES THAT THE VOLUME CHECK (VCK) BIT, A FLAG HELD WITHIN THE M7196 AND APPEARING IN XST0, IS SET BY INITIALIZE AND CLEARED BY EXECUTING A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT SET. IT IS ALSO VERIFIED THAT A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT CLEAR DOES NOT AFFECT THE STATE OF THE VOLUME CHECK BIT. THE ACTUAL FUNCTION OF VOLUME CHECK, THAT OF PREVENTING OR ALLOWING A TAPE MOTION COMMAND DEPENDING UPON WHETHER VOLUME CHECK IS SET OR CLEAR, IS NOT CHECKED BY THIS TEST; THIS FUNCTIONALITY IS CHECKED IN THE INDIVIDUAL TESTS OF TAPE MOTION COMMANDS.

THE TEST PROCEEDS AS FOLLOWS:

1. THE CONTROLLER IS INITIALIZED BY WRITING INTO THE TSSR.
2. A WRITE CHARACTERISTICS COMMAND IS ISSUED (WITH CVC=0) AND XST0 IN THE RETURNED MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD BE CLEAR (0).
3. THE PREVIOUS STEP IS REPEATED TO VERIFY THAT VCK DOES NOT CHANGE (REMAINS AT 0).
4. A WRITE CHARACTERISTICS COMMAND IS ISSUED WITH CVC=1 AND THE VCK BIT IN XST0 IN THE MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD BE CLEAR (0).
5. A WRITE CHARACTERISTICS COMMAND IS ISSUED WITH CVC=0 AND THE VCK BIT IN XST0 IN THE MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD REMAIN CLEAR (0).

1791	034312			BGNTST		
	034312					T8::
1796	034312	012700	035177	MOV	#T8ID,R0	;ASCII MESSAGE TO IDENTIFY TEST
1797	034316	004737	016510	JSR	PC,TSTSETUP	;DO INITIAL TEST SETUP
1798	034322	012737	000024 002214	MOV	#20,.LOOPCNT	;PERFORM 20 ITERATIONS
1799	034330					
1800						
1801	034330	012704	034720	MOV	#T8PACKET,R4	;PACKET FOR WRITE CHARACTERISTICS

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TEST 8: VOLUME CHECK

SEQ 0140

1802	034334	004737	015774	5:	JSR	PC,SOFINIT	;DO SOFT INIT OF CONTROLLER
1803	034340	103405			BCS	10:	;BR IF SOFT INIT = OK
1807	034342	010001			MOV	R0,R1	;SAVE CONTENTS OF TSSR
1808	034344				ERRDF	ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT
	034344	104455					TRAP C\$ERDF
	034346	001441					.WORD 801
	034350	003650					.WORD SFIERR
	034352	012034					.WORD SFIMSG
1809	034354	042714	040000	10:	BIC	#BIT14,(R4)	;CLEAR THE CVC BIT
1810	034360	010465	000000		MOV	R4,TSDB(R5)	;SET THE PACKET ADDRESS FOR WRITE CHAR
1811	034364	004737	J16336		JSR	PC,CHKTSSR	;WAIT FOR SSR TO SET
1812	034370	103405			BCS	15:	;BR IF CARRY SET (GOOD RETURN)
1813	034372	010001			MOV	R0,R1	;SAVE CONTENTS OF TSSR
1817	034374				ERRDF	ERRNO,T8SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET
	034374	104455					TRAP C\$ERDF
	034376	001442					.WORD 802
	034400	035110					.WORD T8SSR
	034402	012046					.WORD PKTSSR
1818	034404			15:	CKLOOP		;LOOP ON ERROR, IF FLAG SET
	034404	104406					TRAP C\$CLP1
1819	034406				ESCAPE	TST	;EXIT IF FATAL ERROR
	034406	104410					TRAP C\$ESCAPE
	034410	000604					.WORD L10064
1820	034412	012702	034742		MOV	#T88FR,R2	;ADDRESS OF THE MESSAGE BUFFER
1821	034416	032762	000020 000006		BIT	#XSOVCK,XST0(R2)	;IS VOLUME CHECK SET IN XST0 ?
1822	034424	001406			BEQ	20:	;OKAY IF VOLUME CHECK IS CLEAR
1826	034426	016501	000002		MOV	TSSR(R5),R1	;CONTENTS OF TSSR FOR ERROR REPORT
1827	034432				ERRHRD	ERRNO,T8NVCK,PKTMES	;VOLUME CHECK NOT CLEAR
	034432	104456					TRAP C\$ERHRD
	034434	001443					.WORD 803
	034436	035017					.WORD T8NVCK
	034440	012110					.WORD PKTMES
1828	034442			20:	CKLOOP		;LOOP ON ERROR ?
	034442	104406					TRAP C\$CLP1
1829	034444	010465	000000		MOV	R4,TSDB(R5)	;SET THE PACKET ADDRESS FOR WRITE CHAR
1830	034450	004737	016336		JSR	PC,CHKTSSR	;WAIT FOR SSR TO SET
1831	034454	103405			BCS	25:	;BR IF CARRY SET (GOOD RETURN)
1832	034456	010001			MOV	R0,R1	;SAVE CONTENTS OF TSSR
1836	034460				ERRDF	ERRNO,T8SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET
	034460	104455					TRAP C\$ERDF
	034462	001444					.WORD 804
	034464	035110					.WORD T8SSR
	034466	012046					.WORD PKTSSR
1837	034470			25:	CKLOOP		;LOOP ON ERROR, IF FLAG SET
	034470	104406					TRAP C\$CLP1
1838	034472				ESCAPE	TST	;EXIT IF FATAL ERROR
	034472	104410					TRAP C\$ESCAPE
	034474	000520					.WORD L10064
1839	034476	032762	000020 000006		BIT	#XSOVCK,XST0(R2)	;IS VOLUME CHECK SET IN XST0 ?
1840	034504	001406			BEQ	30:	;OKAY IF VOLUME CHECK IS SET
1844	034506	016501	000002		MOV	TSSR(R5),R1	;CONTENTS OF TSSR FOR ERROR REPORT
1845	034512				ERRHRD	ERRNO,T8NVCK,PKTMES	;VOLUME CHECK NOT SET
	034512	104456					TRAP C\$ERHRD
	034514	001445					.WORD 805
	034516	035017					.WORD T8NVCK
	034520	012110					.WORD PKTMES
1846	034522			30:	CKLOOP		;LOOP ON ERROR ?

Address	Offset	Hex	Dec	Label	Instruction	Comment	Trap	Trap Value
1847	034522	052714	040000	BIS	#BIT14,(R4)	;SET THE CVC BIT	TRAP	C\$CLP1
1848	034524	010465	000000	MOV	R4,TSDB(R5)	;SET THE PACKET ADDRESS FOR WRITE CHAR		
1849	034530	004737	016336	JSR	PC,CHKTSSR	;WAIT FOR SSR TO SET		
1850	034534	103405		BCS	35\$	;BR IF CARRY SET (GOOD RETURN)		
1851	034540	010001		MOV	R0,R1	;SAVE CONTENTS OF TSSR		
1855	034544			ERRDF	ERRNO,T8SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET		
	034544	104455					TRAP	C\$ERDF
	034546	001446					.WORD	806
	034550	035110					.WORD	T8SSR
	034552	012046					.WORD	PKTSSR
1856	034554			35\$:	CKLOOP	;LOOP ON ERROR, IF FLAG SET		
	034554	104406					TRAP	C\$CLP1
1857	034556			ESCAPE	TST	;EXIT IF FATAL ERROR		
	034556	104410					TRAP	C\$ESCAPE
	034560	000434					.WORD	L10064-
1858	034562	032762	000020	000006	BIT	#XSOVCK,XSTO(R2)		
1859	034570	001406			BEQ	40\$		
1863	034572	016501	000002		MOV	TSSR(R5),R1		
1864	034576				ERRHRD	ERRNO,T8VCK,PKTMES		
	034576	104456					TRAP	C\$ERHRD
	034600	001447					.WORD	807
	034602	034762					.WORD	T8VCK
	034604	012110					.WORD	PKTMES
1865	034606			40\$:	CKLOOP	;LOOP ON ERROR ?		
	034606	104406					TRAP	C\$CLP1
1866	034610	042714	040000		BIC	#BIT14,(R4)		
1867	034614	010465	000000		MOV	R4,TSDB(R5)		
1868	034620	004737	016336		JSR	PC,CHKTSSR		
1869	034624	103405			BCS	45\$		
1870	034626	010001			MOV	R0,R1		
1874	034630				ERRDF	ERRNO,T8SSR,PKTSSR		
	034630	104455					TRAP	C\$ERDF
	034632	001450					.WORD	808
	034634	035110					.WORD	T8SSR
	034636	012046					.WORD	PKTSSR
1875	034640			45\$:	CKLOOP	;LOOP ON ERROR, IF FLAG SET		
	034640	104406					TRAP	C\$CLP1
1876	034642			ESCAPE	TST	;EXIT IF FATAL ERROR		
	034642	104410					TRAP	C\$ESCAPE
	034644	000350					.WORD	L10064
1877	034646	032762	000020	000006	BIT	#XSOVCK,XSTO(R2)		
1878	034654	001406			BEQ	50\$		
1882	034656	016501	000002		MOV	TSSR(R5),R1		
1883	034662				ERRHRD	ERRNO,T8VCK,PKTMES		
	034662	104456					TRAP	C\$ERHRD
	034664	001451					.WORD	809
	034666	034762					.WORD	T8VCK
	034670	012110					.WORD	PKTMES
1884	034672			50\$:	CKLOOP	;LOOP ON ERROR ?		
	034672	104406					TRAP	C\$CLP1
1885	034674	004737	016456		JSR	PC,TSTLOOP		

```

1889
1890
1891
1892
1893
1895 034712
1897 034720
1898 034720 100004
1899 034722 034730
1900 034724 000000
1901 034726 000010
1902
1903 034730
1904 034730 034742
1905 034732 000000
1906 034734 000020
1907 034736 000000 000000
1908
1909 034742
1910
1911
1912
1913
1914
1915
1916 034762 126 157 154 T8VCK: .ASCIZ 'Volume Check Bit Not Cleared'
1917 035017 126 157 154 T8NVCK: .ASCIZ 'Volume Check Bit (VCK) Not Clear After Initialize (XST0)'
1918 035110 103 157 156 T8SSR: .ASCIZ 'Contents of TSSR Incorrect After Write Characteristics'
1919 035177 126 157 154 T8TID: .ASCIZ 'Volume Check'
1920
1921 035214
035214
035214 104401
1922
1923
1924
1925
1926
1927
1928
1929
1930
1931
1932
1933
1934
1935
1936
1937
1938
1939
1940
1941
1942 035216
035216
1947 035216 005037 002224
1948 035222 012700 040241

```

```

; LOCAL STORAGE FOR THIS TEST
;
T8PACKET: .BLKB 10-<.-TSV2&7>
; COMMAND PACKET FOR TEST
; WRITE CHARACTERISTICS COMMAND
; ADDRESS OF CHARACTERISTICS BLOCK
; STARTING VALUE OF COUNTER
T8DA: .WORD T8BFR
; CHARACTERISTICS DATA BLOCK
; ADDRESS OF MESSAGE BUFFER
; LENGTH OF MESSAGE BUFFER
T8BFR: .BLKW 8.
; MESSAGE BUFFER

; LOCAL TEXT MESSAGES FOR TEST
;
T8VCK: .ASCIZ 'Volume Check Bit Not Cleared'
T8NVCK: .ASCIZ 'Volume Check Bit (VCK) Not Clear After Initialize (XST0)'
T8SSR: .ASCIZ 'Contents of TSSR Incorrect After Write Characteristics'
T8TID: .ASCIZ 'Volume Check'
; EVEN
; ENDTST

L10064: TRAP C&ETST

.SBTTL TEST 9: COMPLETION INTERRUPT

; THIS TEST VERIFIES THAT AN INTERRUPT IS GENERATED AT THE
; COMPLETION OF THE WRITE CHARACTERISTICS COMMAND IF THE INTERRUPT
; ENABLE (IE) BIT IN THE COMMAND HEADER WORD IS SET. THIS TEST
; CHECKS THE FUNCTIONING OF THE INTERRUPT LOGIC AND BASIC
; PROCESSING OF THE IE BIT.

; THE SEQUENCES OF TEST 7 ARE REPEATED, EXCEPT THAT THE INTERRUPT
; SERVICE ROUTINE IS SET UP TO EXPECT INTERRUPTS AND EACH WRITE
; CHARACTERISTICS COMMAND IS ISSUED WITH THE IE BIT SET (1). IT
; IS VERIFIED, WHERE APPROPRIATE, THAT THE IE STATUS BIT IN XST0
; OF ANY MESSAGE PACKET IS SET AND THAT A COMPLETION INTERRUPT IS
; GENERATED. FINALLY, A SEQUENCE OF TWO COMMANDS ARE ISSUED, THE
; FIRST WITH IE=1 AND THE SECOND WITH IE=0. IT IS VERIFIED THAT
; NO INTERRUPT IS GENERATED AFTER THE SECOND COMMAND AND THAT THE
; IE BIT IN XST0 IS 0.

; BGNTST
;
; T9::
CLR EXTFEA ; CLEAR EXTENDED FEATURES SWITCH
MOV #TST9ID,R0 ; ASCII MESSAGE TO IDENTIFY TEST

```

Address	Offset	Label	Operation	Operand	Comment
1949	035226	004737	016510		
1950	035232	012737	000024	002214	
1951	035240				
1952	035240				
	035240				
	035240	104402			
1953					
1954	035242	004737	040266		
1955	035246				
	035246	012700	000000		
	035252	104441			
1956	035254	012703	002762		
1957	035260	012704	037170		
1958	035264	012764	000010	000006	
1959	035272				
1960	035272				
	035272	104404			
1961					
1962	035274	004737	015774		
1963	035300	103405			
1967	035302	010001			
1968	035304				
	035304	104455			
	035306	001605			
	035310	003650			
	035312	012034			
1969	035314	005037	002220		
1970	035320	005037	002222		
1971	035324	010465	000000		
1972	035330	004737	016336		
1973	035334	103407			
1974	035336	010001			
1978	035340				
	035340	104455			
	035342	001606			
	035344	037707			
	035346	012046			
1979	035350	005237	002220		
1980	035354				
	035354	104406			
1981	035356				
	035356	104410			
	035360	000056			
1982	035362	005737	002222		
1983	035366	001004			
1987	035370				
	035370	104456			
	035372	001607			
	035374	037776			
	035376	012046			
1988	035400	016501	000002		
1989	035404	012702	000200		
1990	035410	032701	000100		
1991	035414	001402			
1992	035416	052702	000100		
1993	035422	020201			
1994	035424	001404			

```

1998 035426          ERRARD  ERRNO,T9NBA,PKTSSR      ;NBA NOT ZERO
      035426 104456
      035430 001610
      035432 037245
      035434 012046
1999 035436          301:
2000 035436          ENDSEG
      035436 104405
      035436 104405
2001
2002 035440 012364 000006      MOV      (R3),PKBCNT(R4)      ;SET THE TEST WORD
2003 035444 020327 003060      CMP      R3,#TBLEND      ;HAS ALL DATA BEEN TESTED ?
2004 035450 103002          BHS      551      ;BRANCH IF ALL DATA DONE
2005 035452 000137 035272      JMP      51      ;BRANCH TILL BACK TO ZERO
2006
2007 035456          551:      ENDSUB
      035456
      035456 104403
2008
2009 035460 005737 002220      TST      FATFLG      ;ANY FATAL ERRORS ?
2010 035464 001402          BEQ      601      ;BRANCH IF NOT
2011 035466 004737 017202      JSR      PC,CKDROP      ;TRY TO DROP THE UNIT
2012 035472 033727 037224 00C200 601:      BIT      T9BFR.12,#BIT7      ;EXTENDED FEATURES SET?
2013 035500 001402          BEQ      701      ;BR IF NO
2014 035502 005237 002224      INC      EXTFEA      ;SET EXT FEATURE FLAG
2015 035506          701:
2016
2017
2018
2019
2020
2021
2022
2023
2024
2025
2026 035506          BGNSUB
      035506
      035506 104402
2027
2028 035510          SETPRI  #PRI00
      035510 012700 000000
      035514 104441
2029 035516 012703 037232      MOV      #T92DATA,R3      ;START OF TEST DATA FOR SUBTEST
2030 035522 012704 037170      MOV      #T9PACKET,R4      ;GET THE ADDRESS OF COMMAND PACKET
2031 035526 004737 040266      JSR      PC,T9REST      ;RESTORE PACKET TO STARTING VALUES
2032
2033 035532          BGNSEG
      035532 104404
2034
2035 035534 004737 015774      JSR      PC,SOFINIT      ;DO SOFT INIT OF CONTROLLER
2036 035540 103405          BCS      101      ;BR IF SOFT INIT = OK
2040 035542 010001          MOV      R0,R1      ;SAVE CONTENTS OF TSSR
2041 035544          ERADF  ERRNO,SFIERR,SFIMSG      ;DEVICE FATAL ERROR DURING INIT?
      035544 104455
      035546 001611
      035550 003650

```

```

TRAP      CIERARD
.WORD     904
.WORD     T9NBA
.WORD     PKTSSR

```

```

;***** END SEGMENT *****
100001:
TRAP      L0ESLG

```

```

;SET THE TEST WORD
;HAS ALL DATA BEEN TESTED ?
;BRANCH IF ALL DATA DONE
;BRANCH TILL BACK TO ZERO

```

```

;***** END SUBTEST *****
L10066:
TRAP      CIESUB

```

```

;ANY FATAL ERRORS ?
;BRANCH IF NOT
;TRY TO DROP THE UNIT
;EXTENDED FEATURES SET?
;BR IF NO
;SET EXT FEATURE FLAG

```

```

;
;
;TEST 9, SUBTEST 2
;
;CHECK THAT UNUSED BITS BEING SET CAUSES
;WRITE CHARACTERISTICS COMMAND TO BE REJECTED
;
;

```

```

;***** BEGIN SUBTEST *****
T9.2:
TRAP      CIBSUB

```

```

;LOWER PRIORITY TO ALLOW INTERRUPTS
MOV      #PRI00,R0
TRAP      CISPRI

```

```

;START OF TEST DATA FOR SUBTEST
;GET THE ADDRESS OF COMMAND PACKET
;RESTORE PACKET TO STARTING VALUES

```

```

;***** BEGIN SEGMENT *****
TRAP      CIBSEG

```

```

;DO SOFT INIT OF CONTROLLER
;BR IF SOFT INIT = OK
;SAVE CONTENTS OF TSSR
;DEVICE FATAL ERROR DURING INIT?

```

```

TRAP      CIERDF
.WORD     905
.WORD     SFIERR

```

PC	PC+1	PC+2	PC+3	PC+4	PC+5	PC+6	PC+7	PC+8	PC+9	PC+10	PC+11	PC+12	PC+13	PC+14	PC+15	PC+16	PC+17	PC+18	PC+19	PC+20	PC+21	PC+22	PC+23	PC+24	PC+25	PC+26	PC+27	PC+28	PC+29	PC+30	PC+31	PC+32	PC+33	PC+34	PC+35	PC+36	PC+37	PC+38	PC+39	PC+40	PC+41	PC+42	PC+43	PC+44	PC+45	PC+46	PC+47	PC+48	PC+49	PC+50	PC+51	PC+52	PC+53	PC+54	PC+55	PC+56	PC+57	PC+58	PC+59	PC+60	PC+61	PC+62	PC+63	PC+64	PC+65	PC+66	PC+67	PC+68	PC+69	PC+70	PC+71	PC+72	PC+73	PC+74	PC+75	PC+76	PC+77	PC+78	PC+79	PC+80	PC+81	PC+82	PC+83	PC+84	PC+85	PC+86	PC+87	PC+88	PC+89	PC+90	PC+91	PC+92	PC+93	PC+94	PC+95	PC+96	PC+97	PC+98	PC+99	PC+100	PC+101	PC+102	PC+103	PC+104	PC+105	PC+106	PC+107	PC+108	PC+109	PC+110	PC+111	PC+112	PC+113	PC+114	PC+115	PC+116	PC+117	PC+118	PC+119	PC+120	PC+121	PC+122	PC+123	PC+124	PC+125	PC+126	PC+127	PC+128	PC+129	PC+130	PC+131	PC+132	PC+133	PC+134	PC+135	PC+136	PC+137	PC+138	PC+139	PC+140	PC+141	PC+142	PC+143	PC+144	PC+145	PC+146	PC+147	PC+148	PC+149	PC+150	PC+151	PC+152	PC+153	PC+154	PC+155	PC+156	PC+157	PC+158	PC+159	PC+160	PC+161	PC+162	PC+163	PC+164	PC+165	PC+166	PC+167	PC+168	PC+169	PC+170	PC+171	PC+172	PC+173	PC+174	PC+175	PC+176	PC+177	PC+178	PC+179	PC+180	PC+181	PC+182	PC+183	PC+184	PC+185	PC+186	PC+187	PC+188	PC+189	PC+190	PC+191	PC+192	PC+193	PC+194	PC+195	PC+196	PC+197	PC+198	PC+199	PC+200	PC+201	PC+202	PC+203	PC+204	PC+205	PC+206	PC+207	PC+208	PC+209	PC+210	PC+211	PC+212	PC+213	PC+214	PC+215	PC+216	PC+217	PC+218	PC+219	PC+220	PC+221	PC+222	PC+223	PC+224	PC+225	PC+226	PC+227	PC+228	PC+229	PC+230	PC+231	PC+232	PC+233	PC+234	PC+235	PC+236	PC+237	PC+238	PC+239	PC+240	PC+241	PC+242	PC+243	PC+244	PC+245	PC+246	PC+247	PC+248	PC+249	PC+250	PC+251	PC+252	PC+253	PC+254	PC+255	PC+256	PC+257	PC+258	PC+259	PC+260	PC+261	PC+262	PC+263	PC+264	PC+265	PC+266	PC+267	PC+268	PC+269	PC+270	PC+271	PC+272	PC+273	PC+274	PC+275	PC+276	PC+277	PC+278	PC+279	PC+280	PC+281	PC+282	PC+283	PC+284	PC+285	PC+286	PC+287	PC+288	PC+289	PC+290	PC+291	PC+292	PC+293	PC+294	PC+295	PC+296	PC+297	PC+298	PC+299	PC+300	PC+301	PC+302	PC+303	PC+304	PC+305	PC+306	PC+307	PC+308	PC+309	PC+310	PC+311	PC+312	PC+313	PC+314	PC+315	PC+316	PC+317	PC+318	PC+319	PC+320	PC+321	PC+322	PC+323	PC+324	PC+325	PC+326	PC+327	PC+328	PC+329	PC+330	PC+331	PC+332	PC+333	PC+334	PC+335	PC+336	PC+337	PC+338	PC+339	PC+340	PC+341	PC+342	PC+343	PC+344	PC+345	PC+346	PC+347	PC+348	PC+349	PC+350	PC+351	PC+352	PC+353	PC+354	PC+355	PC+356	PC+357	PC+358	PC+359	PC+360	PC+361	PC+362	PC+363	PC+364	PC+365	PC+366	PC+367	PC+368	PC+369	PC+370	PC+371	PC+372	PC+373	PC+374	PC+375	PC+376	PC+377	PC+378	PC+379	PC+380	PC+3
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TSVSA - HARDWARE TESTS MACRO M1113 14-JUN-84 15:15
TEST 9: COMPLETION INTERRUPT

SEQ 0147

036074	104456					TRAP	C1ERMRO
036076	001620					.WORD	912
036100	037420					.WORD	T93REJ
036102	012046					.WORD	PKTSSR
2137	036104			301:			
2138	036104			ENDSEG		;	END SEGMENT
	036104						100001:
	036104	104405				TRAP	C1ESEG
2139							
2140	036106	005203		INC	R3		;NEXT BYTE COUNT
2141	036110	020327	000006	CMP	R3,#6		;TESTED ALL INVALID ?
2142	036114	002002		BGE	591		;BRANCH IF TEST DONE
2143	036116	000137	035734	JMP	51		;BRANCH TILL BACK TO ZERO
2144							
2145	036122			591:	ENDSUB		;/\////////// END SUBTEST \//////////
	036122						L10070:
	036122	104403				TRAP	C1ESUB
2146							
2147				;			
2148				;			
2149				;TEST 9. SUBTEST 4			
2150				;			
2151				;SUBTEST TO VERIFY THAT A WRITE CHARACTERISTICS COMMAND IS			
2152				;REJECTED IF AN ILLEGAL DATA BLOCK ADDRESS IS ISSUED.			
2153				;			
2154				;-			
2155							
2156	036124			BGN SUB		;/\////////// BEGIN SUBTEST \//////////	
	036124						T9.4:
	036124	104402				TRAP	C1BSUB
2157							
2158	036126			SETPRI	#PRI00		;LOWER PRIORITY TO ALLOW INTERRUPTS
	036126	012700	000000			MOV	#PRI00,R0
	036132	104441				TRAP	C1SPRI
2159	036134	012703	037232	MOV	#T92DATA,R3		;START OF TEST DATA FOR SUBTEST
2160	036140	012704	037170	MOV	#T9PACKET,R4		;GET THE ADDRESS OF COMMAND PACKET
2161	036144	004737	040266	JSR	PC,T9REST		;RESTORE PACKET TO STARTING VALUES
2162							
2163							
2164	036150	004737	015774	JSR	PC,SOFINIT		;DO SOFT INIT OF CONTROLLER
2165	036154	103405		BCS	101		;BR IF SOFT INIT = OK
2169	036156	010001		MOV	R0,R1		;SAVE CONTENTS OF TSSR
2170	036160			ERRDF	ERRNO,SFIERR,SFIMSG		;DEVICE FATAL ERROR DURING INIT
	036160	104455				TRAP	C1ERDF
	036162	001621				.WORD	913
	036164	003650				.WORD	SFIERR
	036166	012034				.WORD	SFIMSG
2171	036170	005037	002222	101:	CLR	INTRECV	;CLEAR INTERRUPT RECEIVED FLAG
2172	036174	052737	000001		BIS	#1,T9DATA	;MAKE ADDRESS ODD
2173	036202	010465	000000		MOV	R4,TSDB(R5)	;SET THE PACKET ADDRESS
2174	036206	004737	016250		JSR	PC,WAITF	;WAIT FOR SSR TO SET
2175	036212	103405			BCS	151	;BR IF CARRY SET (GOOD RETURN)
2176	036214	010001			MOV	R0,R1	;SAVE CONTENTS OF TSSR
2180	036216				ERRDF	ERRNO,T9SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET
	036216	104455				TRAP	C1ERDF
	036220	001622				.WORD	914
	036222	037707				.WORD	T9SSR

Address	Offset	Value	Label	Operation	Comments	Register	Value
2181	036224	012046	154:	CKLOOP	;LOOP ON ERROR, IF FLAG SET	WORD	PKTSSR
2182	036226	104406		ESCAPE SUB	;BY-PASS SUBTEST IF FATAL ERROR	TRAP	C#CLP1
2183	036230	104410		TST INTRECV	;DID AN INTERRUPT OCCUR ?	WORD	C#ESCAPE
2184	036232	000056		BNE 224	;BRANCH IF YES	WORD	L10071-
2185	036234	005737	002222	ERRHRD	ERRNO,T9NINT,PKTSSR	WORD	
2186	036240	001004				WORD	
2187	036242	104456				WORD	C#ERHRD
2188	036244	001623				WORD	915
2189	036246	037776				WORD	T9NINT
2190	036250	012046				WORD	PKTSSR
2191	036252	016501	000002	224:	MOV TSSR(R5),R1		
2192	036254	012702	102206		MOV #SC!SSR!TSREJ!NBA,R2		
2193	036256	032701	000100		BIT #OFL,R1		
2194	036258	001402			BEQ 254		
2195	036260	052702	000100		BIS #OFL,R2		
2196	036262	020201		254:	CMP R2,R1		
2197	036264	001404			BEQ 304		
2198	036266	001404			ERRHRD ERRNO,T94REJ,PKTSSR		
2199	036268	001404					
2200	036270	104456				TRAP	C#ERHRD
2201	036272	001624				WORD	916
2202	036274	037513				WORD	T94REJ
2203	036276	012046				WORD	PKTSSR
2204	036278	012046		304:			
2205	036280	012046					
2206	036282	012046					
2207	036284	012046					
2208	036286	012046					
2209	036288	012046					
2210	036290	012046					
2211	036292	012046					
2212	036294	012046					
2213	036296	012046					
2214	036298	012046					
2215	036300	012046					
2216	036302	012046					
2217	036304	012046					
2218	036306	012046					
2219	036308	012046					
2220	036310	012046					
2221	036312	012046					
2222	036314	012046					
2223	036316	012046					
2224	036318	012046					
2225	036320	012046					
2226	036322	012046					
2227	036324	012046					
2228	036326	012046					
2229	036328	012046					
2230	036330	012046					
2231	036332	012046					
2232	036334	012046					
2233	036336	012046					
2234	036338	012046					
2235	036340	012046					
2236	036342	012046					
2237	036344	012046					

Address	Offset	Hex	Dec	Label	Op	Op2	Op3	Op4	Op5	Op6	Op7	Op8	Op9	Op10	Op11	Op12	Op13	Op14	Op15	Op16	Op17	Op18	Op19	Op20	Op21	Op22	Op23	Op24	Op25	Op26	Op27	Op28	Op29	Op30	Op31	Op32	Op33	Op34	Op35	Op36	Op37	Op38	Op39	Op40	Op41	Op42	Op43	Op44	Op45	Op46	Op47	Op48	Op49	Op50	Op51	Op52	Op53	Op54	Op55	Op56	Op57	Op58	Op59	Op60	Op61	Op62	Op63	Op64	Op65	Op66	Op67	Op68	Op69	Op70	Op71	Op72	Op73	Op74	Op75	Op76	Op77	Op78	Op79	Op80	Op81	Op82	Op83	Op84	Op85	Op86	Op87	Op88	Op89	Op90	Op91	Op92	Op93	Op94	Op95	Op96	Op97	Op98	Op99	Op100	Op101	Op102	Op103	Op104	Op105	Op106	Op107	Op108	Op109	Op110	Op111	Op112	Op113	Op114	Op115	Op116	Op117	Op118	Op119	Op120	Op121	Op122	Op123	Op124	Op125	Op126	Op127	Op128	Op129	Op130	Op131	Op132	Op133	Op134	Op135	Op136	Op137	Op138	Op139	Op140	Op141	Op142	Op143	Op144	Op145	Op146	Op147	Op148	Op149	Op150	Op151	Op152	Op153	Op154	Op155	Op156	Op157	Op158	Op159	Op160	Op161	Op162	Op163	Op164	Op165	Op166	Op167	Op168	Op169	Op170	Op171	Op172	Op173	Op174	Op175	Op176	Op177	Op178	Op179	Op180	Op181	Op182	Op183	Op184	Op185	Op186	Op187	Op188	Op189	Op190	Op191	Op192	Op193	Op194	Op195	Op196	Op197	Op198	Op199	Op200	Op201	Op202	Op203	Op204	Op205	Op206	Op207	Op208	Op209	Op210	Op211	Op212	Op213	Op214	Op215	Op216	Op217	Op218	Op219	Op220	Op221	Op222	Op223	Op224	Op225	Op226	Op227	Op228	Op229	Op230	Op231	Op232	Op233	Op234	Op235	Op236	Op237	Op238	Op239	Op240	Op241	Op242	Op243	Op244	Op245	Op246	Op247	Op248	Op249	Op250	Op251	Op252	Op253	Op254	Op255	Op256	Op257	Op258	Op259	Op260	Op261	Op262	Op263	Op264	Op265	Op266	Op267	Op268	Op269	Op270	Op271	Op272	Op273	Op274	Op275	Op276	Op277	Op278	Op279	Op280	Op281	Op282	Op283	Op284	Op285	Op286	Op287	Op288	Op289	Op290	Op291	Op292	Op293	Op294	Op295	Op296	Op297	Op298	Op299	Op300	Op301	Op302	Op303	Op304	Op305	Op306	Op307	Op308	Op309	Op310	Op311	Op312	Op313	Op314	Op315	Op316	Op317	Op318	Op319	Op320	Op321	Op322	Op323	Op324	Op325	Op326	Op327	Op328	Op329	Op330	Op331	Op332	Op333	Op334	Op335	Op336	Op337	Op338	Op339	Op340	Op341	Op342	Op343	Op344	Op345	Op346	Op347	Op348	Op349	Op350	Op351	Op352	Op353	Op354	Op355	Op356	Op357	Op358	Op359	Op360	Op361	Op362	Op363	Op364	Op365	Op366	Op367	Op368	Op369	Op370	Op371	Op372	Op373	Op374	Op375	Op376	Op377	Op378	Op379	Op380	Op381	Op382	Op383	Op384	Op385	Op386	Op387	Op388	Op389	Op390	Op391	Op392	Op393	Op394	Op395	Op396	Op397	Op398	Op399	Op400	Op401	Op402	Op403	Op404	Op405	Op406	Op407	Op408	Op409	Op410	Op411	Op412	Op413	Op414	Op415	Op416
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	036664	037776				.WORD	T9NINT
	036666	012046				.WORD	PKTSSR
2319	036670	016501	000002	22\$:	MOV	TSSR(R5),R1	;GET THE CONTENTS OF TSSR
2320	036674	012702	000200		MOV	#SSR,R2	;EXPECTED CONTENTS OF TSSR
2321	036700	032701	000100		BIT	#OFL,R1	;IS OFF-LINE BIT SET ?
2322	036704	001402			BEQ	25\$	;BRANCH IF NOT OFF-LINE
2323	036706	052702	000100		BIS	#OFL,R2	;SET OFF-LINE IN EXPECTED DATA
2324	036712	020201		25\$:	CMP	R2,R1	;DOES EXPECTED MATCH RECEIVED ?
2325	036714	001404			BEQ	30\$	;OKAY IF MATCH
2329	036716				ERRHRD	ERRNO,T9NBA,PKTSSR	;NBA NOT ZERO
	036716	104456					TRAP C\$ERHRD
	036720	001634					.WORD 924
	036722	037246					.WORD T9NBA
	036724	012046					.WORD PKTSSR
2330	036726			30\$:			
2331	036726				ENDSEG		;<<<<<<<<<<<< END SEGMENT <<<<<<<<<<<<<<
	036726						10000\$:
	036726	104405					TRAP C\$ESEG
2332							
2333	036730	012364	000006		MOV	(R3)+,PKBCNT(R4)	;SET THE TEST WORD
2334	036734	020327	003060		CMP	R3,#TBLEND	;HAS ALL DATA BEEN TESTED ?
2335	036740	103002			BHIS	55\$	;BRANCH IF ALL DATA DONE
2336	036742	000137	036562		JMP	5\$	;BRANCH TILL BACK TO ZERO
2337							
2338	036746			55\$:	ENDSUB		;////////// END SUBTEST //////////
	036746						L10073:
	036746	104403					TRAP C\$ESUB
2339							
2340							
2341				:			
2342				:			
2343				;TEST 9, SUBTEST 7			
2344				:			
2345				;TEST WRITE CHARACTERISTICS WITH/WITHOUT INTERRUPTS ENABLED			
2346				:			
2347				:-			
2348							
2349	036750				BGNSUB		;////////// BEGIN SUBTEST //////////
	036750						T9.7:
	036750	104402					TRAP C\$BSUB
2350							
2351	036752				SETPRI	#PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS
	036752	012700	000000				MOV #PRI00,R0
	036756	104441					TRAP C\$SPRI
2352	036760	012704	037170		MOV	#T9PACKET,R4	;GET THE ADDRESS OF COMMAND PACKET
2353	036764	004737	040266		JSR	PC,T9REST	;SET UP A VALID PACKET
2354	036770	004737	015774		JSR	PC,SOFINIT	;DO SOFT INIT OF CONTROLLER
2355	036774	103405			BCS	10\$	;BR IF SOFT INIT = OK
2359	036776	010001			MOV	R0,R1	;SAVE CONTENTS OF TSSR
2360	037000				ERRDF	ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT
	037000	104455					TRAP C\$ERDF
	037002	001635					.WORD 925
	037004	003650					.WORD SFIERR
	037006	012034					.WORD SFIMSG
2361	037010	005037	002222	10\$:	CLR	INTRECV	;CLEAR INTERRUPT RECEIVED FLAG
2362	037014	052714	000200		BIS	#BIT7,(R4)	;ENABLE INTERRUPTS
2363	037020	010465	000000		MOV	R4,TSD0(R5)	;SET THE PACKET ADDRESS

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TEST 9: COMPLETION INTERRUPT

SEQ 0152

2364	037024	004737	016336	JSR	PC,CHKTSSR	;WAIT FOR SSR TO SET	
2365	037030	103405		BCS	15\$	;BR IF CARRY SET (GOOD RETURN)	
2366	037032	010001		MOV	R0,R1	;SAVE CONTENTS OF TSSR	
2370	037034			ERRDF	ERRNO,T9SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET	
	037034	104455				TRAP	C\$ERDF
	037036	001636				.WORD	926
	037040	037707				.WORD	T9SSR
	037042	012046				.WORD	PKTSSR
2371	037044		15\$:	CKLOOP		;LOOP ON ERROR, IF FLAG SET	
	037044	104406				TRAP	C\$CLP1
2372	037046			ESCAPE	SUB	;BY PASS SUBTEST IF FATAL ERROR	
	037046	104410				TRAP	C\$ESCAPE
	037050	000102				.WORD	L10074-
2373	037052	005737	002222	TST	INTRECV	;DID AN INTERRUPT OCCUR ?	
2374	037056	001004		BNE	22\$	;BRANCH IF YES	
2378	037060			ERRHRD	ERRNO,T9NINT,PKTSSR		
	037060	104456				TRAP	C\$ERHRD
	037062	001637				.WORD	927
	037064	037776				.WORD	T9NINT
	037066	012046				.WORD	PKTSSR
2379	037070		22\$:	CKLOOP		;LOOP ON ERROR ?	
	037070	104406				TRAP	C\$CLP1
2380							
2381	037072	005037	002222	CLR	INTRECV	;CLEAR INTERRUPT RECEIVED FLAG	
2382	037076	042714	000200	BIC	#BIT7,(R4)	;DISABLE INTERRUPTS	
2383	037102	010465	000000	MOV	R4,TSD8(R5)	;SET THE PACKET ADDRESS	
2384	037106	004737	016336	JSR	PC,CHKTSSR	;WAIT FOR SSR TO SET	
2385	037112	103405		BCS	25\$	;BR IF CARRY SET (GOOD RETURN)	
2386	037114	010001		MOV	R0,R1	;SAVE CONTENTS OF TSSR	
2390	037116			ERRDF	ERRNO,T9SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET	
	037116	104455				TRAP	C\$ERDF
	037120	001640				.WORD	928
	037122	037707				.WORD	T9SSR
	037124	012046				.WORD	PKTSSR
2391	037126		25\$:	CKLOOP		;LOOP ON ERROR, IF FLAG SET	
	037126	104406				TRAP	C\$CLP1
2392	037130			ESCAPE	SUB	;BY-PASS SUBTEST IF FATAL ERROR	
	037130	104410				TRAP	C\$ESCAPE
	037132	000020				.WORD	L10074 .
2393	037134	005737	002222	TST	INTRECV	;DID AN INTERRUPT OCCUR ?	
2394	037140	001404		BEQ	30\$	;BRANCH IF NOT	
2398	037142			ERRHRD	ERRNO,T9INT,PKTSSR		
	037142	104456				TRAP	C\$ERHRD
	037144	001641				.WORD	929
	037146	040067				.WORD	T9INT
	037150	012046				.WORD	PKTSSR
2399	037152		30\$:				
2400	037152			ENDSUB		;////////// END SUBTEST //////////	
	037152					L10074:	
	037152	104403				TRAP	C\$ESUB
2401							
2402	037154			EXIT	TST	;ALL DONE THIS TEST	
	037154	104432				TRAP	C\$EXIT
	037156	001162				.WORD	L10065 .
2403							
2404							
2405							

;*
;LOCAL STORAGE FOR THIS TEST

```

2406 ;
2407 ;
2409 037160 .BLKB 10-<. TSV2&7>
2411 037170 T9PACKET: ;COMMAND PACKET FOR TEST
2412 037170 100204 .WORD 100204 ;WRITE CHAR COMMAND, WITH IE, ACK
2413 037172 037200 .WORD T9DATA ;ADDRESS OF CHARACTERISTICS BLOCK
2414 037174 000000 .WORD 0
2415 037176 000010 .WORD 8. ;STARTING VALUE OF BLOCK SIZE
2416 ;
2417 037200 T9DATA: ;CHARACTERISTICS DATA BLOCK
2418 037200 037212 .WORD T98FR ;ADDRESS OF MESSAGE BUFFER
2419 037202 000000 .WORD 0
2420 037204 000016 .WORD 14. ;LENGTH OF MESSAGE BUFFER
2421 037206 000000 000000 .WORD 0,0
2422 ;
2423 037212 T98FR: .BLKW 8. ;MESSAGE BUFFER
2424 ;
2425 ;*
2426 ;
2427 ;TEST DATA FOR SUBTEST TWO
2428 ;
2429 ;DATA HAS FORMAT:
2430 ;
2431 ; 1ST WORD OFFSET TO TEST WORD IN PACKET
2432 ; 2ND WORD BITS TO SET FOR TEST
2433 ;
2434 ;
2435 ;
2436 037232 T92DATA:
2437 037232 000000 037140 .WORD 0,BIT5!BIT6!BIT9!BIT10!BIT11!BIT12!BIT13
2438 037236 000002 000001 .WORD 2,BIT0
2439 037242 000004 100100 .WORD 4,BIT6!BIT15
2440 037246 T92DONE=.
2441 ;
2442 ;*
2443 ;LOCAL TEXT MESSAGES FOR TEST
2444 ;-
2445 ;-
2446 ;
2447 037246 127 122 111 T9NBA: .ASCIZ 'WRITE CHARACTERISTICS Command Not Accepted'
2448 037321 127 122 111 T92REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Non-Zero Unused Fields'
2449 037420 127 122 111 T93REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Data Count'
2450 037513 127 122 111 T94REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Block Address'
2451 037611 127 122 111 T95REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Buffer Length'
2452 037707 103 157 156 T9SSR: .ASCIZ 'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
2453 037776 105 170 160 T9NINT: .ASCIZ 'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
2454 040067 125 156 145 T9INT: .ASCIZ 'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
2455 040156 111 156 143 T9TSBA: .ASCIZ 'Incorrect TSBA Address After WRITE CHARACTERISTICS'
2456 040241 103 157 155 T9T9ID: .ASCIZ 'Completion Interrupt'
2457 .EVEN
2458 ;
2459 ;*
2460 ;
2461 ;ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
2462 ;
2463 ;
2464 ;-

```


2465				T9REST:		
2466	040266			SAVREG		;SAVE THE REGISTERS
2467	040266			MOV #T9PACKET,R1		;START OF THE PACKET
2468	040272	012701	037170	MOV #100204,(R1)+		;WRITE CHARACTERISTICS WITH ACK, IE
2469	040276	012721	100204	MOV #T9DATA,(R1)+		;ADDRESS OF CHAR DATA BLOCK
2470	040302	012721	037200	CLR (R1)+		;EXTENDED ADDRESS
2471	040306	005021		MOV #8.,(R1)+		;SIZE OF DATA BLOCK IN BYTES
2472	040310	012721	000010	MOV #T9BFR,(R1)+		;ADDRESS OF MESSAGE BUFFER
2473	040314	012721	037212	CLR (R1)+		
2474	040320	005021		MOV #14.,(R1)+		;LENGTH OF MESSAGE BUFFER
2475	040322	012721	000016	CLR (R1)+		
2476	040326	005021		CLR (R1)		
2477	040330	005011		CLR T9BFR		;CLEAR 1ST LOC IN MESSAGE BUFFER
2478	040332	005037	037212	RTS PC		;RETURN
2479	040336	000207		ENDTST		
2480	040340					
	040340				L10065:	
	040340	104401			TRAP C#ETST	
2481				.SBTTL TEST 10. BASIC PACKET PROTOCOL		
2482						
2483				:		
2484				: THIS TEST VERIFIES BASIC OPERATION OF THE MESSAGE BUFFER RELEASE		
2485				: COMMAND, THE FUNCTION OF THE ACK BIT IN THE COMMAND HEADER WORD,		
2486				: AND THE REGISTER MODIFICATION REFUSED (RMR) LOGIC.		
2487				:		
2488				:		
2489				:*		
2490				;TEST 10 SUBTEST 1		
2491				:		
2492				;CHECKS THAT THE MESSAGE BUFFER RELEASE COMMAND WORKS		
2493				;PROPERLY AND THAT NO INTERRUPT IS GENERATED EVEN		
2494				;IF THE "IE" BIT IS SET IN THE COMMAND PACKET		
2495				:		
2496				;-		
2497	040342			BGNTST		
	040342					T10::
2502	040342	012700	043277	MOV #TST10ID,R0		;ASCII MESSAGE TO IDENTIFY TEST
2503	040346	004737	016510	JSR PC,TSTSETUP		;DO INITIAL TEST SETUP
2504	040352	012737	000024	MOV #20.,LOOPCNT		;PERFORM 20 ITERATIONS
2505	040360			T10LOOP:		
2506						
2507	040360			BGNSUB		;////////// BEGIN SUBTEST //////////
	040360					T10.1:
	040360	104402				TRAP C#BSUB
2508						
2509	040362	004737	043326	JSR PC,T10RST		;SET PACKET TO INITIAL VALUES
2510	040366			SETPRI #PRI00		;LOWER PRIORITY TO ALLOW INTERRUPTS
	040366	012700	000000			MOV #PRI00,R0
	040372	104441				TRAP C#SPRI
2511	040374	012704	042470	MOV #T10PACKET,R4		;GET THE ADDRESS OF COMMAND PACKET
2512	040400	012764	000010	MOV #8.,PKBCNT(R4)		;START WITH MINIMUM ALLOWABLE VALUE
2513	040406			S\$:		
2514	040406			BGNSEG		;>>>>>>>>> BEGIN SEGMENT >>>>>>>>
	040406	104404				TRAP C#BSEG
2515						
2516	040410	004737	015774	JSR PC,SOFINIT		;DO SOFT INIT OF CONTROLLER
2517	040414	103405		BCS 10\$		;BR IF SOFT INIT = OK

Address	Offset	Hex	Dec	Label	Op	Op2	Op3	Op4	Op5	Op6	Op7	Op8	Op9	Op10	Op11	Op12	Op13	Op14	Op15	Op16	Op17	Op18	Op19	Op20	Op21	Op22	Op23	Op24	Op25	Op26	Op27	Op28	Op29	Op30	Op31	Op32	Op33	Op34	Op35	Op36	Op37	Op38	Op39	Op40	Op41	Op42	Op43	Op44	Op45	Op46	Op47	Op48	Op49	Op50	Op51	Op52	Op53	Op54	Op55	Op56	Op57	Op58	Op59	Op60	Op61	Op62	Op63	Op64	Op65	Op66	Op67	Op68	Op69	Op70	Op71	Op72	Op73	Op74	Op75	Op76	Op77	Op78	Op79	Op80	Op81	Op82	Op83	Op84	Op85	Op86	Op87	Op88	Op89	Op90	Op91	Op92	Op93	Op94	Op95	Op96	Op97	Op98	Op99	Op100	Op101	Op102	Op103	Op104	Op105	Op106	Op107	Op108	Op109	Op110	Op111	Op112	Op113	Op114	Op115	Op116	Op117	Op118	Op119	Op120	Op121	Op122	Op123	Op124	Op125	Op126	Op127	Op128	Op129	Op130	Op131	Op132	Op133	Op134	Op135	Op136	Op137	Op138	Op139	Op140	Op141	Op142	Op143	Op144	Op145	Op146	Op147	Op148	Op149	Op150	Op151	Op152	Op153	Op154	Op155	Op156	Op157	Op158	Op159	Op160	Op161	Op162	Op163	Op164	Op165	Op166	Op167	Op168	Op169	Op170	Op171	Op172	Op173	Op174	Op175	Op176	Op177	Op178	Op179	Op180	Op181	Op182	Op183	Op184	Op185	Op186	Op187	Op188	Op189	Op190	Op191	Op192	Op193	Op194	Op195	Op196	Op197	Op198	Op199	Op200	Op201	Op202	Op203	Op204	Op205	Op206	Op207	Op208	Op209	Op210	Op211	Op212	Op213	Op214	Op215	Op216	Op217	Op218	Op219	Op220	Op221	Op222	Op223	Op224	Op225	Op226	Op227	Op228	Op229	Op230	Op231	Op232	Op233	Op234	Op235	Op236	Op237	Op238	Op239	Op240	Op241	Op242	Op243	Op244	Op245	Op246	Op247	Op248	Op249	Op250	Op251	Op252	Op253	Op254	Op255	Op256	Op257	Op258	Op259	Op260	Op261	Op262	Op263	Op264	Op265	Op266	Op267	Op268	Op269	Op270	Op271	Op272	Op273	Op274	Op275	Op276	Op277	Op278	Op279	Op280	Op281	Op282	Op283	Op284	Op285	Op286	Op287	Op288	Op289	Op290	Op291	Op292	Op293	Op294	Op295	Op296	Op297	Op298	Op299	Op300	Op301	Op302	Op303	Op304	Op305	Op306	Op307	Op308	Op309	Op310	Op311	Op312	Op313	Op314	Op315	Op316	Op317	Op318	Op319	Op320	Op321	Op322	Op323	Op324	Op325	Op326	Op327	Op328	Op329	Op330	Op331	Op332	Op333	Op334	Op335	Op336	Op337	Op338	Op339	Op340	Op341	Op342	Op343	Op344	Op345	Op346	Op347	Op348	Op349	Op350	Op351	Op352	Op353	Op354	Op355	Op356	Op357	Op358	Op359	Op360	Op361	Op362	Op363	Op364	Op365	Op366	Op367	Op368	Op369	Op370	Op371	Op372	Op373	Op374	Op375	Op376	Op377	Op378	Op379	Op380	Op381	Op382	Op383	Op384	Op385	Op386	Op387	Op388	Op389	Op390	Op391	Op392	Op393	Op394	Op395	Op396	Op397	Op398	Op399	Op400	Op401	Op402	Op403	Op404	Op405	Op406	Op407	Op408	Op409	Op410	Op411	Op412	Op413	Op414	Op415	Op416	Op417	Op418	Op419	Op420	Op421	Op422	Op423	Op424	Op425	Op426	Op427	Op428	Op429	Op430	Op431	Op432	Op433	Op434	Op435	Op436	Op437	Op438	Op439	Op440	Op441	Op442	Op443	Op444	Op445	Op446	Op447	Op448	Op449	Op450	Op451	Op452	Op453	Op454	Op455	Op456	Op457	Op458	Op459	Op460	Op461	Op462	Op46
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TSVSA HARDWARE TESTS MACRO M1113 14 JUN 84 15:15
TEST 10: BASIC PACKET PROTOCOL

SEQ 0157

2613	040744			BGN SUB	////////// BEGIN SUBTEST //////////
	040744				T10.2:
	040744	104402			TRAP C10SUB
2614					
2615	040746	004737	043326	JSR PC,T10RST	;SET PACKET TO INITIAL VALUES
2616	040752			SETPRI @PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS
	040752	012700	000000		MOV @PRI00,R0
	040756	104441			TRAP C10PRI
2617	040760	012704	042470	MOV @T10PACKET,R4	;GET THE ADDRESS OF COMMAND PACKET
2618	040764	012764	000010	MOV @B..PKBCNT(R4)	;START WITH MINIMUM ALLOWABLE VALUE
2619	040772			51:	
2620	040772			BGN SEG	;***** BEGIN SEGMENT *****
	040772	104404			TRAP C10SEG
2621					
2622	040774	004737	015774	JSR PC,SOFINIT	;DO SOFT INIT OF CONTROLLER
2623	041000	103405		BCS 104	;BR IF SOFT INIT - OK
2627	041002	010001		MOV R0,R1	;SAVE CONTENTS OF TSSR
2628	041004			ERRDF ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT
	041004	104455			TRAP C10ERDF
	041006	001761			.WORD 1009
	041010	003650			.WORD SFIERR
	041012	012034			.WORD SFIMSG
2629	041014	005037	002220	101: CLR FATFLG	;CLEAR FATAL ERROR FLAG
2630	041020	005037	002222	CLR INTRECV	;CLEAR INTERRUPT RECEIVED FLAG
2631	041024	012737	000020	MOV @000020,T10DATA.6	;SET ERI IN CHARACTERISTICS DATA
2632	041032	010465	000000	MOV R4,TSSB(R5)	;SET THE PACKET ADDRESS
2633	041036	004737	016336	JSR PC,CHKTSSR	;WAIT FOR SSR TO SET
2634	041042	103407		BCS 151	;BR IF CARRY SET (GOOD RETURN)
2635	041044	010001		MOV R0,R1	;SAVE CONTENTS OF TSSR
2639	041046			ERRDF ERRNO,T10SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET
	041046	104455			TRAP C10ERDF
	041050	001762			.WORD 1010
	041052	043030			.WORD T10SSR
	041054	012046			.WORD PKTSSR
2640	041056	005237	002220	151: INC FATFLG	;SET FATAL ERROR FLAG
2641	041062			CKLOOP	;LOOP ON ERROR, IF FLAG SET
	041062	104406			TRAP C10CLP1
2642	041064			ESCAPE SEG	;BY-PASS SUBTEST IF FATAL ERROR
	041064	104410			TRAP C10ESCAPE
	041066	000056			.WORD 100008
2643	041070	005737	002222	TST INTRECV	;DID AN INTERRUPT OCCUR ?
2644	041074	001004		BNE 221	;BRANCH IF YES
2648	041076			ERRHRO ERRNO,T10NINT,PKTSSR	
	041076	104456			TRAP C10ERHRO
	041100	001763			.WORD 1011
	041102	043117			.WORD T10NINT
	041104	012046			.WORD PKTSSR
2649	041106	016501	000002	221: MOV TSSR(R5),R1	;GET THE CONTENTS OF TSSR
2650	041112	012702	000200	MOV @SSR,R2	;EXPECTED CONTENTS OF TSSR
2651	041116	032701	000100	BIT @OFL,R1	;IS OFF-LINE BIT SET ?
2652	041122	001402		BEQ 251	;BRANCH IF NOT OFF-LINE
2653	041124	052702	000100	BIS @OFL,R2	;SET OFF-LINE IN EXPECTED DATA
2654	041130	020201		251: CMP R2,R1	;DOES EXPECTED MATCH RECEIVED ?
2655	041132	001404		BEQ 301	;OKAY IF MATCH
2659	041134			ERRHRO ERRNO,T10NBA,PKTSSR	;NBA NOT ZERO
	041134	104456			TRAP C10ERHRO
	041136	001764			.WORD 1012

PC	PC+1	PC+2	PC+3	PC+4	PC+5	PC+6	PC+7	PC+8	PC+9	PC+10	PC+11	PC+12	PC+13	PC+14	PC+15	PC+16	PC+17	PC+18	PC+19	PC+20	PC+21	PC+22	PC+23	PC+24	PC+25	PC+26	PC+27	PC+28	PC+29	PC+30	PC+31	PC+32	PC+33	PC+34	PC+35	PC+36	PC+37	PC+38	PC+39	PC+40	PC+41	PC+42	PC+43	PC+44	PC+45	PC+46	PC+47	PC+48	PC+49	PC+50	PC+51	PC+52	PC+53	PC+54	PC+55	PC+56	PC+57	PC+58	PC+59	PC+60	PC+61	PC+62	PC+63	PC+64	PC+65	PC+66	PC+67	PC+68	PC+69	PC+70	PC+71	PC+72	PC+73	PC+74	PC+75	PC+76	PC+77	PC+78	PC+79	PC+80	PC+81	PC+82	PC+83	PC+84	PC+85	PC+86	PC+87	PC+88	PC+89	PC+90	PC+91	PC+92	PC+93	PC+94	PC+95	PC+96	PC+97	PC+98	PC+99	PC+100	PC+101	PC+102	PC+103	PC+104	PC+105	PC+106	PC+107	PC+108	PC+109	PC+110	PC+111	PC+112	PC+113	PC+114	PC+115	PC+116	PC+117	PC+118	PC+119	PC+120	PC+121	PC+122	PC+123	PC+124	PC+125	PC+126	PC+127	PC+128	PC+129	PC+130	PC+131	PC+132	PC+133	PC+134	PC+135	PC+136	PC+137	PC+138	PC+139	PC+140	PC+141	PC+142	PC+143	PC+144	PC+145	PC+146	PC+147	PC+148	PC+149	PC+150	PC+151	PC+152	PC+153	PC+154	PC+155	PC+156	PC+157	PC+158	PC+159	PC+160	PC+161	PC+162	PC+163	PC+164	PC+165	PC+166	PC+167	PC+168	PC+169	PC+170	PC+171	PC+172	PC+173	PC+174	PC+175	PC+176	PC+177	PC+178	PC+179	PC+180	PC+181	PC+182	PC+183	PC+184	PC+185	PC+186	PC+187	PC+188	PC+189	PC+190	PC+191	PC+192	PC+193	PC+194	PC+195	PC+196	PC+197	PC+198	PC+199	PC+200	PC+201	PC+202	PC+203	PC+204	PC+205	PC+206	PC+207	PC+208	PC+209	PC+210	PC+211	PC+212	PC+213	PC+214	PC+215	PC+216	PC+217	PC+218	PC+219	PC+220	PC+221	PC+222	PC+223	PC+224	PC+225	PC+226	PC+227	PC+228	PC+229	PC+230	PC+231	PC+232	PC+233	PC+234	PC+235	PC+236	PC+237	PC+238	PC+239	PC+240	PC+241	PC+242	PC+243	PC+244	PC+245	PC+246	PC+247	PC+248	PC+249	PC+250	PC+251	PC+252	PC+253	PC+254	PC+255	PC+256	PC+257	PC+258	PC+259	PC+260	PC+261	PC+262	PC+263	PC+264	PC+265	PC+266	PC+267	PC+268	PC+269	PC+270	PC+271	PC+272	PC+273	PC+274	PC+275	PC+276	PC+277	PC+278	PC+279	PC+280	PC+281	PC+282	PC+283	PC+284	PC+285	PC+286	PC+287	PC+288	PC+289	PC+290	PC+291	PC+292	PC+293	PC+294	PC+295	PC+296	PC+297	PC+298	PC+299	PC+300	PC+301	PC+302	PC+303	PC+304	PC+305	PC+306	PC+307	PC+308	PC+309	PC+310	PC+311	PC+312	PC+313	PC+314	PC+315	PC+316	PC+317	PC+318	PC+319	PC+320	PC+321	PC+322	PC+323	PC+324	PC+325	PC+326	PC+327	PC+328	PC+329	PC+330	PC+331	PC+332	PC+333	PC+334	PC+335	PC+336	PC+337	PC+338	PC+339	PC+340	PC+341	PC+342	PC+343	PC+344	PC+345	PC+346	PC+347	PC+348	PC+349	PC+350	PC+351	PC+352	PC+353	PC+354	PC+355	PC+356	PC+357	PC+358	PC+359	PC+360	PC+361	PC+362	PC+363	PC+364	PC+365	PC+366	PC+367	PC+368	PC+369	PC+370	PC+371	PC+372	PC+373	PC+374	PC+375	PC+376	PC+377	PC+378	PC+379	PC+380	PC+3
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Address	Offset	Label	OpCode	OpName	OpValue	Comment	Trap	TrapWord	TrapValue
2750	041460	001004							
2754	041462		BNE	ERRHRD	22:	ERRNO,T10NINT,PKTSSR			
	041462	104456					TRAP		C#ERHRD
	041464	001773					.WORD		1019
	041466	043117					.WORD		T10NINT
	041470	012046					.WORD		PKTSSR
2755	041472	016501	000002	22:	MOV	TSSR(R5),R1			
2756	041476	012702	000200		MOV	#SSR,R2			
2757	041502	032701	000100		BIT	#OFL,R1			
2758	041506	001402			BEQ	25:			
2759	041510	052702	000100		BIS	#OFL,R2			
2760	041514	020201		25:	CMP	R2,R1			
2761	041516	001404			BEQ	30:			
2765	041520				ERRHRD	ERRNO,T10NBA,PKTSSR			
	041520	104456					TRAP		C#ERHRD
	041522	001774					.WORD		1020
	041524	042671					.WORD		T10NBA
	041526	012046					.WORD		PKTSSR
2766	041530			30:					
2767	041530				ENDSEG				
	041530								
	041530	104405							
2768	041532				BGNSEG				
	041532	104404							
2769	041534	004737	043326		JSR	PC,T10RST			
2770	041540	005037	002222		CLR	INTRECV			
2771	041544	012737	025252	042512	MOV	#025252,T10BFR			
2772	041552	012714	100212		MOV	#100212,(R4)			
2773	041556	010465	000000		MOV	R4,TSDB(R5)			
2774	041562	004737	016336		JSR	PC,CHKTSSR			
2775	041566	103407			BCS	45:			
2776	041570	010001			MOV	R0,R1			
2780	041572				ERRDF	ERRNO,T10SSR,PKTSSR			
	041572	104455					TRAP		C#ERDF
	041574	001775					.WORD		1021
	041576	043030					.WORD		T10SSR
	041600	012046					.WORD		PKTSSR
2781	041602	005237	002220		INC	FATFLG			
2782	041606			45:	CKLOOP				
	041606	104406							
2783	041610	005737	002222		TST	INTRECV			
2784	041614	001404			BEQ	52:			
2788	041616				ERRHRD	ERRNO,T10INT,PKTSSR			
	041616	104456					TRAP		C#ERHRD
	041620	001776					.WORD		1022
	041622	043210					.WORD		T10INT
	041624	012046					.WORD		PKTSSR
2789	041626	016501	000002	52:	MOV	TSSR(R5),R1			
2790	041632	012702	000200		MOV	#SSR,R2			
2791	041636	032701	000100		BIT	#OFL,R1			
2792	041642	001402			BEQ	55:			
2793	041644	052702	000100		BIS	#OFL,R2			
2794	041650	020201		55:	CMP	R2,R1			
2795	041652	001404			BEQ	60:			
2799	041654				ERRHRD	ERRNO,T10NBA,PKTSSR			
	041654	104456					TRAP		C#ERHRD

Address	Offset	Label	Instruction	Comment	Trap	Code
041660	042753					T10NNBA
041662	012046					PKTSSR
2800	041664	604:				
2801	041664	013701	042512	MOV T10BFR,R1	PICK UP THE 1ST WORD OF MESSAGE BUFFER	
2802	041670	012702	025252	MOV #025252,R2	SET UP EXPECTED DATA	
2803	041674	020102		CMP R1,R2	WAS ANY MESSAGE REC'D	
2804	041676	001404		BEQ 704	BR, IF OK (EQUAL)	
2808	041700			ERRHRD ERRNO,T10MBF,EXPREC	MESSAGE BUFFER WAS MODIFIED	
	041700	104456			TRAP	C\$ERRHRD
	041702	002000			.WORD	1024
	041704	042574			.WORD	T10MBF
	041706	015474			.WORD	EXPREC
2809						
2810	041710			704: CKLOOP	LOOP ON ERROR IF FLAG SET	
	041710	104406			TRAP	C\$CLP1
2811						
2812	041712	005037	002222	CLR INTRECV	CLEAR INTERRUPT RECEIVED FLAG	
2813	041716	004737	043326	JSR PC,T10RST	RESET THE PACKETS AND COMMANDS	
2814	041722	042714	100000	BIC #100000,(R4)	CLEAR THE ACK BIT	
2815	041726	010465	000000	MOV R4,TSD8(R5)	SET THE PACKET ADDRESS	
2816	041732	004737	016336	JSR PC,CHKTSSR	WAIT FOR SSR TO SET	
2817	041736	103407		BCS 754	BR IF CARRY SET (GOOD RETURN)	
2818	041740	010001		MOV R0,R1	SAVE CONTENTS OF TSSR	
2822	041742			ERRDF ERRNO,T10SSR,PKTSSR	DEVICE FATAL SSR FAILED TO SET	
	041742	104455			TRAP	C\$ERDF
	041744	002001			.WORD	1025
	041746	043030			.WORD	T10SSR
	041750	012046			.WORD	PKTSSR
2823	041752	005237	002220	754: INC FATFLG	SET FATAL ERROR FLAG	
2824	041756			CKLOOP	LOOP ON ERROR, IF FLAG SET	
	041756	104406			TRAP	C\$CLP1
2825	041760			ESCAPE SEG	BY-PASS SUBTEST IF FATAL ERROR	
	041760	104410			TRAP	C\$ESCAPE
	041762	000062			.WORD	10001\$-
2826	041764	005737	002222	TST INTRECV	DID AN INTERRUPT OCCUR ?	
2827	041770	001006		BNE 824	BRANCH IF YES	
2831	041772	016500	000002	MOV TSSR(R5),R0	GET TSSR FOR ERROR REPORT	
2832	041776			ERRHRD ERRNO,T10NINT,PKTSSR		
	041776	104456			TRAP	C\$ERRHRD
	042000	002002			.WORD	1026
	042002	043117			.WORD	T10NINT
	042004	012046			.WORD	PKTSSR
2833	042006	016501	000002	824: MOV TSSR(R5),R1	GET THE CONTENTS OF TSSR	
2834	042012	012702	000200	MOV #SSR,R2	EXPECTED CONTENTS OF TSSR	
2835	042016	032701	000100	BIT #OFL,R1	IS OFF-LINE BIT SET ?	
2836	042022	001402		BEQ 854	BRANCH IF NOT OFF-LINE	
2837	042024	052702	000100	BIS #OFL,R2	SET OFF-LINE IN EXPECTED DATA	
2838	042030	020201		854: CMP R2,R1	DOES EXPECTED MATCH RECEIVED ?	
2839	042032	001404		BEQ 904	OKAY IF MATCH	
2843	042034			ERRHRD ERRNO,T10SSR,PKTSSR	NBA NOT ZERO	
	042034	104456			TRAP	C\$ERRHRD
	042036	002003			.WORD	1027
	042040	043030			.WORD	T10SSR
	042042	012046			.WORD	PKTSSR
2844	042044			904:		
2845	042044			ENDSEG	END SEGMENT	
	042044				10001\$:	

Address	Offset	Hex	Label	Instruction	Comment	Trap	Code
2846	042044	005737	002220	TST	FATFLG	;ANY FATAL ERRORS	C#ESEG
2847	042052	001403		BEQ	95:	;BR, IF NO FATAL ERRORS	
2848	042054	004737	017202	JSR	PC,CKDROP	;TRY TO DROP THE UNIT	
2849							
2850	042060			BGNSEG		;***** BGN SEGMENT *****	
2851	042060	104404				TRAP	C#BSEG
2852	042062	005037	002222	95:	CLR	INTRECV	
2853	042066	004737	043326	JSR	PC,T10RST	;RESET THE PACKETS AND COMMANDS	
2854	042072	010465	000000	MOV	R4,TSD8(R5)	;SET THE PACKET ADDRESS	
2855	042076	004737	016336	JSR	PC,CHKTSSR	;WAIT FOR SSR TO SET	
2856	042102	103407		BCS	100:	;BR IF CARRY SET (GOOD RETURN)	
2857	042104	010001		MOV	R0,R1	;SAVE CONTENTS OF TSSR	
2858	042106			ERRDF	ERRNO,T10SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET	
2859	042106	104455				TRAP	C#ERDF
2860	042110	002004				.WORD	1028
2861	042112	043030				.WORD	T10SSR
2862	042114	012046				.WORD	PKTSSR
2863	042116	005237	002220	100:	INC	FATFLG	
2864	042122			CKLOOP		;SET FATAL ERROR FLAG	
2865	042122	104406				;LOOP ON ERROR, IF FLAG SET	
2866	042124			ESCAPE	SEG	TRAP	C#CLP1
2867	042124	104410				;BY-PASS SUBTEST IF FATAL ERROR	
2868	042126	000062				TRAP	C#ESCAPE
2869	042130	005737	002222			.WORD	10002:-.
2870	042134	001006		TST	INTRECV	;DID AN INTERRUPT OCCUR ?	
2871	042136	016500	000002	BNE	112:	;BRANCH IF YES	
2872	042142			MOV	TSSR(R5),R0	;GET TSSR FOR ERROR REPORT	
2873	042142	104456		ERRHRD	ERRNO,T10NINT,PKTSSR		
2874	042144	002005				TRAP	C#ERHRD
2875	042146	043117				.WORD	1029
2876	042150	012046				.WORD	T10NINT
2877	042152	016501	000002	112:	MOV	TSSR(R5),R1	
2878	042156	012702	000200			;GET THE CONTENTS OF TSSR	
2879	042162	032701	000100			;EXPECTED CONTENTS OF TSSR	
2880	042166	001402		BIT	#OFL,R1	;IS OFF-LINE BIT SET ?	
2881	042170	052702	000100	BEQ	125:	;BRANCH IF NOT OFF-LINE	
2882	042174	020201		BIS	#OFL,R2	;SET OFF-LINE IN EXPECTED DATA	
2883	042176	001405		125:	CMP	R2,R1	
2884	042200			BEQ	130:	;DOES EXPECTED MATCH RECEIVED ?	
2885	042200	104456		ERRHRD	ERRNO,T10SSR,PKTSSR	;OKAY IF MATCH	
2886	042200	002006				;NBA NOT ZERO	
2887	042202	043030				TRAP	C#ERHRD
2888	042204	012046				.WORD	1030
2889	042206					.WORD	T10SSR
2890	042210					.WORD	PKTSSR
2891	042210			ENDSEG		;***** END SEGMENT *****	
2892	042210	104405				10002:-.	
2893	042212	005737	002220	130:	TST	FATFLG	
2894	042216	001402		BEQ	140:	;CHECK FATAL FLAG FOR SET	
2895	042220	004737	017202	JSR	PC,CKDROP	;BR, IF NOT SET (OK)	
2896	042224					;DROP DEVICE IF INDICATED	
2897	042224			ENDSUB		////////// END SUBTEST //////////	

2891					;TEST 10 SUBTEST 4	
2892						
2893					;CHECKS THAT THE REGISTER MODIFICATION REFUSED (RMR) BIT IN	
2894					;THE TSSR WILL BE SET IF A WRITE CHARACTERISTICS COMMAND	
2895					;BEING EXECUTED AND ANOTHER "WC" COMMAND IS ATTEMPTED	
2896						
2897						
2898						
2899	042226		BGNSUB		;/!!!!!!!!!!!!!! BEGIN SUBTEST !!!!!!!!!!!!!!!	
	042226				T10.4:	
	042226	104402			TRAP C#BSUB	
2900						
2901	042230	004737	043400		JSR PC,T10RT2	;SET SECOND PACKET UP
2902	042234	004737	043326		JSR PC,T10RST	;SET PACKET TO INITIAL VALUES
2903	042240				SETPRI #PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS
	042240	012700	000000			MOV #PRI00,R0
	042244	104441				TRAP C\$SPRI
2904	042246	012704	042470		MOV #T10PACKET,R4	;GET THE ADDRESS OF COMMAND PACKET
2905	042252	012703	042532		MOV #T10PKT,R3	;GET THE ADDRESS OF 2ND CMD PACKET
2906	042256	012764	000010	000006	MOV #B.,PKBCNT(R4)	;START WITH MINIMUM ALLOWABLE VALUE
2907	042264	012763	000010	000006	MOV #B.,PKBCNT(R3)	;START WITH MINIMUM ALLOWABLE VALUE
2908	042272			5\$:		
2909	042272		BGNSEG		;>>>>>>>>> BEGIN SEGMENT >>>>>>>>>	
	042272	104404			TRAP C#BSEG	
2910	042274	004737	015774		JSR PC,SOFINIT	;DO SOFT INIT OF CONTROLLER
2911	042300	103405			BCS 10\$	;BR IF SOFT INIT = OK
2915	042302	010001			MOV R0,R1	;SAVE CONTENTS OF TSSR
2916	042304				ERRDF ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT
	042304	104455				TRAP C\$ERDF
	042306	002007				.WORD 1031
	042310	003650				.WORD SFIERR
	042312	012034				.WORD SFIMSG
2917	042314	005037	002220	10\$:	CLR FATFLG	;CLEAR FATAL ERROR FLAG
2918	042320	005037	002222		CLR INTRECV	;CLEAR INTERRUPT RECEIVED FLAG
2919	042324	010465	000000		MOV R4,TSD8(R5)	;SET THE PACKET ADDRESS
2920	042330	010365	000000		MOV R3,TSD8(R5)	;SECOND COMMAND PACKET
2921	042334	004737	016250		JSR PC,WAITF	;WAIT FOR SSR TO SET
2922	042340	016501	000002		MOV TSSR(R5),R1	;GET CONTENTS OF TSSR REGISTER
2923	042344	032701	000200		BIT #SSR,R1	;CHECK FOR SSR (TSSR) SET
2924	042350	001006			BNE 15\$	;BR. IF SSR SET (C00D)
2928	042352				ERRDF ERRNO,T10SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET
	042352	104455				TRAP C\$ERDF
	042354	002010				.WORD 1032
	042356	043030				.WORD T10SSR
	042360	012046				.WORD PKTSSR
2929	042362	005237	002220		INC FATFLG	;SET FATAL ERROR FLAG
2930	042366			15\$:	CKLOOP	;LOOP ON ERROR, IF FLAG SET
	042366	104406				TRAP C\$CLP1
2931	042370				ESCAPE SEG	;BY-PASS SUBTEST IF FATAL ERROR
	042370	104410				TRAP C\$ESCAPE
	042372	000056				.WORD 10000\$.
2932	042374	005737	002222		TST INTRECV	;DID AN INTERRUPT OCCUR ?
2933	042400	001004			BNE 22\$	;BRANCH IF YES
2934						
2935						
2939	042402		ERRHRD		ERRNO,T10NINT,PKTSSR	
	042402	104456				TRAP C\$ERHRD

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TEST 10: BASIC PACKET PROTOCOL

SEQ 0164

042404	002011					.WORD	1033
042406	043117					.WORD	T10NINT
042410	012046					.WORD	PKTSSR
2940	042412	016501	000002	22:	MOV	TSSR(R5),R1	;GET THE CONTENTS OF TSSR
2941	042416	012702	110200		MOV	#SSR!RMR!SC,R2	;EXPECTED CONTENTS OF TSSR
2942	042422	032701	000100		BIT	#OFL,R1	;IS OFF-LINE BIT SET ?
2943	042426	001402			BEQ	25:	;BRANCH IF NOT OFF-LINE
2944	042430	052702	000100		BIS	#OFL,R2	;SET OFF-LINE IN EXPECTED DATA
2945	042434	020201		25:	CMP	R2,R1	;DOES EXPECTED MATCH RECEIVED ?
2946	042436	001404			BEQ	30:	;OKAY IF MATCH
2950	042440				ERRHRD	ERRNO,T10SSR,PKTSSR	;NBA NOT ZERO
	042440	104456					TRAP C!ERHRD
	042442	002012					.WORD 1034
	042444	043030					.WORD T10SSR
	042446	012046					.WORD PKTSSR
2951	042450			30:			
2952	042450				ENDSEF		;***** END SEGMENT *****
	042450						10000:
	042450	104405					TRAP C!ESEG
2953	042452				ENDSUB		;////////// END SUBTEST //////////
	042452						L10101:
	042452	104403					TRAP C!ESUB
2954	042454				EXIT	TST	;ALL DONE WITH THIS TEST
	042454	104432					TRAP C!EXIT
	042456	000774					.WORD L10075
2955							
2956							
2957							
2958							
2959							
2961	042460				.BLKB	10-<.-TSV2&7>	
2963	042470				T10PACKET:		;COMMAND PACKET FOR TEST
2964	042470	100204			.WORD	100204	;WRITE CHAR COMMAND, WITH IE, ACK
2965	042472	042500			.WORD	T10DATA	;ADDRESS OF CHARACTERISTICS BLOCK
2966	042474	000000			.WORD	0	
2967	042476	000010			.WORD	8.	;STARTING VALUE OF BLOCK SIZE
2968							
2969	042500				T10DATA:		;CHARACTERISTICS DATA BLOCK
2970	042500	042512			.WORD	T10BFR	;ADDRESS OF MESSAGE BUFFER
2971	042502	000000			.WORD	0	
2972	042504	000016			.WORD	14.	;LENGTH OF MESSAGE BUFFER
2973	042506	000000	000000()		.WORD	0,0	
2974							
2975	042512				T10BFR:	.BLKW 8.	;MESSAGE BUFFER
2976							
2977							
2978							
2979							
2980							
2981	042532				T10PKT:		;COMMAND PACKET FOR TEST
2982	042532	100204			.WORD	100204	;WRITE CHAR COMMAND, WITH IE, ACK
2983	042534	042542			.WORD	T10DTA	;ADDRESS OF CHARACTERISTICS BLOCK
2984	042536	000000			.WORD	0	
2985	042540	000010			.WORD	8.	;STARTING VALUE OF BLOCK SIZE
2986							
2987	042542				T10DTA:		;CHARACTERISTICS DATA BLOCK
2988	042542	042554			.WORD	T10BUFR	;ADDRESS OF MESSAGE BUFFER

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TEST 10: BASIC PACKET PROTOCOL

SEQ 0165

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2989 042544 000000          .WORD 0
2990 042546 000016          .WORD 14.          ;LENGTH OF MESSAGE BUFFER
2991 042550 000000 000000    .WORD 0,0
2992
2993 042554          T10BUF: .BLKW 8.          ;MESSAGE BUFFER
2994
2995          ;*
2996          ;LOCAL TEXT MESSAGES FOR TEST
2997          ;
2998
2999
3000 042574          115      145      163 T10MBF: .ASCIIZ 'Message Buffer Modified after MESSAGE BUFFER RELEASE Command
3001 042671          116      102      101 T10NBA: .ASCIIZ 'NBA Not Clear After WRITE CHARACTERISTICS Command'
3002 042753          116      102      101 T10NNBA: .ASCIIZ 'NBA Set After MESSAGE BUFFER RELEASE Command
3003 043030          103      157      156 T10SSR: .ASCIIZ 'Contents of TSSR Incorrect After WRITE CHARACTERISTICS
3004 043117          105      170      160 T10NINT: .ASCIIZ 'Expected Interrupt Not Received On WRITE CHARACTERISTICS
3005 043210          125      156      145 T10INT: .ASCIIZ 'Unexpected Interrupt Received On WRITE CHARACTERISTICS
3006 043277          102      141      163 TST10ID: .ASCIIZ 'Basic Packet Protocol'
3007          .EVEN
3008
3009
3010
3011          ;*
3012          ;
3013          ;ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
3014          ;
3015          ;
3016
3017 043326          T10RST:
3018 043326          SAVREG          ;SAVE THE REGISTERS
3019 043332 012701 042470          MOV #T10PACKET,R1          ;START OF THE PACKET
3020 043336 012721 100204          MOV #100204,(R1)+          ;WRITE CHARACTERISTICS WITH ACK, IE
3021 043342 012721 042500          MOV #T10DATA,(R1)+          ;ADDRESS OF CHAR DATA BLOCK
3022 043346 005021          CLR (R1)+          ;EXTENDED ADDRESS
3023 043350 012721 000010          MOV #8,(R1)+          ;SIZE OF DATA BLOCK IN BYTES
3024 043354 012721 042512          MOV #T10BFR,(R1)+          ;ADDRESS OF MESSAGE BUFFER
3025 043360 005021          CLR (R1)+
3026 043362 012721 000016          MOV #14,(R1)+          ;LENGTH OF MESSAGE BUFFER
3027 043366 005021          CLR (R1)+
3028 043370 005011          CLR (R1)
3029 043372 005037 042512          CLR T10BFR          ;CLEAR 1ST LOC IN MESSAGE BUFFER
3030 043376 000207          RTS PC          ;RETURN
3031
3032          ;*
3033          ;
3034          ;ROUTINE TO RESTORE COMMAND PACKET #2 TO START-UP (DEFAULT) VALUES
3035          ;
3036          ;
3037 043400          T10RT2:
3038 043400          SAVREG          ;SAVE THE REGISTERS
3039 043404 012701 042532          MOV #T10PKT,R1          ;START OF THE PACKET
3040 043410 012721 100204          MOV #100204,(R1)+          ;WRITE CHARACTERISTICS WITH ACK, IE
3041 043414 012721 042542          MOV #T10DTA,(R1)+          ;ADDRESS OF CHAR DATA BLOCK
3042 043420 005021          CLR (R1)+          ;EXTENDED ADDRESS
3043 043422 012721 000010          MOV #8,(R1)+          ;SIZE OF DATA BLOCK IN BYTES
3044 043426 012721 042554          MOV #T10BUFR,(R1)+          ;ADDRESS OF MESSAGE BUFFER
3045 043432 005021          CLR (R1)+

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3046	043434	012721	000016		MOV	#14.,(R1)+	;LENGTH OF MESSAGE BUFFER	
3047	043440	005021			CLR	(R1)+		
3048	043442	005011			CLR	(R1)		
3049	043444	005037	042554		CLR	T10BUFR	;CLEAR 1ST LOC IN MESSAGE BUFFER	
3050	043450	000207			RTS	PC	;RETURN	
3051	043452				ENDTST			
	043452						L10075:	
	043452	104401					TRAP	C#ETST
3052					.SBttl	TEST 11: NON-TAPE MOTION COMMANDS		
3053								
3054								
3055								
3056					*			
3057					:			
3058					:THIS TEST VERIFIES PROPER OPERATION OF THE INITIALIZE			
3059					:COMMAND. TWO SUBTESTS ARE USED. THE FIRST VERIFIES THAT			
3060					:THE COMMAND RUNS TO COMPLETION AND STORES A VALID			
3061					:MESSAGE PACKET. THE SECOND VERIFIES THAT NON-ZERO			
3062					:VALUES IN THE COMMAND MODE FIELD CAUSES COMMAND REJECT.			
3063					:			
3064					:			
3065	043454				BGNTST			
	043454						T11::	
3070	043454	012700	045462		MOV	#TST11ID,R0	;ASCII MESSAGE TO IDENTIFY TEST	
3071	043460	004737	016510		JSR	PC,TSTSETUP	;DO INITIAL TEST SETUP	
3072	043464	012737	000024	002214	MOV	#20.,LOOPCNT	;PERFORM 20 ITERATIONS	
3073	043472				T11LOOP:			
3074	043472				BGNsub		////////// BEGIN SUBTEST //////////	
	043472						T11.1:	
	043472	104402					TRAP	C#BSUB
3075								
3076	043474				SETPRI	#PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS	
	043474	012700	000000				MOV	#PRI00,R0
	043500	104441					TRAP	C\$SPPI
3077	043502	004737	015774		JSR	PC,SOFINIT	;DO SOFT INIT OF CONTROLLER	
3078	043506	103405			BCS	3\$	;BR IF SOFT INIT = OK	
3082	043510	010001			MOV	R0,R1	;SAVE CONTENTS OF TSSR	
3083	043512				ERRDF	ERRNO,\$FIERR,\$FIMSG	;DEVICE FATAL ERROR DURING INIT	
	043512	104455					TRAP	C\$ERDF
	043514	002115					.WORD	1101
	043516	003650					.WORD	\$FIERR
	043520	012034					.WORD	\$FIMSG
3084	043522				3\$:			
3085	043522	012704	044710		MOV	#T11PK2,R4	;WRITE CHARACTERISTICS PACKET	
3086	043526	004737	010662		JSR	PC,WRTCHR	;ISSUE WRITE CHARACTERISTICS	
3087	043532	103404			BCS	4\$	;BR, IF COMMAND ISSUED OK	
3091	043534				ERRHRD	ERRNO,WRTMSG,\$FIMSG	;WRITE CHARACTERISTISC FAILED	
	043534	104456					TRAP	C\$ERHRD
	043536	002116					.WORD	1102
	043540	005054					.WORD	WPTMSG
	043542	012034					.WORD	\$FIMSG
3092	043544				4\$:			
3093	043544	004737	045514		JSR	PC,T11REST	;SET UP PACKET FOR COMMAND	
3094	043550	012704	044640		MOV	#T11PACKET,R4	;GET THE ADDRESS OF COMMAND PACKET	
3095	043554				5\$:			
3096	043554				BGNSEG		>>>>>>>>> BEGIN SEGMENT >>>>>>>>>	
	043554	104404					TRAP	C\$BSEG

3097									
3098	043556	005037	002220	104:	CLR	FATFLG	;CLEAR FATAL ERROR FLAG		
3099	043562	005037	002222		CLR	INTRECV	;CLEAR INTERRUPT RECEIVED FLAG		
3100	043566	010465	000000		MOV	R4,TSD8(R5)	;SET THE PACKET ADDRESS		
3101	043572	004737	016336		JSR	PC,CHKTSSR	;WAIT FOR SSR TO SET		
3102	043576	103407			BCS	154	;BR IF CARRY SET (GOOD RETURN)		
3103	043600	C10001			MOV	R0,R1	;SAVE CONTENTS OF TSSR		
3107	043602				ERRDF	ERRNO,T11SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET		
	043602	104455						TRAP	C\$ERDF
	043604	002117						.WORD	1103
	043606	045204						.WORD	T11SSR
	043610	012046						.WORD	PKTSSR
3108	043612	005237	002220		INC	FATFLG	;SET FATAL ERROR FLAG		
3109	043616			154:	CKLOOP		;LOOP ON ERROR, IF FLAG SET		
	043616	104406						TRAP	C\$CLP1
3110	043620				ESCAPE	SEG	;BY-PASS SUBTEST IF FATAL ERROR		
	043620	104410						TRAP	C\$ESCAPE
	043622	000074						.WORD	100004
3111	043624	005737	002222		ST	INTRECV	;DID AN INTERRUPT OCCUR ?		
3112	043630	001004			BNE	224	;BRANCH IF YES		
3116	043632				ERRHRD	ERRNO,T11NINT,PKTSSR			
	043632	104456						TRAP	C\$ERHRD
	043634	002120						.WORD	1104
	043636	045334						.WORD	T11NINT
	043640	012046						.WORD	PKTSSR
3117	043642	016501	000002	224:	MOV	TSSR(R5),R1	;GET THE CONTENTS OF TSSR		
3118	043646	012702	000200		MOV	#SSR,R2	;EXPECTED CONTENTS OF TSSR		
3119	043652	032701	000100		BIT	#OFL,R1	;IS OFF-LINE BIT SET ?		
3120	043656	001402			BEQ	254	;BRANCH IF NOT OFF-LINE		
3121	043660	052702	000100		BIS	#OFL,R2	;SET OFF-LINE IN EXPECTED DATA		
3122	043664	020201		254:	CMP	R2,R1	;DOES EXPECTED MATCH RECEIVED ?		
3123	043666	001404			BEQ	304	;OKAY IF MATCH		
3127	043670				ERRHRD	ERRNO,T11NSA,PKTSSR	;NSA NOT ZERO		
	043670	104456						TRAP	C\$ERHRD
	043672	002121						.WORD	1105
	043674	044752						.WORD	T11NSA
	043676	012046						.WORD	PKTSSR
3128	043700			304:					
3129	043700	004737	011114	354:	JSR	PC,CKRAM	;CHECK RAM TO MEMORY		
3130	043704	103405			BCS	594	;RAM OK GO ON		
3134	043706				ERRHRD	ERRNO,PKTRAM,RAMERR	;THEY DON'T MATCH		
	043706	104456						TRAP	C\$ERHRD
	043710	002122						.WORD	1106
	043712	004743						.WORD	PKTRAM
	043714	015510						.WORD	RAMERR
3135	043716				ENDSEG		;***** END SEGMENT *****		
	043716						100004:		
	043716	104405						TRAP	C\$ESEG
3136									
3137									
3138	043720			594:	ENDSUB		;////////// END SUBTEST //////////		
	043720						L10103:		
	043720	104403						TRAP	C\$ESUB
3139									
3140	043722	005737	002220		TST	FATFLG	;ANY FATAL ERRORS ?		
3141	043726	001402			BEQ	604	;BRANCH IF NOT		
3142	043730	004737	017202		JSR	PC,CKDROP	;TRY TO DROP THE UNIT		

	044056	104410					TRAP	C\$ESCAPE
	044060	000074					.WORD	10000\$.
3191	044062	005737	002222	TST	INTRECV	;DID AN INTERRUPT OCCUR ?		
3192	044066	001004		BNE	22\$	;BRANCH IF YES		
3196	044070			ERRHRD	ERRNO,T11NINT,PKTSSR			
	044070	104456					TRAP	C\$ERHRD
	044072	002126					.WORD	1110
	044074	045334					.WORD	T11NINT
	044076	012046					.WORD	PKTSSR
3197	044100	016501	000C02	22\$: MOV	TSSR(R5),R1	;GET THE CONTENTS OF TSSR		
3198	044104	012702	100206	MOV	#SC!SSR!TSREJ,R2	;EXPECTED CONTENTS OF TSSR		
3199	044110	032701	000100	BIT	#OFL,R1	;IS OFF-LINE BIT SET ?		
3200	044114	001402		BEQ	25\$	;BRANCH IF NOT OFF-LINE		
3201	044116	052702	000100	BIS	#OFL,R2	;SET OFF-LINE IN EXPECTED DATA		
3202	044122	020201		25\$: CMP	R2,R1	;DOES EXPECTED MATCH RECEIVED ?		
3203	044124	001404		BEQ	30\$	;OKAY IF MATCH		
3207	044126			ERRHRD	ERRNO,T112REJ,PKTSSR	;COMMAND NOT REJECTED		
	044126	104456					TRAP	C\$ERHRD
	044130	002127					.WORD	1111
	044132	045012					.WORD	T112REJ
	044134	012046					.WORD	PKTSSR
3208	044136			30\$: JSR	PC,CKRAM	;CHECK RAM TO MEMORY		
3209	044136	004737	011114	35\$: BCS	59\$	;RAM OK GO ON		
3210	044142	103405		ERRHRD	ERRNO,PKTRAM,RAMERR	;THEY DON'T MATCH		
3214	044144							
	044144	104456					TRAP	C\$ERHRD
	044146	002130					.WORD	1112
	044150	004743					.WORD	PKTRAM
	044152	015510					.WORD	RAMERR
3215	044154			ENDSEG		;<<<<<<<<<<<< END SEGMENT <<<<<<<<<<<<<<		
	044154					10000\$:		
	044154	104405					TRAP	C\$ESEG
3216								
3217	044156			59\$: ENDSUB		;////////// END SUBTEST //////////		
	044156					L10104:		
	044156	104403					TRAP	C\$ESUB
3218								
3219				;*				
3220				:				
3221				;TEST 11. SUBTEST 3				
3222				:				
3223				;CHECK THAT THE GET STATUS COMMAND IS ACCEPTED				
3224				:				
3225				;-				
3226								
3227	044160			BGNSUB		;////////// BEGIN SUBTEST //////////		
	044160					T11.3.		
	044160	104402					TRAP	C\$BSUB
3228								
3229	044162			SETPRI	#PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS		
	044162	012700	000000				MOV	#PRI00,R0
	044166	104441					TRAP	C\$SPRI
3230	044170			BGNSEG		;>>>>>>>>> BEGIN SEGMENT >>>>>>>		
	044170	104404					TRAP	C\$BSEC
3231	044172	004737	015774	JSR	PC,SOFINIT	;DO SOFT INIT OF CONTROLLER		
3232	044176	103405		BCS	3\$	;BR IF SOFT INIT - OK		
3236	044200	010001		MOV	R0,R1	;SAVE CONTENTS OF TSSR		

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TEST 11: NON TAPE MOTION COMMAND

SEQ 0170

3237	044202			ERRDF	ERRNO, SFIERR, SFI MSG	:DEVICE FATAL ERROR DURING INIT	
	044202	104455				TRAP	C0ERRDF
	044204	002131				.WORD	1113
	044206	003650				.WORD	SFIERR
	044210	012034				.WORD	SFI MSG
3238	044212		34:				
3239	044212	012704	044710	MOV	@T11PK2, R4	:WRITE CHARACTERISTICS PACKET	
3240	044216	004737	010662	JSR	PC, WRCHR	:ISSUE WRITE CHARACTERISTICS	
3241	044222	103404		BCS	48	:BR, IF COMMAND ISSUED OK	
3245	044224			ERRMRO	ERRNO, WRMSG, SFI MSG	:WRITE CHARACTERISTICS FAILED	
	044224	104456				TRAP	C0ERRMRO
	044226	002132				.WORD	1114
	044230	005054				.WORD	WRMSG
	044232	012034				.WORD	SFI MSG
3246	044234		44:				
3247	044234	004737	045514	JSR	PC, T11REST	:SET UP PACKET FOR COMMAND	
3248	044240	012704	044640	MOV	@T11PACKET, R4	:GET THE ADDRESS OF COMMAND PACKET	
3249	044244						
3250	044244	005037	002222	54:			
3251	044250	010465	000000	104:	CLR	INTRECV	:CLEAR INTERRUPT RECEIVED FLAG
3252	044254	004737	016336		MOV	R4, TSD8(R5)	:SET THE PACKET ADDRESS
3253	044260	103405			JSR	PC, CHKTSR	:WAIT FOR SSR TO SET
3254	044262	010001			BCS	158	:BR IF CARRY SET (GOOD RETURN)
3258	044264				MOV	R0, R1	:SAVE CONTENTS OF TSSR
	044264	104455			ERRDF	ERRNO, T11SR2, PKTSSR	:DEVICE FATAL SSR FAILED TO SET
	044266	002133				TRAP	C0ERRDF
	044270	045260				.WORD	1115
	044272	012046				.WORD	T11SR2
						.WORD	PKTSSR
3259	044274		154:	CKLOOP		:LOOP ON ERROR, IF FLAG SET	
	044274	104406				TRAP	C0CLP1
3260	044276			ESCAPE	SEG	:BY-PASS SUBTEST IF FATAL ERROR	
	044276	104410				TRAP	C0ESCAPE
	044300	000074				.WORD	100001
3261	044302	005737	002222	TST	INTRECV	:DID AN INTERRUPT OCCUR ?	
3262	044306	001004		BNE	224	:BRANCH IF YES	
3266	044310			ERRMRO	ERRNO, T11NINT, PKTSSR		
	044310	104456				TRAP	C0ERRMRO
	044312	002134				.WORD	1116
	044314	045334				.WORD	T11NINT
	044316	012046				.WORD	PKTSSR
3267	044320	016501	000002	224:	MOV	TSSR(R5), R1	:GET THE CONTENTS OF TSSR
3268	044324	012702	000200		MOV	@SSR, R2	:EXPECTED CONTENTS OF TSSR
3269	044330	032701	000100		BIT	@OFL, R1	:IS OFF-LINE BIT SET ?
3270	044334	001402			BEQ	254	:BRANCH IF NOT OFF-LINE
3271	044336	052702	000100		BIS	@OFL, R2	:SET OFF-LINE IN EXPECTED DATA
3272	044342	020201		254:	CMR	R2, R1	:DOES EXPECTED MATCH RECEIVED ?
3273	044344	001404			BEQ	304	:OKAY IF MATCH
3277	044346			ERRMRO	ERRNO, T113REJ, PKTSSR	:COMMAND NOT ACCEPTED	
	044346	104456				TRAP	C0ERRMRO
	044350	002135				.WORD	1117
	044352	045073				.WORD	T113REJ
	044354	012046				.WORD	PKTSSR
3278	044356		304:				
3279	044356	004737	011114	354:	JSR	PC, CKRAM	:CHECK RAM TO MEMORY
3280	044362	103405			BCS	594	:RAM OK GO ON
3284	044364			ERRMRO	ERRNO, PKTRAM, RAMERR	:THEY DON'T MATCH	
	044364	104456				TRAP	C0ERRMRO

PC	PC+4	PC+8	PC+12	PC+16	PC+20	PC+24	PC+28	PC+32	PC+36	PC+40	PC+44	PC+48	PC+52	PC+56	PC+60	PC+64	PC+68	PC+72	PC+76	PC+80	PC+84	PC+88	PC+92	PC+96	PC+100	PC+104	PC+108	PC+112	PC+116	PC+120	PC+124	PC+128	PC+132	PC+136	PC+140	PC+144	PC+148	PC+152	PC+156	PC+160	PC+164	PC+168	PC+172	PC+176	PC+180	PC+184	PC+188	PC+192	PC+196	PC+200	PC+204	PC+208	PC+212	PC+216	PC+220	PC+224	PC+228	PC+232	PC+236	PC+240	PC+244	PC+248	PC+252	PC+256	PC+260	PC+264	PC+268	PC+272	PC+276	PC+280	PC+284	PC+288	PC+292	PC+296	PC+300	PC+304	PC+308	PC+312	PC+316	PC+320	PC+324	PC+328	PC+332	PC+336	PC+340	PC+344	PC+348	PC+352	PC+356	PC+360	PC+364	PC+368	PC+372	PC+376	PC+380	PC+384	PC+388	PC+392	PC+396	PC+400	PC+404	PC+408	PC+412	PC+416	PC+420	PC+424	PC+428	PC+432	PC+436	PC+440	PC+444	PC+448	PC+452	PC+456	PC+460	PC+464	PC+468	PC+472	PC+476	PC+480	PC+484	PC+488	PC+492	PC+496	PC+500	PC+504	PC+508	PC+512	PC+516	PC+520	PC+524	PC+528	PC+532	PC+536	PC+540	PC+544	PC+548	PC+552	PC+556	PC+560	PC+564	PC+568	PC+572	PC+576	PC+580	PC+584	PC+588	PC+592	PC+596	PC+600	PC+604	PC+608	PC+612	PC+616	PC+620	PC+624	PC+628	PC+632	PC+636	PC+640	PC+644	PC+648	PC+652	PC+656	PC+660	PC+664	PC+668	PC+672	PC+676	PC+680	PC+684	PC+688	PC+692	PC+696	PC+700	PC+704	PC+708	PC+712	PC+716	PC+720	PC+724	PC+728	PC+732	PC+736	PC+740	PC+744	PC+748	PC+752	PC+756	PC+760	PC+764	PC+768	PC+772	PC+776	PC+780	PC+784	PC+788	PC+792	PC+796	PC+800	PC+804	PC+808	PC+812	PC+816	PC+820	PC+824	PC+828	PC+832	PC+836	PC+840	PC+844	PC+848	PC+852	PC+856	PC+860	PC+864	PC+868	PC+872	PC+876	PC+880	PC+884	PC+888	PC+892	PC+896	PC+900	PC+904	PC+908	PC+912	PC+916	PC+920	PC+924	PC+928	PC+932	PC+936	PC+940	PC+944	PC+948	PC+952	PC+956	PC+960	PC+964	PC+968	PC+972	PC+976	PC+980	PC+984	PC+988	PC+992	PC+996	PC+1000	PC+1004	PC+1008	PC+1012	PC+1016	PC+1020	PC+1024	PC+1028	PC+1032	PC+1036	PC+1040	PC+1044	PC+1048	PC+1052	PC+1056	PC+1060	PC+1064	PC+1068	PC+1072	PC+1076	PC+1080	PC+1084	PC+1088	PC+1092	PC+1096	PC+1100	PC+1104	PC+1108	PC+1112	PC+1116	PC+1120	PC+1124	PC+1128	PC+1132	PC+1136	PC+1140	PC+1144	PC+1148	PC+1152	PC+1156	PC+1160	PC+1164	PC+1168	PC+1172	PC+1176	PC+1180	PC+1184	PC+1188	PC+1192	PC+1196	PC+1200	PC+1204	PC+1208	PC+1212	PC+1216	PC+1220	PC+1224	PC+1228	PC+1232	PC+1236	PC+1240	PC+1244	PC+1248	PC+1252	PC+1256	PC+1260	PC+1264	PC+1268	PC+1272	PC+1276	PC+1280	PC+1284	PC+1288	PC+1292	PC+1296	PC+1300	PC+1304	PC+1308	PC+1312	PC+1316	PC+1320	PC+1324	PC+1328	PC+1332	PC+1336	PC+1340	PC+1344	PC+1348	PC+1352	PC+1356	PC+1360	PC+1364	PC+1368	PC+1372	PC+1376	PC+1380	PC+1384	PC+1388	PC+1392	PC+1396	PC+1400	PC+1404	PC+1408	PC+1412	PC+1416	PC+1420	PC+1424	PC+1428	PC+1432	PC+1436	PC+1440	PC+1444	PC+1448	PC+1452
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3331 044510          ERRDF  ERRNO,T11SR2,PKTSSR      ;DEVICE FATAL SSR FAILED TO SET
      044510 104455                                     TRAP      C#ERRDF
      044512 002141                                     .WORD      1121
      044514 045260                                     .WORD      T11SR2
      044516 012046                                     .WORD      PKTSSR
3332 044520          151:  CKLOOP                    ;LOOP ON ERROR, IF FLAG SET
      044520 104406                                     TRAP      C#CLP1
3333 044522          ESCAPE  SUB                      ;BY-PASS SUBTEST IF FATAL ERROR
      044522 104410                                     TRAP      C#ESCAPE
      044524 000076                                     .WORD      L10106
3334 044526 005737 002222          TST      INTRECV    ;DID AN INTERRUPT OCCUR ?
3335 044532 001004          BNE      221              ;BRANCH IF YES
3339 044534          ERRHRD  ERRNO,T11NINT,PKTSSR
      044534 104456                                     TRAP      C#ERRRD
      044536 002142                                     .WORD      1122
      044540 045334                                     .WORD      T11NINT
      044542 012046                                     .WORD      PKTSSR
3340 044544 016501 000002          221:  MOV      TSSR(R5),R1      ;GET THE CONTENTS OF TSSR
3341 044550 012702 100206          MOV      #SC!SSR!TSREJ,R2      ;EXPECTED CONTENTS OF TSSR
3342 044554 032701 000100          BIT      #OFL,R1              ;IS OFF-LINE BIT SET ?
3343 044560 001402          BEQ      251              ;BRANCH IF NOT OFF-LINE
3344 044562 052702 000100          BIS      #OFL,R2              ;SET OFF-LINE IN EXPECTED DATA
3345 044566 020201          251:  CMP      R2,R1              ;DOES EXPECTED MATCH RECEIVED ?
3346 044570 001404          BEQ      301              ;OKAY IF MATCH
3350 044572          ERRHRD  ERRNO,T114REJ,PKTSSR      ;COMMAND NOT REJECTED
      044572 104456                                     TRAP      C#ERRRD
      044574 002143                                     .WORD      1123
      044576 045123                                     .WORD      T114REJ
      044600 012046                                     .WORD      PKTSSR
3351 044602          301:
3352
3353 044602 004737 011114          351:  JSR      PC,CKRAM          ;CHECK RAM TO MEMORY
3354 044606 103404          BCS      591              ;RAM OK GO ON
3358 044610          ERRHRD  ERRNO,PKTRAM,RAMERR      ;THEY DON'T MATCH
      044610 104456                                     TRAP      C#ERRRD
      044612 002144                                     .WORD      1124
      044614 004743                                     .WORD      PKTRAM
      044616 015510                                     .WORD      RAMERR
3359 044620          591:
3360 044620          ENDSEG
      044620 104405                                     ;***** END SEGMENT *****
      044620 104405                                     100004:
3361 044622          ENDSUB
      044622 104403                                     ;////////// END SUBTEST //////////
      044622 104403                                     L10106:
3362
3363 044624          EXIT   TST
      044624 104432                                     TRAP      C#EXIT
      044626 000772                                     .WORD      L10102
3364
3365          ;*
3366          ;LOCAL STORAGE FOR THIS TEST
3367          ;*
3368
3370 044630          .BLKB   10-<.-TSV267>
3372 044640          T11PACKET:
3373 044640 100204          .WORD   100204
      ;COMMAND PACKET FOR TEST
      ;WRITE CHAR COMMAND, WITH IE, ACK

```

TSVSA - HARDWARE TESTS MACRO M1113 14 JUN-84 15:15
TEST 11: NON TAPE MOTION COMMANDS

SEQ 0173

```

3374 044642 044650 .WORD T11DATA ;ADDRESS OF CHARACTERISTICS BLOCK
3375 044644 000000 .WORD 0
3376 044646 000010 .WORD 8. ;STARTING VALUE OF BLOCK SIZE
3377
3378 044650 T11DATA: ;CHARACTERISTICS DATA BLOCK
3379 044650 044662 .WORD T11BFR ;ADDRESS OF MESSAGE BUFFER
3380 044652 000000 .WORD 0
3381 044654 000016 .WORD 14. ;LENGTH OF MESSAGE BUFFER
3382 044656 000000 000000 .WORD 0,0
3383
3384 044662 T11BFR: .BLKW 8. ;MESSAGE BUFFER
3385
3386
3388 044702 .BLKB 10 <.-TSV267>
3390 044710 T11PK2: ;COMMAND PACKET FOR TEST
3391 044710 100204 .WORD 100204 ;WRITE CHAR COMMAND, WITH IE, ACK
3392 044712 044720 .WORD T11DTA ;ADDRESS OF CHARACTERISTICS BLOCK
3393 044714 000000 .WORD 0
3394 044716 000010 .WORD 8. ;STARTING VALUE OF BLOCK SIZE
3395
3396 044720 T11DTA: ;CHARACTERISTICS DATA BLOCK
3397 044720 044732 .WORD T11BF2 ;ADDRESS OF MESSAGE BUFFER
3398 044722 000000 .WORD 0
3399 044724 000016 .WORD 14. ;LENGTH OF MESSAGE BUFFER
3400 044726 000000 000000 .WORD 0,0
3401
3402 044732 T11BF2: .BLKW 8. ;MESSAGE BUFFER
3403
3404
3405
3406
3407 ;*
3408 ;LOCAL TEXT MESSAGES FOR TEST
3409 ;-
3410 044752 111 116 111 T11NBA: .ASCIZ 'INITIALIZE Command Not Accepted'
3411 045012 111 116 111 T112REJ: .ASCIZ 'INITIALIZE Not Rejected With Non Zero Mode Field'
3412 045073 107 105 124 T113REJ: .ASCIZ 'GET STATUS Not Accepted'
3413 045123 107 105 124 T114REJ: .ASCIZ 'GET STATUS Not Rejected With Non-Zero Mode Field'
3414 045204 103 157 156 T11SSR: .ASCIZ 'Contents of TSSR Incorrect After INITIALIZE'
3415 045260 103 157 156 T11SR2: .ASCIZ 'Contents of TSSR Incorrect After GET STATUS'
3416 045334 105 170 160 T11NINT: .ASCIZ 'Expected Interrupt Not Received On INITIALIZE'
3417 045412 111 156 143 T11TSBA: .ASCIZ 'Incorrect TSBA Address After INITIALIZE'
3418 045462 116 157 156 TST11ID: .ASCIZ 'Non-Tape Motion Commands'
3419 .EVEN
3420
3421
3422 ;*
3423 ;
3424 ;ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
3425 ;INITIALIZE COMMAND
3426 ;
3427 ;-
3428
3429 045514 T11REST:
3430 045514 SAVREG ;SAVE THE REGISTERS
3431 045520 012701 044640 MOV #T11PACKET,R1 ;START OF THE PACKET
3432 045524 012721 100213 MOV #100213,(R1) ;INITIALIZE WITH ACK, IE

```

TSV5A - HARDWARE TESTS MACRO M1113 14 JUN 84 15:15
TEST 11: NON TAPE MOTION COMMANDS

SEQ 0174

```

3433 045530 005021          CLR      (R1).      ;ADDRESS OF CHAR DATA BLOCK
3434 045532 005021          CLR      (R1).      ;EXTENDED ADDRESS
3435 045534 005021          CLR      (R1).      ;SIZE OF DATA BLOCK IN BYTES
3436 045536 005021          CLR      (R1).      ;ADDRESS OF MESSAGE BUFFER
3437 045540 005021          CLR      (R1).
3438 045542 005021          CLR      (R1).      ;LENGTH OF MESSAGE BUFFER
3439 045544 005021          CLR      (R1).
3440 045546 005011          CLR      (R1)
3441 045550 005037 044662   CLR      T11BFR      ;CLEAR 1ST LOC IN MESSAGE BUFFER
3442 045554 000207          RTS      PC          ;RETURN
3443
3444
3445 ;ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
3446 ;GET STATUS COMMAND
3447
3448 ;
3449
3450 T11RT2:
3451 045556          SAVREG          ;SAVE THE REGISTERS
3452 045562 012701 044640   MOV      @T11PACKET,R1 ;START OF THE PACKET
3453 045566 012721 100217   MOV      @100217,(R1). ;GET STATUS WITH ACK, IE
3454 045572 005021          CLR      (R1).      ;ADDRESS OF CHAR DATA BLOCK
3455 045574 005021          CLR      (R1).      ;EXTENDED ADDRESS
3456 045576 005021          CLR      (R1).      ;SIZE OF DATA BLOCK IN BYTES
3457 045600 005021          CLR      (R1).      ;ADDRESS OF MESSAGE BUFFER
3458 045602 005021          CLR      (R1).
3459 045604 005021          CLR      (R1).      ;LENGTH OF MESSAGE BUFFER
3460 045606 005021          CLR      (R1).
3461 045610 005011          CLR      (R1)
3462 045612 005037 044662   CLR      T11BFR      ;CLEAR 1ST LOC IN MESSAGE BUFFER
3463 045616 000207          RTS      PC          ;RETURN
3464 045620          ENDTST
          045620
          045620 104401
3465 045622          ENDMOD

```

L10102: TRAP C#ETST

TSV6 PARAMETER CODING MACRO M1113 14 JUN-84 15:15
 TEST 11: NON TAPE MOTION COMMANDS

SEQ 0175

```

1          .TITLE  TSV6 - PARAMETER CODING
7
12
18
19 045622      BGNMOD  TSV6
    045622      TSV6::
20
21          .SBTTL  HARDWARE PARAMETER CODING SECTION
22
23          ;**
24          ; THE HARDWARE PARAMETER CODING SECTION CONTAINS MACROS
25          ; THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES.  THE
26          ; MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
27          ; INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES.  THE
28          ; MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
29          ; WITH THE OPERATOR.
30          ;--
31 045622      BGNHRD
    045622      .WORD L10107-L#HARD/2
    045624      L#HARD::
32
33 045624      GPRMA  HPM1,0,0,160010,177776,YES      ;GET TSBA/TSDB REGISTER ADDRESS.
    045624      .WORD  T#CODE
    045626      .WORD  HPM1
    045630      .WORD  T#LOLIM
    045632      .WORD  T#HILIM
34 045634      GPRMA  HPM2,2,0,0,776,YES              ;GET VECTOR ADDRESS.
    045634      .WORD  T#CODE
    045636      .WORD  HPM2
    045640      .WORD  T#LOLIM
    045642      .WORD  T#HILIM
35          ;GPRMC  HPM3,4,0,340,0,7,YES              ;GET INTERRUPT PRIORITY.
36 045644      ENDHRD
    045644      .EVEN
    045644      L10107:
37 045644      104      105      126  HPM1:  .ASCIZ  'DEVICE ADDRESS (TSBA/TSDB) '
38 045700      111      116      124  HPM2:  .ASCIZ  'INTERRUPT VECTOR '
39 045724      111      116      124  HPM3:  .ASCIZ  'INTERRUPT PRIORITY '
40          .EVEN

```

```

42          .SBTTL  SOFTWARE PARAMETER CODING SECTION
43
44          ;**
45          ; THE SOFTWARE PARAMETER CODING SECTION CONTAINS MACROS
46          ; THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES.  THE
47          ; MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
48          ; INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES.  THE
49          ; MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
50          ; WITH THE OPERATOR.
51          ;**
52 045754      BGNSFT
53 045754      .WORD L10110-L$SOFT/2
54 045756      L$SOFT::
55          ;      GPRML  SPM1,0, 1,YES          ; GET TRANSPORT TEST FLAG.
56          ;      GPRML  SPM4,2,-1,YES         ; GET ITERATION CONTROL.
57          ;      .WORD  T$CODE
58          ;      .WORD  SPM4
59          ;      .WORD  1
60          ;      GPRMD  SPM6,4,D,7777,0,7777,YES ; GET LOCAL ERROR LIMIT
61          ;      GPRMD  SPM7,6,D,7777,0,7777,YES ; GET GLOBAL ERROR LIMIT
62          ;      ENDSFT
63          ;      .EVEN
64
65          L10110:
66          SPM1:  .ASCIZ  'ENABLE TRANSPORT TESTS '
67          SPM4:  .ASCIZ  'INHIBIT ITERATIONS '
68          SPM6:  .ASCIZ  'PER TEST ERROR LIMIT '
69          SPM7:  .ASCIZ  'PER UNIT ERROR LIMIT '
70          .SBTTL  PATCH AREA
71
72          ;
73          ; FINALLY A GENEROUS PATCH AREA.
74          ;
75          ; AND AN ADJUSTMENT TO ACCOUNT FOR THE "LASTAD BIT7" HACK
76          ; DESCRIBED IN "SUPPRG.MEM" (FOR REV C).
77          ;
78
79 PATCH::
80          .BLKW  32.
81
82          .,.,377*1
83          LASTAD          ;SET LAST USED ADDRESS.
84          .EVEN
85          .WORD  0
86          .WORD  0
87
88          L$LAST::
89          ENDMOD
90          .END

```

SYMBOL TABLE

ADDSR	012126	G	C#AU	=	000052	DEVDR0	023332	FRESIZ	003124	G	INTFLA	016145						
ADR	=	000020	G	C#AUTO	=	000061	DEVNRD	023251	FUSI	004115	INTMAS	016144						
AMBTSS	006633		C#BRK	=	000022	DEVNXR	023167	F#AU	=	000015	INTR	016216	G					
ASSEMB	=	000010	C#BSEG	=	000004	DEVONL	023117	F#AUTO	=	000020	INTREC	002222	G					
A1716	=	000003	C#BSUB	=	000002	DEVSUM	023062	F#BGN	=	000040	INTVEC	016146						
BADDAT	003154	G	C#CEFG	=	000045	DFPTBL	002154	G	F#CLEA	=	000007	INTX	004276					
BADSSR	015700	G	C#CLCK	=	000062	DIAGMC	=	000000	F#DU	=	000016	INVERT	021142	G				
BDVPCR	=	177520	G	C#CLEA	=	000012	DICEA	=	000001	F#END	=	000041	IOCKKI	=	000200			
BENBSW	002226	G	C#CLOS	=	000035	DSBINT	016204	F#HARD	=	000004	IOKSTP	=	000001					
BIE	=	040000	C#CLP1	=	000006	DUAD12	004641	F#HW	=	000013	IPRI	=	002210	G				
BIT0	=	000001	G	C#CVEC	=	000036	DUFLG	003110	G	F#INIT	=	000006	ISR	=	000100	G		
BIT00	=	000001	G	C#DCLN	=	000044	DUMMY	003060	F#JMP	=	000050	IVEC	=	002206	G			
BIT01	=	000002	G	C#DODU	=	000051	EF CON	=	000036	G	F#MOD	=	000000	IXE	=	004000	G	
BIT02	=	000004	G	C#DRPT	=	000024	EF NEW	=	000035	G	F#MSG	=	000011	I#AU	=	000041		
BIT03	=	000010	G	C#DU	=	000053	EF PWR	=	000034	G	F#PR0T	=	000021	I#AUTO	=	000041		
BIT04	=	000020	G	C#EDIT	=	000003	EF RES	=	000037	G	F#PWR	=	000017	I#CLN	=	000041		
BIT05	=	000040	G	C#ERDF	=	000055	EF STA	=	000040	G	F#RPT	=	000012	I#DU	=	000041		
BIT06	=	000100	G	C#ERHR	=	000056	EMAXDU	016777	F#SEG	=	000003	I#HRD	=	000041				
BIT07	=	000200	G	C#ERRO	=	000060	EN	=	000000	F#SOFT	=	000005	I#INIT	=	000041			
BIT08	=	000400	G	C#ERSF	=	000051	ENAINI	016152	F#SRV	=	000010	I#MOD	=	000041				
BIT09	=	001000	G	C#ERSO	=	000057	ENVIRN	020630	F#SUB	=	000002	I#MSG	=	000041				
BIT1	=	000002	G	C#ESCA	=	000010	EPRTSW	002176	G	F#SW	=	000014	I#PR0T	=	000040			
BIT10	=	002000	G	C#ESEG	=	000005	EPRT1	006356	F#TEST	=	000001	I#PTAB	=	000041				
BIT11	=	004000	G	C#ESUB	=	000003	EPRT2	006356	GDDAT	003156	G	I#PWR	=	000041				
BIT12	=	010000	G	C#ETST	=	000001	ERCM	011733	GERRMA	002172	G	I#RPT	=	000041				
BIT13	=	020000	G	C#EXIT	=	000032	ERRHI	002234	G	GETPAT	020174	G	I#SEG	=	000041			
BIT14	=	040000	G	C#GETB	=	000026	ERRK	016756	GETSEL	020256	G	I#SETU	=	000041				
BIT15	=	100000	G	C#GETW	=	000027	ERRLO	002236	G	G#CNT0	=	000200	I#SFT	=	000041			
BIT2	=	000004	G	C#GMAN	=	000043	ERRNU	=	002144	G#DELM	=	000372	I#SRV	=	000041			
BIT3	=	000010	G	C#GPHR	=	000042	ERRVEC	=	000004	G	G#DISP	=	000003	I#SUB	=	000041		
BIT4	=	000020	G	C#GPLO	=	000030	ERTABE	003374	G#EXCP	=	000400	I#TST	=	000041				
BIT5	=	000040	G	C#GPRI	=	000040	ERTABL	003174	G#HILI	=	000002	J#JMP	=	000167				
BIT6	=	000100	G	C#INIT	=	000011	ESUM	016760	G#LOLI	=	000001	KIPAR0	=	172340				
BIT7	=	000200	G	C#INLP	=	000020	EVL	=	000004	G	G#NO	=	000000	KIPAR1	=	172342		
BIT8	=	000400	G	C#MANI	=	000050	EXBCNT	=	000010	G#OFFS	=	000400	KIPAR2	=	172344			
BIT9	=	001000	G	C#MEM	=	000031	EXPBRE	015502	G	G#OF SI	=	000376	KIPAR3	=	172346			
BOE	=	000400	G	C#MSG	=	000023	EXPD	002230	G	G#PRMA	=	000001	KIPAR4	=	172350			
BRINIT	004455		C#OPEN	=	000034	EXPGET	004531	G#PRMD	=	000002	KIPAR5	=	172352					
BSELO	=	000000	C#PNTB	=	000014	EXPGET2	004565	G#PRML	=	000000	KIPAR6	=	172354					
BSEL1	=	000001	C#PNTF	=	000017	EXPMMSG	002320	G	G#RADA	=	000140	KIPAR7	=	172356				
CHKAMB	016044		C#PNTS	=	000016	EXPREC	015474	G	G#RAD8	=	000000	KIPDR0	=	172300				
CHKMAN	020500	G	C#PNTX	=	000015	EXTA	005770	G#RAD0	=	000040	KIPDR1	=	172302					
CHKTSS	016336		C#QIO	=	000377	EXTEND	005766	G#RADL	=	000120	KIPDR2	=	172304					
CKDROP	017202		C#RDBU	=	000007	EXTFEA	002224	G	G#RADO	=	000020	KIPDR3	=	172306				
CKEMAX	017102		C#REFG	=	000047	E#END	=	002100	G#XFER	=	000004	KIPDR4	=	172310				
CKMSG	011360	G	C#RESE	=	000033	E#LOAD	=	000035	G#YES	=	000010	KIPDR5	=	172312				
CKMSG2	011500	G	C#REVI	=	000003	FATERR	=	000060	HIADDR	=	001400	KIPDR6	=	172314				
CKRAM	011114	G	C#RFLA	=	000021	FATFLG	002220	G	HOE	=	100000	G	KIPDR7	=	172316			
CKRAM2	011224	G	C#RPT	=	000025	FERCH	011722	HPM1	=	045644	KTENAB	=	003132	G				
CHDPKT	021214	G	C#SEFG	=	000046	FIFEXP	012170	G	HPM2	=	045700	KTFLG	=	003130	G			
CHPMEM	017660		C#SPRI	=	000041	FIF1MS	012242	HPM3	=	045724	KTINIT	=	021010					
CONFIG	017250		C#SVEC	=	000037	FIF2MS	012311	IBE	=	010000	G	KTOFF	=	017274				
COUNT	002306	G	C#TPRI	=	000013	FILLME	017422	IDU	=	000040	G	KTON	=	017256				
CSRADD	002204	G	DATA	=	002310	G	FNOINT	004213	IER	=	020000	G	LERRMA	=	002170	G		
CTAB	003162	G	DATASC	=	020232	FORCER	002174	G	IFAU LT	=	004254	LISTAL	=	000001				
CTABE	003174	G	DEBUGM	=	011632	FREE	003122	G	INCERK	=	017044	LOE	=	040000	G			
CTABM	003162	G	DEVCNT	=	002216	G	FREEHI	003126	INTCPC	=	016150	LOOPCN	=	002214	G			

SYMBOL TABLE

LOOPCO	013126	L10001	002174	L10073	036746	NXR	003736	PRI05	= 000240 G
LOOPFL	003160 G	L10002	005764	L10074	037152	NXRERR	005734 G	PRI06	= 000300 G
LOT	= 000010 G	L10003	012044	L10075	043452	NXRX	003775	PRI07	= 000340 G
L\$ACP	002110 G	L10004	012062	L10076	040742	NXTU	021756	PRMESS	014242
L\$APT	002036 G	L10005	012100	L10077	041334	OFL	= 000100	PRMNO	002316 G
L\$AU	022306 G	L10006	012106	L10100	042224	ONEFIL	= 000000	PRMSG	014552 G
L\$AUT	002070 G	L10007	012124	L10101	042452	O\$APTS	= 000000	PRMSG0	014732
L\$AUTO	022512 G	L10010	012142	L10102	045620	O\$AU	= 000001	PRMSG1	014777
L\$CCP	002106 G	L10011	012156	L10103	043720	O\$BGNR	= 000001	PRMSG2	015035
L\$CLEA	022572 G	L10012	012240	L10104	044156	O\$BGNS	= 000001	PROFSC	014420
L\$CO	002032 G	L10013	012410	L10105	044376	O\$DU	= 000001	PR1ASC	014465
L\$DEPO	002011 G	L10014	013124	L10106	044622	O\$ERRT	= 000000	PST32W	003150 G
L\$DESC	003406 G	L10015	013752	L10107	045644	O\$GNSW	= 000001	PUNIT	022240
L\$DESP	002076 G	L10016	013774	L10110	045764	O\$POIN	= 000001	PW.D11	= 000021
L\$DEVP	002060 G	L10017	015500	MEMADD	013754 G	O\$SETU	= 000000	PW.D13	= 000022
L\$DISP	002124 G	L10020	015506	MEMCK	021232 G	PASRPT	022010	PW.D22	= 000020
L\$DLY	002116 G	L10021	015514	MENASC	020447	PATCH	046124 G	PW.NOP	= 000000
L\$DTP	002040 G	L10022	015526	MENERR	020374	PATDAT	020230	PW.NO1	= 000023
L\$DTYP	002034 G	L10023	015550	MENRES	020476	PC.ERA	= 002400	PW.RDE	= 000024
L\$DU	022404 G	L10024	015576	MHVEC	= 000250	PC.IER	= 002000	PW.RDR	= 000001
L\$DUT	002072 G	L10025	015736	MSA.FR	= 000006	PC.NOO	= 001000	PW.RDS	= 000005
L\$DVTY	003400 G	L10026	016246	MSA.NO	= 000000	PC.REL	= 000000	PW.RFI	= 000003
L\$EF	002052 G	L10030	022236	MSA.NR	= 000004	PC.REW	= 000400	PW.WCT	= 000006
L\$ENVI	002044 G	L10031	022402	MSA.VO	= 000002	PKBCNT	= 000006	PW.WFI	= 000004
L\$ETP	002102 G	L10032	022510	MSGEXP	012144 G	PKHI	= 000004	PW.WFM	= 000007
L\$EXP1	002046 G	L10033	022570	MSGLOO	013064 G	PKLOW	= 000002	PW.WMI	= 000010
L\$EXP4	002064 G	L10034	022616	MSGSTA	012350 G	PKTADD	007552	PW.WNP	= 000011
L\$EXP5	002066 G	L10035	023060	MSGSUB	013742 G	PKTFRM	007514	PW.WTR	= 000002
L\$HARD	045624 G	L10036	023620	MS.ATT	= 000006	PKTGET	012064 G	P.ACK	= 100000
L\$HIME	002120 G	L10037	023466	MS.EXT	= 000200	PKTMES	012110 G	P.CMD	= 000037
L\$HPCP	002016 G	L10040	023550	MS.RSD	= 000001	PKTRAM	004743 G	P.CONT	= 000012
L\$HPTP	002022 G	L10041	024316	MS.RSF	= 000020	PKTSSR	012046 G	P.CVC	= 040000
L\$HW	002154 G	L10042	025010	MS.RST	= 000010	PNT	= 001000 G	P.FMT	= 000140
L\$ICP	002104 G	L10043	026344	M2901	026070	PRAMPK	013776	P.FORM	= 000011
L\$JMIT	021512 G	L10044	025252	M8186	005552	PRASC	014523	P.GETS	= 000017
L\$LADP	002026 G	L10045	025552	M8189	005643	PRBEXP	015470	P.IE	= 000200
L\$LAST	046404 G	L10046	026050	NBA	= 002000	PRBMSG	015336	P.INIT	= 000013
L\$LOAD	002100 G	L10047	027450	NEWPAS	021744	PRBREC	015472	P.MODE	= 007400
L\$LUN	002074 G	L10050	026716	NODEV	003112 G	PRBTOT	015423	P.OPP	= 020000
L\$MREV	002050 G	L10051	027266	NOINIT	004333	PRBYTE	015122 G	P.POSI	= 000010
L\$NAME	002000 G	L10052	030722	NOINTR	004217	PRI	= 002000 G	P.READ	= 000001
L\$PRIO	002042 G	L10053	030000	NOITS	002166 G	PRIADD	010156	P.SWB	= 010000
L\$PRNT	021502 G	L10054	030324	NOMAN	020534	PRIAO	010226	P.WRIT	= 000005
L\$PRT	002112 G	L10055	034310	NOMEM	005456	PRIBXO	007610 G	P.WRTC	= 000004
L\$REPP	002062 G	L10056	031254	NP.IR	= 000200	PRIEQU	010056	P.WRTS	= 000006
L\$REV	002010 G	L10057	031540	NP.LOO	= 000040	PRIPKT	007366 G	QVP	002202 G
L\$RPT	022620 G	L10060	032004	NP.OUT	= 000100	PRIRAM	010064	RAMASC	014156
L\$SOFT	045756 G	L10061	032232	NP.WRP	= 000020	PRITAD	010272	RAMDAT	002240 G
L\$SPC	002056 G	L10062	032476	NSI	004150	PRITSS	006022	RAMERR	015510 G
L\$SPCP	002020 G	L10063	033036	NSINIT	004405	PRITO	010354	RAMEXP	015530 G
L\$SPTP	002024 G	L10064	035214	NUL	004525	PRITI	010417	RAMFOR	010114
L\$STA	002030 G	L10065	040340	NULCR	004526	PRIXOR	007740 G	RAMSIZ	002300 G
L\$SW	002164 G	L10066	035456	NXM	= 004000	PRI00	= 000000 G	RAMTAD	015516 G
L\$TEST	002114 G	L10067	035716	NXMFLG	003134 G	PRI01	= 000040 G	RCVHIA	002302 G
L\$TIML	002014 G	L10070	036122	NXMHI	003140 G	PRI02	= 000100 G	RCVLOA	002304 G
L\$UNIT	002012 G	L10071	036310	NXMLO	003136 G	PRI03	= 000140 G	RDERR	005204
L10000	002162	L10072	036514	NXMTST	021406	PRI04	= 000200 G	RECMSG	002464 G

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SYMBOL TABLE

SEQ 0179

RECV	002232	G	S1.IEO=	010000	TST6ID	030703	T1.1	023422	T4	025012	G
REGSAV	020140		S1.IFM=	001000	TST7ID	034213	T1.2	023502	T4LOOP	025032	
RETErr	005370		S1.IHE=	000400	TST8ID	035177	T10	040342	T4.1	025012	
REWIND	011014	G	S1.IID=	004000	TST9ID	040241	T10BFR	042512	T4.2	025254	
RMCHBE=	000167		S1.IIR=	020000	TSV2	002000	T10BUF	042554	T4.3	025554	
RMCHEN=	000200		S1.I2R=	040000	TSV3	002174	T10DAT	042500	T5	026346	G
RMHSGB=	000215		S1.PAR=	100000	TSV4	021502	T10DTA	042542	T5ADDR	027406	
RMHSGE=	000234		S2.ATI=	000010	TSV5	023402	T10INT	043210	T5LOOP	026364	
RMPKTB=	000201		S2.BTI=	000004	TSV6	045622	T10LOO	040360	T5MEM	027350	
RMPKTE=	000210		S2.DIM=	000200	TTIBFR=	177562	T10MBF	042574	T5.1	026370	
RMR	= 010000		S2.ILW=	000100	TTICSR=	177560	T10NBA	042671	T5.2	026736	
RMPACK	011110		S2.INR=	000020	TTIVEC=	000060	T10NIN	043117	T6	027452	G
SC	= 100000		S2.OUT=	000040	T\$ARGC=	000003	T10NNB	042753	T6INT	030473	
SCE	= 020000		S2.UND=	000003	T\$CODE=	001130	T10PAC	042470	T6LOOP	027470	
SCHERR	005276		TBLEND=	003060	T\$ERRN=	002144	T10PKT	042532	T6NBA	030370	
SCME	005011		TCOASC	006474	T\$EXCP=	000000	T10RST	043326	T6NINT	030551	
SDELAY	010660		TCOCOC	006674	T\$FLAG=	000040	T10RT2	043400	T6PACK	030360	
SELASC	020442		TEMP1	003114	T\$GMAN=	000000	T10SSR	043030	T6SSR	030415	
SELDAT=	000004		TEMP2	003116	T\$HILI=	000776	T10.1	040360	T6TSBA	030631	
SEL2	= 000002		TERCLS=	000016	T\$LAST=	000001	T10.2	040744	T6.1	027470	
SETMAP	017316		TESTNO=	000013	T\$LOLI=	000000	T10.3	041336	T6.2	030014	
SETU	022042		TEXASC	006433	T\$LSYM=	010000	T10.4	042226	T7	030724	G
SFFMSG	012102	G	TFCASC	006535	T\$LTNO=	000013	T11	043454	T7BFR	033104	
SFHERR	003703		TIMEXP	015552	T\$NEST=	177777	T11BFR	044662	T7DATA	033070	
SFIERR	003650		TIMSGO	015600	T\$NSO =	000000	T11BF2	044732	T7INT	034041	
SFIMSG	012034	G	TINERR	012021	T\$NS1 =	000005	T11DAT	044650	T7LOOP	030742	
SFPTBL	002164	G	TMPBFR	002630	T\$NS2 =	000002	T11DTA	044720	T7NBA	033220	
SIFLAG	003152	G	TNAM	016704	T\$NS3 =	000003	T11LOO	043472	T7NINT	033750	
SIMSG	011766		TRANST	002164	T\$PTNU=	000000	T11NBA	044752	T7PACK	033060	
SKIPT	003376		TSBA	= 000000	T\$SAVL=	177777	T11NIN	045334	T7REST	034242	
SOFINI	015774	G	TSBAH =	000001	T\$SEGL=	177777	T11PAC	044540	T7SP	033100	
SPACE	010464	G	TSBAM2	026160	T\$SEKO=	010000	T11PK2	044710	T7SSR	033661	
SPM1	045764		TSBAM3	026242	T\$SUBN=	000004	T11RES	045514	T7TSBA	034130	
SPM4	046014		TSDB	= 000000	T\$TAGL=	177777	T11RT2	045556	T7.1	030742	
SPM6	046044		TSDBH =	000001	T\$TAGN=	010111	T11SR2	045260	T7.2	031270	
SPM7	046074		TSFCOD	007234	T\$TEMP=	000000	T11SSR	045204	T7.3	031542	
SR0	= 177572		TSREJ =	000006	T\$TEST=	000013	T11TSB	045412	T7.4	032006	
SR1	= 177574		TSSDEF	006604	T\$TSTM=	177777	T1.1	043472	T7.5	032234	
SR2	= 177576		TSSR	= 000002	T\$TSTS=	000001	T11.2	043734	T7.6	032500	
SR3	= 172516		TSSRBI	003500	T\$#AU =	010031	T11.3	044160	T72DAT	033126	
SSR	= 000200		TSSRFO	006413	T\$#AUT=	010033	T11.4	044400	T72DON=	033142	
STATCO	012412		TSSRH =	000003	T\$#CLE=	010034	T112RE	045012	T72NBA	033142	
SVCGBL=	000000		TSSX	003016	T\$#DU =	010032	T113RE	045073	T72REJ	033273	
SVCINS=	000000		TSTBLK	002750	T\$#HAR=	010107	T114RE	045123	T73REJ	033372	
SVCSUB=	000001		TSTCNT	002212	T\$#HW =	010000	T2	023622	T74REJ	033465	
SVCTAG=	000000		TSTEND	016720	T\$#INI=	010030	T2LOOP	023640	T75REJ	033563	
SVCTST=	000001		TSTFLA	002312	T\$#MSG=	010025	T2SSR	024216	T8	034312	G
S\$LSYM=	010000		TSTL00	016456	T\$#PRO=	010027	T2TSBA	024104	T8BFR	034742	
SO.IDB=	000010		TSTPTR	002314	T\$#RPT=	010035	T2TSSR	024151	T8DATA	034730	
SO.IFB=	000002		TSTSET	016510	T\$#SEG=	010000	T23A	003142	T8LOOP	034330	
SO.IFP=	000001		TST1ID	023600	T\$#SOF=	010110	T23B	003144	T8NVCK	035017	
SO.ILD=	000020		TST10I	043277	T\$#SRV=	010026	T3	024320	T8PACK	034720	
SO.ION=	000040		TST11I	045462	T\$#SUB=	010106	T3BFLG	003146	T8SSR	035110	
SO.IRD=	000100		TST2ID	024270	T\$#SW =	010001	T3LOOP	024336	T8VCK	034762	
SO.IRW=	000004		TST3ID	024763	T\$#TES=	010102	T3SSR	024712	T9	035216	G
SO.ISP=	000200		TST4ID	026322	T1	023402	T3TSBA	024602	T9BFR	037212	
S1.ICE=	002000		TST5ID	027320	T1LOOP	023420	T3TSSR	024646	T9DATA	037200	

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SYMBOL TABLE

SEQ 0180

T9INT 040067	UAM = 000200 G	WRTCHR 010662 G	XSOONL = 000100	X2.EXT = 000200
T9LOOP 035240	UNITN = 002200 G	WRTERR 005111	XSOPEL = 000010	X2.OPM = 100000
T9NBA 037246	UNREC = 000006	WRTMSG 005054	XSORLL = 010000	X2.RCE = 040000
T9NINT 037776	USI = 004121	WSMBK 021224 G	XSORLS = 040000	X2.REV = 000077
T9PACK 037170	WAITF 016250 G	XFERAS 015740	XSOYMK = 100000	X2.SPA = 035400
T9REST 040266	WC.IFA = 000200	XNXM 016376	XSOVCK = 000020	X2.UNI = 000007
T9SSR 037707	WC.IFE = 000002	XORBF0 007672	XSOVLE = 004000	X2.WCF = 002000
T9TSBA 040156	WC.IG0 = 000001	XORFOR 010010	XSOVLK = 000004	X3.DCK = 000010
T9.1 035240	WC.IRE = 000010	XST0 = 000006 G	XXCOMM 003120 G	X3.MBZ = 000006
T9.2 035506	WC.IRW = 000004	XST1 = 000010 G	X1ALWA = 000000	X3.MDE = 177400
T9.3 035720	WC.IOT = 000100	XST2 = 000012 G	X1FALS = 000040	X3.OPI = 000100
T9.4 036124	WC.IIT = 000040	XST3 = 000014 G	X1OFFS = 000400	X3.REV = 000040
T9.5 036312	WC.ISR = 000020	XST4 = 000016 G	X1TRUE = 000020	X3.RIB = 000001
T9.6 036516	WF.IED = 000010	XSOBOT = 000002	X1.COR = 020000	X3.SPA = 000200
T9.7 036750	WF.IER = 000004	XSOEOT = 000001	X1.DLT = 100000	X3.TRF = 000020
T92DAT 037232	WF.IHI = 000200	XSOIE = 000040	X1.MBZ = 017375	X4.HSP = 100000
T92DON = 037246	WF.IRE = 000040	XSOILA = 000400	X1.RBP = 000400	X4.MBZ = 017400
T92REJ 037321	WF.IWF = 000020	XSOILC = 001000	X1.SPA = 040000	X4.RCE = 040000
T93REJ 037420	WF.IWR = 000100	XSOLET = 020000	X1.UNC = 000002	X4.TSM = 020000
T94REJ 037513	WF.I3R = 000002	XSOMOT = 000200	X2.BUF = 000100	X4.WRC = 000377
T95REJ 037611	WF.I4R = 000001	XSONEF = 002000		

. ABS. 046404 000
000000 001
ABS 000000 002
ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 28224 WORDS (111 PAGES)
DYNAMIC MEMORY: 20614 WORDS (79 PAGES)
ELAPSED TIME: 00:39:38
CVTSAB,CVTSAB/-SP=SVC/ML,TSV1A,TSV22A,TSV38,TSV4,TSV5A,TSV6