

IDV11-0

IDV11-0 5 CHA CNT DIA
CZIDVAO

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USER DOCUMENTATION

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IDENTIFICATION

Product Code: AC-T976A-MC

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Maintainer: CSS Munich

Authors: Peter Seebach

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1.0 General information

1.1 Program abstract

The CZIDV?? diagnostic provides a series of tests to verify the integrity and functionality of the IDV11-D five channel counter. This diagnostic can be used by field service for functional testing, by the engineer for design tests, and by manufacturing for checkout and repair.

The following special features are implemented.

The diagnostic is set up for field service so that if the user types 'N' to the "CHANGE HARDWARE" and "CHANGE SOFTWARE" questions, an automatic configuration routine will be run. This finds all devices in the address range 171000 to 171770, prints a list of all IAV/IDV11 devices found, and carries out the five channel counter test if a counter is found. This feature has special significance when the XXDP+ SETUP utility is used (see section 2.9).

The "PRINT" command can be used to obtain a list of test titles, a printout of the IAV/IDV11 configuration that the diagnostic is using, or a printout of the error statistics accumulated by the diagnostic. Help on how to repeat the autoconfiguration can also be obtained. For more information, see section 4.0.

If the evaluate flag "EVL" is set, any unit on which more than 5 errors are detected following a "START" command is dropped from testing.

The program supports up to 16 units, all selected tests being run on one unit before proceeding to the next unit.

This diagnostic has been written for use with the diagnostic runtime services software (supervisor). These services provide the interface to the operator and to the software environment. This program can be used with XXDP+.

For a complete description of the runtime services, refer to the XXDP+ user's manual. There is a brief description of the runtime services in section 2 of this document.

1.2 System requirements

- a. LSI processor with a minimum of 28k of memory.
- b. Console terminal with interface address 777560.
- c. XXDP+ load device (RX,RK,RL ECT.)
- d. IDV11-D five channel counter module to be checked.
- e. Test connector (2G-M008A-00) and voltage source (21-24V DC)

1.3 Running the diagnostic on a FALCON

To run the diagnostic on a FALCON based system, a bootstrap program is needed in addition to the above requirements. This could be in the FALCON MACRO ODT rom (KXT11-A2), or on a MXV-11 board.

If you have installed the Falcon Macro-ODT rom KXT11-A2 for booting the XXDP+ media with the diagnostic on it, some of the IAV/IDV-11 default addresses are used, preventing the diagnostics automatic configuration routine from working correctly. To use the diagnostic, the addresses must be entered manually using the startup questions.

NOTE:

- A) Once the XXDP+ media is booted, the console "BREAK" key should not be pressed as it may cause error messages to be printed.
- B) I/O Page addresses from 160000 to 173776 are used by the KXT11-A2 ODT prom, so the first IAV/IDV11 address must be 174000 or higher.
- C) FALCON does not support vectors over 374. Therefore you can't use IAV/IDV11 vectors over 374.

1.4 Related documents and standards

XXDP+ User manual (CHQUS)
IAV/IDV11 Option description YG-C03NC-00

1.5 Diagnostic hierarchy prerequisites

Before running this diagnostic, the appropriate LSI-11 CPU, memory and peripheral standard diagnostics should be run to verify correct operation of the system.

1.6 Execution time

Execution times vary with the CPU type. The following times are typical on a PDP-11/23 + system for one unit:

quick verify = no , execution time for 1 pass = 30 sec.
quick verify = yes, execution time for 1 pass = 9 sec.

2.0 Operating instructions

This section contains a brief description of the runtime services. For detailed information, refer to the XXDP+ user's manual (CHQUS).

2.1 Commands

There are eleven legal commands for the diagnostic runtime services (supervisor). This section lists the commands and gives a very brief description of them. The XXDP+ user's manual has more details.

COMMAND	EFFECT
-----	-----
START	start the diagnostic from an initial state
RESTART	start the diagnostic without initializing
CONTINUE	continue at test that was interrupted (after tc)
PROCEED	continue from an error halt
EXIT	return to XXDP+ monitor (XXDP+ operation only!)
ADD	activate a unit for testing (all units are considered to be active at start time)
DROP	deactivate a unit
PRINT	print test titles, IAV/IDV-11 configuration, error statistics, or how to reconfigure. (see section 4.0)
DISPLAY	type a list of all device information
FLAGS	type the state of all flags (see section 2.3)
ZFLAGS	clear all flags (see section 2.3)

A command can be recognized by the first three characters. So you may, for example, type "STA" instead of "START".

2.2 Switches

There are several switches which are used to modify supervisor operation. These switches are appended to the legal commands. All of the legal switches are tabulated below with a brief description of each. In the descriptions below, a decimal number is designated by "dddd".

SWITCH	EFFECT
-----	-----
/TESTS:LIST	execute only those tests specified in the list. list is a string of test numbers, for example - /tests:1:5:7-10. this list will cause tests 1,5,7,8,9,10 to be run. all other tests will not be run.
/PASS:DDDDD	execute ddddd passes (dddd = 1 to 64000)
/FLAGS:FLGS	set specified flags. flags are described in section 2.3.
/EOP:DDDDD	report end of pass message after every ddddd passes only. (dddd = 1 to 64000)
/UNITS:LIST	TEST/ADD/DROP only those units specified

in the list. list example - /UNITS:0:5:10-12
 use units 0,5,10,11,12 (unit numbers = 0-63)

Example of switch usage:

START/TESTS:1-5/PASS:1000/EOP:100

The effect of this command will be: 1) tests 1 through 5 will be executed, 2) all units will tested 1000 times and 3) the end of pass messages will be printed after each 100 passes only. A switch can be recognized by the first three characters. You may, for example, type "/TES:1-5" instead of "/TESTS:1-5".

Below is a table that specifies which switches can be used by each command.

	TESTS	PASS	FLAGS	EOP	UNITS
START	X	X	X	X	X
RESTART	X	X	X	X	X
CONTINUE		X	X	X	
PROCEED			X		
DROP					X
ADD					X
PRINT					
DISPLAY					X
FLAGS					
ZFLAGS					
EXIT					

2.3 Flags

Flags are used to set up certain operational parameters such as looping on error. All flags are cleared at startup and remain cleared until explicitly set using the flags switch. Flags are also cleared after a start command unless set using the flag switch. The ZFLAGS command may also be used to clear all flags.

With the exception of the start and zflags commands, no commands affect the state of the flags; they remain set or cleared as specified by the last flag switch.

FLAG	EFFECT
HOE	halt on error - control is returned to runtime services command mode
LOE	loop on error
IER*	inhibit all error reports
IBE*	inhibit all error reports except first level (first level contains error type, number, pc, test and unit)
IXE*	inhibit extended error reports (those called by PRINTX macros)
PRI	direct messages to line printer
BOE	"Bell" on error

PNT	print test number as test executes
UAM	unattended mode (no manual intervention)
ISR	inhibit statistical reports (not applicable)
IDR	inhibit program dropping of units (not required since units are only dropped if EVL is used.
ADR	execute autodrop code
LOT	loop on test
EVL	execute evaluation ie. drop unit if more than 5 errors occur after a START or RESTART command.

*error messages are described in section 3.1

See the XXDP+ user's manual for more details on flags. You may specify more than one flag with the flag switch. For example, to cause the program to loop on error, inhibit error reports and type a "bell" on error, you may use the following string:

/FLAGS:LOE:IER:BOE

2.4 Hardware questions

When the diagnostic is started, the runtime services will prompt the user for hardware information by typing "CHANGE HW (L) ?"

This diagnostic has been preloaded such that if you answer "N" to the question, it will automatically search for up to 16 IAV/IDV11 units in the address range 171000 to 171770. Default vectors equal to the low 9 address bits will be assumed for modules with addresses over 171400.

To run the diagnostic with specific modules which need not be in the above address range, you must answer "Y" to the "CHANGE HARDWARE" question. The runtime services will then ask for the number of units (in decimal). To keep down memory requirements, the maximum number of units supported is 16. You will then be asked the following questions for each unit:

MODE REGISTER ADDRESS (0) 171400 ?

In reply, you should enter an address in octal in the range 160000 to 177770.

VECTOR ADDRESS (0) 400 ?

PRIORITY LEVEL (FOR OUTPUT MODULES, TYPE "0") (0) 4 ?
(FOR LSI WITH FIXED PRI. USE LEVEL 4 ONLY)

The following illustrates the response to the hardware questions. In this example, the user response is underlined :-

CHANGE HARDWARE (L) ? Y <cr>

#UNITS (0) ? 2 <cr>

UNIT 0

MODE REGISTER ADDRESS (0) 171400 ? <cr>
-----VECTOR ADDRESS (0) 400 ? <cr>
-----PRIORITY LEVEL (FOR OUTPUT MODULES, TYPE "0") (0) 4 ? 0 <cr>
(FOR LSI WITH FIXED PRI. USE LEVEL 4 ONLY) -----

UNIT 1

MODE REGISTER ADDRESS (0) 171000 ? 171410 <cr>
-----VECTOR ADDRESS (0) 0 ? 410 <cr>
-----PRIORITY LEVEL (FOR OUTPUT MODULES, TYPE RETURN) (0) 0 ? 4 <cr>
(FOR LSI WITH FIXED PRI. USE LEVEL 4 ONLY) -----

Notice that the default value for the priority level changes when a non-default response is given. This is true for all of the hardware questions, so be careful when specifying multiple units!

2.5 Software questions

After you have answered the hardware questions or after a restart or continue command, the runtime services will ask for software parameters. These parameters govern the diagnostic operating modes. you will be prompted by "CHANGE SW (L) ?". the normal response for field service is to type "N".

The next question to be "QUICK VERIFY MODE ?". This is intended for a quick test of the module integrity before connecting up test equipment for full tests.

QUICK VERIFY MODE (L) N ?

If the answer to this question is "Y", only one iteration of each test will be performed. Otherwise, some testing is done more than once. Repeatedly testing a piece of logic in this way often detects faults which a single test would not. Therefore, to fully test the hardware, the answer to this question should be "N".

The following illustrates the response to the software questions. The user response is underlined:

CHANGE SOFTWARE (L) ? Y <cr>
-----QUICK VERIFY MODE (L) N ? <cr>

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2.6 Extended p-table dialogue

When you answer the hardware questions, you are building entries in a table that describes the devices under test. The simplest way to build this table is to answer all questions for each unit to be tested. If you are testing several identical devices, this becomes tedious since most of the answers are the same for each unit.

To illustrate a more efficient method, suppose you are testing four IDV-11 D modules. You could answer the hardware questions for each of the four units as shown in section 2.4.

The runtime services can take multiple unit specifications however. Let's build the same table using the multiple specification feature:

```
CHANGE HARDWARE (L) ? Y <cr>
-----
#UNITS (D) ? 4 <cr>
-----
UNIT 0
MODE REGISTER ADDRESS (O) 171400 ? 171400,171410,171420,171430 <cr>
-----
VECTOR ADDRESS (O) 400 ? 400,410,420,430<cr>
-----
PRIORITY LEVEL (FOR OUTPUT MODULES, TYPE "0") (O) 4 ? <cr>
(FOR LSI WITH FIXED PRI. USE LEVEL 4 ONLY) -----
```

As you can see in the above dialogue, the runtime services will build as many entries as it can with the information given in any one pass through the questions. In this example, the 4 IDV11-D modules at addresses 174000, 174010, 174020 and 174030 are set up with vectors 400,410,420 and 430. All have priority level 4

2.7 Clock questions

If there is no line time clock on the system, the user is asked to type 2 characters 6 seconds apart on the console. This should be done as accurately as possible since the interval is used by the diagnostic to calculate values for device timeouts.

2.8 Quick start-up procedure (XXDP+)

To start-up this program:

1. Boot XXDP+
2. Give the date and answer XXDP + questions

3. Type "R ZIDV??". (Normally the revision and patch level are typed insted of the question marks. The form shown here causes the latest version to be run.)
4. Type "START"
5. For standard configurations using addresses 171400 to 171770 answer the "CHANGE HW" question with "N". To test specific devices or those at non-standard addresses answer "Y" and answer all of the hardware questions.
6. Answer the "CHANGE SW" question with "N"

When you follow this procedure you will be using only the defaults for flags and software parameters. These defaults are described in sections 2.3 and 2.5.

2.9 Using the XXDP+ SETUP utility

To enable the diagnostic to automatically establish the IXV11 configuration in the field, the hardware p table is preset for 16 units, each with a mode address of 0. If the XXDP+ SETUP utility is used to preset the p table for a particular configuration, then the diagnostic will not automatically establish the configuration at startup. In this case, the diagnostic can be made to do automatic configuration by answering the hardware questions to give 16 units with mode addresses of 0.

EG. CHANGE HARDWARE (L) ? Y <cr>

#UNITS (D) ? 16 <cr>

UNIT 0

MODE REGISTER ADDRESS (0) 0 ? 0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0 <cr>

VECTOR ADDRESS (FOR OUTPUT MODULES, TYPE "0") (0) 0 ? 0 <cr>

PRIORITY LEVEL (FOR OUTPUT MODULES, TYPE "0") (0) 4 ? 0 <cr>
(FOR LSI WITH FIXED PRI. USE LEVEL 4 ONLY) -----

3.0 Error information

3.1 Types of error messages

There are three levels of error messages that may be issued by a diagnostic : general, basic and extended. General error messages are always printed unless the "IER" flag is set (section 2.3). The general error message is of the form :

NAME TYPE NUMBER ON UNIT NUMBER TST NUMBER PC:XXXXXX
ERROR MESSAGE

where NAME = diagnostic name
TYPE = error type (SYS FATAL, DEV FATAL, HARD or SOFT)
NUMBER = error number
UNIT NUMBER = 0 - N (N is last unit in ptable)
TST NUMBER = test and subtest where error occurred
PC:XXXXXX = address of error message call

Basic error messages are messages that contain some additional information about the error. These are always printed unless the "IER" or "IBR" flags are set (section 2.3). These messages are printed after the associated general message.

Extended error messages contain supplementary error information such as register contents or good/bad data. These are always printed unless the "IER", "IBR" or "IXR" flags are set (section 2.3). These messages are printed after the associated general error message and any associated basic error messages.

3.2 Specific error messages

All specific error messages are explained with the test descriptions in section 6.0.

4.0 Performance and progress reports

At the end of each pass, the pass count is given along with the total number of errors reported since the diagnostic was started. The "EOP" switch can be used to control how often the end of pass message is printed. Section 2.2 describes switches.

4.1 Print command utilization

The "PRINT" command can be used to find out how many errors have occurred on each unit since the diagnostic was started.

In addition, the command can be used to display the configuration that the diagnostic is currently using, to print a list of test titles, or to show how to make the diagnostic reestablish the configuration.. The following examples show how the print command can be used. User input is underlined :

PRINT <cr>

TYPE T,R,C,S OR HELP (S) H ? <cr>

THE FOLLOWING COMMANDS ARE ACCEPTED :-

T - PRINT TEST TITLES
R - PRINT HOW TO REESTABLISH THE SYSTEM CONFIGURATION
C - PRINT CONFIGURATION TABLE CURRENTLY USED BY DIAGNOSTIC
S - PRINT STATISTICS TABLE
TYPE T,R,C,S OR HELP (S) H ?

If you type "H", "HELP" or any character other than "T", "R", "C" or "S", the routine prints the above help message listing the acceptable commands.

PRINT <cr>

TYPE T,R,C,S OR HELP (S) H ? T <cr>

TEST TITLES.

- 1 REGISTER NXM TEST
- 2 RESET TEST
- 3 R-W BIT TEST
- 4 AM 9513 SUBREGISTER TEST
- 5 INTERRUPT TEST
- 6 REFERENCE FREQUENCY TEST
- 7 SIGNAL GENERATION ON COUNTER INPUT
- 8 EXTERNAL LOOPBACK, NOT ISOLATED
- 9 EXTERNAL LOOPBACK, ISOLATED
- 10 UP/DOWN COUNTING APPLICATION TEST
- 11 VISUAL LED TEST - SPECIFICALLY SELECTABLE

DR> PRINT <cr>

TYPE T,R,C,S OR HELP (S) T ? S <cr>

IDV/IAV11 MODULE STATISTICS.

UNIT ERRORS DROPPED

0	0	NO
1	6	YES
2	UNTESTED	NO

Here, unit 0 has shown no faults, unit 1 has had 6 errors and been dropped from testing, and unit 2 has not yet been tested. Unit 2 is shown as not dropped. If the diagnostic had not yet been started, the unit would still not be shown as dropped (unlike the display command).

DR> PRINT <cr>

TYPE T,R,C,S OR HELP (S) S ? C <cr>

IDV/IAV11 MODULE CONFIGURATION.

UNIT	ADDRESS	VECTOR ASSUMED	ID/MODE	MODULE TYPE	COMMENT
0	171000	NONE	060/000	DIG. OUT	CANNOT BE TESTED WITH THIS DIAG
1	171410	410	030/002	DIG. IN	CANNOT BE TESTED WITH THIS DIAG
2	171010	NONE	260/000	AN. OUT	CANNOT BE TESTED WITH THIS DIAG
3	171420	420	100/000	AN. IN	CANNOT BE TESTED WITH THIS DIAG
4	171700	NONE	320/000	***	CANNOT BE TESTED WITH THIS DIAG ***
5	171710	NONE	UNKNOWN	UNKNOWN	
6	171400	400	300/000	FIVE CHA. CNT	

The third column is labeled "VECTOR ASSUMED" for the following reasons. If the diagnostic is configured automatically by answering "NO" to the "CHANGE HARDWARE" QUESTION, devices with addresses lower than 171400 are assumed to have no vector. Above this address, the vector is assumed to be the same as the low 9 bits of the address. If the hardware questions were answered, the vector is printed as it was typed, except that zero vectors are printed as "NONE".

The above example is selected to illustrate the different features of the configuration printout.

* NOTE: This diagnostic can only test the five channel counter module

If addressing the unit under test causes a bus timeout, then "UNKNOWN" is printed for the ID/MODE and MODULE TYPE. This is shown for unit 5. This would normally only occur if the address was incorrectly typed in the hardware questions.

DR> PRINT <cr>

TYPE T,R,C,S OR HELP (S) C ? R <cr>

To reestablish the system configuration, answer the
hardware question typing "0" as the mode address for 16 units.

eg. mode register address (0) 0 ? 0.....

Typing "R" gives you information on how to reestablish the
system configuration. This is necessary if the H/W questions
have been answered or the hardware itself has been changed and
you want to find out what is in the system without rebooting the
diagnostic.

5.0 Device information tables

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The hardware p tables contain 3 words for each device. These are used to save the answers to the startup hardware questions, and can be displayed on the console by using either the "DISPLAY" command described in section 2.1 or the "PRINT" command described in section 4.

The hardware p table is set up for field service for 16 units, each with a mode address of 0. If the user types "NO" to the "CHANGE HARDWARE" and "CHANGE SOFTWARE" questions, an automatic configuration routine will be run. This finds all devices in the address range 171000 to 171770, prints a list of all IXV11 devices found, and carries out the IDV11-D tests if a counter was found. The hardware table set up by the configuration routine remains in effect until changed by the hardware questions, even if the hardware configuration itself is altered.

Using the XXDP. SETUP utility, the tables can be preloaded to contain information for specific systems. However, special care must be taken if it is desired to restore the self configuring feature of the diagnostic. See section 2.9.

6.0 Test summaries

Test 1 - Register NXM Test.

This test checks that accessing the device MODE, CSR, CCR and INR registers does not cause a NXM trap.

The following error may be printed :

Error 101 : REGISTER ADDRESSING ERROR - TRAP TO 4
REGISTER AT XXXXXX DOES NOT RESPOND

This could mean that the device address switch is incorrectly set, that the address was entered incorrectly in the startup questions, or that the device does not respond.

Test 2 - Reset Test.

This test checks that the device MOD AND INR registers are correctly set or reset after a bus reset.
In the MOD register, only the LED bit is tested.
NOTE: The CSR and the CCR register can not be cleared by init so they will not be tested in this test.

Error 201 : LED BIT IN MOD REGISTER NOT CLEARED AFTER BUS RESET

Error 202 : LED BIT IN MOD REGISTER CAN'T BE SET

Error 203 : REGISTER INCORRECT AFTER BUS RESET
ADDRESS:AAAAAA, GOOD:GGGGGG, BAD:BBBBBB

Test 3 - Register R/W Bit Test.

This test checks that the read/write bits of each register can all be set, all cleared and individually set.

the following errors may be printed :

Error 301 : REGISTER READ/WRITE BITS COULD NOT BE SET
ADDRESS:AAAAAA, GOOD:GGGGGG, BAD:BBBBBB, R/W BITS:RRRRRR

Error 302 : REGISTER READ/WRITE BITS COULD NOT BE CLEARED
ADDRESS:AAAAAA, GOOD:GGGGGG, BAD:BBBBBB, R/W BITS:RRRRRR

Error 303 : REGISTER READ/WRITE BITS COULD NOT BE INDIVIDUALLY SET
ADDRESS:AAAAAA, GOOD:GGGGGG, BAD:BBBBBB, R/W BITS:RRRRRR

TEST 4 - AM 9513 SUBREGISTER TEST - FIVE CHANNEL COUNTER

```

ERROR 501,502  ERROR - INR REGISTER INCORRECT AFTER LOAD 'CLEAR TOUT' COMMAND
                  GOOD:NNNNNN      BAD:NNNNNN

ERROR 503      ERROR - IR BIT IN THE INR REGISTER NOT RESETED AFTER BIT CLEAR
                  GOOD:NNNNNN      BAD:NNNNNN

```


- ERROR 504,505 ERROR - UNEXPECTED INTERRUPT DETECTED
AN INTERRUPT WAS DETECTED BEFORE ALL CONDITIONS
REQUIRED FOR AN INTERRUPT WERE SATISFIED.
- ERROR 506 ERROR - EXPECTING ONE INTERRUPT, ENCOUNTERED :XXX
A 'SET TOUT' COMMAND WAS LOADED TO PRODUCE ONE
INTERRUPT, HOWEVER, THEN INDICATED NUMBER WAS
DETECTED.
- ERROR 507 ERROR - BAD INTERRUPT VECTOR DETECTED
SET UP VECTOR:NNN FOUND VECTOR:NNN
- ERROR 508 ERROR - INTERRUPT DID NOT OCCURED AT THE CORRECT PRIORITY LEVEL
SET UP:NNN FOUND:NNN
- ERROR 509 ERROR - MASTER CLEAR BIT IN INR REGISTER NOT RESETEO AFTER INTERRUPT
INR CONTENS IS : NNNNNN

TEST 6 - AM 9513 REFERENCE FREQUENCY TEST - FIVE CHANNEL COUNTER

THIS IS A TEST OF THE REFERENCE FREQUENCY.
THE 5MHZ FREQUENCY IS SELECTED AND THE LOAD
REGISTER IS LOADED WITH 10000. ALL FIVE COUNTERS
ARE LOADED AND ARMED FOR THE DURATION OF THREE
NOP INSTRUCTIONS. A TEST IS MADE THAT ALL COUNTERS
HAVE A VALUE DIFFERENT THAT 10000 AND THAT THE
DIFFERENCE BETWEEN ALL FIVE COUNTERS IS NOT GREATER
THAN TWO.

- ERROR 601 UNEXPECTED DIFFERENCE BETWEEN COUNTER
SHOULD NOT BE GREATER THAN 2
- COUNT DOWN FROM 10,000 AT 5 MHZ FOR 3 NOP INSTRUCTIONS
- COUNTER 1 VALUE: XXXXXX
COUNTER 2 VALUE: XXXXXX
COUNTER 3 VALUE: XXXXXX
COUNTER 4 VALUE: XXXXXX
COUNTER 5 VALUE: XXXXXX

TEST 7 - SIGNAL GENERATION ON COUNTER INPUT - FIVE CHANNEL COUNTER

THIS IS A TEST OF SIGNAL GENERATION ON TIMER INPUT.
THE INTERNAL LOOPBACK MODE IS SELECTED AND THE OUTPUT
FROM CHANNEL N-1 IS USED AS INPUT TO THE COUNTER.
THE COUNTER IS ALLOWED TO RUN FOR FIVE NOP INSTRUCTIONS.
A TEST IS THEN MADE THAT THE COUNTER INCREMENTED.

THIS IS REPEATED FOR ALL FIVE COUNTER.

ERROR 701 COUNTER X DID NOT INCREMENT

THE COUNTER SHOULD HAVE INCREMENTED AWAY FROM ZERO.

TEST 8 - EXTERNAL LOOPBACK, NOT ISOLATED

THIS IS A TEST OF EXTERNAL LOOPBACK, NOT ISOLATED.
THE FREQUENCY OUTPUT CONTROL REGISTER AND THE
5MHZ LOOPBACK FREQUENCY ARE SELECTED. THE SOURCE
AND GATE OUTPUTS ARE THEN USED AS INPUT TO THE
COUNTER. THE COUNTER IS ALLOWED TO RUN FOR THREE
NOP INSTRUCTIONS, AND A TEST IS MADE THAT THE COUNTER
INCREMENTED. THIS IS REPEATED FOR ALL FIVE COUNTERS.

ERROR 801, COUNTER X DID NOT INCREMENT, SOURCE X
ENSURE THAT TEST CONNECTOR IS INSTALLED

802 COUNTER X DID NOT INCREMENT, GATE X
ENSURE THAT TEST CONNECTOR IS INSTALLED

THE COUNTER SHOULD HAVE INCREMENTED AWAY FROM ZERO.
THE INPUT WAS EITHER A SOURCE OR A GATE SIGNAL.

TEST 9 - EXTERNAL LOOPBACK, ISOLATED

THIS IS A TEST OF EXTERNAL LOOPBACK, ISOLATED.
THE FREQUENCY OUTPUT FOUT IS SWITCHED OFF THEN
COUNTER 1 IS SET UP FOR 100 KHZ SIGNAL GENERATION ON
OUT1. OUT1 IS LOOPBACK TO SOURCE 1+2 OF COUNTER 2.
THEN THE COUNTER IS ALLOWED TO RUN FOR 100 PASSES OF A
THREE NOP LOOP.
A TEST IS THEN MADE THAT THE COUNTER INCREMENTED.
THIS SEQUENCE ABOVE IS REPEATED WITH ALL COUNTERS.
COUNTER 2 IS USED FOR SIGNAL GENERATION AND OUT2
IS LOOPBACK TO SOURCE 3+4 OF COUNTER 3.
COUNTER 3 IS USED FOR SIGNAL GENERATION AND OUT3
IS LOOPBACK TO SOURCE 5 + GATE 1.
E.T.C.

THE LOOPBACK SIGNAL FREQUENCY IS APPROXIMATELY 100 kHz.

ERROR 901, COUNTER X DID NOT INCREMENT, SOURCE X
ENSURE THAT TEST CONNECTOR IS INSTALLED

902 COUNTER X DID NOT INCREMENT, GATE X
ENSURE THAT TEST CONNECTOR IS INSTALLED

THE COUNTER SHOULD HAVE INCREMENTED AWAY FROM ZERO.
THE INPUT WAS EITHER A SOURCE OR A GATE SIGNAL.

TEST 10 - UP/DOWN COUNTING APPLICATION TEST

THIS IS AN UP/DOWN APPLICATION TEST. THIS TEST USES THE OUTPUTS FROM TOUT N= 3 AND 4 AS INPUT TO COUNTERS 1 AND 2. WHEN TOUT N=4 SETS AND TOUT N=3 IS SET, THEN COUNTER 1 SHOULD INCREMENT. THE SIGNAL TOUT N=3 SETS WHILE TOUT N=4 IS RESET, AND COUNTER 2 SHOULD NOT INCREMENT. THIS SYNCHRONOUS SETTING AND CLEARING OF TOUT N=3 AND 4, OUT OF PHASE BY 90 DEGREES, IS REPEATED 10000 TIMES. AFTER THIS LOOP COUNTER 1 MUST HAVE A VALUE OF 10000 AND COUNTER 2 MUST HAVE A VALUE OF ZERO. THE SAME LOOP IS THEN PERFORMED WITH TOUT N=3,4 OUT OF PHASE BY 90 DEGREES IN THE OTHER DIRECTION. FOR THIS TEST COUNTER 2 MUST HAVE A VALUE OF 10000 AND COUNTER 1 MUST CONTAIN A VALUE OF ZERO.

- ERROR 1001 BAD COUNTER VALUE, EXPECTED 10000, DETECTED XXX
- OUTPUTS 3 AND 4 WERE TOGGLED TO PRODUCE A COUNT OF 10000. THESE SIGNALS WERE INPUTED TO COUNTER 1 WHICH WAS SET TO THE ACTIVE HIGH MODE. A COUNT OF 10000 SHOULD HAVE RESULTED, BUT THE INDICATED VALUE WAS DETECTED.
- ERROR 1002 BAD COUNTER VALUE, EXPECTED 0, ENCOUNTERED XXX
- OUTPUTS 3 AND 4 WERE USED AS INPUT TO COUNTER 2. COUNTER 2 WAS SET TO ACTIVE LOW, AND THESE SIGNALS SHOULD NOT HAVE INCREMENTED THE COUNTER.
- ERROR 1003 BAD COUNTER VALUE, EXPECTED 0, ENCOUNTERED XXX
- OUTPUTS 3 AND 4 WERE USED AS INPUT TO COUNTER 1. COUNTER 1 WAS SET TO ACTIVE LOW, AND THESE SIGNALS SHOULD NOT HAVE INCREMENTED THE COUNTER.
- ERROR 1004 BAD COUNTER VALUE, EXPECTED 10000, DETECTED XXX
- OUTPUTS 3 AND 4 WERE TOGGLED TO PRODUCE A COUNT OF 10000. THESE SIGNALS WERE INPUTED TO COUNTER 2 WHICH WAS SET TO THE ACTIVE HIGH MODE. A COUNT OF 10000 SHOULD HAVE RESULTED, BUT THE INDICATED VALUE WAS DETECTED.
- ERROR 1005 ERROR LOADING TOGGLE, WRITTEN XXX, READ: XXX
- EVERY TIME A TOGGLE OUTPUT IS SET OF CLEARED, THE SIGNAL IS READ BACK AND TESTED FOR THE CORRECT STATE. IF THE EXPECTED STATE IS NOT DETECTED, THIS ERROR MESSAGE IS PRODUCED.

6.1 SPECIFICALLY SELECTABLE TEST

Test 11 - Visual LED Test - Specifically Selectable.

This test is a visual test. It flashes the LED on and off on every module in the system that is found by the automatic configuration routine or selected in the hardware questions.

The test runs until "CNTL C" is typed.

Errors : This test has no error messages.

PROGRAM HEADER AND TABLES

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```
1046      .TITLE PROGRAM HEADER AND TABLES
1047      .SBTTL PROGRAM HEADER
1073
1078
1080 000000      .ENABL ABS,AMA
1081      002000      .      2000
1083
1084 002000      BGNMOD
1085
1086      ;++
1087      ; THE PROGRAM HEADER IS THE INTERFACE BETWEEN
1088      ; THE DIAGNOSTIC PROGRAM AND THE SUPERVISOR.
1089      ;--
1090
1091 002000      POINTER ALL
1092
1109
1110 002000      HEADER CZIDV,A,0,150,0,340
1111
```

PROGRAM HEADER AND TABLES
DISPATCH TABLE

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1123
1124
1125
1126
1127
1128
1129
1130 002122
1131

.SBTTL DISPATCH TABLE

; THE DISPATCH TABLE CONTAINS THE STARTING ADDRESS OF EACH TEST.
; IT IS USED BY THE SUPERVISOR TO DISPATCH TO EACH TEST.

DISPATCH 11.

PROGRAM HEADER AND TABLES
DEFAULT HARDWARE P-TABLE

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```
1139      .SBTTL  DEFAULT HARDWARE P-TABLE
1140
1141      ;**
1142      ; THE DEFAULT HARDWARE P-TABLE CONTAINS DEFAULT VALUES OF
1143      ; THE TEST-DEVICE PARAMETERS.  THE STRUCTURE OF THIS TABLE
1144      ; IS IDENTICAL TO THE STRUCTURE OF THE HARDWARE P-TABLES,
1145      ; AND IS USED AS A "TEMPLATE" FOR BUILDING THE P-TABLES.
1146      ;--
1147
1148      002152      BGNHW  DFPTBL
1149
1159      002154      171400      .WORD  171400      ; MODE REGISTER ADDRESS
1160      002156      000400      .WORD  400          ; VECTOR ADDRESS
1161      002160      000200      .WORD  PRI04         ; PRIORITY LEVEL
1162
1163      002162      ENDNHW
```


PROGRAM HEADER AND TABLES
SOFTWARE P-TABLE

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1165
1166
1167
1168
1169
1170
1171
1172
1173
1174 002162
1175
1183
1184 002164 000000
1185
1186 002166

.SBTTL SOFTWARE P-TABLE

;
; THE SOFTWARE TABLE CONTAINS VARIOUS DATA USED BY THE
; PROGRAM AS OPERATIONAL PARAMETERS. THESE PARAMETERS ARE
; SET UP AT ASSEMBLY TIME AND MAY BE VARIED BY THE OPERATOR
; AT RUN TIME.
;--

BGNSW SFPTBL

QVP:: .WORD 0 ; QUICK VERIFY ? (0 = NO)

ENDSW

PROGRAM HEADER AND TABLES MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 14
 HARDWARE PARAMETER CODING SECTION

```

1188      .SBTTL  HARDWARE PARAMETER CODING SECTION
1189
1190      ;**
1191      ; THE HARDWARE PARAMETER CODING SECTION CONTAINS MACROS
1192      ; THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES.  THE
1193      ; MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
1194      ; INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES.  THE
1195      ; MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
1196      ; WITH THE OPERATOR.
1197      ;--
1198
1199 002166      BGNHRD
1200
1210
1211 002170      GPRMA  G1.0.0.0.177776.YES      ; MODE ADDRESS
1212 002200      CPRMA  G2.2.0.0.770.YES        ; VECTOR
1213 002210      GPRMD  G3.4.0.340.0.6.YES      ; PRIORITY
1214
1215 002222      104:  ENDRD
1216
1223
1224
1225 002222      115      117      104  G1:  .NLIST  BEX
1226 002250      126      105      103  G2:  .ASCIZ  /MODE REGISTER ADDRESS/
1227 002270      120      122      111  G3:  .ASCIZ  /VECTOR ADDRESS /
1228 002307      012      015      050  G3:  .ASCII  /PRIORITY LEVEL /
1229
1230      .ASCIZ  <12><15>/(FOR LSI WITH FIXED PRI. USE LEVEL 4 ONLY)/
      .LIST  BEX
      .EVEN

```

PROGRAM HEADER AND TABLES MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 15
SOFTWARE PARAMETER CODING SECTION

```

1232      .SBTTL  SOFTWARE PARAMETER CODING SECTION
1233
1234      ; --
1235      ; THE SOFTWARE PARAMETER CODING SECTION CONTAINS MACROS
1236      ; THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES.  THE
1237      ; MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
1238      ; INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES.  THE
1239      ; MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
1240      ; WITH THE OPERATOR.
1241      ; --
1242
1243 002364      BGNSFT
1244
1253
1254 002366      104:   GPRML   G16.0.-1.YES           ; QUICK VERIFY MODE ?
1255
1256      .EVEN
1257
1258 002374      ENDSFT
1259
1266
1267      .MLIST  BEX
1268 002374      121    125    111  G16:  .ASCIZ  /QUICK VERIFY MODE/
1269      .LIST   BEX
1270      .EVEN
1271
1272
1273 002416      ENDMOD
1274

```

1286
1287
1315
1325
1326 002416
1327
1328
1329
1330
1331
1332
1347
1348 002416

.TITLE GLOBAL AREAS
.SBTTL GLOBAL EQUATES SECTION

BGNMOD

!--
; THE GLOBAL EQUATES SECTION CONTAINS PROGRAM EQUATES THAT
; ARE USED IN MORE THAN ONE TEST.
!--

EQUALS

; BIT DEFINITIONS

100000	BIT15-- 100000
040000	BIT14-- 40000
020000	BIT13-- 20000
010000	BIT12-- 10000
004000	BIT11-- 4000
002000	BIT10-- 2000
001000	BIT09-- 1000
000400	BIT08-- 400
000200	BIT07-- 200
000100	BIT06-- 100
000040	BIT05-- 40
000020	BIT04-- 20
000010	BIT03-- 10
000004	BIT02-- 4
000002	BIT01-- 2
000001	BIT00-- 1
001000	BIT9-- BIT09
000400	BIT8-- BIT08
000200	BIT7-- BIT07
000100	BIT6-- BIT06
000040	BIT5-- BIT05
000020	BIT4-- BIT04
000010	BIT3-- BIT03
000004	BIT2-- BIT02
000002	BIT1-- BIT01
000001	BIT0-- BIT00

; EVENT FLAG DEFINITIONS

; EF32:EF17 RESERVED FOR SUPERVISOR TO PROGRAM COMMUNICATION

000040	EF.START-- 32.	; START COMMAND WAS ISSUED
000037	EF.RESTART-- 31.	; RESTART COMMAND WAS ISSUED
000036	EF.CONTINUE-- 30.	; CONTINUE COMMAND WAS ISSUED
000035	EF.NEW-- 29.	; A NEW PASS HAS BEEN STARTED
000034	EF.PWR-- 28.	; A POWER-FAIL/POWER-UP OCCURRED

; PRIORITY LEVEL DEFINITIONS

000340 PRI07-- 340

GLOBAL AREAS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 16-1
GLOBAL EQUATES SECTION

```
000300      PRI06== 300
000240      PRI05== 240
000200      PRI04== 200
000140      PRI03== 140
000100      PRI02== 100
000040      PRI01== 40
000000      PRI00== 0
```

```
;
; OPERATOR FLAG BITS
```

```
000004      EVL==      4
000010      LOT==     10
000020      ADR==     20
000040      IDU==     40
000100      ISR==    100
000200      UAM==    200
000400      BOE==    400
001000      PNT==   1000
002000      PRI==   2000
004000      IXE==   4000
010000      IBE==  10000
020000      IER==  20000
040000      LOE==  40000
100000      HOE== 100000
```

1349
1350
1351
1352
1353
1354
1355
1356
1357
1358
1359
1360
1361
1362
1363
1364
1365
1366
1367
1368
1369
1370
1371

```
000002      MREA== 2
000004      MREB== 4
000006      MREC== 6

177400      B==      177400
177500      C#LOA== 100!B
177440      C#ARM== 40!B
177600      C#DAS== 200!B
177777      C#MAR== 377!B
177750      C#IN1== 350!B
177757      C#IN2== 357!B
177740      C#CTN== 340!B
177750      C#STN== 350!B
177700      C#DAC== 300!B
000042      M#TOG== 42

171000      IXSTA== 171000
171770      IXEND== 171770
```

```
; COUNTER COMMAND AND STATUS REGISTER
; COUNTER CONTROL REGISTER
; COUNTER INTERRUPT REGISTER

; HIGH BITS MUST BE SET
; LOAD COUNTER
; ARM ALL SELECTED COUNTERS
; SAVE AND DISARM ALL SELECTED COUNTERS
; MASTER RESET
; INITIALIZE COMMAND #1
; INITIALIZE COMMAND #2
; CLEAR TOUT
; SET TOUT
; DISARM ALL COUNTERS
; COUNTER MODE REGISTER, OUT TOGGLE

; FIRST STANDARD IDV/IAV-11 ADDRESS
; LAST STANDARD IDV/IAV-11 ADDRESS
```

GLOBAL AREAS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 17
GLOBAL DATA SECTION

```

1373      .SBTTL  GLOBAL DATA SECTION
1374
1375      ;**
1376      ; THE GLOBAL DATA SECTION CONTAINS DATA THAT ARE USED
1377      ; IN MORE THAN ONE TEST.
1378      ;--
1379
1380 002416 000000 MOD::      .WORD  0      ; MODE REGISTER ADDRESS OF CURRENT UUT
1381 002420 000000 CSR::      .WORD  0      ; COMMAND AND STATUS REGISTER ADDRESS OF UUT
1382 002422 000000 CCR::      .WORD  0      ; COUNTER CONTROL REGISTER A ADDRESS OF UUT
1383 002424 000000 INR::      .WORD  0      ; INTERRUPT REGISTER ADDRESS OF CURRENT UUT
1384 002426 000000 VEC::      .WORD  0      ; VECTOR ADDRESS OF CURRENT UUT
1385 002430 000000 PRIO::     .WORD  0      ; PRIORITY LEVEL OF CURRENT UUT
1386
1387      ;
1388      ; WORKING STORAGE
1389      ;
1390 002432 000000 SAVPRI::     .WORD  0      ; SAVED PRIORITY
1391 002434 000000 BITMSK::     .WORD  0      ; FOR BIT MAP
1392 002436 000000 BITMS1::     .WORD  0      ; GENERAL BIT MASK
1393 002440 000000 BITMS2::     .WORD  0      ; GENERAL BIT MASK
1394 002442 000000 NUMBER::     .WORD  0      ; GENERAL NUMBER
1395 002444 000000 COUNTR::     .WORD  0      ; GENERAL COUNTER
1396 002446 000000 GROUP::      .WORD  0      ; CURRENT GROUP
1397 002450 000000 ELEMNT::     .WORD  0      ; CURRENT ELEMENT
1398 002452 000000 CNTVAL::     .BLKW  5      ; HOLDS VALUES FROM COUNTERS
1399 002464 000000 SRC::        .WORD  0      ; SOURCE FOR A TEST
1400 002466 000000 LOOP::      .WORD  0      ; GENERAL LOOP COUNT
1401
1402 002470 000000 NXMFLG::     .WORD  0      ; SET IF NXM TRAP OCCURS
1403 002472 000000 BIV::        .WORD  0      ; HOLD VECTOR OF INTERRUPT
1404                                     ; IF INTERRUPT DID NOT OCCUR THROUGH
1405                                     ; SPECIFIED VECTOR
1406
1407 002474 000000 GOOD::        .WORD  0      ; EXPECTED CONTENTS
1408 002476 000000 BAD::        .WORD  0      ; ACTUAL CONTENTS
1409 002500 000000 SFI::        .WORD  0      ; FLAG TO FORCE ERROR PRINTOUTS
1410
1411
1412 002502 000000 ANS::         .WORD  0      ; TEMPORARY STORE FOR MANUAL INPUTS
1413 002504 000000 ITRCNT::     .WORD  0      ; ITERATION COUNTER
1414 002506 000010 ITRDEF::     .WORD 10      ; ITERATION DEFAULT
1415 002510 000000 INTFLA::     .WORD  0      ; DONE INTERRUPT FLAG
1416 002512 000000 INTFL2::     .WORD  0      ; ERROR INTERRUPT FLAG
1417
1418
1419
1420
1421
1422
1423 002514 100000 ECNT::      .WORD 100000 ; ERROR COUNT FOR UUT 0 - BIT 15 IS SET TO
1424 002516 100000          .WORD 100000 ; ERROR COUNT FOR UUT 1 - FLAG NOT TESTED.
1425 002520 100000          .WORD 100000 ; ERROR COUNT FOR UUT 2
1426 002522 100000          .WORD 100000 ; ERROR COUNT FOR UUT 3
1427 002524 100000          .WORD 100000 ; ERROR COUNT FOR UUT 4
1428 002526 100000          .WORD 100000 ; ERROR COUNT FOR UUT 5
1429 002530 100000          .WORD 100000 ; ERROR COUNT FOR UUT 6

```


GLOBAL AREAS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 17-1
GLOBAL DATA SECTION

1430	002532	100000	.WORD	100000	; ERROR COUNT FOR UUT 7
1431	002534	100000	.WORD	100000	; ERROR COUNT FOR UUT 8
1432	002536	100000	.WORD	100000	; ERROR COUNT FOR UUT 9
1433	002540	100000	.WORD	100000	; ERROR COUNT FOR UUT 10
1434	002542	100000	.WORD	100000	; ERROR COUNT FOR UUT 11
1435	002544	100000	.WORD	100000	; ERROR COUNT FOR UUT 12
1436	002546	100000	.WORD	100000	; ERROR COUNT FOR UUT 13
1437	002550	100000	.WORD	100000	; ERROR COUNT FOR UUT 14
1438	002552	100000	.WORD	100000	; ERROR COUNT FOR UUT 15
1439					
1440	002554	000000	GPADD::	.WORD 0	; PARAMETER ADDRESS FOR UNIT 0
1441	002556	000000		.WORD 0	; PARAMETER ADDRESS FOR UNIT 1
1442	002560	000000		.WORD 0	; PARAMETER ADDRESS FOR UNIT 2
1443	002562	000000		.WORD 0	; PARAMETER ADDRESS FOR UNIT 3
1444	002564	000000		.WORD 0	; PARAMETER ADDRESS FOR UNIT 4
1445	002566	000000		.WORD 0	; PARAMETER ADDRESS FOR UNIT 5
1446	002570	000000		.WORD 0	; PARAMETER ADDRESS FOR UNIT 6
1447	002572	000000		.WORD 0	; PARAMETER ADDRESS FOR UNIT 7
1448	002574	000000		.WORD 0	; PARAMETER ADDRESS FOR UNIT 8
1449	002576	000000		.WORD 0	; PARAMETER ADDRESS FOR UNIT 9
1450	002600	000000		.WORD 0	; PARAMETER ADDRESS FOR UNIT 10
1451	002602	000000		.WORD 0	; PARAMETER ADDRESS FOR UNIT 11
1452	002604	000000		.WORD 0	; PARAMETER ADDRESS FOR UNIT 12
1453	002606	000000		.WORD 0	; PARAMETER ADDRESS FOR UNIT 13
1454	002610	000000		.WORD 0	; PARAMETER ADDRESS FOR UNIT 14
1455	002612	000000		.WORD 0	; PARAMETER ADDRESS FOR UNIT 15
1456					
1457	002614		DROPED::	.BLKB 16.	; UNIT DROPPED FLAGS
1458					
1459	002634	000000	TSTFLG::	.WORD 0	; CLEARED AT START OF EACH PASS
1460					; SET IF ANY TEST IS SELECTED
1461	002636	000000	TSUFLG::	.WORD 0	; CLEARED AT START OF EACH UNIT'S PASS
1462					; SET IF ANY TEST IS SELECTED
1463					
1464					
1465					
1466	002640	000000	LOPFLG::	.WORD 0	; SET IF LOOP CHECK ROUTINE CALLED FOR UNIT 0
1467	002642	000000		.WORD 0	; SET IF LOOP CHECK ROUTINE CALLED FOR UNIT 1
1468	002644	000000		.WORD 0	; SET IF LOOP CHECK ROUTINE CALLED FOR UNIT 2
1469	002646	000000		.WORD 0	; SET IF LOOP CHECK ROUTINE CALLED FOR UNIT 3
1470	002650	000000		.WORD 0	; SET IF LOOP CHECK ROUTINE CALLED FOR UNIT 4
1471	002652	000000		.WORD 0	; SET IF LOOP CHECK ROUTINE CALLED FOR UNIT 5
1472	002654	000000		.WORD 0	; SET IF LOOP CHECK ROUTINE CALLED FOR UNIT 6
1473	002656	000000		.WORD 0	; SET IF LOOP CHECK ROUTINE CALLED FOR UNIT 7
1474	002660	000000		.WORD 0	; SET IF LOOP CHECK ROUTINE CALLED FOR UNIT 8
1475	002662	000000		.WORD 0	; SET IF LOOP CHECK ROUTINE CALLED FOR UNIT 9
1476	002664	000000		.WORD 0	; SET IF LOOP CHECK ROUTINE CALLED FOR UNIT 10
1477	002666	000000		.WORD 0	; SET IF LOOP CHECK ROUTINE CALLED FOR UNIT 11
1478	002670	000000		.WORD 0	; SET IF LOOP CHECK ROUTINE CALLED FOR UNIT 12
1479	002672	000000		.WORD 0	; SET IF LOOP CHECK ROUTINE CALLED FOR UNIT 13
1480	002674	000000		.WORD 0	; SET IF LOOP CHECK ROUTINE CALLED FOR UNIT 14
1481	002676	000000		.WORD 0	; SET IF LOOP CHECK ROUTINE CALLED FOR UNIT 15
1482					
1483	002700	000000	LOTFLA::	.WORD 0	; SET BY INIT CODE IF LOOP ON TEST IS SELECTED
1484					
1485	002702	000000	CONMSK::	.WORD 0	; TEST CONTROL MASK SET UP BY INIT CODE
1486					

GLOBAL AREAS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 17-2
GLOBAL DATA SECTION

1487
1488
1489
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1494
1495
1496
1497
1498
1499
1500
1501
1502
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1517
1518
1519
1520

LOW BYTE IS DEVICE CODE

```

; BIT 0 IS SET IF UUT IS DIGITAL INPUT
; BIT 1 IS SET IF UUT IS DIGITAL OUTPUT
; BIT 2 IS SET IF UUT IS ANALOGUE INPUT
; BIT 3 IS SET IF UUT IS ANALOGUE OUTPUT
; BIT 4 IS SET IF UUT IS NONE OF THE
; ABOVE
; BITS 5, 6 AND 7 ARE UNUSED

; BIT 8 IS ALWAYS SET TO SELECT BASIC
; INTERNAL LOGIC TESTS

; BIT 9 SET TO SELECT FIELD INPUT/OUTPUT
; TESTS

; BIT 10 IS SET IF LOOPBACK TESTING IS
; SELECTED AND ALLOWED FOR CURRENT UUT

; BIT 11 IS SET IF MANUFACTURING LOOPBACK
; AND INPUT/OUTPUT TESTS ARE SELECTED

; BIT 12 IS SET IF A SPECIFICALLY
; SELECTABLE TEST IS CHOSEN

; BITS 13, 14 AND 15 ARE UNUSED

; MODE FOR DIGITAL/ANALOGUE CONVERSIONS
; GAIN FOR DIGITAL/ANALOGUE CONVERSIONS

; ADDRESS OF PROMPT FOR DECIMAL INPUT ROUTINE

```

```
MODE:: .WORD 0
GAIN:: .WORD 0
PADD:: .WORD 0
```

.EVEN

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GLOBAL DATA SECTION

```

1522          000012          .RADIX 10
1523          .MLIST BEX
1524
1525          ; ANALOGUE/DIGITAL CONVERSION TABLES USED BY ROUTINES DACON AND ADCON.
1526
1527          ; VOLTAGE UNIPOLAR TABLE MODE 0 (0-10V)
1528
1529          ; BITS      11      10      9      8      7      6      5      4      3      2      1      0
1530
1531 002712 011610 004704 002342 VUPTAB:: 5000,2500,1250, 625, 312, 156,  78,  39,  19,   9,   4,   2 ; mV G=1
1532 002742 000000 000000 000000      0,   0,   0,   0, 500, 250, 125,  63, 531, 766, 883, 441 ; uV
1533
1534 002772 004704 002342 001161      2500,1250, 625, 312, 156,  78,  39,  19,   9,   4,   2,   1 ; mV G=2
1535 003022 000000 000000 000000      0,   0,   0, 500, 250, 125,  63, 531, 766, 883, 441, 221 ; uV
1536
1537 003052 001750 000754 000372      1000, 500, 250, 125,  62,  31,  15,   7,   3,   1,   0,   0 ; mV G=5
1538 003102 000000 000000 000000      0,   0,   0,   0, 500, 250, 625, 813, 906, 953, 977, 488 ; uV
1539
1540 003132 000764 000372 000175      500, 250, 125,  62,  31,  15,   7,   3,   1,   0,   0,   0 ; mV G=10
1541 003162 000000 000000 000000      0,   0,   0, 500, 250, 625, 813, 906, 953, 977, 488, 244 ; uV
1542
1543 003212 000372 000175 000076      250, 125,  62,  31,  15,   7,   3,   1,   0,   0,   0,   0 ; mV G=20
1544 003242 000000 000000 000764      0,   0, 500, 250, 625, 813, 906, 953, 977, 488, 244, 122 ; uV
1545
1546 003272 000144 000062 000031      100,  50,  25,  12,   6,   3,   1,   0,   0,   0,   0,   0 ; mV G=50
1547 003322 000000 000000 000000      0,   0,   0, 500, 250, 125, 563, 781, 391, 195,  98,  49 ; uV
1548
1549 003352 000062 000031 000014      50,  25,  12,   6,   3,   1,   0,   0,   0,   0,   0,   0 ; mV G=100
1550 003402 000000 000000 000764      0,   0, 500, 250, 125, 563, 781, 391, 195,  98,  49,  24 ; uV
1551
1552 003432 000031 000014 000006      25,  12,   6,   3,   1,   0,   0,   0,   0,   0,   0,   0 ; mV G=200
1553 003462 000000 000764 000372      0, 500, 250, 125, 563, 781, 391, 195,  98,  49,  24,  12 ; uV
1554

```

GLOBAL AREAS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 19
GLOBAL DATA SECTION

```

1556          ;VOLTAGE BIPOLAR TABLE (-10 - +10V) MODE 1
1557
1558          ; BITS   11   10   9   8   7   6   5   4   3   2   1   0
1559
1560 003512 023420 011610 004704 VBPTAB::10000,5000,2500,1250, 625, 312, 156, 78, 39, 19, 9, 4 ; mV G=1
1561 003542 000000 000000 000000      0, 0, 0, 0, 0, 500, 250, 125, 63, 31, 15, 7, 4 ; uV
1562
1563 003572 011610 004704 002342      5000,2500,1250, 625, 312, 156, 78, 39, 19, 9, 4, 2 ; mV G=2
1564 003622 000000 000000 000000      0, 0, 0, 0, 500, 250, 125, 63, 31, 15, 7, 4, 2 ; uV
1565
1566 003652 003720 001750 000764      2000,1000, 500, 250, 125, 62, 31, 15, 7, 3, 1, 0 ; mV G=5
1567 003702 000000 000000 000000      0, 0, 0, 0, 500, 250, 625, 813, 906, 953, 977 ; uV
1568
1569 003732 001750 000764 000372      1000, 500, 250, 125, 62, 31, 15, 7, 3, 1, 0, 0 ; mV G=10
1570 003762 000000 000000 000000      0, 0, 0, 0, 500, 250, 625, 813, 906, 953, 977, 488 ; uV
1571
1572 004012 000764 000372 000175      500, 250, 125, 62, 31, 15, 7, 3, 1, 0, 0, 0 ; mV G=20
1573 004042 000000 000000 000000      0, 0, 0, 500, 250, 625, 813, 906, 953, 977, 488, 244 ; uV
1574
1575 004072 000310 000144 000062      200, 100, 50, 25, 12, 6, 3, 1, 0, 0, 0, 0 ; mV G=50
1576 004122 000000 000000 000000      0, 0, 0, 0, 500, 250, 125, 563, 781, 391, 195, 98 ; uV
1577
1578 004152 000144 000062 000031      100, 50, 25, 12, 6, 3, 1, 0, 0, 0, 0, 0 ; mV G=100
1579 004202 000000 000000 000000      0, 0, 0, 500, 250, 125, 563, 781, 391, 195, 98, 49 ; uV
1580
1581 004232 000062 000031 000014      50, 25, 12, 6, 3, 1, 0, 0, 0, 0, 0, 0 ; mV G=200
1582 004262 000000 000000 000764      0, 0, 500, 250, 125, 563, 781, 391, 195, 98, 49, 24 ; uV
1583

```

GLOBAL AREAS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 20
GLOBAL DATA SECTION

```

1585          ;CURRENT 0-20 mA TABLE - MODE 2
1586
1587          ; BITS   11   10   9   8   7   6   5   4   3   2   1   0
1588
1589 004312 023420 011610 004704 IOTAB:: 10000,5000,2500,1250, 625, 312, 156, 78, 39, 19, 9, 4 ; uA G=1
1590 004342 000000 000000 000000      0, 0, 0, 0, 0, 500, 250, 125, 63, 531, 766, 883 ; nA
1591
1592 004372 011610 004704 002342      5000,2500,1250, 625, 312, 156, 78, 39, 19, 9, 4, 2 ; uA G=2
1593 004422 000000 000000 000000      0, 0, 0, 0, 500, 250, 125, 63, 531, 766, 883, 441 ; nA
1594
1595 004452 003720 001750 000764      2000,1000, 500, 250, 125, 62, 31, 15, 7, 3, 1, 0 ; mA G=5
1596 004502 000000 000000 000000      0, 0, 0, 0, 0, 500, 250, 625, 813, 906, 953, 977 ; nA
1597
1598 004532 001750 000764 000372      1000, 500, 250, 125, 62, 31, 15, 7, 3, 1, 0, 0 ; uA G=10
1599 004562 000000 000000 000000      0, 0, 0, 0, 500, 250, 625, 813, 906, 953, 977, 488 ; nA
1600
1601 004612 000764 000372 000175      500, 250, 125, 62, 31, 15, 7, 3, 1, 0, 0, 0 ; uA G=20
1602 004642 000000 000000 000000      0, 0, 0, 500, 250, 625, 813, 906, 953, 977, 488, 244 ; nA
1603
1604 004672 000310 000144 000062      200, 100, 50, 25, 12, 6, 3, 1, 0, 0, 0, 0 ; uA G=50
1605 004722 000000 000000 000000      0, 0, 0, 0, 500, 250, 125, 563, 781, 391, 195, 98 ; nA
1606
1607 004752 000144 000062 000031      100, 50, 25, 12, 6, 3, 1, 0, 0, 0, 0, 0 ; uA G=100
1608 005002 000000 000000 000000      0, 0, 0, 500, 250, 125, 563, 781, 391, 195, 98, 49 ; nA
1609
1610 005032 000062 000031 000014      50, 25, 12, 6, 3, 1, 0, 0, 0, 0, 0, 0 ; uA G=200
1611 005062 000000 000000 000764      0, 0, 500, 250, 125, 563, 781, 391, 195, 98, 49, 24 ; nA
1612

```

GLOBAL AREAS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 21
GLOBAL DATA SECTION

```

1614
1615      ; CURRENT 4 - 20 MA TABLE (MODE 3)
1616
1617      ; BITS      11      10      9      8      7      6      5      4      3      2      1      0
1618
1619 005112 017500 007640 003720 I4TAB:: 8000,4000,2000,1000, 500, 250, 125, 62, 31, 15, 7, 3 ; uA G=1
1620 005142 000000 000000 000000      0, 0, 0, 0, 0, 0, 0, 0, 500, 250, 625, 813, 906 ; nA
1621
1622 005172 007640 003720 001750      4000,2000,1000, 500, 250, 125, 62, 31, 15, 7, 3, 1 ; uA G=2
1623 005222 000000 000000 000000      0, 0, 0, 0, 0, 0, 0, 500, 250, 625, 813, 906, 953 ; nA
1624
1625 005252 003100 001440 000620      1600, 800, 400, 200, 100, 50, 25, 12, 6, 3, 1, 0 ; uA G=5
1626 005302 000000 000000 000000      0, 0, 0, 0, 0, 0, 0, 500, 250, 125, 563, 781 ; nA
1627
1628 005332 001440 000620 000310      800, 400, 200, 100, 50, 25, 12, 6, 3, 1, 0, 0 ; uA G=10
1629 005362 000000 000000 000000      0, 0, 0, 0, 0, 0, 0, 500, 250, 125, 563, 781, 390 ; nA
1630
1631 005412 000620 000310 000144      400, 200, 100, 50, 25, 12, 6, 3, 1, 0, 0, 0 ; uA G=20
1632 005442 000000 000000 000000      0, 0, 0, 0, 0, 0, 500, 250, 125, 563, 781, 390, 195 ; nA
1633
1634 005472 000240 000120 000050      160, 80, 40, 20, 10, 5, 2, 1, 0, 0, 0, 0 ; uA G=50
1635 005522 000000 000000 000000      0, 0, 0, 0, 0, 0, 500, 250, 625, 313, 156, 78 ; nA
1636
1637 005552 000120 000050 000024      80, 40, 20, 10, 5, 2, 1, 0, 0, 0, 0, 0 ; uA G=100
1638 005602 000000 000000 000000      0, 0, 0, 0, 0, 0, 500, 250, 625, 313, 156, 78, 39 ; nA
1639
1640 005632 000050 000024 000012      40, 20, 10, 5, 2, 1, 0, 0, 0, 0, 0, 0 ; uA G=200
1641 005662 000000 000000 000000      0, 0, 0, 0, 0, 500, 250, 625, 313, 156, 78, 39, 20 ; nA
1642
1643
1644      000010      .RADIX 8
1645      .LIST BEX

```

GLOBAL AREAS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 22
GLOBAL DATA SECTION

1647

1660

1661 005712

005712 000000

005714 000000

005716 000000

005720 000000

ERRTBL

ERRTYP:: .WORD 0

ERRNBR:: .WORD 0

ERRMSG:: .WORD 0

ERRBLK:: .WORD 0

GLOBAL AREAS MACRO V05.00 Wednesday 03 Oct-84 14:03 Page 23
GLOBAL TEXT SECTION

```

1663      .SBTTL  GLOBAL TEXT SECTION
1664
1665      ;**
1666      ; THE GLOBAL TEXT SECTION CONTAINS FORMAT STATEMENTS,
1667      ; MESSAGES, AND ASCII INFORMATION THAT ARE USED IN
1668      ; MORE THAN ONE TEST.
1669      ;--
1670
1671      .MLIST  9EX
1672
1673      ;
1674      ; NAMES OF DEVICES SUPPORTED BY PROGRAM
1675      ;
1676      DEVTYP  <IDV11-D>
1677
1678      ; TEST DESCRIPTION
1679      ;
1680      DESCRIPT      <FIVE CHANNEL COUNTER DIAGNOSTIC>
1681
1682      ;
1683      ; FORMAT STATEMENTS USED IN PRINT CALLS
1684      ;
1685
1686      005732
1687
1688
1689
1690
1691
1692
1693
1694
1695
1696
1697
1698
1699
1700
1701
1702
1703
1704
1705
1706
1707
1708
1709
1710      005772      045      116      045  NODEV:: .ASCIZ  \###A*** NO DEVICES FOUND IN RANGE 171000 TO 171770 ***\
1711
1712      .LIST  BEX
1713      .EVEN
1714

```


GLOBAL AREAS MACRO V05.00 Wednesday 03-Oct 84 14:03 Page 24
GLOBAL ERROR REPORT SECTION

.SBTTL GLOBAL ERROR REPORT SECTION

; THE GLOBAL ERROR REPORT SECTION CONTAINS MESSAGE PRINTING AREAS
; USED BY MORE THAN TEST TO OUTPUT ADDITIONAL ERROR INFORMATION. PRINTB
; (BASIC) AND PRINTX (EXTENDED) CALLS ARE USED TO CALL PRINT SERVICES.
:-

1723
1724
1725
1726
1727
1728
1729
1730
1731
1747

1748 006064
1749 006064
1750 006114 004737 014434
1751 006120

BGNMSG EER1
PRINTB @GOOBAD,GOOD,BAD ; PRINT GOOD AND BAD
JSR PC,CHKMAX ; CHECK FOR TOO MANY ERRORS
ENDMSG

1752
1753
1754 006122
1755 006122
1756 006156 004737 014434
1757 006162

BGNMSG EER2
PRINTB @EMG3,GOOD,BAD,BMOD
JSR PC,CHKMAX
ENDMSG

1758
1759 006164
1760 006164
1761 006210 004737 014434
1762 006214

BGNMSG EER3
PRINTB @EMG4,BCSR
JSR PC,CHKMAX
ENDMSG

1763
1764
1765
1766 006216
1767 006216
1768 006242 004737 014434
1769 006242

BGNMSG EER6
PRINTB @EMG4,BAD
JSR PC,CHKMAX
ENDMSG

1770
1771
1772
1773 006250
1774 006250
1775 006272 004737 014434
1776 006276

BGNMSG EERA
PRINTB @EMG1,R1
JSR PC,CHKMAX
ENDMSG

1777
1778
1779 006300
1780 006300
1781 006332 004737 014434
1782 006336

BGNMSG EERB
PRINTB @EMG2,R5,GOOD,BAD
JSR PC,CHKMAX
ENDMSG

1783
1784
1785 006340
1786 006340 004737 014434
1787 006344

BGNMSG EERG
JSR PC,CHKMAX
ENDMSG

1788
1789
1790
1791
1792
1793 006346
1794 006346

BGNMSG ERR104
PRINTB @E104,NUMBER ; ERROR MESSAGE

GLOBAL AREAS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 24-1
GLOBAL ERROR REPORT SECTION

1795	006372	004737	014434	JSR	PC,CHKMAX	
1796	006376			ENDMSG		
1797						
1798	006400			BGNMSG	ERR105	
1799	006400			PRINTB	#E105,NUMBER	;ERROR MESSAGE
1800	006424	004737	014434	JSR	PC,CHKMAX	
1801	006430			ENDMSG		
1802						
1803	006432			BGNMSG	ERR106	
1804	006432			PRINTB	#E106,GROUP	;ERROR MESSAGE
1805	006456			PRINTB	#E106A,COUNTR,R3	;ERROR MESSAGE
1806	006504	004737	014434	JSR	PC,CHKMAX	
1807	006510			ENDMSG		
1808						
1809	006512			BGNMSG	ERR107	
1810	006512			PRINTB	#E107,GROUP	;ERROR MESSAGE
1811	006536			PRINTB	#E107A,COUNTR,R3	;ERROR MESSAGE
1812	006564	004737	014434	JSR	PC,CHKMAX	
1813	006570			ENDMSG		
1814						
1815	006572			BGNMSG	ERR108	
1816	006572			PRINTB	#E108,NUMBER	;ERROR MESSAGE
1817	006616	004737	014434	JSR	PC,CHKMAX	
1818	006622			ENDMSG		
1819						
1820	006624			BGNMSG	ERR109	
1821	006624			PRINTB	#E109,NUMBER	;ERROR MESSAGE
1822	006650	004737	014434	JSR	PC,CHKMAX	
1823	006654			ENDMSG		
1824						
1825	006656			BGNMSG	ERR110	
1826	006656			PRINTB	#E110,NUMBER	;ERROR MESSAGE
1827	006702	004737	014434	JSR	PC,CHKMAX	
1828	006706			ENDMSG		
1829						
1830	006710			BGNMSG	ERR111	
1831	006710			PRINTB	#E111,NUMBER	;ERROR MESSAGE
1832	006734	004737	014434	JSR	PC,CHKMAX	
1833	006740			ENDMSG		
1834						
1835	006742			BGNMSG	ERR112	
1836	006742			PRINTB	#E112,NUMBER	;ERROR MESSAGE
1837	006766	004737	014434	JSR	PC,CHKMAX	
1838	006772			ENDMSG		
1839						
1840	006774			BGNMSG	ERR501	
1841	006774			PRINTB	#E501	
1842	007014			PRINTB	#GOOBAD,R2,BAD	;ERROR MESSAGE
1843	007042	004737	014434	JSR	PC,CHKMAX	
1844	007046			ENDMSG		
1845						
1846	007050			BGNMSG	ERR502	
1847	007050			PRINTB	#E502	;ERROR MESSAGE
1848	007070			PRINTB	#GOOBAD,R2,BAD	;ERROR MESSAGE
1849	007116	004737	014434	JSR	PC,CHKMAX	
1850	007122			ENDMSG		
1851						

GLOBAL AREAS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 24-2
GLOBAL ERROR REPORT SECTION

1852 007124		BGNMSG	ERR503	
1853 007124		PRINTB	#E113	;ERROR MESSAGE
1854 007144	004737 014434	JSR	PC,CHKMAX	
1855 007150		ENDMSG		
1856				
1857 007152		BGNMSG	ERR504	
1858 007152		PRINTB	#E114,INTFLA	;ERROR MESSAGE
1859 007176	004737 014434	JSR	PC,CHKMAX	
1860 007202		ENDMSG		
1861				
1862 007204		BGNMSG	ERR505	
1863 007204		PRINTB	#E114A	;ERROR MESSAGE
1864 007224		PRINTB	#E114B,VEC,BIV	;ERROR MESSAGE
1865 007254	004737 014434	JSR	PC,CHKMAX	
1866 007260		ENDMSG		
1867				
1868				
1869 007262		BGNMSG	ERR506	
1870 007262		PRINTB	#E506	;ERROR MESSAGE
1871 007302		PRINTB	#GOOBAD,GOOD,BAD	;ERROR MESSAGE
1872 007332	004737 014434	JSR	PC,CHKMAX	
1873 007336		ENDMSG		
1874				
1875				
1876 007340		BGNMSG	ERR507	
1877 007340		PRINTB	#E507	;ERROR MESSAGE
1878 007360		PRINTB	#E507A,BAD	;ERROR MESSAGE
1879 007404	004737 014434	JSR	PC,CHKMAX	
1880 007410		ENDMSG		
1881				
1882				
1883 007412		BGNMSG	ERR115	
1884 007412		PRINTB	#E115,NUMBER	;ERROR MESSAGE
1885 007436	004737 014434	JSR	PC,CHKMAX	
1886 007442		ENDMSG		
1887				
1888 007444		BGNMSG	ERR130	
1889 007444		PRINTB	#STRVA1	
1890 007464		PRINTB	#STRVA2	
1891 007504	012702 000001	MOV	#1,R2	
1892 007510	012701 002452	MOV	#CNTVAL,R1	
1893 007514	012103	MOV	(R1)+,R3	
1894 007516		PRINTB	#CNTXT,R2,R3	
1895 007542	005202	INC	R2	;CHANNEL NUMBER
1896 007544	020227 000005	CHP	R2,#5	;ALL CHANNELS?
1897 007550	003761	BLE	601	;MORE TO PRINT
1898 007552	004737 014434	JSR	PC,CHKMAX	
1899 007556		ENDMSG		
1900				
1901 007560		BGNMSG	ERR116	
1902 007560		PRINTB	#E116,NUMBER,R2	;SOURCE
1903 007606		PRINTB	#E116B	
1904 007626	004737 014434	JSR	PC,CHKMAX	
1905 007632		ENDMSG		
1906				
1907 007634		BGNMSG	ERR116A	
1908 007634		PRINTB	#E116A,NUMBER,R2	

601:

GLOBAL AREAS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 24-3
GLOBAL ERROR REPORT SECTION

1909	007662			PRINTB	#E116B	
1910	007702	004737	014434	JSR	PC,CHKMAX	
1911	007706			ENDMSG		
1912						
1913	007710			BGNMSG	ER116B	
1914	007710			PRINTB	#E116,R2,R3	;SOURCE
1915	007734			PRINTB	#E116B	
1916	007754	004737	014434	JSR	PC,CHKMAX	
1917	007760			ENDMSG		
1918						
1919	007762			BGNMSG	ER116C	
1920	007762			PRINTB	#E116A,R2,R3	
1921	010006			PRINTB	#E116B	
1922	010026	004737	014434	JSR	PC,CHKMAX	
1923	010032			ENDMSG		
1924						
1925	010034			BGNMSG	ERR120	
1926	010034			PRINTB	#E120,R5	;ERROR MESSAGE
1927	010056			PRINTB	#E116B	
1928	010076	004737	014434	JSR	PC,CHKMAX	
1929	010102			ENDMSG		
1930						
1931	010104			BGNMSG	ERR121	
1932	010104			PRINTB	#E121,R5	;ERROR MESSAGE
1933	010126	004737	014434	JSR	PC,CHKMAX	
1934	010132			ENDMSG		
1935						
1936	010134			BGNMSG	ERR122	
1937	010134			PRINTB	#E121,R5	;ERROR MESSAGE
1938	010156	004737	014434	JSR	PC,CHKMAX	
1939	010162			ENDMSG		
1940						
1941	010164			BGNMSG	ERR123	
1942	010164			PRINTB	#E120,R5	;ERROR MESSAGE
1943	010206			PRINTB	#E116B	
1944	010226	004737	014434	JSR	PC,CHKMAX	
1945	010232			ENDMSG		
1946						
1947	010234			BGNMSG	ERR124	
1948	010234			PRINTB	#E124,(R5),R0	;ERROR MESSAGE
1949	010260			PRINTB	#E116B	
1950	010300	004737	014434	JSR	PC,CHKMAX	
1951	010304			ENDMSG		
1952						
1953						
1954						
1955						
1956						
1957						
1958						
1959						
1960						
1961	010306	045	101	107	GOOBAD: .ASCIZ	/#AGOOD:#06#A, BAD:#06#N/
1962	010336	045	101	122	EMG1: .ASCIZ	/#AREGISTER AT #06#A DOES NOT RESPOND#N/
1963	010405	045	101	101	EMG2: .ASCIZ	/#AADDRESS: #06#A, GOOD:#06#A, BAD:#06#N/
1964	010455	045	101	107	EMG3: .ASCIZ	/#AGOOD:#06#A, BAD:#06#A, MOD REGISTER CONTENTS:#06#N/
1965	010542	045	101	103	EMG4: .ASCIZ	/#ACSR REGISTER CONTENTS IS :#06#N/

GLOBAL AREAS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 24-4
GLOBAL ERROR REPORT SECTION

```

1966
1967
1968
1969
1970
1971 010604      105      122      122  E102:  .LIST BEX
1972 010662      105      122      122  .EVEN
1973 010732      045      101      105  .NLIST BEX
1974 011007      045      101      105  .ASCIZ /ERROR - SPECIAL MODULE BIT IN MADR IS NOT SET/
1975 011062      045      101      105  E103:  .ASCIZ /ERROR - 'CT MOD' BIT IN MEDR IS NOT SET/
1976 011140      045      101      054  E104:  .ASCIZ /#AERROR - UNABLE TO CLEAR 'OUT' BIT NR: #D#N/
1977 011201      045      101      105  E105:  .ASCIZ /#AERROR - UNABLE TO SET 'OUT' BIT NR: #D#N/
1978 011257      045      101      054  E106:  .ASCIZ /#AERROR - WRITING LOAD REGISTER OF CHA.: #D1#N/
1979 011321      045      101      105  E106A: .ASCIZ /#A, WRITTEN: #06#A, READ: #06#N/
1980 011373      045      101      105  E107:  .ASCIZ /#AERROR - WRITING HOLD REGISTER OF CHA.: #D1#N/
1981 011443      045      101      105  E107A: .ASCIZ /#A, WRITTEN: #06#A, READ: #06#N/
1982 011516      045      101      105  E108:  .ASCIZ /#AERROR - CAN NOT CLEAR 'EN' BIT NR: #D#N/
1983 011567      045      101      105  E109:  .ASCIZ /#AERROR - CAN NOT SET 'EN' BIT NR: #D#N/
1984 011641      045      101      105  E110:  .ASCIZ /#AERROR - CAN NOT CLEAR 'INT' BIT NR: #D#N/
1985 011713      045      101      105  E111:  .ASCIZ /#AERROR - CAN NOT SET 'INT' BIT NR: #D#N/
1986 012000      045      101      105  E112:  .ASCIZ /#AERROR - 'INT' BIT NR: #D1#A HAS RESET#N/
1987 012052      045      101      105  E113:  .ASCIZ /#AERROR - UNEXPECTED INTERRUPT DETECTED#N/
1988 012126      045      101      105  E114:  .ASCIZ /#AERROR - EXPECTING ONE INTERRUPT, ENCOUNTERED #D5#N/
1989 012173      045      101      105  E114A: .ASCIZ /#AERROR - BAD INTERRUPT VECTOR DETECTED#N/
1990 012246      045      101      123  E114B: .ASCIZ /#ASET UP VECTOR :#03#A FOUND VECTOR :#03#N/
1991 012307      045      101      103  E115:  .ASCIZ /#ACOUNTER #D1#A DID NOT INCREMENT#N/
1992 012403      045      101      123  STRVA1: .ASCII /#AUNEXPECTED DIFFERENCE BETWEEN COUNTERS #N/
1993
1994 012442      045      101      103  STAVA2: .ASCIZ /#ASHOULD NOT BE GREATER THAN 2#N/
1995 012525      045      101      103  CNTXT:  .ASCIZ /#ACOUNT DOWN FROM 10,000 AT 5 MHZ FOR 3 NOP INSTRUCTIONS.#N/
1996 012606      045      101      105  .EVEN
1997 012662      045      101      103  E116:  .ASCIZ /#ACOUNTER #D1#A DID NOT INCREMENT#A, SOURCE #D2#N/
1998 012747      045      101      103  E116A: .ASCIZ /#ACOUNTER #D1#A DID NOT INCREMENT#A, GATE #D2#N/
1999 013030      045      101      105  E116B: .ASCIZ /#AENSURE THAT TEST CONNECTOR IS INSTALLED#N/
2000 013113      045      101      102  E120:  .ASCIZ /#ABAD COUNTER VALUE, EXPECTED 10000, DETECTED: #D5#N/
2001 013146      045      101      102  E121:  .ASCIZ /#ABAD COUNTER VALUE, EXPECTED 0, DETECTED: #D5#N/
2002 013235      045      101      105  E124:  .ASCIZ /#AERROR LOADING TOGGLE, WRITTEN #06#A, READ: #06#N/
2003 013333      045      101      111  E501:  .ASCIZ /#AINR REGISTER INCORRECT#N/
2004 013432      045      101      111  E502:  .ASCIZ /#AIR BIT IN INR REGISTER NOT RESETED AFTER BIT CLEAR#N/
2005
2006
2007
2008
2009
2010
2011
2012
2013
2014
2015
2016

```

GLOBAL AREAS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 25
GLOBAL SUBROUTINES SECTION

```

2018 .SBTTL GLOBAL SUBROUTINES SECTION
2019
2020 ;**
2021 ; THE GLOBAL SUBROUTINES SECTION CONTAINS THE SUBROUTINES
2022 ; THAT ARE USED IN MORE THAN ONE TEST.
2023 ; -
2024
2025 ; SUBROUTINE REGTST - GENERAL PURPOSE REGISTER TEST.
2026
2027 ;**
2028 ; FUNCTIONAL DESCRIPTION:
2029 ;
2030 ; CHECKS THAT ALL READ/WRITE BITS OF THE SELECTED REGISTER CAN BE
2031 ; SET, CLEARED, AND INDIVIDUALLY SET (SLIDING ONES PATTERN).
2032 ;
2033 ; INPUTS:
2034 ;
2035 ; IF ENTERED AT LOCATION REGTST, THE LOCATIONS FOLLOWING THE
2036 ; SUBROUTINE CALL MUST CONTAIN THE READ/WRITE BIT MASK, THE
2037 ; ADDRESS OF THE REGISTER TO BE TESTED, AND THE FIRST ERROR NUMBER
2038 ; TO BE USED (SEE CALLING SEQUENCE).
2039 ;
2040 ; IF ENTERED AT LOCATION REGTST1, THE READ/WRITE BIT MASK, REGISTER
2041 ; ADDRESS TO BE TESTED, AND THE FIRST ERROR NUMBER MUST BE LOADED
2042 ; INTO LOCATIONS MASK, REGADD, AND ERRNBR RESPECTIVELY. THIS
2043 ; ALLOWS THE ARGUMENTS TO BE VARIED AT RUN TIME.
2044 ;
2045 ; IMPLICIT INPUTS:
2046 ;
2047 ; NONE.
2048 ;
2049 ; OUTPUTS:
2050 ;
2051 ; ERROR MESSAGES IF ERRORS OCCUR.
2052 ;
2053 ; IMPLICIT OUTPUTS:
2054 ;
2055 ; IF ENTERED AT LOCATION REGTST,
2056 ;
2057 ; MASK - CONTAINS THE READ/WRITE BIT MASK
2058 ; REGADD - CONTAINS THE ADDRESS OF THE REGISTER BEING TESTED
2059 ;
2060 ; ALWAYS,
2061 ;
2062 ; MASCOM - CONTAINS THE COMPLEMENT OF THE MASK
2063 ; GOOD - CONTAINS LAST EXPECTED DATA
2064 ; BAD - CONTAINS LAST ACTUAL DATA
2065 ; ERRNBR - CONTAINS THE INPUT ERROR NUMBER + 2
2066 ; ERRTP - CONTAINS 3 (SOFT ERROR)
2067 ; ERRBLK - CONTAINS ADDRESS OF REGERR (REGISTER ERROR MESSAGE)
2068 ; ERRMSG - CONTAINS 3RD REGISTER ERROR MESSAGE
2069 ;
2070 ; SUBORDINATE ROUTINES USED:
2071 ;
2072 ; INSERT - ERROR INSERTION ROUTINE
2073 ; CHKMAX - ERROR COUNT CHECKING ROUTINE
2074 ; DRS ERROR MACRO

```

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```

2075
2076      ; FUNCTIONAL SIDE EFFECTS:
2077      ;
2078      ;     NONE.
2079      ;
2080      ; CALLING SEQUENCE:
2081      ;
2082      ;     EITHER FIXED PARAMETERS FOLLOW THE SUBROUTINE CALL :
2083      ;
2084      ;     EG.      CALL      REGTST
2085      ;               177              ; BIT MASK OF R/W BITS
2086      ;               CSR              ; REGISTER ADDRESS
2087      ;               200.             ; FIRST ERROR NUMBER
2088      ;
2089      ;     OR PARAMETERS ARE SET DYNAMICALLY :
2090      ;
2091      ;     EG.      MOV      #177,MASK      ; BIT MASK OF R/W BITS
2092      ;               MOV      CSR,REGADD     ; REGISTER ADDRESS
2093      ;               MOV      #200.,ERRNBR   ; FIRST ERROR NUMBER
2094      ;               CALL      REGTS1
2095      ;
2096      ;
2097      ;
2098 013474 REGTST::
2099 013474 017637 000000 014046      MOV      @SP,MASK      ; GET R/W BIT MASK
2100 013502 062716 000002 014046      ADD      #2,(SP)      ; JUMP OVER ARGUMENT
2101 013506 017637 000000 014052      MOV      @SP,REGADD     ; GET REGISTER ADDRESS
2102 013514 062716 000002 014052      ADD      #2,(SP)      ; JUMP OVER ARGUMENT
2103 013520 017637 000000 005714      MOV      @SP,ERRNBR   ; GET FIRST ERROR NUMBER
2104 013526 062716 000002 005714      ADD      #2,(SP)      ; JUMP OVER ARGUMENT
2105 013532
2106 013532 015737 014046 014050 REGTS1::      MOV      MASK,MASCOM      ; SET UP COMPLEMENT
2107 013540 005137 014050 014050      COM      MASCOM      ; OF R/W BIT MASK
2108 013544 012737 000003 005712      MOV      #3,ERRTYP   ; SET UP FOR SOFT ERROR
2109 013552 012737 014054 005720      MOV      @REGERR,ERRBLK ; SET UP ERROR MESSAGE ROUTINE
2110 013560 012737 014122 005716      MOV      @ERR1,ERRMSG  ; FIRST ERROR MESSAGE
2111
2112      ; CHECK THAT ALL R/W BITS CAN BE SET
2113      ;
2114 013566 013737 014046 002474      MOV      MASK,GOOD      ; SET UP EXPECTED DATA
2115 013574      BGNSEG
2116 013576 053777 002474 000246      BIS      GOOD,BREGADD  ; SET ALL R/W BITS
2117 013604 017737 000242 002476      MOV      @BREGADD,BAD ; READ THE RESULT
2118 013612 043737 014050 002476      BIC      MASCOM,BAD    ; KEEP ONLY R/W BITS
2119 013620 023737 002476 002474      CMP      BAD,GOOD     ; ALL R/W BITS SET?
2120 013626 004737 015240 002474      CALL      INSERT     ; ALLOW FORCED ERROR PRINTOUT
2121 013632 001401      BEQ      104      ; IF OK, BRANCH
2122 013634      ERROR
2123 013636      104:      ENDSEG
2124
2125      ; CHECK THAT ALL R/W BITS CAN BE CLEARED
2126      ;
2127 013640 005037 002474      CLR      GOOD      ; SET UP EXPECTED DATA
2128 013644 005237 005714      INC      ERRNBR      ; NEXT ERROR NUMBER
2129 013650 012737 014174 005716      MOV      @ERR2,ERRMSG  ; NEXT ERROR MESSAGE
2130
2131 013656      BGNSEG

```

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```

2132 013660 043777 014046 000164      BIC      MASK,BREGADD      ; CLEAR ALL R/W BITS
2133 013666 017737 000160 002476      MOV      BREGADD,BAD      ; READ THE RESULT
2134 013674 043737 014050 002476      BIC      MASCOM,BAD      ; KEEP ONLY R/W BITS
2135 013702 023737 002476 002474      CMP      BAD,GOOD      ; ALL R/W BITS CLEAR?
2136 013710 004737 015240              CALL     INSERT      ; ALLOW FORCED ERROR PRINTOUT
2137 013714 001401              BEQ      201      ; IF OK, BRANCH
2138 013716              ERROR      ; ELSE REPORT ERROR
2139 013720              201:      ENDSEG
2140              ;
2141              ; CHECK THAT EACH R/W BIT CAN BE SET
2142              ;
2143 013722 005237 005714              INC      ERRNBR      ; NEXT ERROR NUMBER
2144 013726 012737 014252 005716      MOV      @RERR3,ERRMSG      ; NEXT ERROR MESSAGE
2145 013734 012737 000001 002474      MOV      #1,GOOD      ; FIRST BIT TO TEST
2146 013742 033737 002474 014046      301:      BIT      GOOD,MASK      ; R/W BIT?
2147 013750 001004              BNE      501      ; IF YES, TEST IT
2148 013752 006337 002474      401:      ASL      GOOD      ; ELSE FIND NEXT R/W BIT
2149 013756 103427              BCS      701      ; IF ALL DONE, RETURN
2150 013760 000770              BR      301      ; ELSE CHECK IF NEXT IS R/W
2151
2152 013762              501:      BGNSEG
2153 013764 043777 014046 000060      BIC      MASK,BREGADD      ; CLEAR ALL R/W BITS
2154 013772 053777 002474 000052      BIS      GOOD,BREGADD      ; SET ONE BIT
2155 014000 017737 000046 002476      MOV      BREGADD,BAD      ; READ IT BACK
2156 014006 043737 014050 002476      BIC      MASCOM,BAD      ; KEEP ONLY R/W BITS
2157 014014 023737 002476 002474      CMP      BAD,GOOD      ; ALL OTHER BITS CLEAR?
2158 014022 004737 015240              CALL     INSERT      ; ALLOW FORCED ERROR PRINTOUT
2159 014026 001401              BEQ      601      ; IF OK, BRANCH
2160 014030              ERROR      ; ELSE REPORT ERROR
2161 014032              601:      ENDSEG
2162 014034 000746              GR      401      ; TEST NEXT BIT
2163
2164 014036 043777 014046 0000C6      701:      BIC      MASK,BREGADD      ; LEAVE THE R/W BITS CLEAR
2165 014044 000207              RETURN
2166
2167
2168 014046 000000      MASK:: .WORD      0      ; BIT MASK OF READ/WRITE BITS
2169 014050 000000      MASCOM:: .WORD      0      ; COMPLEMENT OF MASK
2170 014052 000000      REGADD:: .WORD      0      ; ADDRESS OF REGISTER TO BE TESTED
2171
2172 014054              BGNMSG      REGERR
2173 014054              PRINTB      @REGMSG,REGADD,GOOD,BAD,MASK
2174 014114 004737 014434              JSR      PC,CHKMAX      ; CHECK FOR TOO MANY ERRORS
2175 014120              ENDMSG
2176
2177              .NLIST BEX
2178
2179 014122      122      105      107 RERR1: .ASCIZ  #REGISTER READ/WRITE BITS COULD NOT BE SET#
2180 014174      122      105      107 RERR2: .ASCIZ  #REGISTER READ/WRITE BITS COULD NOT SE CLEARED#
2181 014252      122      105      107 RERR3: .ASCIZ  #REGISTER READ/WRITE BITS COULD NOT BE INDIVIDUALLY SET#
2182
2183 014341      045      101      101 REGMSG: .ASCIZ  .#ADDRESS: #06#A, GOOD: #06#A, BAD: #06#A, R/W BITS: #06#N.
2184
2185              .LIST BEX
2186              .EVEN

```


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```

2188 ; SUBROUTINE CHKMAX - ERROR COUNT CHECKING ROUTINE.
2189 ;
2190 ; **
2191 ; FUNCTIONAL DESCRIPTION:
2192 ;
2193 ;     SUBROUTINE TO UPDATE UNIT ERROR COUNT. IF THE PROGRAM IS LOOPING
2194 ;     ON AN ERROR, THE SUBROUTINE DOES NOTHING. OTHERWISE, THE ERROR
2195 ;     COUNT FOR THE UNIT IS INCREMENTED. IF THE ERROR COUNT EXCEEDS 5
2196 ;     AND THE USER FLAG EVL HAS BEEN SELECTED AND THE FLAG IDU IS NOT
2197 ;     SELECTED, THE UNIT IS DROPPED FROM THE TEST CYCLE.
2198 ;
2199 ; INPUTS:
2200 ;
2201 ;     NONE.
2202 ;
2203 ; IMPLICIT INPUTS:
2204 ;
2205 ;     L#LUN CONTAINS THE NUMBER OF THE UNIT CURRENTLY BEING TESTED.
2206 ;     ECNT IS THE ADDRESS OF THE ERROR COUNT FOR UNIT 0.
2207 ;
2208 ; OUTPUTS:
2209 ;
2210 ;     NONE.
2211 ;
2212 ; IMPLICIT OUTPUTS:
2213 ;
2214 ;     THE ERROR COUNT FOR THE LOGICAL UNIT BEING TESTED IS
2215 ;     INCREMENTED IF THE PROGRAM IS NOT LOOPING.
2216 ;
2217 ; SUBORDINATE ROUTINES USED:
2218 ;
2219 ;     NONE
2220 ;
2221 ; FUNCTIONAL SIDE EFFECTS:
2222 ;
2223 ;     IF THE ERROR COUNT EXCEEDS 5 AND THE USER EVL FLAG IS SELECTED,
2224 ;     AND THE 'LOOP ON TEST' AND 'INHIBIT DROPPING OF UNITS' FLAGS ARE
2225 ;     NOT SELECTED, THE UNIT WILL BE DROPPED FROM TESTING.
2226 ;
2227 ; CALLING SEQUENCE:
2228 ;
2229 ;     JSR PC,CHKMAX
2230 ;
2231 ; --
2232 ;
2233 014434 CHKMAX: INLOOP ; LOOPING ON ERROR?
2234 014436 BCOMPLETE 104 ; IF YES, EXIT
2235 ;
2236 014440 013700 002074 MOV L#LUN,RO ; GET CURRENT UNIT
2237 014444 006300 ASL RO ; CONVERT TO ERROR COUNT OFFSET
2238 014446 005260 002514 INC ECNT(RO) ; UPDATE THE ERROR COUNT
2239 014452 026027 002514 000005 CMP ECNT(RO),#5 ; TOO MANY ERRORS?
2240 014460 003425 BLE 104 ; IF NOT, JUMP
2241 ;
2242 014462 RFLAGS RO ; GET OPERATOR FLAGS
2243 014464 032700 000040 BIT #IDU,RO ; IS DROPPING INHIBITED?
2244 014470 001021 BNE 104 ; IF YES, EXIT

```

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 GLOBAL SUBROUTINES SECTION

```

2245 014472 032700 000004      BIT    #EVL,R0      ; EVALUATE FLAG SELEC ED ?
2246 014476 001416              BEQ     10$          ; IF NOT, EXIT
2247                               PRINTF  #NERRS,L$LUN ; 'TOO MANY ERRORS'
2248 014500                      DODU    L$LUN       ; DROP THE UNIT
2249 014524                      DOCLN                    ; END THE SUBPASS
2250
2251 014532                      DOCLN                    ; END THE SUBPASS
2252
2253 014534 000207              10$:   RTS     PC
2254
2255                               .NLIST  BEX
2256 014536      045      116      045  NERRS: .ASCIZ /#N#AMORE THAN 5 ERRORS ON UNIT#D2/
2257                               .LIST   BEX
2258                               .EVEN
2259
2260
2261

```


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```

2263      ; SUBROUTINES WT25M, WT500 AND WT25 - DELAY ROUTINES.
2264      ;
2265      ; **
2266      ; FUNCTIONAL DESCRIPTION:
2267      ;
2268      ;     SUBROUTINE TO WAIT FOR 25 MILLISECONDS, 500 MICROSECONDS OR 25
2269      ;     MICROSECONDS.
2270      ;
2271      ;     NOTE. BECAUSE OF THE SMALL NUMBER OF PROGRAM WAIT LOOPS USED FOR
2272      ;     THE 25 MICROSECOND COUNTER, THE ACCURACY OF THE WT25 ROUTINE
2273      ;     IS LOW. THE DELAY MAY LAST UP TO 50 MICROSECONDS ON SOME
2274      ;     SLOW PROCESSORS.
2275      ;
2276      ; INPUTS:
2277      ;
2278      ;     NONE.
2279      ;
2280      ; IMPLICIT INPUTS:
2281      ;
2282      ;     THE VARIABLES CNT25M, CNT500, AND CNT25 MUST HAVE BEEN SET UP BY
2283      ;     ROUTINE SETCLK.
2284      ;
2285      ; OUTPUTS:
2286      ;
2287      ;     NONE.
2288      ;
2289      ; IMPLICIT OUTPUTS:
2290      ;
2291      ;     NONE.
2292      ;
2293      ; SUBORDINATE ROUTINES USED:
2294      ;
2295      ;     NONE.
2296      ;
2297      ; FUNCTIONAL SIDE EFFECTS:
2298      ;
2299      ;     NONE.
2300      ;
2301      ; CALLING SEQUENCE:
2302      ;
2303      ;     JSR      PC,WT25M      ; WAIT FOR 25 MILLISECONDS
2304      ;     OR JSR      PC,WT500    ; WAIT FOR 500 MICROSECONDS
2305      ;     OR JSR      PC,WT25     ; WAIT FOR 25 MICROSECONDS
2306      ;
2307      ; --
2308
2309 014600 013700 014626      WT25M:: MOV      CNT25M,R0      ; GET 25 MILLISECOND WAIT COUNT
2310 014604 000405              BR          WAIT              ;
2311
2312 014606 013700 014630      WT500:: MOV      CNT500,R0      ; GET 500 MICROSECOND WAIT COUNT
2313 014612 000402              BR          WAIT              ;
2314
2315 014614 013700 014632      WT25::  MOV      CNT25,R0       ; GET 25 MICROSECOND WAIT COUNT
2316
2317 014620 005300              WAIT:  DEC      R0              ; ALL DONE?
2318 014622 001376              BNE      WAIT              ; IF NOT, WAIT SOME MORE
2319 014624 000207              RTS      PC                  ; ELSE RETURN

```

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2320

2321 014626 000000

2322 014630 000000

2323 014632 000000

2324

CNT25M::WORD 0

CNT500::WORD 0

CNT25::WORD 0

; COUNTER FOR 25 MILLISECOND DELAY

; COUNTER FOR 500 MICROSECOND DELAY

; COUNTER FOR 25 MICROSECOND DELAY

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 GLOBAL SUBROUTINES SECTION

```

2326      , SUBROUTINE CRLF - ROUTINE TO PRINT CARRIAGE RETURN, LINE FEED.
2327
2328      ;**
2329      ; FUNCTIONAL DESCRIPTION:
2330      ;
2331      ;     PRINTS A CARRIAGE RETURN AND LINE FEED.
2332      ;
2333      ; INPUTS:
2334      ;
2335      ;     NONE.
2336      ;
2337      ; IMPLICIT INPUTS:
2338      ;
2339      ;     NONE.
2340      ;
2341      ; OUTPUTS:
2342      ;
2343      ;     A CARRIAGE RETURN AND LINE FEED ARE PRINTED.
2344      ;
2345      ; IMPLICIT OUTPUTS:
2346      ;
2347      ;     NONE.
2348      ;
2349      ; SUBORDINATE ROUTINES USED:
2350      ;
2351      ;     SUPERVISOR PRINTF MACRO.
2352      ;
2353      ; FUNCTIONAL SIDE EFFECTS:
2354      ;
2355      ;     NONE.
2356      ;
2357      ; CALLING SEQUENCE:
2358      ;
2359      ;     JSR     PC,CRLF
2360      ;
2361      ;--
2362
2363      CRLF::
2364      PRINTF  #LF
2365      RTS     PC
2366
2367      014656      045      116      000  LF:      .ASCIZ  /#N/
2368      .EVEN
  
```

```

2370      ; SUBROUTINE WRDY - SUBROUTINE TO WAIT FOR OPERATOR READY
2371
2372      ;**
2373      ; FUNCTIONAL DESCRIPTION:
2374      ;
2375      ;     THIS PRINTS A MESSAGE FOR THE OPERATOR TO TYPE 'CARRIAGE RETURN'
2376      ;     TO CONTINUE. THE ROUTINE IS NORMALLY USED TO ALLOW A MESSAGE TO
2377      ;     BE READ BEFORE PROCEEDING.
2378      ;
2379      ;     IF MANUAL INTERVENTION IS NOT ALLOWED, THE ROUTINE DOES NOTHING.
2380      ;
2381      ; INPUTS:
2382      ;
2383      ;     NONE.
2384      ;
2385      ; IMPLICIT INPUTS:
2386      ;
2387      ;     NONE.
2388      ;
2389      ; OUTPUTS:
2390      ;
2391      ;     'TYPE 'CARRIAGE RETURN' TO CONTINUE OR 'CONRTOL C' TO ABORT.
2392      ;
2393      ; IMPLICIT OUTPUTS:
2394      ;
2395      ;     NONE.
2396      ;
2397      ; SUBORDINATE ROUTINES USED:
2398      ;
2399      ;     SUPERVISOR GMANID MACRO.
2400      ;
2401      ; FUNCTIONAL SIDE EFFECTS:
2402      ;
2403      ;     NONE.
2404      ;
2405      ; CALLING SEQUENCE:
2406      ;
2407      ;     JSR      PC,WRDY
2408      ;
2409      ;--
2410
2411 014662      WRDY::
2412 014662          MANUAL                                ; IS MANUAL INTERVENTION ALLOWED ?
2413 014664          BNCOMPLETE 10$                        ; IF NOT, EXIT
2414 014666          GMANID  WRDY1,WFLG,A,377,0,1,YES ; 'TYPE RETURN TO CONTINUE'
2415 0147C5      10$:  RTS      PC
2416
2417 014710      WFLG:  .WORD  0                        ; FLAG FOR WARNING MESSAGE INPUT
2418
2419
2420 014712      124      131      120  WRDY1:  .NLIST  BEX
2421          .ASCIZ  /TYPE "CARRIAGE RETURN" TO CONTINUE OR "CONTROL C" TO ABORT./
2422          .LIST   BEX
2423          .EVEN

```

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```

2424      ; SUBROUTINE SELECT - TEST SELECT ROUTINE
2425
2426      ; **
2427      ; FUNCTIONAL DESCRIPTION:
2428
2429      ; THIS IS CALLED BY EACH TEST TO DECIDE WHETHER THE TEST SHOULD
2430      ; BE RUN BASED ON THE DEVICE TYPE AND THE TEST MODE SELECTED IN
2431      ; THE STARTUP QUESTIONS. A TEST SELECT MASK SUPPLIED BY THE TEST
2432      ; IS COMPARED WITH A CONTROL MASK SET UP BY THE INITIALISATION
2433      ; ROUTINE TO DECIDE WHETHER THE TEST IS RUN.
2434
2435      ; IF THE SOFTWARE FAULT INSERTION FLAG SFI IS SET, ALL NON-SPECIFIC
2436      ; TESTS ARE SELECTED.
2437
2438      ; INPUTS:
2439
2440      ; THE LOCATION FOLLOWING THE SUBROUTINE CALL CONTAINS THE TEST
2441      ; SELECT MASK IN THE FOLLOWING FORMAT :
2442
2443      ; BIT 0 IS SET IF DIGITAL INPUT MODULES ARE TO BE TESTED
2444      ; BIT 1 IS SET IF DIGITAL OUTPUT MODULES ARE TO BE TESTED
2445      ; BIT 2 IS SET IF ANALOGUE INPUT MODULES ARE TO BE TESTED
2446      ; BIT 3 IS SET IF ANALOGUE OUTPUT MODULES ARE TO BE TESTED
2447      ; BIT 4 IS SET IF FIVE CHANNEL COUNTER ARE TO BE TESTED
2448      ; BITS 5, 6 AND 7 ARE UNUSED
2449      ; BIT 8 IS SET TO INDICATE A BASIC INTERNAL LOGIC TEST
2450      ; BIT 9 IS SET FOR FIELD INPUT/OUTPUT TESTS
2451      ; BIT 10 IS SET FOR LOOPBACK TESTS
2452      ; BIT 11 IS SET FOR ANALOGUE INPUT/OUTPUT TESTS USED BY
2453      ; MANUFACTURING AND FIELD SERVICE
2454      ; BIT 12 IS SET FOR SPECIFICALLY SELECTABLE TESTS
2455      ; BITS 13, 14 AND 15 ARE UNUSED
2456
2457      ; THE SECOND LOCATION FOLLOWING THE SUBROUTINE CALL CONTAINS THE
2458      ; ADDRESS OF A TEST HEADER MESSAGE TO BE PRINTED IF THE TEST IS
2459      ; SELECTED AND THE USER 'PNT' FLAG IS SELECTED.
2460
2461      ; IMPLICIT INPUTS:
2462
2463      ; CONMSK - TEST CONTROL MASK SET UP BY INIT CODE AT THE BEGINING
2464      ; OF EACH SUBPASS. THE FORMAT IS AS FOLLOWS :
2465
2466      ; BIT 0 IS SET IF UUT IS DIGITAL INPUT
2467      ; BIT 1 IS SET IF UUT IS DIGITAL OUTPUT
2468      ; BIT 2 IS SET IF UUT IS ANALOGUE INPUT
2469      ; BIT 3 IS SET IF UUT IS ANALOGUE OUTPUT
2470      ; BIT 4 IS SET IF UUT IS FIVE CHANNEL COUNTER
2471      ; BITS 5, 6 AND 7 ARE UNUSED
2472      ; BIT 8 IS ALWAYS SET TO SELECT BASIC INTERNAL
2473      ; LOGIC TESTS
2474      ; BIT 9 SET TO SELECT FIELD INPUT/OUTPUT TESTS
2475      ; BIT 10 IS SET IF LOOPBACK TESTING IS SELECTED
2476      ; AND ALLOWED FOR CURRENT UUT
2477      ; BIT 11 IS SET IF MANUFACTURING HAVE SELECTED LOOPBACK
2478      ; AND I/O TESTS
2479      ; BIT 12 IS SET IF A SPECIFICALLY SELECTABLE TEST
2480      ; IS CHOSEN

```

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```

2481                                     : BITS 13, 14 AND 15 ARE UNUSED
2482                                     :
2483                                     : SF1 - IF THIS IS NON ZERO, ALL TESTS ARE SELECTED.
2484                                     :
2485                                     :
2486                                     : OUTPUTS:
2487                                     :
2488                                     : IF THE TEST MATCHES THE DEVICE TYPE AND TEST MODE CHOSEN VIA THE
2489                                     : STARTUP QUESTIONS, THE ROUTINE EXITS WITH THE CARRY BIT CLEAR.
2490                                     : IN THIS CASE, IF THE 'PNT' FLAG IS SELECTED, THE TEST HEADER IS
2491                                     : PRINTED AND THE FLAGS TSTFLG AND TSUFLG ARE SET TO SHOW THAT A
2492                                     : TEST HAS BEEN SELECTED.
2493                                     :
2494                                     : IF THE TEST IS NOT SELECTED, THE CARRY BIT IS SET AND, IF THE
2495                                     : 'PNT' FLAG IS SELECTED, A MESSAGE 'TEST DISABLED BY STARTUP
2496                                     : QUESTIONS' IS PRINTED.
2497                                     :
2498                                     : IF THE TEST IS NOT SPECIFICALLY SELECTABLE (BIT 11 IS CLEAR IN
2499                                     : THE TEST SELECT MASK), BIT 11 IS CLEARED IN THE CONTROL MASK TO
2500                                     : PREVENT SUBSEQUENT SPECIFICALLY SELECTABLE TESTS FROM BEING RUN.
2501                                     :
2502                                     : R0, R1 AND R2 ARE CORRUPTED.
2503                                     :
2504                                     : IMPLICIT OUTPUTS:
2505                                     :
2506                                     : NONE.
2507                                     :
2508                                     : SUBORDINATE ROUTINES USED:
2509                                     :
2510                                     : NONE.
2511                                     :
2512                                     : FUNCTIONAL SIDE EFFECTS:
2513                                     :
2514                                     : NONE.
2515                                     :
2516                                     : CALLING SEQUENCE:
2517                                     :
2518                                     : FIXED PARAMETERS FOLLOW THE SUBROUTINE CALL :
2519                                     :
2520                                     : EG.      CALL      SELECT
2521                                     :          777
2522                                     :                                     : TEST SELECT MASK (BASIC TEST
2523                                     :                                     : ON ALL DEVICE TYPES
2524                                     :          TNAME      : ADDRESS OF TEST HEADER
2525                                     :          BCS      TSTEND : BRANCH IF TEST NOT SELECTED
2526                                     :
2527                                     : ...
2528                                     :
2528 015006 SELECT::
2529 015006      MOV      B(SP),R1      : SAVE TEST SELECT MASK
2530 015012      ADD      #2,(SP)      : JUMP OVER THE ARGUMENT
2531 015016      MOV      B(SP),R2      : SAVE TEST HEADER ADDRESS
2532 015022      ADD      #2,(SP)      : JUMP OVER THE ARGUMENT
2533
2534 015026      RFLAGS  R0      : READ OPERATOR FLAGS INTO R0
2535
2536 015030      BIT      #10000,R1      : SPECIFICALLY SELECTABLE TEST ?
2537 015034      BNE      101      : IF YES, BRANCH

```


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```

2538 015036 042737 010000 002702      BIC      #10000,CONMSK      ; ELSE PREVENT SUCH TESTS
2539 015044 005737 002500      101:  TST      SFI              ; SOFTWARE FAULT INSERTION ?
2540 015050 001007              BNE      201              ; IF YES, SELECT THE TEST
2541 015052 130137 002702      BITB     R1,CONMSK          ; IS DEVICE THE CORRECT TYPE ?
2542 015056 001426              BEQ      301              ; IF NOT, BRANCH
2543 015060 105001              CLRB     R1              ; IF YES, DISCARD LOW BYTE
2544 015062 030137 002702      BIT      R1,CONMSK          ; AND CHECK TEST TYPE
2545 015066 001422              BEQ      301              ; IF WRONG, BRANCH
2546
2547                      ; TEST IS SELECTED
2548
2549 015070 012737 000001 002634 201:  MOV      #1,TSTFLG      ; FLAG THAT TEST IS SELECTED
2550 015076 012737 000001 002636      MOV      #1,TSUFLG      ;
2551
2552 015104 032700 001000              BIT      #PNT,R0          ; PRINT TEST HEADER ?
2553 015110 001425              BEQ      501              ; IF NOT, EXIT (CARRY IS CLEAR)
2554 015112              PRINTF     R2          ; ELSE PRINT THE HEADER
2555 015130 000241              CLC              ; CLEAR THE CARRY
2556 015132 000414              BR       501              ; AND EXIT
2557
2558                      ; TEST IS NOT SELECTED
2559
2560 015134 032700 001000      301:  BIT      #PNT,R0          ; PRINT TEST HEADER ?
2561 015140 001410              BEQ      401              ; IF NOT, EXIT
2562 015142              PRINTF     #TNA        ; ELSE PRINT 'NOT APPLICABLE'
2563 015162 000261      401:  SEC              ; SET THE CARRY BIT
2564
2565 015164 000207      501:  RETURN              ; COMMON RETURN
2566
2567                      .NLIST     BEX
2568 015166      045      101      040  TNA:  .ASCIZ  /#A TEST DISABLED BY STARTUP QUESTIONS#N/
2569                      .LIST      BEX
2570                      .EVEN

```

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GLOBAL SUBROUTINES SECTION

```

2572      ; SUBROUTINE INSERT - SUBROUTINE TO FORCE ERROR PRINTOUTS
2573
2574      ; **
2575      ; FUNCTIONAL DESCRIPTION:
2576      ;
2577      ;     THIS SUBROUTINE CAN BE USED TO FORCE PRINTOUT OF ERROR MESSAGES
2578      ;     FOR QUALITY CHECKING. IF THE FLAG 'SFI' IS NONE ZERO, THE BRANCH
2579      ;     INSTRUCTION FOLLOWING THE SUBROUTINE CALL IS SKIPPED OVER,
2580      ;     CAUSING THE ERROR MESSAGE TO BE PRINTED. IF 'SFI' IS SET TO 1,
2581      ;     THE ADDRESS OF THE SUBROUTINE CALL IS COMPARED WITH THAT OF THE
2582      ;     LAST CALL AND, IF IT HAS NOT CHANGED, THE MESSAGE IS NOT
2583      ;     PRINTED.
2584
2585      ; INPUTS:
2586      ;
2587      ;     SFI - IF ZERO, THE ROUTINE DOES NOTHING.
2588      ;           IF ONE, ERROR MESSAGES ARE PRINTED ONCE.
2589      ;           IF ANY OTHER VALUE, ERROR MESSAGES ARE ALWAYS PRINTED.
2590
2591      ; IMPLICIT INPUTS:
2592      ;
2593      ;     SEE CALLING SEQUENCE.
2594
2595      ; OUTPUTS:
2596      ;
2597      ;     LASTFA - IF THE ERROR MESSAGE IS TO BE PRINTED ONCE ONLY, LASTFA
2598      ;               IS LOADED WITH THIS SUBROUTINE RETURN ADDRESS.
2599
2600      ; IMPLICIT OUTPUTS:
2601      ;
2602      ;     NONE.
2603
2604      ; SUBORDINATE ROUTINES USED:
2605      ;
2606      ;     NONE.
2607
2608      ; FUNCTIONAL SIDE EFFECTS:
2609      ;
2610      ;     IF 'SFI' IS NONE ZERO, THE ROUTINE RETURN ADDRESS IS INCREMENTED
2611      ;     BY ONE WORD.
2612
2613      ; CALLING SEQUENCE:
2614      ;
2615      ;     A ONE WORD BRANCH INSTRUCTION MUST FOLLOW THE SUBROUTINE CALL
2616      ;     BEFORE THE ERROR PRINT CALL.
2617
2618      ;     EG.      CMP      BAD,GOOD      ; REGISTER CORRECT ?
2619      ;               CALL     INSERT        ; SKIP BRANCH IF SFI FLAG SET
2620      ;               BEQ      10$           ; BRANCH IF REGISTER CORRECT
2621      ;               ERROR                                ; ELSE PRINT OUT ERROR MESSAGE
2622
2623      ; --
2624
2625      ; INSERT::
2626      ;     MFPS      -(SP)                  ; SAVE CONDITION CODES
2627      ;     CMP       SFI,#1                 ; INSERT FAULTS ?
2628      ;     BLO       20$                     ; IF NOT, BRANCH

```


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GLOBAL SUBROUTINES SECTION

2629	015252	001007			BNE	10:		; IF ALWAYS, BRANCH
2630	015254	026637	000002	015304	CMF	2(SP),LASTFA		; IS THIS FAULT ALREADY PRINTED?
2631	015262	001406			BEQ	20:		; IF YES, EXIT
2632	015264	016637	000002	015304	MOV	2(SP),LASTFA		; ELSE, SAVE FAULT ADDRESS
2633	015272	062766	000002	000002	10:	ADD	#2,2(SP)	; SKIP BRANCH TO FORCE PRINTOUT
2634	015300	106426			20:	MTPS	(SP).	; RESTORE CONDITION CODES
2635	015302	000207			RETURN			; AND RETURN
2636								
2637	015304	000000			LASTFA: .WORD	0		; ADDRESS OF ROUTINE CALL
2638								

```

2640 ; SUBROUTINE CONFIG - ROUTINE TO FIND THE DEVICE CONFIGURATION AUTOMATICALLY.
2641
2642 ;**
2643 ; FUNCTIONAL DESCRIPTION:
2644
2645 ; THIS ROUTINE SEARCHES THE IDV/IAV-11 ADDRESS RANGE (171000-171770) FOR A
2646 ; RESPONDING ADDRESS. FOR THE FIRST ADDRESS ON A 4 WORD BOUNDARY (171XX0)
2647 ; WHICH DOES NOT CAUSE AN NXM TRAP, AN ENTRY IS MADE IN THE HARDWARE
2648 ; P TABLE FOR UNIT L#LUN. FOR ADDRESSES OVER 171400, A VECTOR IS
2649 ; ASSUMED BASED ON THE LOW 8 BITS OF THE DEVICE ADDRESS.
2650
2651 ; EACH CALL TO THE ROUTINE WILL CAUSE ONE MORE UNIT TO BE SEARCHED FOR.
2652 ; IF THE UNIT FOUND IS THE LAST IN THE IDV/IAV-11 RANGE, L#UNITS IS SET TO
2653 ; THE TOTAL NUMBER OF UNITS. IF NO UNIT IS FOUND, THE PROGRAM EXITS
2654 ; WITH THE CARRY BIT SET AND L#UNIT SET TO L#LUN.
2655
2656 ; MODULES WITH MODE REGISTER CONTENTS NOT CORRESPONDING TO AN IDV/IAV11
2657 ; ARE SET UP, BUT CAUSE AN 'UNIDENTIFIED MODULE' MESSAGE TO BE OUTPUT.
2658
2659 ; INPUTS:
2660
2661 ; L#LUN - NUMBER FOR THE NEXT UNIT FOUND.
2662
2663 ; IMPLICIT INPUTS:
2664
2665 ; STADD MUST BE SET TO 171000 PRIOR TO THE FIRST CALL TO INITIALISE
2666 ; THE SEARCH AREA.
2667
2668 ; ADDRESSES DEFINED BY "IXSTA" TO "IXEND" OF THE I/O PAGE ARE READ.
2669
2670 ; THE WORD L#LUN*2 AFTER LABEL GPADD MUST CONTAIN THE PARAMETER
2671 ; TABLE ADDRESS FOR THE UNIT.
2672
2673 ; OUTPUTS:
2674
2675 ; THE HARDWARE P TABLE AND L#UNIT ARE SET UP TO INCLUDE UP TO 16
2676 ; UNITS FOUND IN THE ABOVE ADDRESS RANGE.
2677
2678 ; A MESSAGE 'UNIDENTIFIED MODULE FOUND AT ADDRESS nnnnn' MAY BE
2679 ; PRINTED.
2680
2681 ; IMPLICIT OUTPUTS:
2682
2683 ; IF THE UNIT FOUND IS THE LAST IN THE IDV/IAV-11 RANGE, L#UNIT IS SET UP
2684 ; TO CONTAIN THE TOTAL NUMBER OF UNITS FOUND (L#LUN+1).
2685
2686 ; IF NO MORE UNITS ARE FOUND, THE CARRY BIT IS SET AND L#LUN IS
2687 ; PLACED IN L#UNIT.
2688
2689 ; SUBORDINATE ROUTINES USED:
2690
2691 ; NXM - NON EXISTANT MEMORY TRAP ROUTINE.
2692 ; WRDY - ROUTINE TO WAIT FOR OPERATOR TO TYPE 'RETURN'.
2693
2694 ; FUNCTIONAL SIDE EFFECTS:
2695
2696 ; NXMFLG MAY BE SET.

```

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GLOBAL SUBROUTINES SECTION

```

2697
2698
2699
2700
2701
2702
2703
2704
2705 015306
2706 015306 010146
2707 015310 010246
2708 015312 010346
2709 015314 010446
2710
2711 015316 013702 015572
2712 015322 012737 000001 015574
2713 015330
2714
2715 015356 005037 002470
2716 015362 005712
2717 015364 005737 002470
2718 015370 001006
2719 015372 005737 015574
2720 015376 001422
2721 015400 005037 015574
2722 015404 010201
2723
2724 015406 062702 000010
2725 015412 020227 171770
2726 015416 003757
2727 015420 012702 171000
2728 015424 013737 002074 002012
2729 015432 005737 015574
2730 015436 001041
2731 015440 005237 002012
2732
2733 015444 021127 177400
2734 015450 103413
2735 015452
2736 015474 004737 014662
2737
2738 015500 013700 002074
2739 015504 006300
2740 015506 016003 002554
2741 015512 010123
2742
2743 015514 042701 177000
2744 015520 032701 000400
2745 015524 001001
2746 015526 005001
2747 015530 010123
2748 015532 001402
2749 015534 012701 000200
2750 015540 010123
2751
2752 015542 010237 015572
2753 015546

```

CALLING SEQUENCE:

EG. JSR PC.CONFIG
BCS INIUUT

IF NO MORE UNITS, START AGAIN

CONFIG::

MOV R1,-(SP) ; SAVE R1 TO R4
MOV R2,-(SP) ;
MOV R3,-(SP) ;
MOV R4,-(SP) ;

MOV STADD,R2 ; START SEARCH FROM THIS ADDRESS
MOV #1,NOUNIT ; ASSUME NO UNIT WILL BE FOUND
SETVEC #4,#NXM,#340 ; SET UP NXM TRAP

10\$: CLR NXMFLG ; CLEAR THE NXM FLAG
TST (R2) ; CHECK THE ADDRESS
TST NXMFLG ; ANYTHING THERE ?
BNE 20\$; IF NOT, TRY NEXT DEVICE ADDRESS
TST NOUNIT ; IF YES, IS IT THE 2ND ONE THIS CALL ?
BEQ 30\$; IF YES, SET UP THE P TABLE
CLR NOUNIT ; IF IT'S THE FIRST, FLAG UNIT FOUND
MOV R2,R1 ; AND SAVE THE ADDRESS

20\$: ADD #10,R2 ; GET THE NEXT ADDRESS
CMP R2,#IXEND ; OUT OF THE IXV11 RANGE ?
BLE 10\$; IF NOT, GO BACK
MOV #IXSTA,R2 ; ELSE START AGAIN NEXT CALL
MOV L\$LUN,L\$UNIT ; SAVE THE UNIT NUMBER
TST NOUNIT ; WERE ANY UNITS FOUND ?
BNE 70\$; IF NOT, EXIT
INC L\$UNIT ; ELSE WE HAVE THE LAST UNIT

30\$: CMP (R1),#177400 ; IS MODULE ID OK ?
BLO 40\$; IF YES, BRANCH
PRINTF #C01,R1 ; ELSE PRINT 'UNIDENTIFIED'
JSR PC,WRDY ; WAIT FOR OPERATOR TO TYPE 'RETURN'

40\$: MOV L\$LUN,R0 ; FORM OFFSET FOR LUN
ASL R0 ;
MOV GPADD(R0),R3 ; GET P TABLE ADDRESS IN R3
MOV R1,(R3)+ ; SAVE THE DEVICE ADDRESS

50\$: BIC #177000,R1 ; CALCULATE THE VECTOR ADDRESS
BIT #400,R1 ; ADDRESS OVER 171400 ?
BNE 50\$; IF YES, BRANCH
CLR R1 ; ELSE ZERO THE VECTOR
MOV R1,(R3)+ ; AND SAVE IT
BEQ 60\$; IF 0, BRANCH
MOV #200,R1 ; ELSE SET UP DEFAULT PRIORITY
MOV R1,(R3)+ ; SAVE THE PRIORITY

60\$:

70\$: MOV R2,STADD ; SAVE THE NEXT SEARCH ADDRESS
CLRVEC #4 ; RESTORE THE NXM TRAP CATCHER

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 GLOBAL SUBROUTINES SECTION

2754	015554	012604				MOV	(SP)+,R4		; RESTORE R4 TO R1
2755	015556	012603				MOV	(SP)+,R3		;
2756	015560	012602				MOV	(SP)+,R2		;
2757	015562	012601				MOV	(SP)+,R1		;
2758	015564	006237	015574			ASR	NOUNIT		; IF NO UNIT, SET THE CARRY BIT
2759	015570	000207				RTS	PC		; AND RETURN
2760									
2761	015572	171000				STADD::	.WORD	IXSTA	; START ADDRESS OF SEARCH AREA
2762	015574	000000				NOUNIT:	.WORD	0	; SET TO SHOW NO UNIT FOUND
2763									
2764						.NLIST	BEX		
2765	015576	045	116	045	C01:	.ASCIZ	/##AUNIDENTIFIED MODULE FOUND AT ADDRESS #06#A./		
2766						.LIST	BEX		
2767						.EVEN			

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GLOBAL SUBROUTINES SECTION

```

2769 ; SUBROUTINE CONPRI - SUBROUTINE TO PRINT A CONFIGURATION TABLE.
2770
2771 ;**
2772 ; FUNCTIONAL DESCRIPTION:
2773 ;
2774 ;     THIS ROUTINE PRINTS A CONFIGURATION TABLE OF ALL UNITS LISTED IN
2775 ;     THE HARDWARE PARAMETER TABLE. LISTED ARE THE MODE REGISTER ADDRESS,
2776 ;     VECTOR AND DEVICE TYPE.
2777 ;
2778 ; INPUTS:
2779 ;
2780 ;     NONE.
2781 ;
2782 ; IMPLICIT INPUTS:
2783 ;
2784 ;     THE HARDWARE P TABLE.
2785 ;
2786 ;     IF NO UNITS ARE CONFIGURED, THE GLOBAL MESSAGE LABELLED "NODEV"
2787 ;     IS PRINTED.
2788 ;
2789 ; OUTPUTS:
2790 ;
2791 ;     THE CONFIGURATION TABLE IS PRINTED.
2792 ;
2793 ; IMPLICIT OUTPUTS:
2794 ;
2795 ;     IF NO UNITS ARE CONFIGURED (L#UNIT = 0), A MESSAGE 'NO DEVICES
2796 ;     FOUND' IS PRINTED.
2797 ;
2798 ; SUBORDINATE ROUTINES USED:
2799 ;
2800 ;     NXM - NON EXISTANT MEMORY TRAP ROUTINE.
2801 ;     CRLF - LINE FEED PRINT ROUTINE.
2802 ;
2803 ; FUNCTIONAL SIDE EFFECTS:
2804 ;
2805 ;     REGISTERS R1 TO R5 ARE CORRUPTED.
2806 ;
2807 ; CALLING SEQUENCE:
2808 ;
2809 ;     JSR      PC,CONPRI
2810 ;
2811 ;--
2812
2813 015656 CONPRI::
2814 015656         SETVEC  #4,#NXM,#340           ; CATCH REFERENCES TO NXM
2815 015704 005737 002012         TST      L#UNIT           ; ANY UNITS CONFIGURED ?
2816 015710 001012         BNE      10#           ; IF YES, BRANCH
2817 015712         PRINTF  #NODEV           ; ELSE PRINT 'NO DEVICES FOUND'
2818 015732 000137 016452         JMP      CONEX           ; AND EXIT
2819
2820 015736 10#:         PRINTF  #CP1           ; PRINT CONFIGURATION HEADER
2821 015756         PRINTF  #CP1A           ;
2822 015776         PRINTF  #CP1B           ;
2823 016016         PRINTF  #CP1C           ;
2824 016036 005001         CLR      R1           ; START WITH FIRST UNIT
2825 016040 010100 20#:         MOV      R1,R0           ; FORM PARAMETER TABLE OFFSET

```

```

2826 016042 00E300          ASL      R0
2827 016044 016002 002554  MOV      GPADD(R0),R2
2828 016050 012203          MOV      (R2)+,R3
2829 016052          PRINTF    #CP2,R1,R3
2830 016076 005722          TST      (R2)+
2831 016100 001413          BEQ      304
2832 016102          PRINTF    #CP3,-2(R2)
2833 016126 000410          BR       404
2834 016130          304:      PRINTF    #CP4
2835
2836 016150 005037 002470  404:      CLR      NXMFLG
2837 016154 112305          MOV      (R3)+,R5
2838 016156 111304          MOV      (R3),R4
2839 016160 042704 177400  BIC      #177400,R4
2840 016164 005737 002470  TST      NXMFLG
2841 016170 001421          BEQ      504
2842 016172          PRINTF    #CP10
2843 016212          PRINTF    #CP10A
2844 016232 000501          BR       1104
2845
2846 016234          504:      PRINTF    #CP5,R4,R5
2847
2848 016260 022704 000300  CMP      #300,R4
2849 016264 101014          BHI      554
2850 016266 012703 017222  MOV      #DCNT,R3
2851 016272          PRINTF    #CP6,R3
2852 016314 000450          BR       1104
2853 016316 020427 000037  554:      CMP      R4,#37
2854 016322 101003          BHI      604
2855 016324 012703 017156  MOV      #DI,R3
2856 016330 000421          BR       904
2857 016332 020427 000077  604:      CMP      R4,#77
2858 016336 101003          BHI      704
2859 016340 012703 017167  MOV      #DO,R3
2860 016344 000413          BR       904
2861 016346 020427 000177  704:      CMP      R4,#177
2862 016352 101003          BHI      804
2863 016354 012703 017200  MOV      #AI,R3
2864 016360 000405          BR       904
2865 016362 020427 000277  804:      CMP      R4,#277
2866 016366 101013          BHI      1004
2867 016370 012703 017211  MOV      #AO,R3
2868 016374          904:      PRINTF    #CP6,R3
2869 016416          1004:     PRINTF    #CP7
2870
2871
2872 016436 005201          1104:     INC      R1
2873 016440 020137 002012  CMP      R1,L#UNIT
2874 016444 002002          BGE      CONEX
2875 016446 000137 016040  JMP      204
2876 016452 004737 014634  CONEX:  JSR      PC,CRLF
2877 016456          CLRVEC    #4
2878 016464 000207          RTS      PC
2879
2880          .NLIST    BEX
2881
2882 016466          045      116      045  CP1:  .ASCIIZ  \#N#S24#AIDV/IAV-11 MODULE CONFIGURATION.\

```


2883	016537		045	116	045	CP1A:	.ASCIZ	/##S24#A-----	/			
2884	016611		045	116	062	CP1B:	.ASCIZ	\N2#AUNIT ADDRESS VECTOR ID/MODE		MODULE		COMMENT#\
2885	016707		045	123	061	CP1C:	.ASCIZ	\S15#AAASSUMED\S13#ATYPE#\				
2886	016741		045	116	045	CP2:	.ASCIZ	\N#D3#O9\				
2887	016752		045	123	065	CP3:	.ASCIZ	\S5#O3\				
2888	016761		045	123	064	CP4:	.ASCIZ	\S4#ANONE\				
2889	016773		045	123	063	CP5:	.ASCIZ	\S3#O3#A/#O3\				
2890	017010		045	123	063	CP6:	.ASCIZ	\S3#T\				
2891	017016		045	101	040	CP7:	.ASCIZ	\#A * CANNOT BE TESTED WITH THIS DIAG *\				
2892	017066		045	101	040	CP8:	.ASCIZ	\#A YES\				
2893	017077		045	101	040	CP9:	.ASCIZ	\#A NO\				
2894	017110		045	101	040	CP10:	.ASCIZ	\#A UNKNOWN\				
2895	017125		045	101	040	CP10A:	.ASCIZ	\#A UNKNOWN \				
2896	017143		045	117	071	CP11:	.ASCIZ	\#O9\				
2897	017147		045	123	065	CP12:	.ASCIZ	\S5#O3\				
2898												
2899	017156		104	111	107	DI:	.ASCIZ	/DIG. IN /				
2900	017167		104	111	107	DO:	.ASCIZ	/DIG. OUT/				
2901	017200		101	116	056	AI:	.ASCIZ	/AN. IN /				
2902	017211		101	116	056	AO:	.ASCIZ	/AN. OUT/				
2903	017222		106	111	126	DCNT:	.ASCIZ	/FIVE CHA. COUNTER/				
2904												
2905							.LIST	BEX				
2906							.EVEN					

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GLOBAL SUBROUTINES SECTION

```

2908 ; SUBROUTINE DACON - DIGITAL TO ANALOGUE CONVERSION ROUTINE.
2909
2910 ;**
2911 ; FUNCTIONAL DESCRIPTION:
2912 ;
2913 ;     THIS CONVERTS A 12 BIT DIGITAL PATTERN INTO A 2 WORD ANALOGUE
2914 ;     OUTPUT VALUE.
2915 ;
2916 ; INPUTS:
2917 ;
2918 ;     MODE : 0 = UNIPOLAR (0 TO 10 VOLTS)      BINARY CODED
2919 ;            1 = BIPOLAR (-10 TO +10 VOLTS)    OFFSET BINARY CODED
2920 ;            2 = 0 - 20 mA                      BINARY CODED
2921 ;            3 = 4 - 20 mA                      BINARY CODED
2922 ;
2923 ;     GAIN : 0 - 7 = GAINS 1,2,5,10,20,50,100,200
2924 ;
2925 ;     R1   : 12 BIT INPUT PATTERN.
2926 ;
2927 ; IMPLICIT INPUTS:
2928 ;
2929 ;     VITAB AND ITAB : DIGITAL/ANALOGUE CONVERSION TABLES.
2930 ;
2931 ; OUTPUTS:
2932 ;
2933 ;     R1 - MILLIVOLTS (MODES 0 AND 1)
2934 ;           MICROAMPS (MODES 2 AND 3)
2935 ;
2936 ;     R2 - MICROVOLTS (MODES 0 AND 1)
2937 ;           NANOAMPS (MODES 2 AND 3)
2938 ;
2939 ; IMPLICIT OUTPUTS:
2940 ;
2941 ;     NONE.
2942 ;
2943 ; SUBORDINATE ROUTINES USED:
2944 ;
2945 ;     NONE.
2946 ;
2947 ; FUNCTIONAL SIDE EFFECTS:
2948 ;
2949 ;     NONE.
2950 ;
2951 ; CALLING SEQUENCE:
2952 ;
2953 ;     EG.  MOV     #1,MODE      ; BIPOLAR CONVERSION
2954 ;           MOV     #6,GAIN     ; GAIN = 100
2955 ;           MOV     #7777,R1    ; ALL BITS SET
2956 ;           JSR     PC,DACON
2957 ;
2958 ; --
2959
2960 017244 DACON::
2961 017244 010346      MOV     R3,-(SP)      ; SAVE R3
2962 017246 010446      MOV     R4,-(SP)      ; AND R4
2963 017250 013703      MOV     MODE,R3      ; GET MODE
2964 017254 005703      TST     R3           ; MODE 0 ?

```


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GLOBAL SUBROUTINES SECTION

```

2965 017256 001003      BNE      10$      ; IF NOT, BRANCH
2966 017260 012700 002712  MOV     #VUPTAB,R0 ; GET CONVERSION TABLE FOR MODE 0
2967 017264 000414      BR       PSDA      ;
2968
2969          10$: DEC      R3      ; MODE 1 ?
2970 017270 001003      BNE      20$      ; IF NOT, BRANCH
2971 017272 012700 003512  MOV     #VBPTAB,R0 ; GET CONVERSION TABLE FOR MODE 1
2972 017276 000407      BR       PSDA      ;
2973
2974          20$: DEC      R3      ; MODE 2 ?
2975 017302 001003      BNE      30$      ; IF NOT, BRANCH
2976 017304 012700 004312  MOV     #IOTAB,R0 ; GET CONVERSION TABLE FOR MODE 2
2977 017310 000402      BR       PSDA      ;
2978
2979          30$: MOV     #I4TAB,R0 ; USE I4TAB FOR MODE 3
2980
2981 017316 013703 002706  PSDA: MOV     GAIN,R3 ; GET THE GAIN
2982 017322 005303      30$: DEC      R3      ; HAVE WE GOT THE CORRECT TABLE ?
2983 017324 100403      BMI      40$      ; IF YES, BRANCH
2984 017326 062700 000060  ADD     #48.,R0 ; ELSE INCREASE THE TABLE OFFSET
2985 017332 000773      BR       30$      ; AND TRY AGAIN
2986
2987          40$: MOV     (R0),R4 ; SAVE HIGH BIT VALUE
2988 017336 010103      MOV     R1,R3 ; SAVE THE BIT PATTERN
2989 017340 005001      CLR      R1      ; CLEAR THE OUTPUT REGISTERS
2990 017342 005002      CLR      R2      ;
2991 017344 006303      ASL      R3      ; SHIFT OUT UNUSED BITS
2992 017346 006303      ASL      R3      ; ( BITS 15 - 12 )
2993 017350 006303      ASL      R3      ;
2994 017352 006303      ASL      R3      ;
2995
2996          50$: ASL      R3      ; TEST A BIT
2997 017356 103011      BCC      70$      ; IF CLEAR, BRANCH
2998 017360 066002 000030  ADD     24.(R0),R2 ; ELSE ADD IN LOW VALUE
2999 017364 020227 001750  CMP     R2,#1000. ; OVERFLOW OF LOW WORD ?
3000 017370 002403      BLT      60$      ; IF NOT, BRANCH
3001 017372 162702 001750  SUB     #1000.,R2 ; ELSE CARRY FROM LOW WORD
3002 017376 005201      INC      R1      ; TO HIGH WORD
3003 017400 061001      60$: ADD     (R0),R1 ; AND ADD IN HIGH VALUE
3004 017402 062700 000002  70$: ADD     #2,R0 ; GET NEXT TABLE ENTRY
3005 017406 005703      TST      R3      ; ALL BITS PROCESSED ?
3006 017410 001361      BNE      50$      ; IF NOT, DO MORE BITS
3007
3008 017412 023727 002704 000001 80$: CMP     MODE,#1 ; BIPOLAR VOLTAGE CONVERSION ?
3009 017420 001007      BNE      90$      ; IF NOT, BRANCH
3010 017422 160401      SUB     R4,R1 ; ELSE MAKE BIPOLAR
3011 017424 002005      BGE      90$      ; IF STILL POSITIVE, BRANCH
3012 017426 005702      TST      R2      ; DECIMAL PART ZERO ?
3013 017430 001403      BEQ      90$      ; IF YES, BRANCH
3014 017432 162702 001750  SUB     #1000.,R2 ; ELSE BORROW FROM HIGH PART
3015 017436 005201      INC      R1      ;
3016
3017 017440 023727 002704 000003 90$: CMP     MODE,#3 ; 4 - 20 mA MODE ?
3018 017446 001002      BNE      100$     ; IF NOT, BRANCH
3019 017450 062701 007640  ADD     #4000.,R1 ; ELSE ADD IN BASE VALUE
3020
3021 017454 012604      100$: MOV     (SP)+,R4 ; RESTORE R4

```

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GLOBAL SUBROUTINES SECTION

3022 017456 012603
3023 017460 000207

MOV (SP),R3 ; AND R3
RTS PC ;

```

3025      ; SUBROUTINE ADCON - ANALOGUE TO DIGITAL CONVERSION ROUTINE.
3026
3027      ;
3028      ; FUNCTIONAL DESCRIPTION:
3029
3030      ;     THIS CONVERTS A 2 WORD ANALOGUE VALUE INTO A 12 BIT DIGITAL OUTPUT
3031      ;     PATTERN. THE INPUT IS ROUNDED UP OR DOWN TO THE NEAREST LSF VALUE.
3032
3033      ; INPUTS:
3034
3035      ;     MODE : 0 = UNIPOLAR (0 TO 10 VOLTS)      BINARY CODED
3036      ;             1 = BIPOLAR (-10 TO +10 VOLTS)  OFFSET BINARY CODED
3037      ;             2 = 0 - 20 mA                    BINARY CODED
3038      ;             3 = 4 - 20 mA                    BINARY CODED
3039
3040      ;     GAIN : 0 - 7 = GAINS 1,2,5,10,20,50,100,200
3041
3042      ;     R1 - MILLIVOLTS (MODES 0 AND 1)
3043      ;           MICROAMPS (MODES 2 AND 3)
3044
3045      ;     R2 - MICROVOLTS (MODES 0 AND 1)
3046      ;           NANOAMPS (MODES 2 AND 3)
3047
3048      ; IMPLICIT INPUTS:
3049
3050      ;     VITAB AND ITAB : DIGITAL/ANALOGUE CONVERSION TABLES.
3051
3052      ; OUTPUTS:
3053
3054      ;     R1 : 12 BIT INPUT PATTERN.
3055
3056      ; IMPLICIT OUTPUTS:
3057
3058      ;     NONE.
3059
3060      ; SUBORDINATE ROUTINES USED:
3061
3062      ;     NONE.
3063
3064      ; FUNCTIONAL SIDE EFFECTS:
3065
3066      ;     NONE.
3067
3068      ; CALLING SEQUENCE:
3069
3070      ; EG.  MOV     #1,MODE      ; BIPOLAR CONVERSION
3071      ;         MOV     #6,GAIN    ; GAIN = 100
3072      ;         MOV     #4,R1      ; -4.001 MILLIVOLTS
3073      ;         MOV     #1,R2      ; IN R1/R2
3074      ;         JSR     PC,ADCON
3075
3076      ; ---
3077
3078      ; ADCON::
3079      ;         MOV     R3,-(SP)    ; SAVE R3
3080      ;         MOV     R4,-(SP)    ; AND R4
3081      ;         MOV     MODE,R3     ; GET MODE
3081 017466 013703 002704

```

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GLOBAL SUBROUTINES SECTION

3082	017472	005703		TST	R3		; MODE 0 ?
3083	017474	001003		BNE	10:		; IF NOT, BRANCH
3084	017476	012700	002712	MOV	#VUPTAB,R0		; GET CONVERSION TABLE FOR MODE 0
3085	017502	000416		BR	PSAD		
3086							
3087	017504	005303		10:	DEC	R3	; MODE 1 ?
3088	017506	001003		BNE	20:		; IF NOT, BRANCH
3089	017510	012700	003512	MOV	#VBPTAB,R0		; GET CONVERSION TABLE FOR MODE 1
3090	017514	000411		BR	PSAD		
3091							
3092	017516	005303		20:	DEC	R3	; MODE 2 ?
3093	017520	001003		BNE	30:		; IF NOT, BRANCH
3094	017522	012700	004312	MOV	#IOTAB,R0		; GET CONVERSION TABLE FOR MODE 2
3095	017526	000404		BR	PSAD		
3096							
3097	017530	012700	005112	30:	MOV	#IATAB,R0	; USE IATAB FOR MODE 3
3098	017534	162701	007640	SUB	#4000.,R1		; AND SUBTRACT BASE VALUE
3099							
3100	017540	013703	002706	PSAD:	MOV	GAIN,R3	; GET THE GAIN
3101	017544	005303		10:	DEC	R3	; HAVE WE GOT THE CORRECT TABLE ?
3102	017546	100403		BMI	20:		; IF YES, BRANCH
3103	017550	062700	000060	ADD	#48.,R0		; ELSE INCREASE THE TABLE OFFSET
3104	017554	000773		BR	10:		; AND TRY AGAIN
3105							
3106	017556	023727	002704 000001	20:	CMP	MODE,#1	; BIPOLAR MODE ?
3107	017564	001011		BNE	30:		; IF NOT, BRANCH
3108	017566	061001		ADD	(R0),R1		; ELSE CONVERT BIPOLAR TO UNIPOLAR
3109	017570	020127	023420	CMP	R1,#10000.		; WAS PREVIOUS VALUE NEGATIVE ?
3110	017574	001005		BNE	30:		; IF NOT, BRANCH
3111	017576	005702		TST	R2		; IS DECIMAL PART ZERO ?
3112	017600	001403		BEG	30:		; IF YES, BRANCH
3113	017602	062702	001750	ADD	#1000.,R2		; ELSE BORROW FROM HIGH PART
3114	017606	005301		DEC	R1		
3115							
3116	017610	016003	000026	30:	MOV	22.(R0),R3	; GET ROUNDING VALUES FROM LOWEST
3117	017614	016004	000056	MOV	46.(R0),R4		; SIGNIFICANT BIT
3118	017620	006203		ASR	R3		; DIVIDE BY 2
3119	017622	103002		BCC	40:		; IF NO CARRY SKIP NEXT COMMAND
3120	017624	062704	001000	ADD	#1000,R4		; ADD CARRY
3121	017630	006204		40:	ASR	R4	; DIDIDE BY 2
3122	017632	060402		ADD	R4,R2		; ROUND UP THE INPUT VALUE
3123	017634	020227	001750	CMP	R2,#1000.		; LOWER PART IS MODULO 1000
3124	017640	002403		BLT	50:		
3125	017642	162702	001750	SUB	#1000.,R2		; IF OVERFLOW, CARRY OVER TO
3126	017646	005201		INC	R1		; HIGH PART
3127	017650	060301		50:	ADD	R3,R1	; ADD IN HIGH PART OF ROUNDING FACTOR
3128	017652	012703	000020	MOV	#20,R3		; INITIALISE WORKING REGISTER
3129							
3130	017656	020110		60:	CMP	R1,(R0)	; COMPARE HIGH VALUE WITH TABLE ENTRY
3131	017660	002415		BLT	90:		; IF LESS, DON'T SET BIT
3132	017662	003003		BGT	70:		; IF MORE, SET THE BIT
3133	017664	020260	000030	CMP	R2,24.(R0)		; OTHERWISE, MUST CHECK THE LOW VALUE
3134	017670	002411		BLT	90:		; IF LESS, DON'T SET THE BIT
3135							
3136	017672	166002	000030	70:	SUB	24.(R0),R2	; SUBTRACT THE TABLE ENTRIES
3137	017676	002003		BGE	80:		; BRANCH IF NO BORROW NEEDED
3138	017700	062702	001750	ADD	#1000.,R2		; ELSE ADD TO LOW WORD

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3139	017704	005301		DEC	R1	; FROM HIGH WORD
3140	017706	161001		SUB	(R0),R1	; AND LOW WORDS
3141	017710	052703	000001	BIS	#1,R3	; AND SET THE OUTPUT BIT
3142	017714	062700	000002	ADD	#2,R0	; AND NEXT TABLE ENTRY
3143	017720	006303		ASL	R3	; READY FOR NEXT BIT
3144	017722	103355		BCC	604	; IF 12 BITS NOT DONE, GO BACK
3145						
3146	017724	006203		ASR	R3	; GET THE PATTERN AGAIN
3147	017726	010301		MOV	R3,R1	; SET UP OUTPUT REGISTER
3148	017730	012604		MOV	(SP)+,R4	; RESTORE R4
3149	017732	012603		MOV	(SP)+,R3	; AND R3
3150	017734	000207		RTS	PC	;

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GLOBAL SUBROUTINES SECTION

```

3152 ; SUBROUTINE DECIN - SIGNED DECIMAL INPUT ROUTINE.
3153
3154 ;**
3155 ; FUNCTIONAL DESCRIPTION:
3156 ;
3157 ;     THIS SOLICITS A SIGNED DECIMAL NUMBER FROM THE OPERATOR.
3158 ;
3159 ; INPUTS:
3160 ;
3161 ;     PADD - THE ADDRESS OF THE PROMPT STRING FOR THE INPUT.
3162 ;
3163 ;     THE OPERATOR IS PROMPTED FOR A NUMBER WHICH CAN BE UP TO 10 DIGITS
3164 ;     LONG INCLUDING AN OPTIONAL + OR - SIGN AND DECIMAL POINT. THE
3165 ;     STRING SHOULD BE IN 'PRINT' FORMAT ( IE..ASCIZ /#N#AINPUT VALUE/ ).
3166 ;
3167 ; IMPLICIT INPUTS:
3168 ;
3169 ;     NONE.
3170 ;
3171 ; OUTPUTS:
3172 ;
3173 ;     R1 - INTEGER PART OF OPERATOR INPUT
3174 ;     R2 - DECIMAL PART OF OPERATOR INPUT
3175 ;
3176 ;
3177 ; IMPLICIT OUTPUTS:
3178 ;
3179 ;     ERROR MESSAGES ARE PRINTED IF THE OPERATOR TYPES AN ILLEGAL CHARACTER,
3180 ;     AN INTEGER PART OVER 32767, OR A DECIMAL PART WITH MORE THAN 3 DIGITS.
3181 ;
3182 ;     AN EXAMPLE STRING IS APPENDED TO THE INPUT PROMPT.
3183 ;
3184 ; SUBORDINATE ROUTINES USED:
3185 ;
3186 ;     NONE.
3187 ;
3188 ; FUNCTIONAL SIDE EFFECTS:
3189 ;
3190 ;     NONE.
3191 ;
3192 ; CALLING SEQUENCE:
3193 ;
3194 ;     EG. MOV    #MADD,PADD          ; LOAD THE PROMPT MESSAGE ADDRESS
3195 ;           JSR    PC,DECIN
3196 ;
3197 ; --
3198
3199 017736 DECIN::
3200 017736 012700 020310      MOV    #NR1,R0          ; CLEAR NUMBER AND STRING LOCATIONS
3201 017742 012701 000007      MOV    #7,R1           ; 2 WORDS FOR INTEGER AND DECIMAL
3202 017746 005020            CLR    (R0)+           ; PARTS AND 10 BYTES FOR THE INPUT
3203 017750 005301            DEC    R1              ; STRING.
3204 017752 001375            BNE    10$             ;
3205
3206 017754            PRINTF PADD          ; PRINT THE PROMPT
3207 017774            GMANID GETNUM,SNUM,A,-1,0,10.,NO ; GET THE NUMBER STRING
3208 020014 012700 020314      MOV    #SNUM,R0       ; POINT TO THE START

```


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3209	020020	012701	020310		MOV	#NR1,R1		; ASSUME INTEGER PART FIRST
3210								
3211	020024	121027	000053		CMPB	(R0),#'		; IS 1ST CHARACTER A . ?
3212	020030	001430			BEQ	60		; IF YES, BRANCH
3213	020032	121027	000055		CMPB	(R0),#'		; IS IT A - ?
3214	020036	001425			BEQ	60		; IF YES, BRANCH
3215	020040	121027	000056	20:	CMPB	(R0),#'		; IS IT A . ?
3216	020044	001017			BNE	50		; IF NOT, BRANCH
3217								
3218	020046	012701	020312	30:	MOV	#NR2,R1		; START ON DECIMAL PART
3219	020052	105760	000002		TSTB	2(R0)		; FORCE TO 3 DIGITS
3220	020056	001003			BNE	40		
3221	020060	112760	000060	000002	MOVB	#'0,2(R0)		; IE. REPLACE NULLS
3222	020066	105760	000003	40:	TSTB	3(R0)		
3223	020072	001007			BNE	60		; WITH ZEROS
3224	020074	112760	000060	000003	MOVB	#'0,3(R0)		
3225	020102	000403			BR	60		
3226								
3227	020104	105710		50:	TSTB	(R0)		; END OF STRING ?
3228	020106	001451			BEQ	110		; IF YES, FINISH UP
3229	020110	000402			BR	70		; ELSE GET NEXT DIGIT
3230								
3231	020112	005200		60:	INC	R0		; SKIP OVER THE SIGN OR POINT
3232	020114	000751			BR	20		
3233								
3234	020116	121027	000060	70:	CMPB	(R0),#60		; IS CHARACTER A VALID NUMBER ?
3235	020122	002403			BLT	80		; IF TOO LOW, ASK AGAIN
3236	020124	121027	000071		CMPB	(R0),#71		
3237	020130	003411			BLE	90		; IF NOT TOO HIGH, BRANCH
3238								
3239	020132			80:	PRINTF	#DECIN3		; PRINT 'ILLEGAL CHARACTER'
3240	020152	000671			BR	DECIN		; AND ASK AGAIN
3241								
3242	020154	021127	006314	90:	CMP	(R1),#3276.		; NUMBER TOO HIGH ?
3243	020160	101013			BHI	100		; IF YES, BRANCH
3244								
3245	020162	006311			ASL	(R1)		; ELSE MULTIPLY BY 10
3246	020164	011102			MOV	(R1),R2		
3247	020166	006311			ASL	(R1)		; READY FOR NEXT CHARACTER
3248	020170	006311			ASL	(R1)		
3249	020172	060211			ADD	R2,(R1)		
3250								
3251	020174	112002			MOVB	(R0),R2		; SAVE THE CHARACTER
3252	020176	162702	000060		SUB	#60,R2		; CONVERT TO NUMBER
3253	020202	060211			ADD	R2,(R1)		; AND ADD TO ACCUMULATOR
3254	020204	100401			BMI	100		; IF OVERFLOW, REPORT ERROR
3255								
3256	020206	000714			BR	20		; AND GET NEXT CHARACTER
3257								
3258	020210			100:	PRINTF	#DECIN1		; PRINT 'NUMBER TOO BIG'
3259	020230	000642			BR	DECIN		; AND GET ANOTHER
3260								
3261	020232	013701	020310	110:	MOV	NR1,R1		; SET UP OUTPUT REGISTERS
3262	020236	013702	020312		MOV	NR2,R2		
3263	020242	020227	001750		CMP	R2,#1000.		; DECIMAL PART TOO BIG ?
3264	020246	103411			BLO	120		; IF NOT, BRANCH
3265	020250				PRINTF	#DECIN2		; PRINT 'ONLY 3 DIGITS ALLOWED'

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```

3266 020270 000622          BR      DECIN
3267
3268 020272 123727 020314 000055 1204: CMPB   SNUM,0' -      ; WAS STRING NEGATIVE ?
3269 020300 001002          BNE     1304      ; IF NOT, BRANCH
3270 020302 005401          NEG     R1        ; ELSE NEGATE THE OUTPUT
3271 020304 005402          NEG     R2        ;
3272
3273 020306 000207          1304: RTS      PC        ; AND RETURN
3274
3275          .NLIST  BEX
3276
3277 020310 000000          NR1:  .WORD  0          ; STORE FOR INTEGER PART
3278 020312 000000          NR2:  .WORD  0          ; STORE FOR DECIMAL PART
3279 020314 055 061 062 SNUM:  .ASCIZ  /-12345.678/ ; STORE FOR INPUT STRING
3280
3281 020327 050 105 107 GETNUM: .ASCIZ  /(EG. 12345.678)/ ; PROMPT FOR INPUT
3282 020347 045 116 045 DECIN1: .ASCIZ  /%N%AMUST BE LESS THAN 32768%N/
3283 020405 045 116 045 DECIN2: .ASCIZ  /%N%ONLY 3 DIGITS MAY FOLLOW THE DECIMAL POINT%N/
3284 020466 045 116 045 DECIN3: .ASCIZ  /%N%ILLEGAL CHARACTER%N/
3285
3286          .LIST  BEX
3287          .EVEN

```


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GLOBAL SUBROUTINES SECTION

```

3289 ; SUBROUTINE DECOUT - SIGNED DECIMAL OUTPUT ROUTINE
3290
3291 ;**
3292 ; FUNCTIONAL DESCRIPTION:
3293 ;
3294 ;     ROUTINE TO PRINT A SIGNED DECIMAL NUMBER.
3295 ;
3296 ; INPUTS:
3297 ;
3298 ;     R1 - INTEGER PART OF NUMBER TO BE PRINTED
3299 ;     R2 - DECIMAL PART OF NUMBER TO BE PRINTED
3300 ;
3301 ; IMPLICIT INPUTS:
3302 ;
3303 ;     NONE.
3304 ;
3305 ; OUTPUTS:
3306 ;
3307 ;     THE NUMBER IS PRINTED AS FOLLOWS : -12345.678
3308 ;     ( NO SIGN IS PRINTED FOR POSITIVE NUMBERS )
3309 ;     IF THE DECIMAL PART (R2) IS ZERO, THE NUMBER
3310 ;     IS PRINTED AS -12345
3311 ;
3312 ; IMPLICIT OUTPUTS:
3313 ;
3314 ;     NONE.
3315 ;
3316 ; SUBORDINATE ROUTINES USED:
3317 ;
3318 ;     NONE.
3319 ;
3320 ; FUNCTIONAL SIDE EFFECTS:
3321 ;
3322 ;     NONE.
3323 ;
3324 ; CALLING SEQUENCE:
3325 ;
3326 ;     EG.  MOV    #-10.,R1          ; PRINT -10.001
3327 ;           MOV    #-1,R2          ;
3328 ;           JSR    PC,DECOUT
3329 ;
3330 ; --
3331
3332 DECOUT::
3333     MOV    R1,-(SP)                ; SAVE R1
3334     MOV    R2,-(SP)                ; SAVE R2
3335
3336     TST    R1                      ; R1 > 0 ?
3337     BLT    10$                     ; IF NOT, BRANCH
3338     TST    R2                      ; R2 POSITIVE ?
3339     BGE    20$                     ; IF YES, BRANCH
3340 10$:     NEG    R1                  ; ELSE MAKE POSITIVE
3341         NEG    R2
3342         PRINTF #DEC01              ; AND PRINT '-'
3343
3344 20$:     PRINTF #DEC02,R1           ; PRINT THE INTEGER PART
3345         TST    R2                  ; DECIMAL PART = 0 ?

```

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```
3346 020602 001411      BEQ      DECEX      ; IF YES BRANCH
3347 020604          PRINTF  #DEC03,R2      ; ELSE, PRINT DECIMAL PART
3348 020626 012602      DECEX: MOV      (SP)+,R2      ; RESTORE R2
3349 020630 012601      MOV      (SP)+,R1      ; AND R1
3350 020632 000207      RTS      PC          ; AND RETURN
3351
3352          .NLIST  BEX
3353 020634      045    101    055 DEC01: .ASCIZ  /#A-/
3354 020640      045    104    065 DEC02: .ASCIZ  /#D5/
3355 020644      045    101    056 DEC03: .ASCIZ  /#A.#Z3/
3356          .LIST  BEX
3357          .EVEN
3358
```

```

3360 .....
3361 : SUBROUTINE - RANDOM
3362 : .....
3363 : FUNCTIONAL DESCRIPTION:
3364 :
3365 : THIS ROUTINE GENERATES A RANDOM PATTERN. THE PATTERN IS
3366 : STORED IN LOCATION RB.
3367 :
3368 : INPUTS:
3369 :
3370 : NONE.
3371 :
3372 : IMPLICIT INPUTS:
3373 :
3374 : RA AND RB
3375 :
3376 : OUTPUTS:
3377 :
3378 : RB - CONTAINS THE RANDOM PATTERN
3379 : RA - CONTAINS A SECOND RANDOM PATTERN
3380 :
3381 : IMPLICIT OUTPUTS:
3382 :
3383 : NONE.
3384 :
3385 : SUBORDINATE ROUTINES USED:
3386 :
3387 : NONE.
3388 :
3389 : CALLING SEQUENCE:
3390 :
3391 : CALL RANDOM OR JSR PC,RANDOM
3392 :
3393 : .....
3394 020654 013746 020712 RANDOM: MOV RA, -(SP) ; PUSH RA TO STACK
3395 020660 013700 020714 MOV RB, R0 ; GET THE LAST RANDOM PATTERN
3396 020664 006316 ASL @SP ; SHIFT SP (=RA) LEFT
3397 020666 005500 ADC R0 ; IF CARRY IS SET ADD TO R0 (=RB)
3398 020670 006200 ASR R0 ; THEN SHIFT THE RESULT RITH
3399 020672 005516 ADC @SP ; IF CARRY IS SET ADD TO SP (=RA)
3400 020674 061600 ADD @SP, R0 ; ADD SP (=RA) AND R0 (=RB)
3401 020676 005600 SBC R0 ; SUBTRACT CARRY IF SET FROM RB
3402 020700 012637 020712 MOV (SP)+, RA ; LOAD NEW VALUE INTO LOCATION RA
3403 020704 010037 020714 MOV R0, RB ; LOAD LOCATION RB WITH NEW PAT.
3404 020710 000207 RETURN ;
3405
3406 020712 135753 RA: .WORD 135753 ; START PATTERN FOR RB
3407 020714 024674 RB: .WORD 24674 ; STORAGE FOR RANDOM PATTERN
3408

```

```

3410 .....
3411 SUBROUTINE - FLASH
3412 .....
3413 FUNCTIONAL DESCRIPTION:
3414
3415 THIS SUBROUTINE FLASHES THE SELECTED MODULE
3416 LED ON AND OFF WHILE READING THE CONSOLE KEYBOARD INPUT.
3417 THE EXPECTED INPUTS ARE 'Y' OR 'N' AND THEN A CR OTHER INPUTS
3418 (EXCEPT THE CNTL C) WILL CAUSE A PROMPT TO REPEAT
3419 WITH THE CORRECT CHARACTER.
3420
3421 INPUTS:
3422
3423 CONSOLE KEYBOARD INPUT.
3424
3425 IMPLICIT INPUTS:
3426
3427 NONE.
3428
3429 OUTPUTS:
3430
3431 FLSANS - CONTAINS THE KEYBOARD INPUT YES OR NO
3432 FLSANS ZERO = NO
3433 FLSANS ONE = YES
3434
3435 IMPLICIT OUTPUTS:
3436
3437 NONE.
3438
3439 SUBORDINATE ROUTINES USED:
3440
3441 BREAK - DRS MACRO (THIS MACRO LOOKS FOR CNTL C)
3442 PRINTF- DRS MACRO, THIS MACRO PRINTS A MESSAGES ON THE CONSOLE
3443
3444 CALLING SEQUENCE:
3445
3446 CALL FLASH OR JSR PC,FLASH
3447 .....
3448
3449 020716 105737 177560 FLASH:: TSTB TKS ;TEST THE KEYBOARD STATUS REG.
3450 020722 100002 BPL 10; ;BRANCH IF NOTHING FOUND
3451 020724 BREAK ;LOOK FOR 'CNTL C'
3452 020726 000421 BR 20; ;
3453 020730 042777 000100 161460 10;: BIC #100,BMOD ;SWITH MODULE LED OFF
3454 020736 012701 000012 40;: MOV #10.,R1 ;SET UP WAIT COUNTER
3455 020742 004737 014600 30;: CALL WT25M ;WAIT FOR 25 MS
3456 020746 005301 DEC R1 ;ARE 250 MS OVER
3457 020750 001374 BNE 30; ;BRANCH IF NO
3458 020752 032777 000100 161436 BIT #100,BMOD ;IS THE MODULE LED SWITCHED ON
3459 020760 001356 BNE FLASH ;BRANCH IF YES
3460 020762 052777 000100 151426 BIS #100,BMOD ;OTHERWISE SWITCH IT ON
3461 020770 000762 BR 40; ;AND BRANCH TO WAIT LOOP
3462 020772 012737 000001 021170 20;: MOV #1,FLSANS ;SAVE 'YES' ANSWER
3463 021000 013737 177562 002476 MOV TKB,BAD ;GET CHARACTER
3464 021006 042737 000200 002476 BIC #200,BAD ;DISCARD PARITY BIT
3465 021014 122737 000131 002476 CMPB #'Y,BAD ;WAS THE TYPED CHARACTER A 'Y' ?
3466 021022 001432 BEQ 50; ;BRANCH IF YES

```

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GLOBAL SUBROUTINES SECTION

```

3467 021024 122737 000171 002476      CMPB    #'y',BAD      ;WAS IT A LOWESCASE 'y' ?
3468 021032 001426                      BEQ      50$          ;BRANCH IF YES
3469 021034 005037 021170              CLR      FLSANS      ;SAVE 'NO' ANSWER
3470 021040 122737 000116 002476      CMPB    #'N',BAD      ;WAS THE TYPED CHARACTER A 'N' ?
3471 021046 001420                      BEQ      50$          ;BRANCH IF YES
3472 021050 122737 000156 002476      CMPB    #'n',BAD      ;WAS IT A LOWERCASE 'n' ?
3473 021056 001414                      BEQ      50$          ;BRANCH IF YES
3474 021060 013737 177562 177566      MOV      TKB,TPB      ;ECHO THE CHARACTER
3475 021066                      PRINTF   #PROMT      ;IF NO PRINT A PROMT
3476 021106 000703                      BR        FLASH      ;AND GO BACK
3477 021110 013737 177562 177566 50$: MOV      TKB,TPB      ;ECHO THE CHARACTER
3478 021116 105737 177560 60$:   TSTB     TKS          ;TEST THE KEYBOARD STATUS REG.
3479 021122 100375                      BPL      60$          ;BRANCH IF NOTHING FOUND
3480 021124 013737 177562 002476      MOV      TKB,BAD      ;GET FOUND CHARACTER
3481 021132 042737 000200 002476      BIC      #200,BAD      ;MASK OUT PARITY BIT
3482 021140 122737 000015 002476      CMPB    #15,BAD      ;WAS IT A <CR>
3483 021146 001404                      BEQ      70$          ;BRANCH IF YES
3484 021150 013737 177562 177566      MOV      TKB,TPB      ;ECHO THE CHARACTER
3485 021156 000657                      BR        FLASH
3486 021160 013737 177562 177566 70$: MOV      TKB,TPB      ;ECHO THE CHARACTER
3487 021166 000207                      RETURN
3488
3489                      ; CONSOLE EQUATES
3490
3491                      177560      TKS = 177560      ;KEYBOARD STATUS REGISTER
3492                      177562      TKB = 177562      ;KEYBOARD DATA REGISTER
3493                      177566      TPB = 177566      ;PRINTER DATA BUFFER
3494
3495 021170 000000      FLSANS: .      .WORD    0      ;SAVE LOCATION FOR KEYBOARD BUFFER
3496
3497                      .NLIST   BEX
3498 021172      045      116      045 PROMT: .ASCIZ  /#UNAUTHORIZED CHARACTER, TYPE ONLY 'Y' OR 'N' THEN <CR> /
3499                      .LIST    BEX
3500                      .EVEN

```

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GLOBAL SUBROUTINES SECTION

```

3502 .....
3503 ; SUBROUTINE CALRET - CONSOLE READ ROUTINE
3504 .....
3505 ;
3506 ; FUNCTIONAL DESCRIPTION:
3507 ;
3508 ;     THIS SUBROUTINE READS THE CONSOLE KEYBOARD WITHOUT STOPPING
3509 ;     THE PROGRAM SEQUENCING. THE READING WILL BE DONE WHEN YOU
3510 ;     CALL THIS SUBROUTINE BUT NOT UNDER INTERRUPT.
3511 ;     CALRET AFFECTS THE CARRY BIT. IF THE OPERATOR TYPED A CARRIGE
3512 ;     RETURN, THE CARRY BIT WILL BE SET. IF THE OPERATOR TYPED
3513 ;     ANY OTHER CHARACTERS OR NO CHARACTER, THE CARRY BIT WILL BE CLEARED.
3514 ;     THE ROUTINE WILL ALSO TAKE CARE OF THE 'CNTL C'.
3515 ;
3516 ; INPUTS:
3517 ;
3518 ;     CONSOLE KEYBOARD BUFFER AND STATUS
3519 ;
3520 ; IMPLICIT INPUTS:
3521 ;
3522 ;     NONE
3523 ;
3524 ; OUTPUTS:
3525 ;
3526 ;     IF READ CHARACTER WAS A CARRIGE RETURN, THE CARRY BIT IS SET.
3527 ;     IF READ CHARACTER WAS ANY OTHER CHARACTERS OR NO CHARACTER
3528 ;     WAS TYPED, THE CARRY BIT WILL BE CLEARED.
3529 ;     IF READ CHARACTER WAS A CNTL C, THE SUPERVISOR WILL HANDLE IT.
3530 ;
3531 ; SUBORDINATE ROUTINES USED:
3532 ;
3533 ;     BREAK - DRS MACRO          ; THIS MACRO TAKES CARE OF CNTL C
3534 ;
3535 ; CALLING SEQUENCE:
3536 ;
3537 ;     CALL CALRET      OR      JSR PC,CALRET
3538 ;
3539 .....
3540 CALRET::
3541 021270 005037 021374      CLR      CARRFL      ; TEMPORY STORE FOR CARRY BIT
3542 021274 105737 177560      TSTB     TKS          ; TEST THE KEYBOARD STATUS REG.
3543 021300 100032              BPL      10$          ; BRANCH IF NOTHING WAS TYPED
3544 021302              BREAK              ; WAS THE TYPED CHAR. A 'CNTL C'
3545 021304 013737 177562 002476  MOV     TKB,BAD      ; GET OPERATOR INPUT
3546 021312 042737 000200 002476  BIC     #200,BAD      ; CLEAR KEYBOARD BUFFER PARITY BIT
3547 021320 022737 000015 002476  CMP     #15,BAD      ; WAS THE TYPED CHARACTER A RETURN ?
3548 021326 001004              BNE     20$          ; BRANCH IF NO
3549 021330 012737 000001 021374  MOV     #1,CARRFL    ; SET CARRY FLAG
3550 021336 000413              BR      10$          ; AND BRANCH
3551 021340 013737 177562 177566 20$: MOV     TKB,TPB      ; ECHO THE CHARACTER
3552 021346              PRINTF    #RETME1          ; PRINT THAT ONLY CARRIGE RETURN WILL
3553 ; DO SOMETHING
3554 021366 006237 021374      10$: ASR      CARRFL      ; AFFECT THE CARRY
3555 021372 000207              RETURN
3556 ;
3557 ; CONSOLE EQUATES
3558

```


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GLOBAL SUBROUTINES SECTION

3559	177560	TKS = 177560	KEYBOARD STATUS REGISTER
3560	177562	TKB = 177562	KEYBOARD DATA REGISTER
3561	177566	TPB = 177566	PRINTER DATA BUFFER
3562			
3563	021374 000000	CARRFL: .WORD 0	SAVE LOCATION FOR CARRY BIT
3564			
3565		.NLIST BEX	
3566	021376 045 116 045 RETME1:	.ASCIZ / EN ATYPE CNTL C TO ABORT OR RETURN TO GO TO THE NEXT STEP/	
3567		.LIST BEX	
3568		.EVEN	
3569			

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GLOBAL SUBROUTINES SECTION

```

3571 ; SUBROUTINE AMREST - AM9513 MODULE RESET ROUTINE.
3572 ;
3573 ;..
3574 ; FUNCTIONAL DESCRIPTION:
3575 ;
3576 ;     THIS SUBROUTINE IS CALLED AT THE START OF TEST WHICH
3577 ;     REFERENCE THE AM9513 COUNTER MODULE. IT RESETS THAT
3578 ;     MODULE AND PLACES IT INTO A KNOWN STATE.
3579 ;
3580 ; INPUTS:
3581 ;
3582 ;     NONE.
3583 ;
3584 ; IMPLICIT INPUTS:
3585 ;
3586 ;     R1 CONTAINS THE BASE ADDRESS OF THE INTERFACE
3587 ;
3588 ; OUTPUTS:
3589 ;
3590 ;     NONE.
3591 ;
3592 ; IMPLICIT OUTPUTS:
3593 ;
3594 ;     THE ERROR COUNT FOR THE LOGICAL UNIT BEING TESTED IS
3595 ;     INCREMENTED IF THE PROGRAM IS NOT LOOPING.
3596 ;
3597 ; SUBORDINATE ROUTINES USED:
3598 ;
3599 ;     NONE.
3600 ;
3601 ; FUNCTIONAL SIDE EFFECTS:
3602 ;
3603 ;     IF THE ERROR COUNT EXCEEDS 5 AND THE USER EVL FLAG IS SELECTED,
3604 ;     AND THE 'LOOP ON TEST' AND 'INHIBIT DROPPING OF UNITS' FLAGS ARE
3605 ;     NOT SELECTED, THE UNIT WILL BE DROPPED FROM TESTING.
3606 ;
3607 ; CALLING SEQUENCE:
3608 ;
3609 ;     JSR PC,AMREST
3610 ;
3611 ;..
3612 AMREST::
3613 021470      SETVEC  #4,NDXM,0PRI07      ;SETUP TO CATCH TRAPS
3614 021470      MOV     #177777,MREA(R1)    ;MASTER RESET
3615 021516 012761 177750 000002      MOV     @CIN1,MREA(R1)    ;INITIALIZE COMMAND #1
3616 021524 012761 177757 000002      MOV     @CIN2,MREA(R1)    ;INITIALIZE COMMAND #2
3617 021532 012761 177637 000002      MOV     #177637,MREA(R1)    ;DISARM ALL COUNTERS
3618 021540 012761 177427 000002      MOV     #177427,MREA(R1)    ;SELECT FREQ.OUTPUT REG.
3619 021546 012761 070000 000002      MOV     #070000,MREA(R1)    ;INIT FREQ.OUTPUT REG.
3620 021554 012761 177411 000002      MOV     #177411,MREA(R1)
3621 021562 012761 177412 000002      MOV     #177412,MREA(R1)    ;SET LOAD REGISTER
3622 021570 012761 000010 000004      MOV     #10,MREB(R1)
3623 021576 012761 177413 000002      MOV     #177413,MREA(R1)    ;SET LOAD REGISTER
3624 021604 012761 000010 000004      MOV     #10,MREB(R1)
3625 021612 012761 177414 000002      MOV     #177414,MREA(R1)    ;SET LOAD REGISTER
3626 021620 012761 000010 000004      MOV     #10,MREB(R1)
3627 021626 012761 177414 000002      MOV     #177414,MREA(R1)    ;SET LOAD REGISTER
3627 021634 012761 000010 000004      MOV     #10,MREB(R1)

```


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GLOBAL SUBROUTINES SECTION

3628	021642	012761	177415	000002	MOV	#177415,MREA(R1)	
3629	021650	012761	000010	000004	MOV	#10,MREB(R1)	;SET LOAD REGISTER
3630	021656	012761	177421	000002	MOV	#177421,MREA(R1)	
3631	021664	012761	000000	000004	MOV	#0,MREB(R1)	;CLEAR HOLD REGISTER
3632	021672	012761	177422	000002	MOV	#177422,MREA(R1)	
3633	021700	012761	000000	000004	MOV	#0,MREB(R1)	;CLEAR HOLD REGISTER
3634	021706	012761	177423	000002	MOV	#177423,MREA(R1)	
3635	021714	012761	000000	000004	MOV	#0,MREB(R1)	;CLEAR HOLD REGISTER
3636	021722	012761	177424	000002	MOV	#177424,MREA(R1)	
3637	021730	012761	000000	000004	MOV	#0,MREB(R1)	;CLEAR HOLD REGISTER
3638	021736	012761	177425	000002	MOV	#177425,MREA(R1)	
3639	021744	012761	000000	000004	MOV	#0,MREB(R1)	;CLEAR HOLD REGISTER
3640	021752	012761	177537	000002	MOV	#177537,MREA(R1)	;LOAD ALL COUNTERS
3641	021760	005037	002510		CLR	INTFLA	;INIT INTERRUPT COUNT
3642	021764	000207			RTS	PC	
3643							

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GLOBAL SUBROUTINES SECTION

```

3646 ;*****;
3647 ; INTERRUPT SERVICE ROUTINES ;
3648 ;*****;
3649
3650 ; INTERRUPT SERVICE ROUTINE NXM - NON EXISTANT MEMORY TRAP.
3651
3652 ;**
3653 ; THIS ROUTINE SETS A FLAG NXMFLG TO 1. IT IS EXECUTED WHEN A NON
3654 ; EXISTANT MEMORY TRAP OCCURS IF VECTOR 4 HAS BEEN LOADED WITH THE
3655 ; ADDRESS NXM.
3656 ;
3657 ; NXMFLG SHOULD BE CLEARED IMMEDIATELY BEFORE EXECUTING CODE WHICH MAY
3658 ; ADDRESS NON EXISTANT MEMORY.
3659 ;--
3660
3661 021766 BGNSRV NXM
3662 021766 012737 000001 002470 MOV #1,NXMFLG ; FLAG NXM TRAP
3663 021774 ENDSRV
3664
3665 ;*****;
3666 ; INTERRUPT SERVICE ROUTINE - INTSR
3667 ;*****;
3668
3669 ;**
3670 ; THIS ROUTINE INCREMENTS A FLAG (INTFLA). IT WILL BE EXECUTED
3671 ; WHEN AN INTERRUPT HAS OCCURRED.
3672 ;--
3673
3674
3675 021776 BGNSRV INTSR
3676 021776 005237 002510 INC INTFLA ; COUNT INTERRUPTS
3677 022002 ENDSRV
3678
3679 ;*****;
3680 ; INTERRUPT SERVICE ROUTINE - BADIV
3681 ;*****;
3682
3683 ;**
3684 ; THIS ROUTINE LOADS VECTOR ADDRESS INTO BIV. IT IS EXECUTED
3685 ; WHEN AN INTERRUPT OCCURS TO THE WRONG VECTOR.
3686 ;
3687 ; BIV SHOULD BE CLEARED IMMEDIATELY BEFORE EXECUTING CODE WHICH MAY
3688 ; CAUSE THE INTERRUPT
3689 ;--
3690
3691
3692 022004 BGNSRV BADIV
3693 022004 011637 002472 MOV (SP),BIV ;LOAD INTERRUPT VECTOR ADDRESS
3694 022010 162737 000004 002472 SUB #4,BIV ;BACK UP TO CORRECT ADDRESS
3695 022016 062706 000004 ADD #4,SP ;CLEAR STACK
3696 022022 ENDSRV
3697
3698 ;*****;
3699 ; INTERRUPT SERVICE ROUTINE - CLINT
3700 ;*****;
3701
3702

```

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GLOBAL SUBROUTINES SECTION

```
3703      ;***  
3704      ;THIS ROUTINE IS A DUMY SERVICE FOR THE LINE TIME CLOCK INTERRUPTS  
3705      ;IT WILL BE EXECUTED WHEN AN INTERRUPT AT VECTOR 100 IS OCCURED.  
3706      ;---  
3707  
3708 022024      BGNSRV CLINT  
3709  
3710 022024      ENDSRV      ;NO ACTION IN THIS ROUTINE  
3711 022026      ENDMOD  
3712  
3713
```

MISCELLANEOUS SECTIONS MACRO V05.00 Wednesday 03-Oct 84 14:03 Page 44
GLOBAL SUBROUTINES SECTION

```

3725 .TITLE MISCELLANEOUS SECTIONS
3726 .SBTTL REPORT CODING SECTION
3754
3755 022026          BGNMOD
3756
3757 ; PRINT ROUTINE
3758
3759 ; **
3760 ; FUNCTIONAL DESCRIPTION:
3761 ;
3762 ; PRINTS OUT TEST TITLES, A CONFIGURATION TABLE OR A STATISTICS
3763 ; TABLE FOR THE UNITS UNDER TEST. WHICH TO PRINT IS DETERMINED BY
3764 ; USER INPUT.
3765 ;
3766 ; THE CONFIGURATION TABLE SHOWS THE HARDWARE CHARACTERISTICS OF
3767 ; THE UNIT UNDER TEST AND IF PRESENT, OF THE UNIT TO WHICH IT IS
3768 ; CONNECTED FOR LOOPBACK TESTING. IF THE FIRST UNIT MODE ADDRESS
3769 ; IS ZERO (IE. NO TESTS HAVE BEEN RUN AND NO HARDWARE PARAMETERS
3770 ; HAVE BEEN SET UP) THE AUTOMATIC CONFIGURATION ROUTINE 'CONFIG'
3771 ; IS CALLED TO ASCERTAIN THE HARDWARE CONFIGURATION.
3772 ;
3773 ; THE STATISTICS TABLE DISPLAYS THE NUMBER OF ERRORS WHICH THE
3774 ; DIAGNOSTIC HAS DETECTED FOR EACH UNIT, AND WHETHER THE UNIT HAS
3775 ; BEEN DROPPED FROM TESTING.
3776 ;
3777 ; INPUTS:
3778 ;
3779 ; THE USER IS ASKED TO TYPE A CHARACTER INDICATING WHETHER TO
3780 ; PRINT THE TEST TITLES, THE CONFIGURATION TABLE, THE STATISTICS
3781 ; TABLE OR HOW TO REESTABLISH THE SYSTEM CONFIGURATION.
3782 ;
3783 ; IMPLICIT INPUTS:
3784 ;
3785 ; THE HARDWARE PARAMETER TABLE IS READ FOR THE CONFIGURATION
3786 ; PRINTOUT.
3787 ;
3788 ; THE ERROR TABLE 'ECNT' IS USED FOR THE STATISTICS PRINTOUT.
3789 ;
3790 ; TEST TITLES ARE ASSUMED TO BE LABELLED WITH THE FORMAT TDNDnn,
3791 ; WHERE nn IS THE TEST NUMBER. NTESTS AT THE START OF THE ROUTINE
3792 ; MUST EQUAL THE NUMBER OF TESTS IN THE DIAGNOSTIC.
3793 ;
3794 ; OUTPUTS:
3795 ;
3796 ; EITHER A LIST OF TEST TITLES, A CONFIGURATION TABLE OR A
3797 ; STATISTICS TABLE ARE OUTPUT.
3798 ;
3799 ; IF ANY UNIT HAS BEEN DROPPED OR DESELECTED USING THE "UNITS"
3800 ; SWITCH, A MESSAGE "PLEASE TYPE ADD" MAY BE PRINTED.
3801 ;
3802 ; IMPLICIT OUTPUTS:
3803 ;
3804 ; NONE.
3805 ;
3806 ; SUBORDINATE ROUTINES USED:
3807 ;
3808 ; CONFIG - AUTOMATIC CONFIGURATION ROUTINE.

```

MISCELLANEOUS SECTIONS
REPORT CODING SECTION

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```

3809      ;      CONPRI - CONFIGURATION PRINT ROUTINE.
3810      ;      CRLF  - LINE FEED PRINT ROUTINE.
3811      ;
3812      ; FUNCTIONAL SIDE EFFECTS:
3813      ;
3814      ;      IF NO HARDWARE PARAMETERS ARE SET UP, THE AUTOMATIC
3815      ;      CONFIGURATION ROUTINE IS CALLED TO GENERATE A HARDWARE P TABLE.
3816      ;
3817      ;      REGISTERS R1 TO R5 ARE CORRUPTED.
3818      ;
3819      ; CALLING SEQUENCE:
3820      ;
3821      ;      INVOKED BY THE OPERATOR PRINT COMMAND.
3822      ;
3823      ; --
3824
3825      000013      NTESTS=11.      ; 11 TESTS FOR TITLE PRINTOUT
3826
3827      022026      BGNRPT
3828      022026      ASK:      MANUAL      ; IS MANUAL INTERVENTION ALLOWED ?
3829      022030      BCOMPLETE 10$      ; IF YES, BRANCH (UAM NOT SET)
3830      022032      PRINTF  #PRA      ; PRINT THAT UAM HAS TO BE SET
3831      022052      EXIT  RPT      ; EXIT PRINT ROUTINE
3832      022056      10$:      GMANID  PR1,CHAR,A,377.1.4.YES; PROMPT FOR A COMMAND
3833      022076      023727  023162  000122      CMP      CHAR,#'R      ; REESTABLISH SYSTEM CONFIG. ?
3834      022104      001457      BEQ      RECON      ; IF YES, OUTPUT INFORMATION
3835      022106      023727  023162  000124      CMP      CHAR,#'T      ; TEST LIST REQUESTED ?
3836      022114      001515      BEQ      TITLE      ; IF YES, OUTPUT TITLES
3837      022116      023727  023162  000103      CMP      CHAR,#'C      ; CONFIGURATION REQUESTED ?
3838      022124      001574      BEQ      CON      ; IF YES, OUTPUT CONFIGURATION
3839      022126      023727  023162  000123      CMP      CHAR,#'S      ; STATISTICS REQUESTED ?
3840      022134      001002      BNE      HEL      ; IF NOT, PRINT THE HELP MESSAGE
3841      022136      000137  022766      JMP      STAT      ; IF YES, OUTPUT STATISTICS
3842
3843      022142      HEL:      PRINTF  #PR2      ; OTHERWISE, PRINT THE HELP MESSAGE
3844      022162      PRINTF  #PR2A
3845      022202      PRINTF  #PR2B
3846      022222      PRINTF  #PR2C
3847      022242      000671      BR      ASK      ; AND PROMPT FOR COMMAND AGAIN
3848
3849      022244      RECON:      PRINTF  #PR2D      ; PRINT HOW TO REESTABLISH
3850      022264      PRINTF  #PR2E      ; THE SYSTEM CONFIGURATION
3851      022304      PRINTF  #PR2F
3852      022324      PRINTF  #PR2G
3853      022344      000137  023152      JMP      PREX      ; ...
3854
3855      022350      TITLE:      PRINTF  #TT      ; TEST LIST HEADER
3856      022370      012701  000001      MOV      #1,R1      ; START WITH TEST 1
3857      022374      012702  023172      MOV      #TADS,R2      ; START OF LIST OF TITLE ADDRESSES
3858
3859      022400      012703  000022      10$:      MOV      #18.,R3      ; WAIT AFTER 18 LINES
3860
3861      022404      20$:      PRINTF  #TNUM,R1      ; PRINT TEST NUMBER
3862      022426      PRINTF  (R2)      ; AND TITLE
3863      022444      062702  000002      ADD      #2,R2      ; GET ADDRESS OF NEXT TITLE
3864      022450      005201      INC      R1      ; AND NEXT TEST NUMBER
3865      022452      020127  000013      CMP      R1,#NTESTS      ; ALL PRINTED ?

```

MISCELLANEOUS SECTIONS
REPORT CODING SECTION

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3866	022456	003015			BGT	304		; IF YES, EXIT
3867	022460	005303			DEC	R3		; 18 LINES OUTPUT ?
3868	022462	001350			BNE	204		; IF NOT, BRANCH
3869	022464				GMANID	RDY,RFLG,A,377,0,1,YES		; ELSE WAIT FOR OPERATOR TO READ
3870	022504	004737	014634		JSR	PC,CRLF		; PRINT A LINE FEED
3871	022510	000733			BR	104		; AND THEN CONTINUE
3872								
3873	022512	000137	023152		304:	JMP	PREX	; EXIT
3874								
3875	022516	005037	023156		CON:	CLR	STFLG	; ASSUME DIAGNOSTIC IS NOT STARTED
3876	022522	005037	023160			CLR	GPFLG	; FLAG NO GPHARDS YET EXECUTED
3877	022526	005737	002554			TST	GPADD	; ARE ANY PARAMETER ADDRESSES SET UP ?
3878	022532	001402				BEQ	104	; IF NOT, BRANCH
3879	022534	005237	023156			INC	STFLG	; ELSE FLAG DIAGNOSTIC IS STARTED
3880								
3881	022540	005737	002012		104:	TST	L#UNIT	; ANY UNITS SET UP ?
3882	022544	001404				BEQ	204	; IF NOT, BRANCH
3883	022546	023727	002012	000020		CMP	L#UNIT,#16.	; TOO MANY UNITS SET UP ?
3884	022554	003403				BLE	304	; IF NOT, BRANCH
3885	022556	012737	000020	002012	204:	MOV	#16.,L#UNIT	; SET UP 16 UNITS
3886								
3887	022564	013746	002074		304:	MOV	L#LUN,-(SP)	; SAVE THE UNIT NUMBER BEING TESTED
3888	022570	005037	002074			CLR	L#LUN	; START WITH UNIT 0
3889								
3890	022574	013701	002554			MOV	GPADD,R1	; GET FIRST PARAM. ADDRESS
3891	022600	005711				TST	(R1)	; ANYTHING IN IT
3892	022602	001003				BNE	404	; IF YES, BRANCH
3893	022604	012737	171000	015572		MOV	#171000,STADD	; ELSE START FOR FIRST IAV/IDV ADDR.
3894								
3895	022612	013701	002074		404:	MOV	L#LUN,R1	; FORM OFFSET FOR UNIT TABLES
3896	022616	006301				ASL	R1	
3897	022620	005761	002554			TST	GPADD(R1)	; PARAMETER ADDRESS SET UP ?
3898	022624	001010				BNE	504	; IF YES, BRANCH
3899	022626					GPHARD	L#LUN,GPADD(R1)	; ELSE, GET P TABLE ADDRESS
3900	022640					BNCOMPLETE	ASKADD	; IF DESELECTED, REQUEST OPERATOR ADD
3901	022642	005237	023160			INC	GPFLG	; FLAG THAT GPHARD HAS BEEN USED
3902								
3903	022646	005771	002554		504:	TST	GPADD(R1)	; MODE ADDRESS OF UNIT = 0 ?
3904	022652	001002				BNE	604	; IF NOT, BRANCH
3905	022654	004737	015306		554:	JSR	PC,CONFIG	; DO AUTO CONFIGURATION
3906								
3907	022660	005237	002074		604:	INC	L#LUN	; NEXT UNIT
3908	022664	023737	002074	002012		CMP	L#LUN,L#UNIT	; ALL DONE ?
3909	022672	002747				BLT	404	; IF NOT, DO THE NEXT
3910	022674	012637	002074			MOV	(SP)+,L#LUN	; RESTORE THE UNIT NUMBER
3911								
3912	022700	004737	015656		704:	JSR	PC,CONPRI	; PRINT THE CONFIGURATION
3913								
3914	022704	005737	023156			TST	STFLG	; WAS DIAGNOSTIC STARTED ?
3915	022710	001520				BEQ	PREX	; IF NOT, EXIT
3916	022712	005737	023160			TST	GPFLG	; HAVE WE USED ANY GPHARDS ?
3917	022716	001515				BEQ	PREX	; IF NOT, EXIT
3918	022720				804:	PRINTF	#PR7	; ELSE PRINT 'USE STA OR RES'
3919	022740	000504				BR	PREX	; AND EXIT
3920								
3921	022742				ASKADD:	PRINTF	#PR6	; ASK OPERATOR TO ADD UNITS
3922	022762	000137	023152			JMP	PREX	; AND EXIT

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REPORT CODING SECTION

```

3923
3924 022766          STAT:  PRINTF  #PR3          ; PRINT STATISTICS HEADER
3925 023006          PRINTF  #PR3A          ;
3926
3927 023026 005001          CLR      R1          ; START WITH FIRST UNIT
3928
3929 023030 020137 002012    20$:  CMP      R1,L$UNIT      ; ALL UNITS REPORTED ?
3930 023034 001444          BEQ      60$          ; IF YES, EXIT
3931 023036 010104          MOV      R1,R4          ; FORM OFFSET TO ERROR COUNT
3932 023040 006304          ASL      R4          ;
3933 023042 016405 002514    MOV      ECNT(R4),R5      ; GET UNIT'S ERROR COUNT
3934 023046 005705          TST      R5          ; IS IT NEGATIVE ?
3935 023050 100423          BMI      40$          ; IF YES, REPORT UNTESTED
3936
3937 023052 012703 025006    MOV      #NO,R3          ; ASSUME UNIT IS NOT DROPPED
3938 023056 105761 002614    TSTB     DROPPED(R1)      ; CHECK IF IT IS
3939 023062 001402          BEQ      30$          ; IF IT IS NOT, BRANCH
3940 023064 012703 025002    MOV      #YES,R3          ; OTHERWISE PRINT YES
3941 023070          30$:  PRINTF  #PR5,R1,R5,R3      ; ELSE PRINT STATISTICS
3942 023116 000411          BR       50$          ; AND LOOK FOR MORE UNITS
3943
3944 023120          40$:  PRINTF  #PR4,R1          ; PRINT 'UNTESTED'
3945
3946 023142 005201          50$:  INC      R1          ; PREPARE FOR NEXT UNIT
3947 023144 000731          BR       20$          ; IF NOT, REPORT THE NEXT
3948
3949 023146 004737 014634    60$:  JSR      PC,CRLF      ; PRINT A LINE FEED
3950
3951 023152          PREX:  EXIT      RPT
3952
3953 023156 000000          STFLG:  .WORD  0          ; SET IF DIAGNOSTIC HAS BEEN STARTED
3954 023160 000000          GPFLG:  .WORD  0          ; SET IF ANY GPHARDS ARE EXECUTED
3955 023162      110      040      040      CHAR:  .ASCIZ  /H      /      ; STORE FOR OPERATOR INPUT
3956 023165      040      040      000
3957 023170 000000          RFLG:  .WORD  0          ; FLAG FOR 'TYPE RETURN FOR MORE TITLES'
3981
3982 023172          TADS:  TITLES          ; LIST OF TEST TITLE ADDRESSES
3983
3984 023172 030206          .WORD  TSHD1
3985 023174 030574          .WORD  TSHD2
3986 023176 031216          .WORD  TSHD3
3987 023200 032120          .WORD  TSHD4
3988 023202 033474          .WORD  TSHD5
3989 023204 034076          .WORD  TSHD6
3990 023206 034576          .WORD  TSHD7
3991 023210 035270          .WORD  TSHD8
3992 023212 036110          .WORD  TSHD9
3993 023214 037122          .WORD  TSHD10
3994 023216 037350          .WORD  TSHD11
3995
3996          .NLIST BEX
3997
3998 023220      045      116      045  TT:  .ASCII  /#N#A TEST TITLES./
3999 023240      045      116      045      .ASCIZ  /#N#A-----#N2/
4000
4001 023264      124      131      120  RDY:  .ASCIZ  /TYPE "RETURN" FOR MORE TITLES/

```

MISCELLANEOUS SECTIONS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 44-4
REPORT CODING SECTION

```

3997
3994 023322      045      104      063  TNUM:  .ASCIZ  /#03/
3995
3996 023326      045      116      045  PRA:   .ASCIZ  /#N#ATO USE THE PRINT COMMAND, PLEASE CLEAR THE "UAM" FLAG/
3997
3998 023420      124      131      120  PR1:   .ASCIZ  /TYPE T,R,C,S OR HELP/
3999
4000 023445      045      116      045  PR2:   .ASCII  /#N#ATHE FOLLOWING COMMANDS ARE ACCEPTED :-/
4001 023517      045      116      062  .ASCIZ  /#N2#AT - PRINT TEST TITLES/
4002 023552      045      116      045  PR2A:  .ASCIZ  /#N#AC - PRINT CONFIGURATION TABLE CURRENTLY USED BY DIAGNOSTIC/
4003 023651      045      116      045  PR2B:  .ASCIZ  /#N#AR - PRINT HOW TO REESTABLISH THE SYSTEM CONFIGURATION/
4004 023743      045      116      045  PR2C:  .ASCIZ  /#N#AS - PRINT STATISTICS TABLE#N/
4005
4006 024004      045      116      045  PR2D:  .ASCIZ  /#N#ATO REESTABLISH THE SYSTEM CONFIGURATION, ANSWER THE/
4007 024074      045      116      045  PR2E:  .ASCIZ  /#N#AHARDWARE QUESTION TYPING "0" AS THE MODE ADDRESS FOR 16 UNITS./
4008 024177      045      116      045  PR2F:  .ASCIZ  /#N#AEG. MODE REGISTER ADDRESS (0) 0 ? 0...../
4009 024273      045      116      045  PR2G:  .ASCIZ  /#N#A-----/
4010 024367      045      116      062  PR3:   .ASCII  \#N2#AIDV/IAV-11 MODULE STATISTICS.\
4011 024431      045      116      045  .ASCIZ  /#N#A-----/
4012 024474      045      116      062  PR3A:  .ASCIZ  /#N2#AUNIT  ERRORS  DROPPED#N/
4013
4014 024533      045      116      045  PR4:   .ASCIZ  /#N#D3#A  UNTESTED  NO/
4015
4016 024564      045      116      045  PR5:   .ASCIZ  /#N#D3#S5#D3#S7#T/
4017
4018 024605      045      116      045  PR6:   .ASCII  /#N#AUNIT DROPPED OR DESELECTED - PLEASE TYPE "ADD"/
4019 024667      040      101      116  .ASCIZ  / AND TRY AGAIN/
4020
4021 024706      045      116      045  PR7:   .ASCIZ  /#N#APLEASE TYPE "START" OR "RESTART" TO CONTINUE TESTING.#N/
4022
4023 025002      131      105      123  YES:   .ASCIZ  /YES/
4024 025006      116      117      000  NO:    .ASCIZ  /NO/
4025
4026                      .LIST BEX
4027                      .EVEN
4028
4029 025012                      ENDRPT

```


MISCELLANEOUS SECTIONS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 45
PROTECTION TABLE

```
4031      .SBTTL  PROTECTION TABLE
4032
4033      ;++
4034      ; THIS TABLE IS USED BY THE RUNTIME SERVICES
4035      ; TO PROTECT THE LOAD MEDIA.
4036      ;--
4037
4038 025014      BGNPROT
4039
4040 025014 000000      0      ;OFFSET INTO P-TABLE FOR MODE ADDRESS
4041 025016 177777      1      ;OFFSET INTO P-TABLE FOR MASSBUS ADDRESS
4042 025020 177777     -1      ;OFFSET INTO P-TABLE FOR DRIVE NUMBER
4043
4044 025022      ENDPROT
4045
```

MISCELLANEOUS SECTIONS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 46
INITIALIZE SECTION

```

4060      .SBTTL  INITIALIZE SECTION
4061
4062      ;**
4063      ; THE INITIALIZE SECTION CONTAINS THE CODING THAT IS PERFORMED
4064      ; AT THE BEGINNING OF EACH PASS.
4065      ;--
4066
4067 025022      BGNINIT
4068
4092
4093 025022 005037 026214      START: CLR      LOOPEX      ; CLEAR LOOP FLAG
4094 025026      REDEF      #EF.START      ; IS THIS A NEW START ?
4095 025034      BNCOMPLETE RESTR      ; IF NOT, BRANCH
4096 025036      SETVEC      #14,#113240,#340 ; *** JUST FOR DEBUG PROGRAM ***
4097 025064      SETVEC      #100,#CLINT,#340 ; IGNORE FURTHER INTERRUPTS TO VECTOR 100
4098 025112      PRINTF      #WARN      ; CONNECT TEST CONNECTOR
4099 025132      PRINTF      #WARN1     ; AND VOLTAGE SOURCE
4100 025152 004737 014662      JSR      PC,WRDY     ; WAIT FOR OPERATOR TO TYPE RETURN
4101 025156 004737 014634      JSR      PC,CRLF     ; PRINT A LINE FEED
4102
4103 025162      10$: BRESET      ; RESET THE SYSTEM
4104 025164 004737 026650      JSR      PC,SETCLK    ; SET UP CLOCK COUNTER
4105
4106 025170 005737 002012      TST      L#UNIT      ; ANY UNITS CONFIGURED ?
4107 025174 001404      BEQ      20$              ; IF NOT, BRANCH
4108 025176 023727 002012 000020      CMP      L#UNIT,#16. ; TOO MANY UNITS SET UP ?
4109 025204 003403      BLE      30$              ; IF NOT, BRANCH
4110 025206 012737 000020 002012 20$: MOV      #16.,L#UNIT ; ELSE, SET UP 16 UNITS
4111
4112 025214 005037 026212      30$: CLR      ACFLG      ; CLEAR AUTO CONFIGURATION FLAG
4113
4114
4115 025220 012700 002640      MOV      #LOPFLG,R0    ; FLAG THAT LOOP CONFIGURATION IS NOT CHECKED
4116 025224 012701 000020      MOV      #16.,R1      ; FOR POSSIBLE 16 UNITS
4117 025230 005020      50$: CLR      (R0)+          ; CLEAR THE FLAG
4118 025232 005301      DEC      R1                  ; ALL DONE ?
4119 025234 001375      BNE      50$                ; IF NOT, DO THE NEXT
4120
4121
4122 025236 012700 002614      MOV      #DROPEL,R0    ; GET UNIT DROPPED TABLE ADDRESS
4123 025242 012701 000020      MOV      #16.,R1      ; THERE ARE 16 UNITS
4124 025246 105020      55$: CLRB      (R0)+          ; CLEAR ALL 16 DROPPED UNIT FLAGS
4125 025250 005301      DEC      R1                  ; ...
4126 025252 001375      BNE      55$                ; ...
4127 025254 012700 002514      MOV      #ECNT,R0     ; GET ERROR COUNT FOR UUT 0
4128 025260 012701 000020      MOV      #16.,R1      ; THERE ARE 16 UUT'S
4129 025264 012720 100000      60$: MOV      #100000,(R0)+ ; INIT THE ERROR COUNT
4130 025270 005301      DEC      R1                  ; ALL CLEARED ?
4131 025272 001374      BNE      60$                ; IF NOT, CLEAR NEXT COUNT
4132 025274 000404      BR      STARES              ; ELSE, START TESTING WITH FIRST UUT
4133
4134 025276      RESTR: REDEF      #EF.RESTART      ; IS THIS A RESTART ?
4135 025304      BNCOMPLETE NEWST      ; IF NOT, BRANCH
4136
4137 025306 005037 002634      STARES: CLR      TSTFLG      ; SHOW NO TESTS HAVE BEEN RUN THIS PASS
4138 025312 005037 002636      CLR      TSUFLG      ; OR FOR THIS UNIT
4139 025316 000534      BR      INIUUT              ; START TESTING WITH FIRST UUT

```

MISCELLANEOUS SECTIONS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 46-1
INITIALIZE SECTION

```

4140
4141 025320          NEWST:  READEF  #EF.NEW          ; IS THIS A NEW PASS ?
4142 025326          BNCOMPLETE CONT          ; IF NOT, BRANCH
4143 025330 005737 002634      TST      TSTFLG          ; WERE ANY TESTS SELECTED LAST TIME ?
4144 025334 001011          BNE      10$          ; IF YES, BRANCH
4145 025336          PRINTF  #NTEST          ; IF NOT, TELL THE USER
4146 025356          DOCLN          ; AND RETURN TO THE SUPERVISOR
4147
4148 025360 005037 002634      10$:  CLR      TSTFLG          ; INIT TEST FLAG AGAIN
4149 025364 005737 026212      TST      ACFLG          ; JUST DONE A RUN OF AUTO CONFIGURATION ?
4150 025370 001507          BEQ      INIUUT          ; IF NOT, TEST FIRST UNIT
4151 025372 005037 026212      CLR      ACFLG          ; ELSE, FLAG THAT THE RUN IS FINISHED
4152 025376 004737 015656      JSR      PC,CONPRI          ; PRINT THE CONFIGURATION
4153 025402 004737 014662      JSR      PC,WRDY          ; WAIT FOR OPERATOR TO TYPE 'RETURN'
4154 025406 000500          BR      INIUUT          ; THEN TEST THE FIRST UNIT
4155
4156 025410          CONT:  READEF  #EF.CONTINUE          ; IS THIS A CONTINUE ?
4157 025416          BNCOMPLETE PWRFL          ; IF NOT, BRANCH
4158 025420          SETVEC  #4,#NXM,#PRI07          ; IGNORE NXM TRAPS
4159 025446 052777 000100 154742  BIS      #100,#MOD          ; ELSE, LIGHT UUT LED
4160 025454          CLRVEC  #4          ; RESTORE THE SUPERVISOR NXM VECTOR
4161 025462 000137 026202      JMP      END          ; AND CONTINUE
4162
4163 025466          PWRFL:  READEF  #EF.PWR          ; IS THIS A POWER FAIL
4164 025474          BNCOMPLETE NXTUUT          ; IF NOT, MUST BE NEXT UNIT
4165 025476          SETVEC  #4,#NXM,#PRI07          ; IGNORE NXM TRAPS
4166 025524 052777 000100 154664  BIS      #100,#MOD          ; ELSE, LIGHT UUT LED
4167 025532          CLRVEC  #4          ; RESTORE THE SUPERVISOR NXM VECTOR
4168 025540 000137 026162      JMP      PSEUL1          ; AND CONTINUE
4169
4170 025544 005737 002636      NXTUUT: TST      TSUFLG          ; WERE ANY TESTS RUN ON THE LAST UNIT ?
4171 025550 001014          BNE      10$          ; IF YES, BRANCH
4172 025552          PRINTF  #NTEST1,L$LUN          ; ELSE PRINT A WARNING
4173 025576 004737 014662      JSR      PC,WRDY          ; LET THE OPERATOR READ IT
4174 025602 005037 002636      10$:  CLR      TSUFLG          ; SHOW NO TESTS FOR NEXT UNIT
4175 025606 000422          BR      NEXT          ; AND TEST THE NEXT UNIT
4176
4177 025610 012737 177777 002074 INIUUT: MOV      #-1,L$LUN          ; INITIALIZE LOGICAL UNIT NUMBER.
4178 025616 005237 026214          INC      LOOPEX          ; ARE WE IN A UNIT LOOP
4179 025622 022737 000002 026214      CMP      #2,LOOPEX          ; ... ?
4180 025630 001011          BNE      NEXT          ; BRANCH IF NO
4181 025632          PRINTF  #WRSEL          ; IF YES PRINT NOT CORRECT UNIT SELECTED
4182 025652          DOCLN          ; AND RETURN TO THE SUPERVISOR
4183 025654 005237 002074          NEXT:  INC      L$LUN          ; NEXT LOGICAL UNIT TO BE TESTED ?
4184 025660 023737 002074 002012      CMP      L$LUN,L$UNIT          ; ALL UNITS TRIED ?
4185 025666 002350          BGE      INIUUT          ; IF YES, START AGAIN
4186
4187 025670 013705 002074          MOV      L$LUN,R5          ; SAVE UNIT NUMBER
4188 025674 006305          ASL      R5          ; FORM OFFSET
4189
4190 025676          GPHARD  L$LUN,R1          ; GET PARAMETER TABLE ADDRESS IN R1
4191 025706          BNCOMPLETE NEXT          ; IF DROPPED, GET THE NEXT
4192 025710 010165 002554          MOV      R1,GPADD(R5)          ; ELSE SAVE THE ADDRESS
4193 025714 005711          TST      (R1)          ; MODE ADDRESS = 0 ?
4194 025716 001024          BNE      ""          ; IF NOT, BRANCH
4195 025720 004737 015306          JSR      PC,CONFIG          ; ELSE DO AUTO CONFIGURATION FOR THIS UNIT
4196 025724 012737 000001 026212      MOV      #1,ACFLG          ; AND FLAG THAT WE ARE DOING IT

```

MISCELLANEOUS SECTIONS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 46-2
INITIALIZE SECTION

```

4197 025732 103014      BCC 100      ; BRANCH IF ANOTHER UNIT FOUND
4198 025734 005737 002012 TST LUNIT    ; ARE THERE ANY UNITS TO TEST ?
4199 025740 001323      BNE INIUNIT  ; IF YES, START AGAIN WITH THE FIRST UNIT
4200 025742      PRINTF #MODEV    ; ELSE PRINT "NO UNITS FOUND"
4201 025762      DOCLN             ; AND RETURN TO THE SUPERVISOR
4202
4203 025764 016501 002554 100:  MOV  GPADD(R5),R1  ; GET PARAMETER TABLE ADDRESS IN R1
4204
4205 025770 011137 002416 200:  MOV  (R1),MOD    ; SAVE NEW MODE REGISTER ADDRESS
4206 025774 011137 002420      MOV  (R1),CSR    ;
4207 026000 062737 000002 002420 ADD  #2,CSR    ;
4208 026006 011137 002422      MOV  (R1),CCR    ; SAVE NEW CCR REGISTER ADDRESS
4209 026012 062737 000004 002422 ADD  #4,CCR    ;
4210 026020 012137 002424      MOV  (R1),,INR    ; SAVE NEW INR REGISTER ADDRESS
4211 026024 062737 000006 002424 ADD  #6,INR    ;
4212
4213 026032 012137 002426      MOV  (R1),,VEC    ; SAVE NEW VECTOR ADDRESS
4214 026036 012137 002430      MOV  (R1),,PRIO   ; SAVE NEW PRIORITY
4215
4216 026042 005037 002470      CLR  MODFLG    ;
4217 026046      SETVEC #4,#MOD,#PRIO7 ; IGNORE MOD TRAPS
4218 026074 017701 154316      MOV  #MOD,R1    ; GET MODE REGISTER CONTENTS
4219 026100      CLRVEC #4              ; RESTORE MOD TRAP CATCHER
4220
4221 026106 000301      SWAB  R1              ; SWAB MOD REGISTER CONTENTS
4222 026110 005737 002470      TST  MODFLG    ; IS THE USED ADDRESS ACCESSABLE ?
4223 026114 001006      BNE  700            ; BRANCH IF NOT
4224 026116 120127 000300      CMPB  R1,#300   ; IS IT A FIVE CHANNEL COUNTER ?
4225 026122 001254      BNE  NEXT           ; IF NOT BRANCH
4226 026124 052777 000100 154264 BIS  #100,#MOD    ; SWITCH ON UUT LED
4227 026132 042765 100000 002514 700: BIC  #100000,ECNT(R5) ; FLAG UNIT IS BEING TESTED
4228 026140 012702 000420      MOV  #420,R2   ; SET UP FOR FIVE CHANNEL COUNTER
4229 026144 000402      BR  600              ; HERE IF ID IS 300 (FIVE CHA. COUNTER)
4230 026146 012702 000040 500:  MOV  #40,R2   ;
4231 026152 052702 010000 600:  BIS  #10000,R2 ; ASSUME SPECIFICALLY SELECTED TEST
4232 026156 010237 002702      MOV  R2,CONMSK ; SAVE TEST CONTROL MASK
4233
4234 026162 005037 002700 PSEUL1: CLR  LOTFLA    ; INIT LOOP ON TEST FLAG
4235 026166      RFLAGS R0              ; GET OPERATOR FLAGS
4236 026170 032700 000010      BIT  #LOT,R0   ; LOOP ON TEST SELECTED ?
4237 026174 001402      BEQ  END           ; IF NOT, BRANCH
4238 026176 005237 002700      INC  LOTFLA    ; ELSE SET FLAG
4239
4240 026202 005037 026214 END:   CLR  LOOPEX    ;
4241 026206      EXIT  INIT              ;
4242
4243 026212 000000      ACFLG: .WORD  0        ; SET IF AUTO CONFIGURATION IS TO BE DONE
4244 026214 000000      LOOPEX: .WORD  0       ; SET IF NOT THE COORECT UNIT IS SELECTED
4245
4246      .NLIST  BEX                      ; IN THE START COMMAND
4247
4248 026216      045      116      045  WARN:  .ASCIZ  /#MACONNECT TEST CONNECTOR AND REQUIRED VOLTAGE/
4249 026276      045      116      045  WARN1: .ASCIZ  /#M1ATO FIVE CHANNEL COUNTER MODULES THAT SHOULD BE TESTED./
4250 026371      045      116      045  NTEST: .ASCIZ  /#M1AND TESTS WERE RUN - CHECK ANSWERS TO STARTUP QUESTIONS./
4251 026465      045      116      045  NTEST1: .ASCIZ  /#M1AND TESTS WERE RUN ON UNIT #D3#A./
4252 026532      045      116      045  WSEL:  .ASCII  /#M1ASELECTED UNIT IS NOT A FIVE CHANNEL COUNTER.#N/
4253 026614      045      101      117      .ASCIZ  /#AOR ID CODE IS INCORRECT/

```

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INITIALIZE SECTION

4254
4255
4256
4257 026646
4258
4270

.LIST BEX
.EVEN
ENDINIT

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INITIALIZE SECTION

```

4272 ;*****
4273 ; SUBROUTINES USED DURING INITIALISATION. *
4274 ;*****
4275
4276 .SBTTL  SETCLK - ROUTINE TO SET UP DELAY COUNTS
4277
4278 ;*
4279 ; FUNCTIONAL DESCRIPTION:
4280 ;
4281 ;     THIS ROUTINE SETS UP 3 DELAY VARIABLES CALLED CNT25M, CNT500, AND
4282 ;     CNT25. THESE GIVE DELAYS OF APPROXIMATELY 25 MILLISECONDS, 500
4283 ;     MICROSECONDS OR 25 MICROSECONDS RESPECTIVELY IF USED AS FOLLOWS:
4284 ;
4285 ;             MOV     CNTXXX,RO
4286 ;             10: DEC     RO
4287 ;             BNE     10
4288 ;
4289 ;     THE COUNTS ARE DERIVED FROM AN L CLOCK IF THERE IS ONE.
4290 ;     OTHERWISE, THE OPERATOR IS ASKED TO TYPE 2 CHARACTERS ON THE
4291 ;     CONSOLE 6 SECONDS APART.
4292 ;
4293 ; INPUTS:
4294 ;
4295 ;     NONE.
4296 ;
4297 ; IMPLICIT INPUTS:
4298 ;
4299 ;     IF CNT25M IS NOT ZERO (ALREADY SET UP), THE ROUTINE DOES NOTHING.
4300 ;
4301 ; OUTPUTS:
4302 ;
4303 ;     CONSOLE MESSAGE IF THERE IS NO L CLOCK ON THE SYSTEM.
4304 ;
4305 ; IMPLICIT OUTPUTS:
4306 ;
4307 ;     CNT25M CONTAINS THE COUNT REQUIRED FOR 25 MILLISECONDS.
4308 ;     CNT500 CONTAINS THE COUNT REQUIRED FOR 500 MICROSECONDS.
4309 ;     CNT25 CONTAINS THE COUNT REQUIRED FOR 25 MICROSECONDS.
4310 ;
4311 ; SUBORDINATE ROUTINES USED:
4312 ;
4313 ;     CRLF - LINE FEED PRINT ROUTINE.
4314 ;     CLINT - DUMMY CLOCK INTERRUPT SERVICE ROUTINE
4315 ;
4316 ; FUNCTIONAL SIDE EFFECTS:
4317 ;
4318 ;     R0 TO R5 ARE CORRUPTED.
4319 ;
4320 ;     IF A LINE TIME CLOCK IS FOUND, VECTOR 100 IS SET UP SO THAT
4321 ;     INTERRUPTS TO IT ARE IGNORED. THE SETVEC MACRO CAN BE USED TO
4322 ;     SET UP THE VECTOR FOR A DEVICE INTERRUPT.
4323 ;
4324 ; CALLING SEQUENCE:
4325 ;
4326 ;     JSR     PC,SETCLK
4327 ;
4328 ;--

```


MISCELLANEOUS SECTIONS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 47-1
 SETCLK - ROUTINE TO SET UP DELAY COUNTS

```

4329
4330 026650 005737 014626      SETCLK: TST      CNT25M      ; COUNTERS ALREADY SET UP?
4331 026654 001402              BEQ        100          ; IF NOT, BRANCH
4332 026656 000137 027524      JMP        SETEX       ; IF YES, EXIT
4333
4334 026662 005004      100:    CLR        R4          ; CLEAR A COUNTER
4335 026664              GETPRI   R2          ; SAVE CURRENT PRIORITY IN R2
4336 026670 005037 027526      CLR        CLKFLG      ; ASSUME THERE IS NO CLOCK WITH A CSR
4337 026674              CLOCK    L,R1         ; GET ADDRESS OF CLOCK TABLE
4338 026704              SETVEC   #4,#NXM,#340    ; SET UP CLOCK CSA TRAP
4339 026732 005037 002470      CLR        NXMFLG      ; CLEAR NXM FLAG
4340 026736 005771 000000      TST        B(R1)       ; ACCESS THE CLOCK ADDRESS
4341 026742 005737 002470      TST        NXMFLG      ; *DON'T DELETE, NEEDED FOR FALCON*
4342 026746 005737 002470      TST        NXMFLG      ; DOES THE CLOCK HAVE A REGISTER ?
4343 026752 001005              BNE        LCLOCK     ; IF NOT, BRANCH
4344 026754 005237 027526      INC        CLKFLG      ; ELSE FLAG THERE IS A CLOCK CSR
4345 026760 012771 000100 000000 MOV      #100,B(R1) ; AND SET IT UP TO INTERRUPT
4346
4347              ; USE THE L CLOCK
4348
4349 026766      LCLOCK:  CLRVEC   #4          ; SET VECTOR 4 TO UNUSED POOL
4350 026774 012703 000006      MOV      #6,R3        ; IF 50 HZ, 100 MS = 5 INTERRUPTS
4351 027000 026127 000006 000062  CMP      6(R1),#50. ; 50 HZ CORRECT?
4352 027006 001401              BEQ        100          ; IF YES, BRANCH
4353 027010 005203              INC        R3          ; ELSE ALLOW 6 INTERRUPTS
4354
4355 027012 010305      100:    MOV      R3,R5        ; SAVE NUMBER OF INTERRUPTS
4356 027014              SETVEC   #100,#KLINT,#340 ; SET UP THE CLOCK VECTOR
4357              ; TO WAIT FOR 1ST INTERRUPT
4358 027042              SETPRI   #0          ; AND DROP THE PRIORITY
4359 027050 005000      200:    CLR        R0          ; CLEAR R0 AND THE CARRY BIT
4360 027052 020305      CMP      R3,R5        ; HAS COUNT BEEN DROPPED ?
4361 027054 001004      BNE        300          ; IF YES, START THE COUNTERS
4362 027056 005300      DEC        R0          ; WAITED TOO LONG ?
4363 027060 001374      BNE        200          ; IF NOT, WAIT LONGER
4364 027062 000137 027174      JMP        USCLOCK    ; IF YES, ASSUME NO CLOCK
4365
4366 027066 005005      300:    CLR        R5          ; CLEAR THE HIGH COUNTER
4367 027070 005204      400:    INC        R4          ; COUNT THE DELAY FOR 5 OR 6 INTERRUPTS
4368 027072 001376      BNE        400          ;
4369 027074 105205      INCB      R5          ;
4370 027076 001374      BNE        400          ;
4371 027100 000435      BR        USCLOCK      ; IF TOO LONG, ASSUME NO CLOCK
4372
4373 027102 005303      KLINT:  DEC        R3          ; 5 OR 6 INTERRUPTS?
4374 027104 001401      BEQ        400          ; IF YES, TIDY UP
4375 027106 000002      RTI              ; ELSE KEEP COUNTING
4376
4377 027110      400:    SETPRI   R2          ; RESTORE THE PRIORITY
4378 027114              SETVEC   #100,#CLINT,#340 ; IGNORE FURTHER INTERRUPTS TO VECTOR 100
4379 027142 022626      CMP      (SP), (SP) ; TIDY UP THE STACK
4380 027144 005737 027526      TST        CLKFLG      ; CAN WE DISABLE A CLOCK ?
4381 027150 001402      BEQ        500          ; IF NOT, BRANCH
4382 027152 005071 000000      CLR        B(R1)       ; ELSE, DISABLE CLOCK INTERRUPTS
4383
4384 027156 000241      500:    CLC              ; DIVIDE THE 100 MILLISECOND COUNTERS
4385 027160 006005      ROR        R5          ; BY 4 TO GIVE 25 MILLISECONDS

```

MISCELLANEOUS SECTIONS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 47-2
 SETCLK - ROUTINE TO SET UP DELAY COUNTS

```

4386 027162 006004      ROR      R4      ;
4387 027164 000241      CLC          ;
4388 027166 006005      ROR      R5      ;
4389 027170 006004      ROR      R4      ;
4390 027172 000524      BR        SAVCNT ; AND SAVE THE COUNT
4391                      ;
4392                      ; COME HERE IF NOT ENOUGH CLOCK INTERRUPTS OCCUR BEFORE THE COUNTERS OVERFLOW
4393                      ;
4394 027174      USCLK:  SETPRI  R2      ; RESTORE THE PRIORITY
4395 027200      SETVEC  #100,#CLINT,#340 ; IGNORE FURTHER INTERRUPTS TO VECTOR 100
4396 027226 005737 027526  TST      CLKFLG ; CAN WE DISABLE A CLOCK ?
4397 027232 001402      BEQ      NOCLOCK ; IF NOT, BRANCH
4398 027234 005071 000000  CLR      @R1) ; ELSE DISABLE CLOCK INTERRUPTS
4399                      ;
4400                      ; USE THE CONSOLE FOR TIMING
4401                      ;
4402                      TKS=177560      ; KEYBOARD STATUS REGISTER
4403                      TKB=177562      ; KEYBOARD DATA BUFFER
4404                      TPS=177564      ; PRINTER STATUS REGISTER
4405                      TPB=177566      ; PRINTER DATA BUFFER
4406                      ;
4407 027240      NOCLOCK: SETVEC  #60,#TTINT,#340 ; SET UP INTERRUPT VECTOR
4408 027266      PRINTF  #TIMMSG ; 'TYPE 2 CHARACTERS 6 SECONDS APART'
4409                      ;
4410 027306 105737 177560  10$:  TSTB   TKS      ; IS FIRST CHARACTER READY?
4411 027312 100375      BPL      10$      ; IF NOT, WAIT
4412 027314 013700 177562  MOV      TKB,R0    ; ELSE GET THE CHARACTER
4413 027320 042700 177600  BIC      #177600,R0 ; DISCARD UNWANTED BITS
4414 027324 020027 000003  CMP      R0,#3   ; IF +C, RETURN TO SUPERVISOR
4415 027330 001001      BNE      20$      ;
4416 027332      DOCLN                      ;
4417                      ;
4418 027334 013737 177562 177566 20$:  MOV      TKB,TPB ; NOW ECHO THE CHARACTER
4419 027342      SETPRI  #0      ; DROP THE PRIORITY
4420 027350 012737 000100 177560  MOV      #100,TKS ; ALLOW INTERRUPTS
4421                      ;
4422 027356 012705 000360      30$:  MOV      #240.,R5 ; SET UP MODULO 240 COUNTER
4423 027362 005305      40$:  DEC      R5      ; START COUNTING
4424 027364 001376      BNE      40$      ; R5 IS MODULO 240 COUNTER
4425 027366 005204      INC      R4      ; UPDATE THE COUNTER
4426 027370 000772      BR        30$      ; 6 SECONDS/240 = 25 MILLISECONDS
4427                      ;
4428 027372      TTINT:  SETPRI  R2      ; RESTORE THE PRIORITY
4429 027376      CLRVEC  #60      ; AND THE KEYBOARD VECTOR
4430 027404 022626      CMP      (SP).,(SP). ; TIDY UP THE STACK
4431 027406 005037 177560      CLR      TKS    ; DISABLE INTERRUPTS
4432 027412 013700 177562  MOV      TKB,R0    ; ELSE GET THE CHARACTER
4433 027416 042700 177600  BIC      #177600,R0 ; DISCARD UNWANTED BITS
4434 027422 020027 000003  CMP      R0,#3   ; IF +C, RETURN TO SUPERVISOR
4435 027426 001001      BNE      10$      ;
4436 027430      DOCLN                      ;
4437 027432 013737 177562 177566 10$:  MOV      TKB,TPB ; ELSE, ECHO THE CHARACTER
4438 027440 004737 014634      JSR      PC,CRLF ; AND PRINT A LINE FEED
4439                      ;
4440                      ; SAVE THE COUNTERS
4441                      ;
4442 027444 010437 014626      SAVCNT: MOV      R4,CNT25M ; SAVE THE 25 MILLISECONDS COUNTER

```


MISCELLANEOUS SECTIONS MACRO V05.00 Wednesday 03-Oct 84 14:03 Page 47 3
 SETCLK - ROUTINE TO SET UP DELAY COUNTS

```

4443 027450 012700 000062      MOV    #50.,R0      ; NOW DIVIDE BY 50
4444 027454 062704 000031      ADD    #25.,R4      ; TO NEAREST 50
4445 027460 005001              CLR    R1          ; INITIALISE RESULT
4446 027462 160004              10$: SUB    R0,R4      ; REMAINDER < 0 ?
4447 027464 002402              BLT     20$          ; IF YES, BRANCH
4448 027466 005201              INC     R1          ; ELSE INCREMENT RESULT
4449 027470 000774              BR      10$          ; AND TRY AGAIN
4450 027472 010137 014630      20$: MOV    R1,CNT500    ; SAVE THE 500 MICROSECONDS COUNTER
4451
4452 027476 012700 000024      MOV    #20.,R0      ; NOW DIVIDE BY 20
4453 027502 062701 000012      ADD    #10.,R1      ; TO NEAREST 20
4454 027506 005002              CLR    R2          ; INITIALISE RESULT
4455 027510 160001              30$: SUB    R0,R1      ; REMAINDER < 0 ?
4456 027512 002402              BLT     40$          ; IF YES, BRANCH
4457 027514 005202              INC     R2          ; ELSE INCREMENT RESULT
4458 027516 0C0774              BR      30$          ; AND TRY AGAIN
4459 027520 010237 014632      40$: MOV    R2,CNT25    ; SAVE THE 25 MICROSECONDS COUNTER
4460
4461 027524 000207              SETEX: RTS    PC          ; RETURN
4462
4463 027526 000000              CLKFLG: .WORD 0          ; SET IF DRS FINDS A CLOCK WITH A CSR
4464
4465                          .NLIST BEX
4466 027530      045      116      045 TIMMSG: .ASCIZ /#NMA*TYPE 2 CHARACTERS 6 SECONDS APART >/
4467                          .LIST BEX
4468                          .EVEN

```

MISCELLANEOUS SECTIONS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 48
 AUTODROP SECTION

```

4470      .SBTTL  AUTODROP SECTION
4471
4472      ;**
4473      ; THIS CODE IS EXECUTED IMMEDIATELY AFTER THE INITIALIZE CODE IF
4474      ; THE "ADR" FLAG WAS SET.  THE UNIT(S) UNDER TEST ARE CHECKED TO
4475      ; SEE IF THEY WILL RESPOND.  THOSE THAT DON'T ARE IMMEDIATELY
4476      ; DROPPED FROM TESTING.
4477      ;--
4478
4479 027600      BGNAUTO
4480
4487 027600      SETVEC  04,0NXM,0PRI07 ; SET UP NON - EXISTENT MEMORY TRAP VECTOR.
4488 027626 005037 002470      CLR      NXMFLG      ; CLEAR NON - EXISTENT MEMORY FLAG
4489 027632 005777 152560      TST      0MOD      ; REFERENCE MEMORY ADDRESS FOR THE DEVICE
4490                                     ; TO SEE IF IT EXISTS.
4491
4492      ; IF THE DEVICE DOESN'T EXIST, THE RESULTANT TRAP TO VECTOR 04 WILL
4493      ; CAUSE THE FLAG NXMFLG TO BE SET (SEE INTERRUPT ROUTINE NXM).
4494
4495 027636 005737 002470      TST      NXMFLG      ; WAS THERE A TRAP ?
4496 027642 001404      BEQ      10$      ; BRANCH IF NOT
4497 027644      DODU      L$LUN      ; ELSE DROP THE DEVICE
4498 027652      DOCLN      ; CLEAN UP CODE.
4499 027654      10$:      CLRVEC  04      ; RETURN VECTOR 04 TO NORMAL STATE
4500 027662      ENDAUTO

```

MISCELLANEOUS SECTIONS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 49
CLEANUP CODING SECTION

4502
4503
4504
4505
4506
4507
4508
4509 027664
4510
4519
4520 027664
4521 027665
4522
4534
4535
4536
4537 027672

.SBTTL CLEANUP CODING SECTION

; THE CLEANUP CODING SECTION CONTAINS THE CODING THAT IS PERFORMED
; AFTER THE HARDWARE TESTS HAVE BEEN PERFORMED.

BGNCLN

BRESET
EXIT CLN

; DO A BUS RESET TO SWITCH OFF ALL LEDS

.EVEN

ENDCLN

MISCELLANEOUS SECTIONS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 50
 DROP UNIT SECTION

```

4539          .SBTTL  DROP UNIT SECTION
4540
4541          ;**
4542          ; THE DROP-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
4543          ; TO NO LONGER BE TESTED.
4544          ;--
4545
4546 027674          BGNDU
4547
4548
4549
4550 027674 010026          MOV      R0,(SP)+          ; SAVE R0 CONTENTS
4551 027676 112760 000001 002614      MOV      #1,DROPED(R0)      ; FLAG UNIT DROPPED IN PARAM TABLE
4552 027704 014600          MOV      -(SP),R0          ; GET ORIGINAL R0 CONTENTS
4553 027706          PRINTF      #DROPD,R0          ; 'UNIT DROPPED'
4554
4555
4556
4557 027730          EXIT      DU
4558
4559
4560
4561
4562
4563 027734          .NLIST      BEX
4564 027734          045      116      045  DROPD: .ASCIZ      /#N#AUNIT #D2#A DROPPED/
4565 027734          .LIST      BEX
4566 027734          .EVEN
4567
4568
4569
4570 027764          ENDDU

```

MISCELLANEOUS SECTIONS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 51
ADD UNIT SECTION

```
4584          .SBTTL  ADD UNIT SECTION
4585
4586          ;**
4587          ; THE ADD-UNIT SECTION CONTAINS ANY CODE THE PROGRAMMER WISHES
4588          ; TO BE EXECUTED IN CONJUNCTION WITH THE ADDING OF A UNIT BACK
4589          ; TO THE TEST CYCLE.
4590          ;--
4591
4592 027766          BGNAU
4593
4602
4603 027766 105060 002614          CLRB  DROPED(R0)          ; FLAG UNIT NOT DROPPED IN PARAM TABLE
4604
4605 027772          EXIT  AU
4606
4618
4619          .EVEN
4620
4621 027776          ENDAU
4622
4623 030000          ENDMOD
4624
4625
```

HARDWARE TESTS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 52
ADD UNIT SECTION

```

4627 .TITLE HARDWARE TESTS
4638
4639 .SBTTL TEST 1: Register NXM Test.
4674
4675 030000 BGNMOD
4676 ;*****
4677 ; Test 1 - Register NXM Test.
4678 ;
4679 ; FOR TEST DESCRIPTION SEE 6.0 TEST SUMMERIES
4680 ;*****
4687
4693 030000 BGNTEST
4694 030000 004737 015006 CALL SELECT ;CALL SELECT ROUTINE
4695 030004 000420 .WORD 420 ;GIVE TEST PARAMETER
4696 030006 030206 TSHD1 ;GIVE TEST HEADER ADDRESS
4697 030010 103467 BCS EXQV1 ;IF CARRY IS SET, EXIT TEST
4698 030012 005037 002504 CLR ITRCNT ;CLEAR ITERATION COUNTER
4699 030016 SETVEC #4,#LOCATE,#PRI07 ;SET UP INTERRUPT ROUTINE
4700 030044 013701 002416 ITRAC1: MOV MOD,R1 ;GET FIRST REGISTER ADDRESS
4701 030050 162701 000002 SUB #2,R1 ;
4702 030054 012702 000004 MOV #4,R2 ;SET COUNTER FOR 4 REGISTERS
4703 030060 005003 CLR R3 ;CLEAR LOCATION FOR ERROR MARK
4704 030062 062701 000002 10#: ADD #2,R1 ;GET REGISTER ADDRESS
4705 030066 BGNSEG ;
4706 030070 005004 CLR R4 ;
4707 030072 005711 TST (R1) ;TEST REGISTER ADDRESS
4708 030074 005704 TST R4 ;WAS THERE A TRAP?
4709 030076 004737 015240 CALL INSERT ;SKIP BRANCH IF "SFI" IS SET
4710 030102 001407 BEQ 20# ;IF NO, BRANCH
4711 030104 004737 015240 CALL INSERT ;SKIP BRANCH IF "SFI" IS SET
4712 030110 005203 INC R3 ;MARK THE ERROR
4713 030112 ERRHRD 101,E101,EERA ;ERROR HANDLER
4714 030122 20#: ENDSEG ;
4715 030124 005302 DEC R2 ;ALL REGISTERS TESTED
4716 030126 001355 BNE 10# ;IF NO, BRANCH
4717 030130 005703 TST R3 ;WAS THERE AN ERROR
4718 030132 001404 BEQ 30# ;IF NO, DON'T DROP THE UNIT
4719 030134 DODU L#LUN ;DROP THE UNIT UNDER TEST
4720 030142 DOCLN ;RUN THE CLEAN UP ROUTINE
4721 030144 005737 002164 30#: TST QVP ;IS QUICK VERIFY PASS SELECTED?
4722 030150 001007 BNE EXQV1 ;IF YES EXIT TEST
4723 030152 005237 002504 INC ITRCNT ;ITERATION COUNTER + 1
4724 030156 023737 002506 002504 CMP ITRDEF,ITRCNT ;DEFAULT ITERATION EXECUTED
4725 030164 001401 BEQ EXQV1 ;IF YES EXIT TEST
4726 030166 000726 BR ITRAC1 ;IF NO, TEST ITERATION
4727 030170 EXQV1: CLRVEC #4 ;
4733 030176 TSTEN1: EXIT TST ;
4734
4746 030202 BGNSRV LOCATE ;SERVICE ROUTINE LOCATE
4747 030202 005204 INC R4 ;INCREMENTS R4 IF A TRAP TO 4
4748 030204 ENDSRV ;HAS OCCURRED
4749
4750 .NLIST BEX
4751 030206 045 123 062 TSHD1: .ASCIZ /#S2#AREGISTER NXM TEST.#N/
4752 030240 122 105 107 E101: .ASCIZ /REGISTER ADDRESSING ERROR - TRAP TO 4/
4753 .LIST BEX
4754 .EVEN

```


HARDWARE TESTS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 52 1
TEST 1: Register NXM Test.

4755 030306

ENDTST

HARDWARE TESTS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 53

TEST 2: Reset Test

```

4757 .SBTTL TEST 2: Reset Test
4758 ;*****
4759 ; Test 2 - Reset Test.
4760 ;
4761 ; FOR TEST DESCRIPTION SEE 6.0 TEST SUMMERIES
4762 ;*****
4763 030310 BGNTST
4764 030310 004737 015006 CALL SELECT ;CALL SELECT ROUTINE
4765 030314 000420 .WORD 420 ;GIVE TEST PARAMETER
4766 030316 030574 TSHD2 ;GIVE TEST HEADER ADDRESS
4767 030320 103523 BCS EXQV2 ;IF CARRY IS SET, EXIT TEST
4768 030322 005037 002504 CLR ITRCNT ;CLEAR ITERATION COUNTER
4769 030326 005001 ITRAC2: CLR R1 ;CLEAR TEMPORARY STORE
4770 ; MOVB CONMSK,R1 ;GET MODULE TYPE (FOR LATER USE)
4771 030330 012701 000001 MOV #1,R1 ;CNMASK FOR 5 CHA. COUNTER
4772 030334 012702 177777 10$: MOV #-1,R2 ;MODULE IDENTIFICATION
4773 030340 005202 20$: INC R2 ;...
4774 030342 006201 ASR R1 ;...
4775 030344 103375 BCC 20$ ;...
4776 030346 006302 ASL R2 ;MULTIPLY BY 10 TO GET TABLE
4777 030350 006302 ASL R2 ;OFFSET
4778 030352 006302 ASL R2 ;...
4779 030354 012703 000004 MOV #4,R3 ;START WITH INR REGISTER
4780 030360 BGNSEG
4781 030362 BRESET ;DO A BUS RESET
4782 030364 032777 000100 152024 BIT #100,BMOD ;IS LED BIT CLEARED ?
4783 030372 004737 015240 CALL INSERT ;SKIP BRANCH IF "SFI" IS SET
4784 030376 001404 BEQ 30$ ;BRANCH IF YES
4785 030400 ERRSOFT 201,E201,EERG ;ERROR HANDLER
4786 030410 30$: CKLOOP ;
4787 030412 052777 000100 151776 BIS #100,BMOD ;SWITCH ON THE MODULE LED
4788 030420 032777 000100 151770 BIT #100,BMOD ;IS LED BIT NOW SET ?
4789 030426 004737 015240 CALL INSERT ;SKIP BRANCH IF "SFI" IS SET
4790 030432 001004 BNE 40$ ;BRANCH IF YES
4791 030434 ERRSOFT 202,E202,EERG ;ERROR HANDLER
4792 030444 40$: ENDSEG ;
4793 030446 013705 002424 MOV INR,R5 ;GET INR REGISTER ADDRESS
4794 030452 062702 000006 ADD #6,R2 ;POINT TABLE OFFSET TO INR CONT.
4795 030456 BGNSEG
4796 030460 016237 031016 002474 50$: MOV R5,BAD ;GET FIRST COMPARE CONTENTS FROM TABLE
4797 030466 011537 002476 MOV GOOD,BAD ;GET FIRST REGISTER CONTENTS
4798 030472 023737 002474 002476 CMP GOOD,BAD ;CMP TABLE CONT.WITH REG. CONT.
4799 030500 004737 015240 CALL INSERT ;SKIP BRANCH IF "SFI" IS SET
4800 030504 001404 BEQ 70$ ;
4801 030506 ERRSOFT 203,E203,EERB ;ERROR HANDLER
4802 030516 70$: CKLOOP ;
4803 030520 022703 000004 CMP #4,R3 ;ALL REGISTER TESTED ?
4804 030524 001406 BEQ 80$ ;BRANCH IF YES
4805 030526 005203 INC R3 ;COUNTER FOR NEXT REGISTER
4806 030530 062702 000002 ADD #2,R2 ;LOAD NEXT TABLE ADDR.
4807 030534 062705 000002 ADD #2,R5 ;LOAD NEXT REGISTER ADDR.
4808 030540 000747 BR 50$ ;
4809 030542 80$: ENDSEG ;
4810 030544 005737 002164 TST QVP ;IS QUICK VERIFY PASS SELECTED?
4811 030550 001007 BNE EXQV2 ;IF YES EXIT TEST
4812 030552 005237 002504 INC ITRCNT ;ITERATION COUNTER + 1
4813 030556 022737 000002 002504 CMP #2,ITRCNT ;TWO ITERATION EXECUTED

```


HARDWARE TESTS MACRO V05.00 Wednesday 03-Oct 84 14:03 Page 53 1
 TEST 2: Reset Test

```

4814 030564 001401      BEQ      EXQV2      ;IF YES EXIT TEST
4815 030566 000657      BR       ITRAC2     ;IF NO. TEST ITERATION
4816 030570              EXQV2:  EXIT      TST
4817                      .MLIST    BEX
4818 030574      045      123      062  TSHD2:: .ASCIZ  /#S2#ARESET TEST.WN/
4819 030617      114      105      104  E201:  .ASCIZ  /LED BIT IN MOD REGISTER NOT CLEARED AFTER BUS RESET /
4820 030704      114      105      104  E202:  .ASCIZ  /LED BIT IN MOD REGISTER CAN'T BE SET/
4821 030751      122      105      107  E203:  .ASCIZ  /REGISTER INCORRECT AFTER BUS RESET /
4822                      .EVEN
4823
4824
4825 031016 000000 000000 000000 1  RSAX:  .WORD    1.REG., 2.REG., 3., 4.
4826                      .LIST    0,      0,      0, 60340 ;REG. MASKS FOR 5 CHA. COUNTER
4827                      .EVEN    BEX
4828 031026                      .EVEN
                                .ENDTST
  
```

HARDWARE TESTS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 54
TEST 3: Register R/W Bit Test.

```

4830 .SBTTL TEST 3: Register R/W Bit Test.
4831 ;*****
4832 ; Test 3 - Register R/W Bit Test.
4833 ;
4834 ; FOR TEST DESCRIPTION SEE 6.0 TEST SUMMERIES
4835 ;*****
4836 031030 BGNST
4837 031030 004737 015006 CALL SELECT ;CALL SELECT ROUTINE
4838 031034 000420 .WORD 420 ;TEST SELECT MASK
4839 031036 031216 TSHD3 ;TEST HEADER ADDRESS
4840 031040 103002 BCC 14 ;IF CARRY IS SET, DON'T BRANCH
4841 031042 EXIT TST ;EXIT TEST IF CARRY IS SET
4842 031046 005037 002504 14: CLR ITRCNT ;CLEAR ITERATION COUNTER
4843 031052 ITRAC3: SETPRI @PRI07 ;DISABLE INTERRUPTS
4844 031060 005001 CLR R1 ;CLEAR TEMPORARY STORE
4845 ; MOVB CONMSK,R1 ;GET MODULE TYPE (FOR LATER USE)
4846 031062 012701 000001 MOV @1,R1 ;MASK FOR 5 CHA. COUNTER
4847 031066 012702 177777 MOV @-1,R2 ;
4848 031072 005202 104: INC R2 ;MODULE IDENTIFICATION
4849 031074 006201 ASR R1 ;...
4850 031076 103375 BCC 104 ;...
4851 031100 006302 ASL R2 ;MULTIPLY BY 8. TO GET TABLE
4852 031102 006302 ASL R2 ;OFFSET
4853 031104 006302 ASL R2 ;
4854 031106 012703 000001 MOV @1,R3 ;START WITH FIRST REGISTER
4855 031112 013737 002416 014052 MOD,REGADD ;LOAD FIRST REGISTER ADDRESS
4856 031120 012737 000455 005714 204: MOV @301,,ERRNBR ;LOAD FIRST ERROR NUMBER
4857 031126 016237 031242 014046 MOV RMMAK(R2),MASK ;GET R/W MASK FORM TABLE
4858 031134 004737 013532 CALL REGTS1 ;CALL REGISTER TEST
4859 031140 022703 000004 CMP @4,R3 ;4 REGISTERS TESTED ?
4860 031144 001407 BEQ 304 ;BRANCH IF YES
4861 031146 005203 INC R3 ;COUNTER FOR NEXT REGISTER
4862 031150 062702 000002 ADD @2,R2 ;LOAD NEXT TABLE ADDR.
4863 031154 062737 000002 014052 ADD @2,REGADD ;NEXT REGISTER ADDRESS
4864 031162 000756 BR 204 ;TEST AGAIN WITH NEW PARAMETER
4865
4866
4867 031164 005737 002164 304: TST QVP ;IS QUICK VERIFY PASS SELECTED?
4868 031170 001010 BNE EXQV3 ;IF YES, EXIT TEST
4869 031172 005237 002504 INC ITRCNT ;ITERATION COUNTER + 1
4870 031176 023737 002506 002504 CMP ITRDEF,ITRCNT ;DEFAULT ITERATION EXECUTED
4871 031204 001402 BEQ EXQV3 ;IF YES, EXIT TEST
4872 031206 000137 031052 JMP ITRAC3 ;IF NO, TEST ITERATION
4873 031212 EXQV3: EXIT TST
4874
4875
4876 031216 045 123 062 TSHD3: .NLIST BEX
4877 .ASCIZ /#S2MAR-W BIT TEST#N/
4878 .LIST BEX
4879 .EVEN
4880
4881 031242 000000 000000 000000 RMMAK: .WORD 1.REG., 2.REG., 3. ,4.
4882 031250 117400 .EVEN ;R/W MASKS FOR 5 CHA. CNT
4883 031252 ENDTST
4884

```

HARDWARE TESTS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 55
 TEST 4 - AM 9513 SUBREGISTER TESTS

```

4886 .SBTTL TEST 4 -- AM 9513 SUBREGISTER TESTS
4887 ;*****
4888 ; TEST 4 -- AM 9513 SUBREGISTER TESTS
4889 ;
4890 ; FOR TEST DESCRIPTION SEE 6.0 TEST SUMMERIES
4891 ;*****
4892
4893 .DSABLE LSB
4894 BGNST
4895 CALL SELECT ;CALL SELECT ROUTINE
4896 .WORD 420 ;TEST SELECT MASK
4897 TSHD4 ;TEST HEADER ADDRESS
4898 BCC 18 ;IF CARRY IS SET, DON'T BRANCH
4899 EXIT TST ;EXIT TEST IF CARRY IS SET
4900 14: CLR ITRCNT ;CLEAR ITERATION COUNTER
4901 MOV MOD,R1 ;GET FIRST REGISTER ADDRESS
4902 JSR PC,AMREST ;RESET THE MODULE
4903 ITRAC4: MOV MOD,R1 ;GET FIRST REGISTER ADDRESS
4904 CLR NDMFLG ;TO DETECT IF A TRAP OCCURRED
4905
4906 ; TEST LOADING CONTROL & STATUS REGISTER AND READING RESULTS
4907 ;
4908 031316 012737 000002 002434 MOV #2,BITMSK ;BIT MASK
4909 031324 012737 000001 002442 MOV #1,NUMBER ;COUNTER CHANNEL
4910
4911 031332 104: BGNSEG ;BEGINNING OF LOOP ON ERROR SEGMENT
4912 031334 012700 177400 MOV #8,R0 ;LOAD MASK FOR MODE REGISTER AND
4913 031340 063700 002442 ADD NUMBER,R0 ;ADD CHANNEL NR
4914 031344 010061 000002 MOV R0,MREA(R1) ;SELECT MODE REGISTER
4915 031350 012761 000042 000004 MOV #M1TOG,MREB(R1) ;SET TO TOGGLE MODE
4916 031356 012700 177740 MOV #C1CTN,R0 ;GET TOUT COMMAND
4917 031362 063700 002442 ADD NUMBER,R0 ;ADD CHANNEL NUMBER
4918 031366 010061 000002 MOV R0,MREA(R1) ;LOAD INTO MREA (CSR)
4919 031372 016102 000002 MOV MREA(R1),R2 ;GET RESULT
4920 031376 033702 002434 BIT BITMSK,R2 ;TEST THAT OUTX BIT IS CLEARED
4921 031402 004737 015240 CALL INSERT ;TEST IF ERROR MESSAGE IS REQUIRED
4922 031406 001405 BEQ 204 ;OK
4923 031410 ERRHRD 401,,ERR104 ;ERROR HANDLER
4924 031420 CKLOOP ;IF LOOP ON ERROR
4925 031422 204: ENDSEG ;END SEGMENT
4926 031424 BGNSEG ;BEGINNING OF LOOP ON ERROR SEGMENT
4927 031426 012700 177750 MOV #C1STN,R0 ;SET TOUT COMMAND
4928 031432 063700 002442 ADD NUMBER,R0 ;BIT NUMBER (1-5)
4929 031436 010061 000002 MOV R0,MREA(R1) ;LOAD INTO MREA
4930 031442 016102 000002 MOV MREA(R1),R2 ;GET RESULT
4931 031446 033702 002434 BIT BITMSK,R2 ;TEST THAT OUTX BIT IS SET
4932 031452 004737 015240 CALL INSERT ;TEST IF ERROR MESSAGE IS REQUIRED
4933 031456 001005 BNE 304 ;OK
4934 031460 ERRHRD 402,,ERR105 ;ERROR HANDLER
4935 031470 CKLOOP ;IF LOOP ON ERROR
4936 031472 304: ENDSEG ;END SEGMENT
4937 ;
4938 031474 BGNSEG ;BEGINNING OF LOOP ON ERROR SEGMENT
4939 031476 012700 177740 MOV #C1CTN,R0 ;CLEAR TOUT COMMAND
4940 031502 063700 002442 ADD NUMBER,R0 ;BIT NUMBER (1-5)
4941 031506 010061 000002 MOV R0,MREA(R1) ;LOAD INTO MREA
4942 031512 016102 000002 MOV MREA(R1),R2 ;GET RESULT

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 TEST 4 -- AM 9513 SUBREGISTER TESTS

4943	031516	033702	002434		BIT	BITMSK,R2	;TEST THAT OUT BIT IS CLEARED AGAIN
4944	031522	004737	015240		CALL	INSERT	;TEST IF ERROR MESSAGE IS REQUIRED
4945	031526	001405			BEQ	404	;OK
4946	031530				ERRHRD	403,,ERR104	;ERROR HANDLER
4947	031540				CKLOOP		;IF LOOP ON ERROR
4948	031542			404:	ENDSEG		;END SEGMENT
4949							
4950	031544	006337	002434		ASL	BITMSK	;SHIFT MASK BIT
4951	031550	005237	002442		INC	NUMBER	;GET NEXT CHA. NUMBER
4952	031554	023727	002442	000005	CMP	NUMBER,#5	;ALL CHA. DONE ?
4953	031562	003663			BLE	104	;NO, THEN BRANCH
4954							
4955							
4956							
4957	031564	012737	177410	002450	MOV	#8!10,ELEMT	;GET LOAD REGISTER MASK
4958	031572	012737	000001	002446	MOV	#1,GROUP	;GET CHA. NUMBER
4959	031600	012737	000002	002444	504:	MOV	#2,COUNTR
4960							;INITIAL VALUE TO LOAD
4961	031606						
4962	031610	013700	002450		BGNSEG		
4963	031614	063700	002446		604:	MOV	ELEMT,R0
4964	031620	010061	000002		ADD	GROUP,R0	;BUILD ADDRESS
4965	031624	013761	002444	000004	MOV	R0,MREA(R1)	;ADD CHA. NUMBER
4966	031632	010061	000002		MOV	COUNTR,MREB(R1)	;SELECT ADDRESS
4967	031636	016103	000004		MOV	R0,MREA(R1)	;LOAD VALUE
4968	031642	020337	002444		MOV	MREB(R1),R3	;SELECT ADDRESS
4969	031646	004737	015240		CMP	R3,COUNTR	;GET RESULT
4970	031652	001410			CALL	INSERT	;IS IT THE SAME VALUE?
4971					BEQ	704	;TEST IF ERROR MESSAGE IS REQUIRED
4972							;OK
4973							
4974	031654						
4975	031664						
4976	031666	004737	015240		ERRHRD	404,,ERR106	;ERROR HANDLER
4977	031672	000404			CKLOOP		;IF LOOP ON ERROR
4978					CALL	INSERT	;SKIP BRANCH IF "SFI" IS SET
4979	031674	062737	000201	002444	BR	734	;TAKE NEXT CHANNEL
4980	031702	103342					
4981	031704				704:	ADD	#201,COUNTR
4982	031706	005237	002446		BCC	604	;BUMP COUNTER
4983	031712	023727	002446	000005	734:	ENDSEG	;GO UNTIL OVERFLOW
4984	031720	003727			INC	GROUP	;BUMP CHA. NUMBER
4985					CMP	GROUP,#5	;HIGH LIMIT
4986					BLE	504	;GO FOR NEXT CHANNEL
4987							
4988	031722	012737	177420	002450			
4989	031730	012737	000001	002446			
4990	031736	012737	000002	002444	804:	MOV	#2,COUNTR
4991							;INITIAL VALUE TO LOAD
4992	031744						
4993	031746	013700	002450		BGNSEG		
4994	031752	063700	002446		904:	MOV	ELEMT,R0
4995	031756	010061	000002		ADD	GROUP,R0	;BUILD ADDRESS
4996	031762	013761	002444	000004	MOV	R0,MREA(R1)	;ADD CHANNEL NUMBER
4997	031770	010061	000002		MOV	COUNTR,MREB(R1)	;SELECT ADDRESS
4998	031774	016103	000004		MOV	R0,MREA(R1)	;LOAD VALUE
4999	032000	020337	002444		MOV	MREB(R1),R3	;SELECT ADDRESS
					CMP	R3,COUNTR	;GET RESULT
							;IS IT THE SAME VALUE?

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TEST 4 -- AM 9513 SUBREGISTER TESTS

5000	032004	004737	015240			CALL	INSERT	;TEST IF ERROR MESSAGE IS REQUIRED
5001	032010	001410				BEG	100;	;OK
5002								
5003						:		
5004						: ERROR DETECTED		
5005	032012							
5006	032022					ERRHRD	405.,ERR107	;ERROR HANDLER
5007	032024	004737	015240			CKLOOP		;IF LOOP ON ERROR
5008	032030	000404				CALL	INSERT	;SKIP BRANCH IF "SFI" IS SET
5009						BR	110;	;LEAVE TEST
5010	032032	062737	000201	002444	100;	ADD	#201,COUNTR	;BUMP COUNTER
5011	032040	103342				BCC	90;	;GO UNTIL OVERFLOW
5012	032042				110;	ENDSEG		
5013	032044	005237	002446			INC	GROUP	;BUMP GROUP NUMBER
5014	032050	023727	002446	000005		CMP	GROUP,#5	;HIGH LIMIT
5015	032056	003727				BLE	80;	;GO FOR NEXT GROUP
5016								
5017	032060	005737	002164			TST	QVP	;IS QUICK VERIFY PASS SELECTED
5018	032064	001010				BNE	EXS4	;YES
5019	032066	005237	002504			INC	ITRCNT	;BUMP ITERATION COUNT
5020	032072	023737	002506	002504		CMP	ITRDEF,ITRCNT	;DEFAULT ITERATION COUNT
5021	032100	001402				BEG	EXS4	;TIME TO EXIT
5022	032102	000137	031306			JMP	ITRAC4	;DO THE TEST AGAIN
5023								
5024	032106				EXS4:	CLRVEC	#4	;RESET TRAP VECTOR
5025	032114					EXIT	TST	
5026								
5027						.NLIST	BEX	
5028	032120	045	123	062	TSHD4::.	ASCIZ	/#S2#AAM 9513 SUBREGISTER TEST #N/	
5029						.LIST	BEX	
5030						.EVEN		
5031								
5032	032162					ENDTST		
5033								

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 TEST 4 AM 9513 SUBREGISTER TESTS

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5035
5036 .SBTTL TEST 5 -- INTERRUPT TEST
5037 ;*****
5038 ; TEST 5 -- INTERRUPT TEST
5039 ;
5040 ; FOR TEST DESCRIPTION SEE 6.0 TEST SUMMERIES
5041 ;*****
5042
5043 032164 BGNTST
5044 032164 004737 015006 CALL SELECT ;CALL SELECT ROUTINE
5045 032170 000420 .WORD 420 ;TEST SELECT MASK
5046 032172 033474 TSHDS ;TEST HEADER ADDRESS
5047 032174 103002 BCC 11 ;IF CARRY IS SET, DON'T BRANCH
5048 032176 EXIT TST ;EXIT TEST IF CARRY IS SET
5049 032202 005037 002504 11: CLR ITRCNT ;CLEAR ITERATION COUNTER
5050
5051 032206 004737 021470 JSR PC,AMREST ;RESET THE MODULE
5052 032212 SETVEC VEC,#INTSR,#PRI07 ;SETUP INTERRUPT SERVICE ROUTINE
5053 032240 CLRVEC #400 ;CATCH SUPERVISOR BUG
5054 032246 SETVEC VEC,#INTSR,#PRI07 ;...
5055 032274 SETVEC #4,#BADIV,#PRI07 ;SETUP TO CATCH TRAPS
5056 032322 GETPRI SAVPRI ;SAVE CURRENT PRIORITY IN R2
5057 032330 SETPRI #0 ;AND DROP THE PRIORITY
5058 032336 005037 002510 ITRACS: CLR INTFLA ;INIT INTERRUPT FLAG
5059 032342 005037 002470 CLR NXMFLG ;TO DETECT IF A TRAP OCCURRED
5060 032346 005037 002472 CLR 9IV ;ZERO COUNT
5061
5062 ; TEST 'IR' BITS WITHOUT MASTER ENABLE SET
5063 ;
5064 032352 012737 000400 002434 MOV #400,BITMSK ;MASK FOR 'EN' BITS
5065 032360 012737 000001 002474 MOV #1,GOOD ;MASK FOR 'IR' BITS
5066 032366 012737 000001 002442 MOV #1,NUMBER ;MASK FOR CHANNEL NUMBER
5067
5068 032374 104: BGNSEG ;BEGINNING OF LOOP ON ERROR SEGMENT
5069 032376 005077 150022 CLR BINR ;DISABLE ENABLE BITS
5070
5071 032402 013777 002442 150010 MOV NUMBER,BCSR ;SELECT MODE REGISTER AND CHANNEL
5072 032410 012777 000042 150004 MOV #1,TOG,BCCR ;LOAD TOGGLE MODE
5073 032416 012702 177740 MOV #C1CTN,R2 ;GET CLEAR TOUT COMMAND
5074 032422 063702 002442 ADD NUMBER,R2 ;ADD CHANNEL NUMBER
5075 032426 010277 147766 MOV R2,BCSR ;OUTPUT TO DEFINATE STATE (CLEAR TOUT)
5076 032432 012702 177750 MOV #C1STN,R2 ;GET SET TOUT COMMAND
5077 032436 063702 002442 ADD NUMBER,R2 ;ADD CHANNEL NUMBER
5078 032442 010277 147752 MOV R2,BCSR ;SET TOUT (COURS IR SET)
5079 032446 017737 147752 002476 MOV BINR,BAD ;GET INTERRUPT REGISTER CONTENTS
5080 032454 022737 060340 002476 CMP #60340,BAD ;IR BITS SHOULD BE CLEARED
5081 032462 004737 015240 CALL INSERT ;TEST IF ERROR MESSAGE IS REQUIRED
5082 032466 001406 BEQ 201 ;OK
5083 032470 012702 060340 MOV #60340,R2 ;SET UP FOR ERROR MESSAGES
5084 032474 ERRHRD 501,,ERR501 ;ERROR HANDLER
5085 032504 201: ENDSEG ;END SEGMENT
5086
5087 032506 BGNSEG
5088 032510 013777 002434 147706 MOV BITMSK,BINR ;SET ENABLE BIT
5089 032516 013777 002442 147674 MOV NUMBER,BCSR ;SELECT MODE REGISTER AND CHANNEL
5090 032524 012777 000042 147670 MOV #1,TOG,BCCR ;SET TO TOGGLE MODE
5091 032532 012702 177740 MOV #C1CTN,R2 ;GET CLEAR TOUT COMMAND

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TEST 5 -- INTERRUPT TEST

5092	032536	063702	002442		ADD	NUMBER,R2	;ADD CHANNEL NUMBER
5093	032542	010277	147652		MOV	R2,BCSR	;OUTPUT TO DEFINATE STATE (CLEAR TOUT)
5094	032546	012702	177750		MOV	#CISTN,R2	;GET SET TOUT COMMAND
5095	032552	063702	002442		ADD	NUMBER,R2	;ADD CHANNEL NUMBER
5096	032556	010277	147636		MOV	R2,BCSR	;SET TOUT (COUSE IR SET)
5097	032562	012777	000037	147634	MOV	#37,BINR	;CLEAR EN BIT ONLY (LEAVE IR BITS)
5098	032570	017737	147630	002476	MOV	BINR,BAD	;GET INTERRUPT REGISTER CONTENTS
5099	032576	013702	002474		MOV	GOOD,R2	;SET UP FOR ERROR MESSAGES
5100	032602	052702	060340		BIS	#60340,R2	;SET UNUSED BITS
5101	032606	020237	002476		CMF	R2,BAD	;IR BITS SHOULD BE SET
5102	032612	004737	015240		CALL	INSERT	;TEST IF ERROR MESSAGE IS REQUIRED
5103	032616	001404			BEQ	304	;OK
5104	032620				ERRHRD	502,,ERR501	;ERROR HANDLER
5105	032630				304:	ENDSEG	
5106							
5107	032632				BGNSEG		
5108	032634	043777	002474	147562	BIC	GOOD,BINR	;CLEAR IR BIT
5109	032642	017737	147556	002476	MOV	BINR,BAD	;GET INR CONTENTS
5110	032650	022737	060340	002476	CMF	#60340,BAD	;IR BIT SOULD BE CLEARED
5111	032656	004737	015240		CALL	INSERT	;TEST IF ERROR MESSAGE IS REQUIRED
5112	032662	001406			BEQ	404	;OK
5113	032664	012702	060340		MOV	#60340,R2	;SET UP FOR ERRO MESSAGES
5114	032670				ERRHRD	503,,ERR502	;ERROR HANDLER
5115	032700				404:	ENDSEG	
5116							
5117	032702	006337	002434		ASL	BITMSK	;SHIFT 'EN' MASK BIT
5118	032706	006337	002474		ASL	GOOD	;SHIFT 'IR' MASK BIT
5119	032712	005237	002442		INC	NUMBER	;INCREMENT CHA. BIT NUMBER
5120	032716	023727	002442	000005	CMF	NUMBER,#5	;ALL 5 CHANNELS DONE ?
5121	032724	003002			BGT	504	;BRANCH IF NO
5122	032726	000137	032374		JMP	104	;YES
5123							
5124							
5125							
5126	032732	005737	002510		504:	TST INTFLA	;TEST INTERRUPT COUNT
5127	032736	004737	015240		CALL	INSERT	;TEST IF ERROR MESSAGE IS REQUIRED
5128	032742	001404			BEQ	604	;OK
5129	032744				ERRHRD	504,,ERR503	;ERROR HANDLER
5130							
5131							
5132							
5133							
5134							
5135	032754				604:	BGNSEG	
5136	032756	012777	117400	147440	MOV	#117400,BINR	;***SET MASTER ENABLE AND INT ENB***
5137	032764	000240			NOP		;VERY SHORT WAIT
5138	032766	005737	002510		TST	INTFLA	;NO INTERRUPT SHOULD APEARE
5139	032772	004737	015240		CALL	INSERT	;TEST IF ERROR MESSAGE IS REQUIRED
5140	032776	001404			BEQ	704	;OK
5141	033000				ERRHRD	505,,ERR503	;ERROR HANDLER
5142							
5143	033010				704:	ENDSEG	
5144	033012	012737	000400	002434	MOV	#400,BITMSK	;MASK FOR 'EN' BITS
5145	033020	012737	000001	002474	MOV	#1,GOOD	;MASK FOR 'IR' BITS
5146	033026	012737	000001	002442	MOV	#1,NUMBER	;MASK FOR CHANNEL NUMBER
5147							
5148	033034				2004:	BGNSEG	;BEGINNING OF LOOP ON ERROR SEGMENT

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TEST 5 -- INTERRUPT TEST

5149											
5150	033036	012701	000340			MOV	#PRI07,R1			;LOAD R1 WITH PRIORITY NUMBER	
5151	033042					SETPRI	#PRI07			;DISABLE INTERRUPT AT THIS POINT	
5152	033050	005037	002472			CLR	BIV				
5153	033054	005037	002510			CLR	INTFLA			;CLEAR INTERRUPT FLAG	
5154	033060	052777	100000	147336		BIS	#100000,BINR			;SET MASTER ENABLE IN INR	
5155	033066	013777	002442	147324		MOV	NUMBER,BCSR			;SELECT MODE REGISTER AND CHANNEL	
5156	033074	012777	000042	147320		MOV	#1TOG,BCCR			;SET TO TOGGLE MODE	
5157	033102	012702	177740			MOV	#C1CTN,R2			;GET CLEAR TOUT COMMAND	
5158	033106	063702	002442			ADD	NUMBER,R2			;ADD CHANNEL NUMBER	
5159	033112	010277	147302			MOV	R2,BCSR			;OUTPUT TO DEFINATE STATE (CLEAR TOUT)	
5160	033116	012702	177750			MOV	#C1STN,R2			;GET SET TOUT COMMAND	
5161	033122	063702	002442			ADD	NUMBER,R2			;ADD CHANNEL NUMBER	
5162	033126	010277	147266			MOV	R2,BCSR			;SET TOUT (GENERATE INTERRUPT)	
5163											
5164	033132	023727	002510	000001	1104:	CMP	INTFLA,#1			;DID INTERRUPT OCCUR	
5165	033140	004737	015240			CALL	INSERT			;TEST IF ERROR MESSAGE IS REQUIRED	
5166	033144	001431				BEQ	904			;OK	
5167	033146	005737	002472			TST	BIV			;DID AN INTERRUPT OCCUR?	
5168	033152	001021				BNE	1004			;YES, AT ANOTHER ADDRESS	
5169	033154	162701	000040			SUB	#40,R1			;DECREMENT PRIORITY	
5170	033160					SETPRI	R1			;SET PRIORITY	
5171	033164	020127	000100			CMP	R1,#PRI02			;IS PRIORITY OVER 2 ?	
5172	033170	004737	015240			CALL	INSERT			;TEST IF ERROR MESSAGE IS REQUIRED	
5173	033174	001356				BNE	1104			;BRANCH IF YES	
5174	033176					ERRHRD	506,,ERR504			;ERROR HANDLER	
5175	033206					CKLOOP					
5176	033210	004737	015240			CALL	INSERT			;TEST IF ERROR MESSAGE IS REQUIRED	
5177	033214	000444				BR	1204			;JOIN COMMON CODE	
5178	033216				1004:	ERRHRD	507,,ERR505			;ERROR HANDLER	
5179	033226					CKLOOP					
5180	033230	062701	000040		904:	ADD	#40,R1			;CORRECT PRI FOR CMP	
5181	033234	020137	002430			CMP	R1,PRI0			;PRIORITY CORRECT ?	
5182	033240	004737	015240			CALL	INSERT			;TEST IF ERROR MESSAGE IS REQUIRED	
5183	033244	001430				BEQ	1204			;BRANCH IF YES	
5184		000005				.REPT	5			;SET UP DATA FOR ERROR MESSAGES	
5185						ASR	R1			...	
5186						.ENDR				...	
5187	033260	010137	002476			MOV	R1,BAD			;SET UP DATA FOR ERROR REPORT	
5188	033264	013737	002430	002474		MOV	PRI0,GOOD			...	
5189		000005				.REPT	5				
5190						ASR	GOOD				
5191						.ENDR					
5192	033316					ERRHRD	508,,ERR506			;ERROR HANDLER	
5193											
5194	033326				1204:	ENDSEG					
5195											
5196	033330	017737	147070	002476		MOV	BINR,BAD			;GET INR REGISTER CONTENTS	
5197	033336	012737	100000	002474		MOV	#100000,GOOD			;SET UP FOR ERROR HANDLER	
5198	033344	033737	002474	002476		BIT	GOOD,BAD			;IS MASTER ENABLE CLEARED ?	
5199	033352	004737	015240			CALL	INSERT			;TEST IF ERROR MESSAGE IS REQUIRED	
5200	033356	001404				BEQ	1224			;BRANCH IF YES	
5201	033360					ERRHRD	509,,ERR507			;ERROR HANDLER	
5202											
5203	033370	006337	002434		1224:	ASL	BITMSK			;SHIFT 'EN' MASK BIT	
5204	033374	005237	002442			INC	NUMBER			;INCREMENT CHANNEL BIT NUMBER	
5205	033400	023727	002442	000005		CMP	NUMBER,#5			;ALL CHANNELS DONE ?	

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 TEST 5 -- INTERRUPT TEST

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5206 033406 003002          BGT      1304          ;IF YES TERMINATE TEST
5207 033410 000137 033034    JMP      2004          ;NO
5208
5209 033414 005737 002164      1304:  TST      QVP          ;IS QUICK VERIFY PASS SELECTED
5210 033420 001010          BNE      EXS5          ;YES
5211 033422 005237 002504          INC      ITRCNT        ;BUMP ITERATION COUNT
5212 033426 023737 002506 002504    CMP      ITRDEF,ITRCNT    ;DEFAULT ITERATION COUNT
5213 033434 001402          BEQ      EXS5          ;TIME TO EXIT
5214 033436 000137 032336    JMP      ITRACS        ;DO THE TEST AGAIN
5215
5216 033442 005077 146756      EXS5:  CLR      @INR          ;DISABLE INTERRUPTS
5217 033446          SETPRI    SAVPRI        ; RESTORE THE PRIORITY
5218 033454          CLRVEC    #4           ;RESET TRAP VECTOR
5219 033462          CLRVEC    VEC          ;DEVICE INTERRUPT ADDRESS
5220 033470          EXIT      TST
5221
5222          .NLIST    BEX
5223 033474      045      123      062  TSHD5: .ASCIZ  /#S2#AINTERRUPT TEST #N/
5224          .LIST    BEX
5225          .EVEN
5226
5227 033524          ENDTST

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 TEST 5 -- INTERRUPT TEST

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5229
5230 .SBTTL TEST 6 -- REFERENCE FREQUENCY TEST
5231 ;*****
5232 ; TEST 6 -- REFERENCE FREQUENCY TEST
5233 ;
5234 ; FOR TEST DESCRIPTION SEE 6.0 TEST SUMMERIES
5235 ;*****
5236
5237 .DSABLE LSB
5238 BGNTST
5239 033526 004737 015006 CALL SELECT ;CALL SELECT ROUTINE
5240 033532 000420 .WORD 420 ;TEST SELECT MASK
5241 033534 034076 TSHD6 ;TEST HEADER ADDRESS
5242 033536 103002 BCC 1# ;IF CARRY IS SET, DON'T BRANCH
5243 033540 EXIT TST ;EXIT TEST IF CARRY IS SET
5244 033544 005037 002504 1# CLR ITRCNT ;CLEAR ITERATION COUNTER
5245
5246 033550 013701 002416 MOV MOD,R1 ;GET FIRST REGISTER ADDRESS
5247 033554 004737 021470 JSR PC,AMREST ;RESET THE MODULE
5248 033560
5249 033560 012737 000001 002442 ITRAC6: MOV #1,NUMBER ;CHANNEL NUMBER
5250
5251 ;SET UP ALL COUNTERS FOR 5 MHZ AND LOAD VALUE 10000
5252
5253 033566 012700 177400 10# MOV #B,R0 ;SELECT MODE REGISTER
5254 033572 063700 002442 ADD NUMBER,R0 ;ADD IN CHANNEL NR
5255 033576 010061 000002 MOV R0,MREA(R1) ;SELECT MODE REGISTER
5256 033602 012761 005440 000004 MOV #005440,MREB(R1) ;SELECT 5MHZ
5257 033610 012700 177410 MOV #B!10,R0 ;SELECT LOAD REGISTER
5258 033614 063700 002442 ADD NUMBER,R0 ;SELECT CHANNEL
5259 033620 010061 000002 MOV R0,MREA(R1) ;ADDRESS
5260 033624 012761 023420 000004 MOV #10000.,MREB(R1) ;LOAD VALUE
5261 033632 005237 002442 INC NUMBER ;BUMP CHANNEL NUMBER
5262 033636 023727 002442 000005 CMP NUMBER,#5 ;ALL CHANNELS?
5263 033644 003750 BLE 10# ;MORE TO DO
5264
5265 ; LOAD AND ARM ALL COUNTERS
5266
5267 033646 012761 177537 000002 MOV #C!LOA!37,MREA(R1) ;START ALL COUNTER
5268 033654 012761 177477 000002 MOV #C!ARM!37,MREA(R1) ;START ALL COUNTER
5269 033662 000240 NOP ;LET COUNTERS RUN
5270 033664 000240 NOP
5271 033666 000240 NOP
5272 033670 012761 177637 000002 MOV #C!DAS!37,MREA(R1) ;SAVE COUNTERS
5273
5274 ; READ OUT COUNTERS AND SAVE IN CNTVAL TABLE
5275
5276 033676 012704 002452 MOV #CNTVAL,R4 ;SAVE VALUES HERE
5277 033702 012737 000001 002442 MOV #1,NUMBER ;A. NUMBER
5278 033710 012700 177420 20# MOV #B!20,R0 ;T HOLD REGISTER
5279 033714 063700 002442 ADD NUMBER,R0 ;CHANNEL NUMBER
5280 033720 010061 000002 MOV R0,MREA(R1) ;ELECT COUNTER
5281 033724 016124 000004 MOV MREB(R1),(R4)+ ;GET COUNTER VALUE
5282 033730 005237 002442 INC NUMBER ;BUMP CHANNEL NUMBER
5283 033734 023727 002442 000005 CMP NUMBER,#5 ;ALL CHANNELS?
5284 033742 003762 BLE 20# ;MORE TO DO
5285

```

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TEST 6 -- REFERENCE FREQUENCY TEST

```

5286                                     ; COMPUTE MIN AND MAX VALUES OF ALL COUNTERS
5287                                     ;
5288 033744 012702 002452                MOV    #CNTVAL,R2                ;
5289 033750 012703 000005                MOV    #5,R3                    ;LOAD LOOP COUNT
5290 033754 012700 023420                MOV    #10000.,R0                ;MIN
5291 033760 005004                        CLR    R4                        ;MAX
5292 033762 020012 304:                   CMP    R0,(R2)                  ;LOAD NEW MINIMUM?
5293 033764 003401                        BLE    404                      ;NO
5294 033766 011200                        MOV    (R2),R0                  ;LOAD NEW MINIMUM
5295 033770 020412 404:                   CMP    R4,(R2)                  ;NEW MAXIMUM?
5296 033772 003001                        BGT    504                      ;NO
5297 033774 011204                        MOV    (R2),R4                  ;NEW MAXIMUM
5298 033776 005722 504:                   TST    (R2)+                    ;BUMP POINTER
5299 034000 005303                        DEC    R3                        ;
5300 034002 001367                        BNE    304                      ;LOOP TO FIND MAX & MIN
5301                                     ;
5302                                     ; SEE IF THE COUNTER VALUES WERE IN EXPECTED RANGE
5303                                     ;
5304 034004 020427 023420                CMP    R4,#10000.                ;DID COUNTER MOVE?
5305 034010 002015                        BGE    704                      ;STRANGE VALUES
5306 034012 005700                        TST    R0                        ;ROUGH TEST
5307 034014 003413                        BLE    704                      ;
5308 034016 010402                        MOV    R4,R2                    ;GET DIFFERENCE (MAX-MIN)
5309 034020 160002                        SUB    R0,R2
5310 034022 020227 000002                CMP    R2,#2                    ;ALLOWABLE DIFFERENCE
5311 034026 004737 015240                CALL   INSERT                    ;SKIP BRANCH IF "SFI" IS SET
5312 034032 003404                        BLE    704                      ;BRANCH IF YES (LESS THEN 3)
5313 034034                        ERRHRD  601.,ERR130
5314 034044 005737 002164 704:           TST    QVP                    ;IS QUICK VERIFY PASS SELECTED
5315 034050 001010                        BNE    EXS6                    ;YES
5316 034052 005237 002504                INC    ITRCNT                    ;BUMP ITERATION COUNT
5317 034056 023737 002506 002504        CMP    ITRDEF,ITRCNT                ;DEFAULT ITERATION COUNT
5318 034064 001402                        BEQ    EXS6                    ;TIME TO EXIT
5319 034066 000137 033560                JMP    ITRAC6                    ;DO THE TEST AGAIN
5320                                     ;
5321 034072                EXS6:          EXIT    TST
5322                                     ;
5323                                     ;.NLIST BEX
5324 034076      045      123      062 TSHD6:..ASCIZ /#S2#AREFERENCE FREQUENCY TEST #N/
5325                                     ;.LIST BEX
5326                                     ;.EVEN
5327                                     ;
5328 034140                ENDTST
5329

```

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TEST 7 -- SIGNAL GENERATION ON COUNTER INPUT

```

5331 .SBTTL TEST 7 -- SIGNAL GENERATION ON COUNTER INPUT
5332 ;*****
5333 ; TEST 7 -- SIGNAL GENERATION ON COUNTER INPUT
5334 ;
5335 ; FOR TEST DESCRIPTION SEE 6.0 TEST SUMMERIES
5336 ;*****
5337
5338
5339 .DSABLE LSB
5340 BGNTST
5341 034142 004737 015006 CALL SELECT ;CALL SELECT ROUTINE
5342 034146 000420 .WORD 420 ;TEST SELECT MASK
5343 034150 034576 TSHD7 ;TEST HEADER ADDRESS
5344 034152 103002 BCC 14 ;IF CARRY IS SET, DON'T BRANCH
5345 034154 EXIT TST ;EXIT TEST IF CARRY IS SET
5346 034160 005037 002504 14: CLR ITRCNT ;CLEAR ITERATION COUNTER
5347
5348 034164 013701 002416 MOV MOD,R1 ;GET FIRST REGISTER ADDRESS
5349 034170 004737 021470 JSR PC,AMREST ;RESET THE MODULE
5350 034174 ITRAC7:
5351 034174 012737 000001 002442 MOV #1,NUMBER ;CHANNEL NUMBER
5352 034202 012737 000001 002434 MOV #1,BITMSK
5353 034210 012700 177400 104: MOV #8,R0 ;SELECT MODE REGISTER
5354 034214 063700 002442 ADD NUMBER,R0 ;ADD IN CHANNEL NR
5355 034220 010061 000002 MOV R0,MREA(R1) ;SELECT MODE REGISTER
5356 034224 012761 000050 000004 MOV #000050,MREB(R1) ;SELECT INTERNAL LOOP BACK
5357 034232 012700 177410 MOV #8!10,R0 ;SELECT LOAD REGISTER
5358 034236 063700 002442 ADD NUMBER,R0 ;SELECT CHANNEL
5359 034242 010061 000002 MOV R0,MREA(R1) ;ADDRESS
5360 034246 005061 000004 CLR MREB(R1) ;INIT COUNTER
5361 034252 012700 177500 MOV #C#LOA,R0 ;LOAD
5362 034256 063700 002434 ADD BITMSK,R0 ;ADD IN CHANNEL NR
5363 034262 010061 000002 MOV R0,MREA(R1) ;STARTS COUNTER
5364
5365 034266 BGNSEG ;BEGINNING OF LOOP ON ERROR SEGMENT
5366 034270 013737 002434 002436 MOV BITMSK,BITMS1 ;BUILD SECOND BIT MASK
5367 034276 006237 002436 ASR BITMS1
5368 034302 013702 002442 MOV NUMBER,R2 ;COMPUTE TCN-1
5369 034306 005302 DEC R2
5370 034310 001005 BNE 204
5371 034312 012702 000005 MOV #5,R2 ;WRAP AROUND
5372 034316 012737 177420 002436 MOV #8!20,BITMS1 ;BIT MASK
5373 034324 012700 177400 204: MOV #8,R0 ;SELECT MODE REGISTER
5374 034330 060200 ADD R2,R0 ;ADD IN CHANNEL NR
5375 034332 010061 000002 MOV R0,MREA(R1) ;SELECT MODE REGISTER
5376 034336 012761 005440 000004 MOV #5440,MREB(R1) ;SELECT 5MHZ FREQUENCY
5377 034344 012700 177410 MOV #8!10,R0 ;LOAD AND ARM TAK COUNTER
5378 034350 060200 ADD R2,R0 ;CHANNEL NUMBER
5379 034352 010061 000002 MOV R0,MREA(R1) ;ADDRESS
5380 034356 012761 000003 000004 MOV #3,MREB(R1) ;SETUP COUNTER
5381 034364 012700 177500 MOV #C#LOA,R0
5382 034370 063700 002436 ADD BITMS1,R0
5383 034374 010061 000002 MOV R0,MREA(R1) ;START COUNTER
5384 034400 012700 177440 MOV #C#ARM,R0
5385 034404 063700 002436 ADD BITMS1,R0
5386 034410 063700 002434 ADD BITMSK,R0 ;ADD IN CHANNEL NR
5387 034414 010061 000002 MOV R0,MREA(R1) ;START COUNTER

```

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 TEST 7 -- SIGNAL GENERATION ON COUNTER INPUT

5388	034420	000240				NOP	
5389	034422	000240				NOP	
5390	034424	000240				NOP	
5391	034426	000240				NOP	;LET COUNTER GO
5392	034430	000240				NOP	
5393	034432	012700	177700			MOV	#C#DAC,R0 ;DISARM COUNTER
5394	034436	063700	002436			ADD	BITMS1,R0 ;CHANNEL NUMBER
5395	034442	010061	000002			MOV	R0,MREA(R1)
5396	034446	012700	177600			MOV	#C#DAS,R0 ;DISARM COUNTER
5397	034452	063700	002434			ADD	BITMSK,R0 ;CHANNEL NUMBER
5398	034456	010061	000002			MOV	R0,MREA(R1)
5399							
5400	034462	012700	177420			MOV	#B!20,R0 ;HOLD REGISTER
5401	034466	063700	002442			ADD	NUMBER,R0
5402	034472	010061	000002			MOV	R0,MREA(R1)
5403	034476	016103	000004			MOV	MREB(R1),R3
5404	034502	004737	015240			CALL	INSERT
5405	034506	001005				BNE	11\$
5406	034510					ERRHRD	701,,ERR115
5407	034520					CKLOOP	
5408	034522					ENDSEG	
5409	034524	006337	002434			ASL	BITMSK
5410	034530	005237	002442			INC	NUMBER
5411	034534	023727	002442	000005		CMP	NUMBER,#5
5412	034542	003622				BLE	10\$
5413							
5414	034544	005737	002164			TST	QVP ;IS QUICK VERIFY PASS SELECTED
5415	034550	001010				BNE	EXS7 ;YES
5416	034552	005237	002504			INC	ITRCNT ;BUMP ITERATION COUNT
5417	034556	023737	002506	002504		CMP	ITRDEF,ITRCNT ;DEFAULT ITERATION COUNT
5418	034564	001402				BEQ	EXS7 ;TIME TO EXIT
5419	034566	000137	034174			JMP	ITRAC7 ;DO THE TEST AGAIN
5420							
5421	034572				EXS7:	EXIT	TST
5422							
5423						.NLIST	BEX
5424	034576	045	123	062	TSHD7::	.ASCIZ	/#S2#ASIGNAL GENERATION ON COUNTER INPUT #N/
5425						.LIST	BEX
5426						.EVEN	
5427							
5428	034652					ENDTST	
5429							

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 TEST 8 -- EXTERNAL LOOPBACK, NOT ISOLATED

```

S431 .SBTTL TEST 8 -- EXTERNAL LOOPBACK, NOT ISOLATED
S432 ;*****
S433 ; TEST 8 -- EXTERNAL LOOPBACK, NOT ISOLATED
S434 ;
S435 ; FOR TEST DESCRIPTION SEE 6.0 TEST SUMMARIES
S436 ;*****
S437
S438 .DSABLE LSB
S439 034654 BGNST
S440 034654 004737 015006 CALL SELECT ;CALL SELECT ROUTINE
S441 034660 000420 .WORD 420 ;TEST SELECT MASK
S442 034662 035270 TSHDB ;TEST HEADER ADDRESS
S443 034664 103002 BCC 14 ;IF CARRY IS SET, DON'T BRANCH
S444 034666 EXIT TST ;EXIT TEST IF CARRY IS SET
S445 034672 005037 002504 14: CLR ITRCNT ;CLEAR ITERATION COUNTER
S446
S447 034676 013701 002416 MOV MOD,R1 ;GET FIRST REGISTER ADDRESS
S448 034702 004737 021470 JSR PC,AMREST ;RESET THE MODULE
S449 034706 012761 177427 000002 MOV #B!27,MREA(R1) ;SELECT FREQ OUTPUT CNTRL REG
S450 034714 012761 061000 000004 MOV #61000,MREB(R1) ;SELECT 5MH LOOPBACK FREQUENCY
S451 034722
S452 034722 012737 000001 002442 ITRAC8: MOV #1,NUMBER ;CHANNEL NUMBER
S453 034730 012737 000001 002434 MOV #1,BITMSK
S454 034736 012737 000400 002464 104: MOV #400,SRC ;INIT SOURCE
S455 034744 012737 000012 002466 MOV #10,LOOP ;SETUP LOOP COUNT
S456 034752 204: BGNSEG ;BEGINNING OF LOOP ON ERROR SEGMENT
S457 034754 012700 177400 MOV #B,RO ;SELECT MODE REGISTER
S458 034760 063700 002442 ADD NUMBER,RO ;ADD IN CHANNEL NR
S459 034764 010061 000002 MOV RO,MREA(R1) ;SELECT MODE REGISTER
S460 034770 012700 000010 MOV #10,RO ;COUNT UP
S461 034774 063700 002464 ADD SRC,RO ;SOURCE OR GATE
S462 035000 010061 007004 MOV RO,MREB(R1) ;SELECT SOURCE
S463 035004 012700 177410 MOV #B!10,RO ;SELECT LOAD REGISTER
S464 035010 063700 002442 ADD NUMBER,RO ;SELECT CHANNEL
S465 035014 010061 000002 MOV RO,MREA(R1) ;ADDRESS
S466 035020 005061 000004 CLR MREB(R1) ;INIT COUNTER
S467 035024 012700 177500 MOV #C!LOA,RO ;LOAD
S468 035030 063700 002434 ADD BITMSK,RO ;ADD IN CHANNEL NR
S469 035034 010061 000002 MOV RO,MREA(R1) ;STARTS COUNTER
S470 035040 012700 177440 MOV #C!ARM,RO ;ARM
S471 035044 063700 002434 ADD BITMSK,RO ;ADD IN CHANNEL NR
S472 035050 010061 000002 MOV RO,MREA(R1) ;STARTS COUNTER
S473
S474 035054 000240 NOP ;LET COUNTER GO
S475 035056 000240 NOP
S476 035060 000240 NOP
S477
S478 035062 012700 177600 MOV #C!DAS,RO ;DISARM COUNTER
S479 035066 063700 002434 ADD BITMSK,RO ;CHANNEL NUMBER
S480 035072 010061 000002 MOV RO,MREA(R1)
S481
S482 035076 012700 177420 MOV #B!20,RO ;HOLD REGISTER
S483 035102 063700 002442 ADD NUMBER,RO
S484 035106 010061 000002 MOV RO,MREA(R1) ;COUNTER -> HOLD REGISTER
S485 035112 016103 000004 MOV MREB(R1),R3
S486 035116 004737 015240 CALL INSERT ;TEST IF ERROR MESSAGE IS REQUIRED
S487 035122 001026 BNE 504 ;DID CLOCK TICK

```


HARDWARE TESTS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 59 1
 TEST 8 -- EXTERNAL LOOPBACK, NOT ISOLATED

5488	035124	013702	002464		MOV	SRC,R2	;SOURCE OR GATE
5489	035130	000302			SWAB	R2	;GET TO LOW ORDER
5490	035132	042702	177740		BIC	#177740,R2	;CLEAR OTHER BITS
5491	035136	020227	000005		CMF	R2,#5	;SOURCE
5492	035142	004737	015240		CALL	INSERT	;TEST IF ERROR MESSAGE IS REQUIRED
5493	035146	003005			BGT	304	;NO, GATE
5494	035150				ERRHRD	801,,ERR116	;ERROR HANDLER
5495	035160	000402			BR	404	
5496	035162	162702	000005	304:	SUB	#5,R2	
5497	035166			404:	ERRHRD	802,,ER116A	
5498	035176				CKLOOP		;IF LOOP ON ERROR
5499	035200			504:	ENDSEG		;END SEGMENT
5500							
5501	035202	062737	000400	002464	ADD	#400,SRC	;BUMP TO NEXT SOURCE
5502	035210	005337	002466		DEC	LOOP	;ADJUST LOOP COUNT
5503	035214	001256			BNE	204	;LOOP FOR NEXT SOURCE
5504							
5505	035216	006337	002434		ASL	BITHSK	;SHIFT TO NEXT BIT
5506	035222	005237	002442		INC	NUMBER	;BUMP CHANNEL NUMBER
5507	035226	023727	002442	000005	CMF	NUMBER,#5	;ALL CHANNELS?
5508	035234	003640			BLE	104	;MORE TO DO
5509							
5510	035236	005737	002164		TST	QVP	;IS QUICK VERIFY PASS SELECTED
5511	035242	001010			BNE	EXS8	;YES
5512	035244	005237	002504		INC	ITRCNT	;BUMP ITERATION COUNT
5513	035250	023737	002506	002504	CMF	ITRDEF,ITRCNT	;DEFAULT ITERATION COUNT
5514	035256	001402			BEO	EXS8	;TIME TO EXIT
5515	035260	000137	034722		JMP	ITRAC8	;DO THE TEST AGAIN
5516							
5517	035264			EXS8:	EXIT	TST	
5518							
5519					.NLIST	BEX	
5520	035270	045	123	062	TSHD8::ASCIZ	/#S2#EXTERNAL LOOPBACK TEST, NOT ISOLATED#N/	
5521					.LIST	BEX	
5522					.EVEN		
5523							
5524	035344				ENDTST		
5525							

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 TEST 9 -- EXTERNAL LOOPBACK, ISOLATED

```

5527 .SBTTL TEST 9 -- EXTERNAL LOOPBACK, ISOLATED
5528 ;*****
5529 ; TEST 9 -- EXTERNAL LOOPBACK, ISOLATED
5530 ;
5531 ; FOR TEST DESCRIPTION SEE 6.0 TEST SUMMERIES
5532 ;*****
5533
5534 .DSABLE LSB
5535 BGNST
5536 035346 004737 015006 CALL SELECT ;CALL SELECT ROUTINE
5537 035352 000420 .WORD 420 ;TEST SELECT MASK
5538 035354 036110 TSHD9 ;TEST HEADER ADDRESS
5539 035356 103002 BCC 14 ;IF CARRY IS SET, DON'T BRANCH
5540 035360 EXIT TST ;EXIT TEST IF CARRY IS SET
5541 035364 005037 002504 14: CLR ITRCNT ;CLEAR ITERATION COUNTER
5542
5543 035370 013701 002416 MOV MOD,R1 ;GET FIRST REGISTER ADDRESS
5544 035374 004737 021470 JSR PC,AMREST ;RESET THE MODULE
5545 035400 012761 177427 000002 MOV #B:27,MREA(R1) ;SELECT FREQ OUTPUT CNTRL REG
5546 035406 012761 070000 000004 MOV #70000,MREB(R1) ;SELECT SM LOOPBACK FREQ. FOUT=OFF
5547 035414 ITRAC9:
5548 035414 012737 000001 002442 MOV #1,NUMBER ;CHANNEL NUMBER (FOR INC)
5549 035422 012737 000001 002434 MOV #1,BITMSK ;CHANNEL NUMBER (FOR SHIFT)
5550 035430 012737 000400 002464 MOV #400, SRC ;INIT SOURCE
5551
5552 ;SETUP FIRST COUNTER FOR 100 KHZ GENERATION ON COUNTER OUTPUT.
5553
5554 035436 012700 177400 104: MOV #B,R0 ;GET MODE REGISTER MASK
5555 035442 063700 002442 ADD NUMBER,R0 ;ADD IN CHANNEL NR
5556 035446 010061 000002 MOV R0,MREA(R1) ;SELECT MODE REGISTER
5557 035452 012761 005442 000004 MOV #5442,MREB(R1) ;LOAD TOGGLE AT 5 MHZ
5558 035460 012700 177410 MOV #B:10,R0 ;GET LOAD REGISTER MASK
5559 035464 063700 002442 ADD NUMBER,R0 ;ADD IN CHANNEL NR
5560 035470 010061 000002 MOV R0,MREA(R1) ;SELECT LOAD REGISTER
5561 035474 012761 000031 000004 MOV #25.,MREB(R1) ;LOAD COUNT VALUE
5562
5563 ; SELECT SECOND COUNTER
5564
5565 035502 013737 002434 002436 MOV BITMSK,BITMS1 ;NEXT COUNTER
5566 035510 006337 002436 ASL BITMS1 ;NEXT COUNTER
5567 035514 013702 002442 MOV NUMBER,R2 ;USE N+1
5568 035520 005202 INC R2 ;...
5569 035522 020227 000005 CMP R2,#5 ;TEST FOR WRAP AROUND (ALL CNT DONE?)
5570 035526 003405 BLE 204 ;BRANCH IF NOT
5571 035530 012702 000001 MOV #1,R2 ;START OVER WITH FIRST COUNTER
5572 035534 012737 000001 002436 MOV #1,BITMS1 ;...
5573
5574 035542 012704 000002 204: MOV #2,R4 ;DO TWICE
5575 035546 304: BGNSEG ;BEGINNING OF LOOP ON ERROR SEGMENT
5576 035550 012700 177400 MOV #B,R0 ;GET MODE REGISTER MASK
5577 035554 060200 ADD R2,R0 ;ADD IN CHANNEL NR
5578 035556 010061 000002 MOV R0,MREA(R1) ;SELECT MODE REGISTER
5579 035562 012700 000050 MOV #50,R0 ;GET COUNT UP MASK
5580 035566 063700 002464 ADD SRC,R0 ;SOURCE OR GATE
5581 035572 010061 000004 MOV R0,MREB(R1) ;SELECT COUNT UP
5582 035576 012700 177410 MOV #B:10,R0 ;GET LOAD REGISTER MASK
5583 035602 060200 ADD R2,R0 ;ADD IN CHA. NUMBER

```


HARDWARE TESTS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 60-1
 TEST 9 -- EXTERNAL LOOPBACK, ISOLATED

5584	035604	010061	000002	MOV	R0,MREA(R1)	;AND LOAD IT
5585	035610	005061	000004	CLR	MPEB(R1)	;INIT COUNTER
5586	035614	012700	177500	MOV	#C#LOA,R0	;GET LOAD COMMAND
5587	035620	063700	002436	ADD	BITMS1,R0	;ADD IN CHANNEL NR
5588	035624	010061	000002	MOV	R0,MREA(R1)	;READY TO START SECOND COUNTER
5589						
5590	035630	012700	177500	MOV	#C#LOA,R0	;GET LOAD MASK
5591	035634	063700	002434	ADD	BITMSK,R0	;ADD IN CHANNEL NR
5592	035640	010061	000002	MOV	R0,MREA(R1)	;READY TO START FIRST COUNTER
5593						
5594						
5595						
5596	035644	012700	177440	MOV	#C#ARM,R0	;GET ARM COMMAND
5597	035650	063700	002434	ADD	BITMSK,R0	;ADD IN COUNTER NR
5598	035654	063700	002436	ADD	BITMS1,R0	;ADD IN SECOND COUNTER NR
5599	035660	010061	000002	MOV	R0,MREA(R1)	;START BOTH COUNTER (LOAD ARM)
5600						
5601	035664	012700	000144	MOV	#100.,R0	;LET COUNTER RUN AT 100 KZ
5602	035670	000240		404: NOP		
5603	035672	000240		NOP		;LET COUNTER GO
5604	035674	000240		NOP		
5605	035676	005300		DEC	R0	;SHORT LOOP COUNT
5606	035700	001373		BNE	404	
5607						
5608						
5609						
5610	035702	012700	177600	MOV	#C#DAS,R0	;GET DISARM COMMAND
5611	035706	063700	002434	ADD	BITMSK,R0	;ADD IN COUNTER NR
5612	035712	063700	002436	ADD	BITMS1,R0	;ADD IN SECOND COUNTER NUMBER
5613	035716	010061	000002	MOV	R0,MREA(R1)	;STOP BOTH COUNTER (DISARM)
5614						
5615						
5616						
5617	035722	012700	177420	MOV	#B#20,R0	;HOLD REGISTER
5618	035726	060200		ADD	R2,R0	;ADD CNT NUMBER
5619	035730	010061	000002	MOV	R0,MREA(R1)	;COUNTER -> HOLD REGISTER
5620	035734	016103	000004	MOV	MREB(R1),R3	;GET COUNTER CONTENTS
5621	035740	004737	015240	CALL	INSERT	;TEST IF ERROR MESSAGE IS REQUIRED
5622	035744	001024		BNE	604	;DID CLOCK TICK
5623	035746	013703	002464	MOV	SRC,R3	;SOURCE OR GATE
5624	035752	000303		SWAB	R3	;GET TO LOW ORDER
5625	035754	042703	177740	BIC	#177740,R3	;CLEAR OTHER BITS
5626	035760	020327	000005	CMF	R3,#5	;SOURCE
5627	035764	003006		BGT	504	;NO, GATE
5628	035766			ERRHRD	901.,ER116B	;ERROR HANDLER FOR SOURCE
5629	035776			CKLOOP		;IF LOOP ON ERROR
5630	036000	000406		BR	604	
5631	036002	162703	000005	504: SUB	#5,R3	
5632	036006			ERRHRD	902.,ER116C	;ERROR HANDLER FOR GATE
5633	036016			604: ENDSEG		;END SEGMENT
5634						
5635	036020	062737	000400 002464	ADD	#400,SRC	;BUMP TO NEXT SOURCE
5636	036026	005304		DEC	R4	;ADJUST LOOP COUNT
5637	036030	001246		BNE	304	;LOOP FOR NEXT SOURCE
5638						
5639	036032	006337	002434	ASL	BITMSK	;SHIFT TO NEXT COUNTER
5640	036036	005237	002442	INC	NUMBER	;BUMP CHANNEL COUNTER

HARDWARE TESTS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 60-2
 TEST 9 -- EXTERNAL LOOPBACK, ISOLATED

5641	036042	023727	002442	000005		CMP	NUMBER, #5		;ALL COUNTERS DONE ?
5642	036050	003002				BGT	704		;EXIT IF YES
5643	036052	000137	035436			JMP	104		;MORE TO DO
5644									
5645	036056	005737	002164		704:	TST	QVP		;IS QUICK VERIFY PASS SELECTED
5646	036062	001010				BNE	EXS9		;YES
5647	036064	005237	002504			INC	ITRCNT		;BUMP ITERATION COUNT
5648	036070	023737	002506	002504		CMP	ITRDEF, ITRCNT		;DEFAULT ITERATION COUNT
5649	036076	001402				BEQ	EXS9		;TIME TO EXIT
5650	036100	000137	035414			JMP	ITRAC9		;DO THE TEST AGAIN
5651									
5652	036104				EXS9:	EXIT	TST		
5653									
5654						.NLIST	BEX		
5655	036110	045	123	062	TSHD9::	.ASCIZ	/S2#AEXTERNAL LOOPBACK TEST, ISOLATED.#N/		
5656						.LIST	BEX		
5657						.EVEN			
5658									
5659	036160					ENDTST			

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 TEST 10 UP/DOWN COUNTING APPLICATION TEST

```

5661 .SBTTL TEST 10 -- UP/DOWN COUNTING APPLICATION TEST
5662 ;*****
5663 ; TEST 10 - UP/DOWN COUNTING APPLICATION TEST
5664 ;
5665 ; FOR TEST DESCRIPTION SEE 6.0 TEST SUMMERIES
5666 ;*****
5667 036162 BGNTST
5668 036162 004737 015006 CALL SELECT ;CALL SELECT ROUTINE
5669 036166 000420 .WORD 420 ;TEST SELECT MASK
5670 036170 037122 TSHD10 ;TEST HEADER ADDRESS
5671 036172 103002 BCC 11 ;IF CARRY IS SET, DON'T BRANCH
5672 036174 EXIT TST ;EXIT TEST IF CARRY IS SET
5673 036200 012737 000004 002504 14: MOV #4,ITRCNT ;ITERATION COUNTER
5674 036206 005037 000000G CLR WORFLA ;CLEAR WORKING FLAG
5675 036212 013701 002416 MOV MOD,R1 ;GET FIRST REGISTER ADDRESS
5676 036216 004737 021470 JSR PC,AMREST ;RESET THE MODULE
5677 036222 ITR10:
5678 036222 012761 177427 000002 MOV #8:27,MREA(R1) ;SELECT FREQ OUTPUT CNTRL REG
5679 036230 012761 070000 000004 MOV #70000,MREB(R1) ;FOUT OFF
5680 036236 012761 177403 000002 MOV #8:3,MREA(R1) ;SELECT MODE REGISTER
5681 036244 012761 000042 000004 MOV #42,MREB(R1) ;OUT TOGGLE
5682 036252 012761 177404 000002 MOV #8:4,MREA(R1) ;SELECT MODE REGISTER
5683 036260 012761 000042 000004 MOV #42,MREB(R1) ;OUT TOGGLE
5684 036266 012761 177401 000002 MOV #8:1,MREA(R1) ;SELECT MODE REG
5685 036274 012761 042450 000004 MOV #42450,MREB(R1) ;ACTIVE HIGH LEVEL, GATE 2, SRC 5
5686 036302 004537 037022 JSR R5,SETHRA ;SET & TEST MREA
5687 036306 000344 .WORD 344 ;TOUT N = 4
5688 036310 004537 037022 JSR R5,SETHRA ;SET & TEST MREA
5689 036314 000343 .WORD 343 ;TOUT N = 3
5690 036316 012761 177411 000002 MOV #8:11,MREA(R1) ;SELECT 1 LOAD REGISTER
5691 036324 005061 000004 CLR MREB(R1) ;INIT COUNTER
5692 036330 012761 177402 000002 MOV #8:2,MREA(R1) ;SELECT MODE REG
5693 036336 012761 122450 000004 MOV #122450,MREB(R1) ;ACTIVE LOW, GATE 2, SRC 5
5694 036344 012761 177412 000002 MOV #8:12,MREA(R1) ;SELECT 2 LOAD REGISTER
5695 036352 005061 000004 CLR MREB(R1) ;INIT COUNTER
5696 036356 012703 023420 MOV #10000.,R3 ;LOOP COUNT
5697 036362 012761 177503 000002 MOV #C#LOA:3,MREA(R1) ;STARTS COUNTER
5698 036370 012761 177443 000002 MOV #C#ARM:3,MREA(R1) ;STARTS COUNTER
5699 036376 004537 037022 104: JSR R5,SETHRA ;SET & TEST MREA
5700 036402 000354 .WORD 354 ;SET TOUT N = 4
5701 036404 004537 037022 JSR R5,SETHRA ;SET & TEST MREA
5702 036410 000353 .WORD 353 ;SET TOUT N = 3
5703 036412 004537 037022 JSR R5,SETHRA ;SET & TEST MREA
5704 036416 000344 .WORD 344 ;CLEAR TOUT N = 4
5705 036420 004537 037022 JSR R5,SETHRA ;SET & TEST MREA
5706 036424 000343 .WORD 343 ;CLEAR TOUT N = 3
5707 036426 BREAK
5708 036430 077316 SOB R3,104 ;LOOP
5709
5710 036432 012761 177603 000002 MOV #C#DAS:3,MREA(R1) ;DISARM AND SAVE COUNTERS
5711 036440 012761 177421 000002 MOV #8:21,MREA(R1) ;COUNTER -> HOLD REGISTER #1
5712 036446 016105 000004 MOV MREB(R1),R5
5713 036452 020527 023420 CMP R5,#10000.
5714 036456 004737 015240 CALL INSERT ;TEST IF ERROR MESSAGE IS REQUIRED
5715 036462 001404 BEQ 204 ;IF EXPECTED NUMBER OF COUNTS
5716 036464 ERRHRD 1001.,ERR120 ;ERROR HANDLER
5717

```

HARDWARE TESTS MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 61-1
TEST 10 -- UP/DOWN COUNTING APPLICATION TEST

5718	036474	012761	177422	000002	204:	MOV	#B!22,MREA(R1)	;SELECT HOLD REGISTER #2
5719	036502	016105	000004			MOV	MREB(R1),R5	
5720	036506	005705				TST	R5	;OTHER SHOULD HAVE NOT COUNTED
5721	036510	004737	015240			CALL	INSERT	;TEST IF ERROR MESSAGE IS REQUIRED
5722	036514	001404				BEQ	304	;IF EXPECTED NUMBER OF COUNTS
5723	036516					ERRHRD	1002,,ERR121	;ERROR HANDLER
5724								
5725								
5726								
5727	036526	012761	177411	000002	304:	MOV	#B!11,MREA(R1)	;SELECT 1 LOAD REGISTER
5728	036534	005061	000004			CLR	MREB(R1)	;INIT COUNTER
5729	036540	012761	177412	000002		MOV	#B!12,MREA(R1)	;SELECT 2 LOAD REGISTER
5730	036546	005061	000004			CLR	MREB(R1)	;INIT COUNTER
5731	036552	012703	023420			MOV	#10000.,R3	;LOOP COUNT
5732	036556	004537	037022			JSR	R5,SETHRA	;SET & TEST MREA
5733	036562	000344				.WORD	344	;CLEAR TOUT N = 4
5734	036564	004537	037022			JSR	R5,SETHRA	;SET & TEST MREA
5735	036570	000343				.WORD	343	;CLEAR TOUT N = 3
5736	036572	012761	177503	000002		MOV	#C!LOA!3,MREA(R1)	;STARTS COUNTER
5737	036600	012761	177443	000002		MOV	#C!ARM!3,MREA(R1)	;STARTS COUNTER
5738	036606	004537	037022		404:	JSR	R5,SETHRA	;SET & TEST MREA
5739	036612	000353				.WORD	353	;SET TOUT N = 3
5740	036614	004537	037022			JSR	R5,SETHRA	;SET & TEST MREA
5741	036620	000354				.WORD	354	;SET TOUT N = 4
5742	036622	004537	037022			JSR	R5,SETHRA	;SET & TEST MREA
5743	036626	000343				.WORD	343	;CLEAR TOUT N = 3
5744	036630	004537	037022			JSR	R5,SETHRA	;SET & TEST MREA
5745	036634	000344				.WORD	344	;CLEAR TOUT N = 4
5746	036636	077315				SQB	R3,404	
5747								
5748	036640	012761	177603	000002		MOV	#C!DAS!3,MREA(R1)	;DISARM AND SAVE COUNTERS
5749	036646	012761	177421	000002		MOV	#B!21,MREA(R1)	;COUNTER -> HOLD REGISTER #1
5750	036654	016105	000004			MOV	MREB(R1),R5	
5751	036660	005705				TST	R5	;OTHER SHOULD HAVE NOT COUNTED
5752	036662	004737	015240			CALL	INSERT	;TEST IF ERROR MESSAGE IS REQUIRED
5753	036666	001404				BEQ	504	;DID CLOCK TICK
5754	036670					ERRHRD	1003,,ERR122	;ERROR HANDLER
5755	036700				504:			
5756	036700	012761	177422	000002		MOV	#B!22,MREA(R1)	;COUNTER -> HOLD REGISTER #1
5757	036706	016105	000004			MOV	MREB(R1),R5	
5758	036712	020527	023420			CMF	R5,#10000.	
5759	036716	004737	015240			CALL	INSERT	;TEST IF ERROR MESSAGE IS REQUIRED
5760	036722	001404				BEQ	604	;DID CLOCK TICK
5761	036724					ERRHRD	1004,,ERR123	;ERROR HANDLER
5762								
5763	036734	005737	002164		604:	IST	QVP	;IS QUICK VERIFY PASS SELECTED
5764	036740	001026				BNE	EXS10	;YES
5765	036742					BREAK		
5766	036744	005737	000000G			TST	WORFLA	;IS WORKING PRINTED ONCE ?
5767	036750	001012				BNE	1204	;BRANCH IF YES
5768	036752					PRINTF	#WOR	;PRINT TEST IS WORKING
5769	036772	005237	000000G			INC	WORFLA	
5770	036776	005237	002504		1204:	INC	ITRCNT	;BUMP ITERATION COUNT
5771	037002	023737	002506	002504		CMF	ITRDEF,ITRCNT	;DEFAULT ITERATION COUNT
5772	037010	001402				BEQ	EXS10	;TIME TO EXIT
5773	037012	000137	036222			JMP	ITR10	;DO THE TEST AGAIN
5774								

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 TEST 10 -- UP/DOWN COUNTING APPLICATION TEST

```

5775 037016          EXS10: EXIT   TST
5776
5777 037022 011500    SETMRA: MOV   (R5),R0
5778 037024 052700 177400      BIS   #0,R0          ;SET HIGH ORDER BYTE
5779 037030 010061 000002      MOV   R0,MREA(R1)      ;SET OUTPUT
5780 037034 016100 000002      MOV   MREA(R1),R0      ;GET VALUE BACK
5781 037040 011502      MOV   (R5),R2
5782 037042 042702 177770      BIC   #177770,R2      ;GET NUMBER
5783 037046 011546      MOV   (R5),-(SP)              ;WHAT TYPE OF COMMAND
5784 037050 042716 177407      BIC   #177407,(SP)      ;GET COMMAND
5785 037054 022627 000350      CMP   (SP),#350         ;SET?
5786 037060 001004      BNE   1#                      ;NO
5787 037062 136200 037113      BITB  BITMAP-1(R2),R0    ;IS SOURCE SET?
5788 037066 001010      BNE   10#                     ;YES, AS IT SHOULD BE
5789 037070 000403      BR     3#                      ;ERROR
5790 037072 136200 037113      1#:  BITB  BITMAP-1(R2),R0 ;OUTPUT CLEARED?
5791 037076 001404      BEQ   10#                     ;YES, AS IT SHOULD BE
5792 037100      3#:  ERRHRD 1005,,ERR124              ;ERROR HANDLER
5793 037110 005725      10#:  TST   (R5),#
5794 037112 000205      RTS    R5
5795
5796 037114 002 004 010 BITMAP: .BYTE 2,4,10,20,40
5797 037117 020 040      .EVEN
5798
5799      .NLIST BEX
5800 037122 045 123 062 TSHD10: .ASCIZ /#S2#AUP\DOWN COUNTING APPLICATION TEST #N/
5801 037174 045 116 045 WOR:  .ASCIZ /#N#AWORKING#N/
5802      .LIST BEX
5803      .EVEN
5804
5805 037212      ENDTST

```

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TEST 11: Visual LED Test - Specifically Selectable.

```

5807 .SBTTL TEST 11: Visual LED Test - Specifically Selectable.
5808 ;*****
5809 ; Test 11 - Visual LED Test - Specifically Selectable.
5810 ;
5811 ; FOR TEST DESCRIPTION SEE 6.0 TEST SUMMERIES
5812 ;*****
5813
5814 037214 BGNTST
5815 037214 004737 015006 CALL SELECT ; CALL THE SELECT ROUTINE
5816 037220 010377 .WORD 10377 ; SPECIFIC TEST, ALL DEVICES
5817 037222 037350 TSHD11 ; TEST HEADER ADDRESS
5818 037224 103002 BCC 10$ ; BRANCH IF THE TEST IS SELECTED
5819 037226 EXIT TST ; OTHERWISE, EXIT THE TEST
5820
5821 037232 005001 10$: CLR R1 ; START BY SWITCHING LEDS ON
5822
5823 037234 062701 000100 20$: ADD #100,R1 ; IF BIT 6 IS CLEAR, SET IT
5824 037240 042701 000200 BIC #200,R1 ; IF IT IS SET, CLEAR IT
5825
5826 037244 005037 002074 CLR L$LUN ; START WITH UNIT 0
5827 037250 013703 002074 30$: MOV L$LUN,R3 ; FORM OFFSET FOR PARAMETER ADDRESS
5828 037254 006303 ASL R3 ;
5829 037256 016302 002554 MOV GPADD(R3),R2 ; GET PARAMETER ADDRESS IN R2
5830 037262 001006 BNE 35$ ; IF ADDRESS IS SET UP, BRANCH
5831 037264 GPWARD L$LUN,R2 ; ELSE, GET PARAM ADDRESS FROM DRS
5832 037274 010263 002554 MOV R2,GPADD(R3) ; AND SAVE IT IN THE TABLE
5833 037300 005712 35$: TST (R2) ; MODE ADDRESS = 0 ?
5834 037302 001002 BNE 40$ ; IF NOT, BRANCH
5835 037304 004737 015306 JSR PC,CONFIG ; ELSE DO AUTO CONFIGURATION FOR THIS UNIT
5836 037310 010172 000000 40$: MOV R1,R(R2) ; SWITCH THE LED OVER
5837 037314 005237 002074 INC L$LUN ; GO TO NEXT UNIT
5838 037320 023737 002074 002012 CMP L$LUN,L$UNITS ; ALL CHANGED ?
5839 037326 002750 BLT 30$ ; IF NOT, SWITCH OVER THE NEXT
5840
5841 037330 012703 000024 MOV #20.,R3 ; WAIT FOR 0.5 SECONDS
5842 037334 004737 014600 50$: JSR PC,WT25M ; 25 MILLISECOND WAIT ROUTINE
5843 037340 005303 DEC R3 ; * 20 = 0.5 SECONDS
5844 037342 001374 BNE 50$ ; GO ON IF 0.5 SEC. ARE OVER
5845 037344 BREAK ; ALLOW OPERATOR INPUT
5846 037346 000732 BR 20$ ; DO IT ALL AGAIN
5847
5848
5849 037350 045 123 062 TSHD11: .NLIST BEX
5850 .ASCIZ /#S2#AVISUAL LED TEST - SPECIFICALLY SELECTABLE.#N/
5851 .LIST BEX
5852 .EVEN
5853 037432 ENDTST
5854
5855 037434 ENDMOD
5856

```


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TEST 11: Visual LED Test - Specifically Selectable.

```
5860          .TITLE CLOSE SECTION
5871
5900          BGNMOD
5901 037434
5902
5903 037434      $PATCH::
5904 037434          .BLKW    500
5905
5912
5914 040634          .BLKB    400-< .E377>          ; SHIFT TO CORRECT FOR LSI BUG
5916 041000          LASTAD
5917 041004      L$LAST::
                    ENDMOD
```

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TEST 11: Visual LED Test - Specifically Selectable.

5919			
5920			
5921			
5934			
5935	041004		BGNSETUP 16.
5936		000020	.REPT 16.
5937			BGNPTAB
5938			.WORD 0,0,0,0,0,0,0,0
5939			ENDPTAB
5940			.ENDR
5941	041504		ENDSETUP
5942		000001	
		.END	

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Symbol table

ACFLG	026212	CON	022516	C#GETW=	000027	EF.NEW=	000035 G	E104	010732
ADCON	017462 G	CONEX	016452	C#GMAN=	000043	EF.PWR=	000034 G	E105	011007
ADR	= 000020 G	CONFIG	015306 G	C#GPHR=	000042	EF.RES=	000037 G	E106	011062
AI	017200	CONMSK	002702 G	C#GPLO=	000030	EF.STA=	000040 G	E106A	011140
AMREST	021470 G	CONPRI	015656 G	C#GPRI=	000040	ELEMNT	002450 G	E107	011201
ANS	002502 G	CONT	025410	C#INIT=	000011	EMG1	010336	E107A	011257
AO	017211	CJUNTR	002444 G	C#INLP=	000020	EMG2	010405	E108	011321
ASK	022026	CO1	015576	C#IN1 =	177750 G	EMG3	010455	E109	011373
ASKADD	022742	CP1	016466	C#IN2 =	177757 G	EMG4	010542	E110	011443
ASSEMB=	000010	CP1A	016537	C#LOA =	177500 G	END	026202	E111	011516
B	= 177400 G	CP1B	016611	C#MANI=	000050	ERRBLK	005720 G	E112	011567
BAD	002476 G	CP1C	016707	C#MAR =	177777 G	ERRMSG	005716 G	E113	011641
BADIV	022004 G	CP10	017110	C#MEM =	000031	ERRNBR	005714 G	E114	011713
BITHAP	037114	CP10A	017125	C#MSG =	000023	ERRTYP	005712 G	E114A	012000
BITHSK	002434 G	CP11	017143	C#OPEN=	000034	ERR104	006346 G	E114B	012052
BITHS1	002436 G	CP12	017147	C#PNTB=	000014	ERR105	006400 G	E115	012126
BITHS2	002440 G	CP2	016741	C#PNTF=	000017	ERR106	006432 G	E116	012442
BIT0	= 000001 G	CP3	016752	C#PNTS=	000016	ERR107	006512 G	E116A	012525
BIT00	= 000001 G	CP4	016761	C#PNTX=	000015	ERR108	006572 G	E116B	012606
BIT01	= 000002 G	CP5	016773	C#QIO =	000377	ERR109	006624 G	E120	012662
BIT02	= 000004 G	CP6	017010	C#RDBU=	000007	ERR110	006656 G	E121	012747
BIT03	= 000010 G	CP7	017016	C#REFG=	000047	ERR111	006710 G	E124	013030
BIT04	= 000020 G	CP8	017066	C#RESE=	000033	ERR112	006742 G	E201	030617
BIT05	= 000040 G	CP9	017077	C#REVI=	000003	ERR115	007412 G	E202	030704
BIT06	= 000100 G	CRLF	014634 G	C#RFLA=	000021	ERR116	007560 G	E203	030751
BIT07	= 000200 G	CSR	002420 G	C#RPT =	000025	ERR120	010034 G	E501	013113
BIT08	= 000400 G	C#ARM =	177440 G	C#SEFG=	000046	ERR121	010104 G	E502	013146
BIT09	= 001000 G	C#AU =	000052	C#SPRI=	000041	ERR122	010134 G	E506	013235
BIT1	= 000002 G	C#AUTO=	000061	C#STN =	177750 G	ERR123	010164 G	E507	013333
BIT10	= 002000 G	C#BRK =	000022	C#SVEC=	000037	ERR124	010234 G	E507A	013432
BIT11	= 004000 G	C#BSEG=	000004	C#TPRI=	000013	ERR130	007444 G	FLASH	020716 G
BIT12	= 010000 G	C#BSUB=	000002	DACON	017244 G	ERR501	006774 G	FLSANS	021170 G
BIT13	= 020000 G	C#CEFG=	000045	DCNT	017222	ERR502	007050 G	F#AU =	000015
BIT14	= 040000 G	C#CLK=	000062	DECEX	020626	ERR503	007124 G	F#AUTO=	000020
BIT15	= 100000 G	C#CLEA=	000012	DECIN	017736 G	ERR504	007152 G	F#BGN =	000040
BIT2	= 000004 G	C#CLOS=	000035	DECIN1	020347	ERR505	007204 G	F#CLEA=	000007
BIT3	= 000010 G	C#CLP1=	000006	DECIN2	020405	ERR506	007262 G	F#DU =	000016
BIT4	= 000020 G	C#CTN =	177740 G	DECIN3	020466	ERR507	007340 G	F#END =	000041
BIT5	= 000040 G	C#CVEC=	000036	DECOUT	020516 G	ER116A	007634 G	F#HARD=	000004
BIT6	= 000100 G	C#DAC =	177700 G	DEC01	020634	ER116B	007710 G	F#HW =	000013
BIT7	= 000200 G	C#DAS =	177600 G	DEC02	020640	ER116C	007762 G	F#INIT=	000006
BIT8	= 000400 G	C#DCLN=	000044	DEC03	020644	EVL	= 000004 G	F#JMP =	000050
BIT9	= 001000 G	C#DODU=	000051	DFPTBL	002154 G	EXQV1	030170	F#MOD =	000000
BIV	002472 G	C#DRPT=	000024	DI	017156	EXQV2	030570	F#MSG =	000011
BOE	= 000400 G	C#DU =	000053	DIAGMC=	000000	EXQV3	031212	F#PROT=	000021
CALRET	021270 G	C#EDIT=	000003	DO	017167	EXS10	037016	F#PWR =	000017
CARRFL	021374	C#ERDF=	000055	DROPD	027734	EXS4	032106	F#RPT =	000012
CCR	002422 G	C#ERHR=	000056	DROPED	002614 G	EXS5	033442	F#SEG =	000003
CHAR	023162	C#ERRO=	000060	ECNT	002514 G	EXS6	034072	F#SOFT=	000005
CHKMAX	014434 G	C#ERSF=	000054	EERA	006250 G	EXS7	034572	F#SRV =	000010
CLINT	022024 G	C#ERSO=	000057	EERB	006300 G	EXS8	035264	F#SUB =	000002
CLKFLG	027526	C#ESCA=	000010	EERG	006340 G	EXS9	036104	F#SW =	000014
CNTVAL	002452 G	C#ESEG=	000005	EER1	006064 G	E#END =	002100	F#TEST=	000001
CNTXT	012403	C#ESUB=	000003	EER2	006122 G	E#LOAD=	000035	GAIN	002706 G
CNT25	014632 G	C#ETST=	000001	EER3	006164 G	E101	030240	GETNUM	020327
CNT25M	014626 G	C#EXIT=	000032	EER6	006216 G	E102	010604	GOOBAD	010306
CNT500	014630 G	C#GETB=	000026	EF.CON=	000036 G	E103	010662	GOOD	002474 G

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Symbol table

GPADD	002554	G	I\$DU	=	000041	L\$HPCP	002016	G	L10037	007760	L10131	041440
GPFLG	023160		I\$HRD	=	000041	L\$HPTP	002022	G	L10040	010032	L10132	041434
GROUP	002446	G	I\$INIT	=	000041	L\$HW	002154	G	L10041	010102	L10133	041464
G\$CNTD	=	000200	I\$MOD	=	000041	L\$ICP	002104	G	L10042	010132	L10134	041460
G\$DELM	=	000372	I\$MSG	=	000041	L\$INIT	025022	G	L10043	010162	L10136	041504
G\$DISP	=	000003	I\$PROT	=	000040	L\$LADP	002026	G	L10044	010232	MASCOM	014050
G\$EXCP	=	000400	I\$PTAB	=	000041	L\$LAST	041004	G	L10045	010304	MASK	014046 G
G\$HILI	=	000002	I\$PWR	=	000041	L\$LOAD	002100	G	L10046	014120	MOD	002416 G
G\$LQI	=	000001	I\$RPT	=	000041	L\$LUN	002074	G	L10047	021774	MODE	002704 G
G\$NO	=	000000	I\$SEG	=	000041	L\$MREV	002050	G	L10050	022002	MREA	= 000002 G
G\$OFFS	=	000400	I\$SETU	=	000041	L\$NAME	002000	G	L10051	022022	MREB	= 000004 G
G\$OFSI	=	000376	I\$SFT	=	000041	L\$PRIO	002042	G	L10052	022024	MREC	= 000006 G
G\$PRMA	=	000001	I\$SRV	=	000041	L\$PROT	025014	G	L10053	025012	M\$TOG	= 000042 G
G\$PRMD	=	000002	I\$SUB	=	000041	L\$PRT	002112	G	L10055	026646	NERRS	014536
G\$PRML	=	000000	I\$TST	=	000041	L\$REPP	002062	G	L10056	027662	NEWST	025320
G\$RADA	=	000140	IOTAB	=	004312 G	L\$REV	002010	G	L10057	027672	NEXT	025654
G\$RADB	=	000000	IATAB	=	005112 G	L\$RPT	022026	G	L10060	027764	NH	= 000014
G\$RADD	=	000040	J\$JMP	=	000167	L\$SOFT	002366	G	L10061	027776	NO	025006
G\$RADL	=	000120	KLINT	=	027102	L\$SPC	002056	G	L10062	030306	NOCLOK	027240
G\$RADO	=	000020	LASTFA	=	015304	L\$SPCP	002020	G	L10063	030204	NODEV	005772 G
G\$XFER	=	000004	LCLOCK	=	026766	L\$SPTP	002024	G	L10064	031026	NOUNIT	015574
G\$YES	=	000010	LF	=	014656	L\$STA	002030	G	L10065	031252	NR1	020310
G1	=	002222	LOCATE	=	030202 G	L\$SW	002164	G	L10066	032162	NR2	020312
G16	=	002374	LOE	=	040000 G	L\$TEST	002114	G	L10067	033524	NTEST	026371
G2	=	002250	LOOP	=	002466 G	L\$TIML	002014	G	L10070	034140	NTESTS	= 000013
G3	=	002270	LOOPEX	=	026214	L\$UNIT	002012	G	L10071	034652	NTEST1	026465
HEL	=	022142	LOPFLG	=	002640 G	L10000	002162		L10072	035344	NUMBER	002442 G
HELP	=	000000	LOT	=	000010 G	L10001	002166		L10073	036160	NXH	021766 G
HOE	=	100000 G	LOTFLA	=	002700 G	L10002	002222		L10074	037212	NXHFLG	002470 G
IBE	=	010000 G	L\$ACP	=	002110 G	L10003	002374		L10075	037432	NXTUUT	025544
IDU	=	000040 G	L\$APT	=	002036 G	L10004	006120		L10076	041010	ONEFIL	= 000001
IER	=	020000 G	L\$AU	=	027766 G	L10005	006162		L10077	041034	O\$APTS	= 000001
INIUT	=	025610	L\$AUT	=	002070 G	L10006	006214		L10100	041030	O\$AU	= 000001
INR	=	002424 G	L\$AUTO	=	027600 G	L10007	006246		L10101	041060	O\$BGNR	= 000001
INSERT	=	015240 G	L\$CCP	=	002106 G	L10010	006276		L10102	041054	O\$BGNS	= 000001
INTFLA	=	002510 G	L\$CLEA	=	027664 G	L10011	006336		L10103	041104	O\$DU	= 000001
INTFL2	=	002512 G	L\$CO	=	002032 G	L10012	006344		L10104	041100	O\$ERRT	= 000001
INTSR	=	021776 G	L\$DEPO	=	002011 G	L10013	006376		L10105	041130	O\$GNSW	= 000001
ISR	=	000100 G	L\$DESC	=	005732 G	L10014	006430		L10106	041124	O\$POIN	= 000001
ITRAC1	=	030044	L\$DESP	=	002076 G	L10015	006510		L10107	041154	O\$SETU	= 000001
ITRAC2	=	030326	L\$DEVP	=	002060 G	L10016	006570		L10110	041150	PADD	002710 G
ITRAC3	=	031052	L\$DISP	=	002124 G	L10017	006622		L10111	041200	PNT	= 001000 G
ITRAC4	=	031306	L\$DLY	=	002116 G	L10020	006654		L10112	041174	PRA	023326
ITRAC5	=	032336	L\$DTP	=	002040 G	L10021	006706		L10113	041224	PREX	023152
ITRAC6	=	033560	L\$DTYP	=	002034 G	L10022	006740		L10114	041220	PRI	= 002000 G
ITRAC7	=	034174	L\$DU	=	027674 G	L10023	006772		L10115	041250	PRI0	002430 G
ITRAC8	=	034722	L\$DUT	=	002072 G	L10024	007046		L10116	041244	PRI00	= 000000 G
ITRAC9	=	035414	L\$DVTY	=	005722 G	L10025	007122		L10117	041274	PRI01	= 000040 G
ITRCNT	=	002504 G	L\$EF	=	002052 G	L10026	007150		L10120	041270	PRI02	= 000100 G
ITRDEF	=	002506 G	L\$ENVI	=	002044 G	L10027	007202		L10121	041320	PRI03	= 000140 G
ITR10	=	036222	L\$ERPT	=	005712 G	L10030	007260		L10122	041314	PRI04	= 000200 G
IXE	=	004000 G	L\$ETP	=	002102 G	L10031	007336		L10123	041344	PRI05	= 000240 G
IXEND	=	171770 G	L\$EXP1	=	002046 G	L10032	007410		L10124	041340	PRI06	= 000300 G
IXSTA	=	171000 G	L\$EXP4	=	002064 G	L10033	007442		L10125	041370	PRI07	= 000340 G
I\$AU	=	000041	L\$EXP5	=	002066 G	L10034	007556		L10126	041364	PROMT	021172
I\$AUTO	=	000041	L\$HARD	=	002170 G	L10035	007632		L10127	041414	PR1	023420
I\$CLN	=	000041	L\$HIME	=	002120 G	L10036	007706		L10130	041410	PR2	023445

CLOSE SECTION MACRO V05.00 Wednesday 03-Oct-84 14:03 Page 65 3
Symbol table

PR2A	023552	RSAY	031016	TPS	- 177564	T#PTAB=	010135	T10	036162 G
PR2B	023651	RMMAX	031242	TSHD1	030206 G	T#PTHV=	000020	T11	037214 G
PR2C	023743	SAVCNT	027444	TSHD10	037122 G	T#PTMU=	000020	T2	030310 G
PR2D	024004	SAVPRI	002432 G	TSHD11	037350 G	T#SAVL=	177777	T3	031030 G
PR2E	024074	SELECT	015006 G	TSHD2	030574 G	T#SEGL=	177777	T4	031254 G
PR2F	024177	SETCLK	026650	TSHD3	031216 G	T#SEKO=	010000	T5	032164 G
PR2G	024273	SETEX	027524	TSHD4	032120 G	T#SIZE=	000240	T6	033526 G
PR3	024367	SETHRA	037022	TSHD5	033474 G	T#SUBN=	000000	T7	034142 G
PR3A	024474	SFI	002500 G	TSHD6	034076 G	T#TAGL=	177777	T8	034654 G
PR4	024533	SFPTBL	002164 G	TSHD7	034576 G	T#TAGN=	010137	T9	035346 G
PR5	024564	SNUM	020314	TSHD8	035270 G	T#TEMP=	000000	UAM	- 000200 G
PR6	024605	SRC	002464 G	TSHD9	036110 G	T#TEST=	000013	USCLOK	027174
PR7	024706	STADD	015572 G	TSTEN1	030176	T#TSTM=	177777	VBPTAB	003512 G
PSAD	017540	STARES	025306	TSTFLG	002634 G	T#TSTS=	000001	VEC	002426 G
PSDA	017316	START	025022	TSUFLG	002636 G	T#AU=	010061	VUPTAB	002712 G
PSEUL1	026162	STAT	022766	TT	023220	T#AUT=	010056	WAIT	014620
PURFL	025466	STAVA2	012307	TTINT	027372	T#CLE=	010057	WARN	026216
QVP	002164 G	STFLG	023156	T#ARGC=	000001	T#DAT=	010136	WARN1	026276
RA	020712 G	STRVA1	012173	T#CODE=	000152	T#DU=	010060	WFLG	014710
RANDOM	020654 G	STRVA2=	***** GX	T#ERRN=	001755	T#HAR=	010002	WOR	037174
RB	020714 G	SVCGBL=	000000	T#EXCP=	000000	T#HM=	010000	WORFLA=	***** GX
RDY	023264	SVCINS=	177777	T#FLAG=	000040	T#INI=	010055	WRDY	014662 G
RECON	022244	SVCSUB=	177777	T#FREE=	041504	T#MSG=	010046	WRDY1	014712
REGADD	014052 G	SVCTAG=	177777	T#GMAN=	000000	T#PC=	000020	WRSEL	026532
REGERR	014054 G	SVCTST=	177777	T#HLI=	000001	T#PRO=	010054	WT25	014614 G
REGMSG	014341	S#LSYM=	010000	T#LAST=	000001	T#PTA=	010135	WT2SH	014600 G
REGTST	013474 G	TADS	023172	T#LCL=	000000	T#RPT=	010053	WT500	014606 G
REGTS1	013532 G	TIDMSG	027530	T#LSYM=	010000	T#SEG=	010000	X#ALMA=	000000
RIERR1	014122	TITLE	022350	T#LTNO=	000013	T#SOF=	010003	X#FALS=	000040
RIERR2	014174	TKB	- 177562	T#NEST=	177777	T#SRV=	010063	X#OFFS=	000400
RIERR3	014252	TKS	- 177560	T#NSO=	000000	T#SW=	010001	X#TRUE=	000020
RESTR	025276	TNA	015166	T#NS1=	000001	T#TES=	010075	YES	025002
RETHE1	021376	TNUM	023322	T#NS2=	000003	T:	030000 G	SPATCH	037434 G
RFLG	023170	TPB	- 177566	T#PCNT=	000000				

. ABS. 041504 000 (RM,I,GBL,ABS,OVR)
000000 001 (RM,I,LCL,REL,CON)

Errors detected: 0

*** Assembler statistics

Work file reads: 233
Work file writes: 236
Size of work file: 28944 Words (114 Pages)
Size of core pool: 19990 Words (76 Pages)
Operating system: RSX-11M/PLUS

Elapsed time: 00:08:43.64
ZIDVA0.BIN,ZIDVA0.SEG/-SP=[50,200]SVC/ML,(53,53)ZIDVA0.SRC