

DU11

OFF-LINE COMBINED TEST
MD-11-DZDUF-C

EP-DZDUF-C-DL-A

NOV 1976

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digital

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MADE IN U.S.A.

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6	7	8	9	10
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91	92	93	94	95
96	97	98	99	100

DDJUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 1
HELLO.P11 03-AUG-76 00:00

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GENERAL DESCRIPTION

THIS DIAGNOSTIC CAN CHAIN 16 D11'S. THIS MEANS THAT 16 DEVICES CAN BE SEQUENTIALLY EXERCISED. THE DIAGNOSTIC MAKES ONE PASS BEFORE PROCEEDING TO THE NEXT DEVICE, AND CONTINUES EXERCISING ALL DEVICES IN THIS FASHION UNTIL HALTED.

2. REQUIREMENTS

PDP-11 FAMILY STANDARD COMPUTER WITH OR WITHOUT HARDWARE SWITCH REGISTER (LOC. 177570)

D11 SYNCHRONOUS/ISOCRONOUS OPTION

ONE CONSOLE TELETYPE OR EQUIVALENT

2.2 STORAGE

3. LOADING PROCEDURE

THE STANDARD PROCEDURE FOR LOADING ABSOLUTE BINARY TAPES IS TO BE USED.

	STARTING ADDRESS FOR ABSOLUTE LOADER
4K	017500
8K	037500
12K	057500
16K	077500
20K	117500
24K	137500
28K	157500

4. STARTING PROCEDURE

4.1 CONTROL SWITCH SETTINGS

NOTE: SOFTWARE SWITCH REGISTER IS DEFINED AS LOC. 176, WHILE THE SOFTWARE DISPLAY REGISTER IS DEFINED AS LOC. 174.

4.1.1 AFTER PROGRAM LOAD (INITIAL PROGRAM START)
ALL CONSOLE SWITCHES DOWN4.1.2 TO MODIFY DEVICE VECTOR AND CONTROL REGISTER ADDRESSES
AFTER PROGRAM RESTART OR TO RUN MULTIPLE DEVICES

SW00=1

4.1.3 TO START PROGRAM AT SELECTED TEST AFTER A PROGRAM RESTART

(ONLY IN SINGLE DEVICE TESTS)
SW01=1

- 4.1.4 TO LOCK ON SELECTED TEST AFTER A PROGRAM RESTART
(ONLY IN SINGLE DEVICE TESTS)

SW02=1
NOTE1: IN GENERAL SW01 WILL BE USED WHEN SW02=1 IS USED
NOTE2: WITHOUT SW01=1 "LOCK ON TEST" WILL DEFAULT TO TEST 1
STARTING ADDRESS

- 4.2 THE STARTING ADDRESS FOR ALL TESTS IS 000200
THE RETARTING ADDRESS FOR ALL TESTS IS 000200
THE STARTING ADDRESS TO ENTER A SELECTED TEST IS 000200
THE STARTING ADDRESS TO LOCK ON TEST IS 000200

- 4.3 PROGRAM AND/OR OPERATOR ACTION

- 4.3.1 INITIAL PROGRAM START

- 4.3.1.1 LOAD PROGRAM INTO MEMORY WITH ABSOLUTE LOADER
4.3.1.2 LOAD ADDRESS 000200
4.3.1.3 CLEAR CONSOLE SWITCHES
4.3.1.4 PRESS START

NOTE: IF THE SOFTWARE SWITCH REGISTER IS SELECTED THEN THE FOLLOWING
WILL BE TYPED AFTER THE PROGRAM IDENTIFIES ITSELF:
SWR=XXXXXX NEW= (REFER TO SECTION 5. FOR OPERATOR'S OPTION)

- 4.3.1.7 THE PROGRAM WILL TYPE "R" TO INDICATE THAT IT IS ABOUT
TO START TESTING ,AND THEN TESTING WILL BEGIN

- 4.3.2 PROGRAM RESTART WITH ALL SWITCHES DOWN

- 4.3.2.1 THE PROGRAM WILL TYPE "R" AND WILL COMMENCE TESTING

- 4.3.3 PROGRAM RESTART WITH SW00=1

- 4.3.3.1 LOAD ADDRESS 000200
4.3.3.2 SET SW00=1
4.3.3.3 PRESS START

NOTE: IF THE SOFTWARE SWITCH REGISTER IS SELECTED THEN THE FOLLOWING
WILL BE TYPED AFTER THE PROGRAM IDENTIFIES ITSELF:
SWR=XXXXXX NEW= (REFER TO SECTION 5. FOR OPERATOR'S OPTION)

- 4.3.3.4 THE PROGRAM WILL TYPE " 1ST DEVICE: RECEIVER CONTROL REGISTER

ADDRESS" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

- 4.3.3.5 TYPE IN THE ADDRESS OF THE FIRST RECEIVER CONTROL REGISTER ADDRESS OF THE DUI TO BE TESTED FOLLOWED BY A <CARRIAGE RETURN>

IF AN INCORRECT ADDRESS IS TYPED, THE PROGRAM WILL TYPE "?" AND WILL THEN REPEAT THE MESSAGE OF 4.3.3.4

- 4.3.3.6 THE PROGRAM WILL TYPE "VECTOR ADDRESS-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

- 4.3.3.7 TYPE IN THE BASE RECEIVER INTERRUPT VECTOR ADDRESS FOR THE DUI TO BE TESTED FOLLOWED BY A <CARRIAGE RETURN>

IF AN INCORRECT ADDRESS IS TYPED, THE PROGRAM WILL TYPE "?" AND WILL THEN REPEAT THE MESSAGE OF 4.3.3.6

- 4.3.3.8 THE PROGRAM WILL TYPE "ARE YOU RUNNING MULTIPLE DEVICES ?" (Y OR N)-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

- 4.3.3.9 TYPE IN THE APPROPRIATE ANSWER YES OR NO FOLLOWED BY A <CARRIAGE RETURN>

IF AN INCORRECT ANSWER IS GIVEN, THE PROGRAM WILL TYPE "?" AND WILL THEN REPEAT THE MESSAGE OF 4.3.3.8

IF A "NO" ANSWER IS GIVEN: JUMP TO SECTION 4.3.3.12
IF A "YES" ANSWER IS GIVEN: THE NEXT QUESTION IS ASKED

- 4.3.3.10 THE PROGRAM WILL TYPE "LAST DEVICE: RECEIVER CONTROL REGISTER ADDRESS-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

- 4.3.3.11 TYPE IN THE ADDRESS OF THE LAST RECEIVER CONTROL REGISTER ADDRESS OF THE DUI TO BE TESTED FOLLOWED BY A <CARRIAGE RETURN>

IF AN INCORRECT ANSWER IS TYPED, THE PROGRAM WILL TYPE "?" AND WILL THEN REPEAT THE MESSAGE OF 4.3.3.10
NOTE: ALL ADDRESSES SHALL BE CONTIGUOUS

- 4.3.3.11.1 IF AN "OUT OF RANGE" ADDRESS IS TYPED
IE. MORE THAN 16 (10) DEVICES AWAY (UPWARDS).....THE PROGRAM WILL TYPE "OUT OF RANGE: RETYPE LAST DEVICE RXCSR ADDRESS-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

- 4.3.3.11.2 TYPE IN THE ADDRESS OF THE LAST RECEIVER CONTROL REGISTER ADDRESS OF THE DUI TO BE TESTED FOLLOWED BY A <CARRIAGE RETURN>

IF AN INCORRECT ANSWER IS TYPED, THE PROGRAM WILL TYPE "?"

AND WILL REPEAT THE MESSAGE OF 4.3.3.11.1

IF A DEVICE ADDRESS LOWER THAN 1ST DEVICE ADDRESS IS TYPED.....
 SCHWARTZ OUT..... THERE IS NO PROTECTION FOR THIS.
 THE PROGRAM WILL DEFAULT TO TWO DEVICES ACTIVE (UPWARDS FROM
 1ST DEVICE ADDRESS). THE SAME APPLIES TO IDENTICAL ADDRESSES
 TYPED FOR FIRST AND LAST DEVICE.
 OBSERVE LOCATION 2 ACTREG: SEE SECTION 7.2

4.3.3.12 THE PROGRAM WILL TYPE "DU PRIORITY LEVEL-" AND
 WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.3.13 TYPE IN THE APPROPRIATE DEVICE PRIORITY LEVEL OF THE
 DULL OR DULL'S TO BE TESTED FOLLOWED BY A (CARRIAGE RETURN)
 (NOTE THAT ALL MULTIPLE DEVICES MUST BE AT THE SAME PRIORITY
 LEVEL). IE "5"

IF AN INCORRECT LEVEL IS TYPED, THE PROGRAM WILL TYPE "?"
 AND REPEAT THE MESSAGE OF 4.3.3.12

4.3.3.14 THE PROGRAM WILL TYPE "# OF SYNC CHARS
 SELECTED (1 OR 2)-" AND WAIT FOR AN INPUT FROM THE TELETYPE
 KEYBOARD

4.3.3.15 TYPE IN THE APPROPRIATE ANSWER "1" OR "2" FOLLOWED
 BY A (CARRIAGE RETURN). (NOTE: ALL MULTIPLE DEVICES MUST
 BE THE SAME)

IF AN INCORRECT ANSWER IS TYPED, THE PROGRAM WILL TYPE "?"
 AND WILL REPEAT THE MESSAGE OF 4.3.3.14

4.3.3.16 THE PROGRAM WILL TYPE "IS SEC XMIT JUMPER #6 IN ? (Y OR N)-"
 AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.3.17 TYPE IN THE APPROPRIATE ANSWER YES OR NO FOLLOWED
 BY A (CARRIAGE RETURN). (NOTE THAT ALL MULTIPLE DEVICES
 MUST BE THE SAME)

IF AN INCORRECT ANSWER IS TYPED, THE PROGRAM WILL TYPE "?"
 AND WILL REPEAT THE MESSAGE OF 4.3.3.16

4.3.3.18 THE PROGRAM WILL TYPE "IS SEC REC JUMPER # 5 IN ?
 (Y OR N)-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.3.19 TYPE IN THE APPROPRIATE ANSWER YES OR NO FOLLOWED
 BY A (CARRIAGE RETURN). (NOTE: ALL MULTIPLE DEVICES MUST BE THE SAME)

IF AN INCORRECT ANSWER IS TYPED, THE PROGRAM WILL TYPE "?"
 AND WILL REPEAT THE MESSAGE OF 4.3.3.18

4.3.3.20 THE PROGRAM WILL TYPE "IS OPT CLR ENABLE JUMPER

4 IN ? (Y OR N)-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.3.21 TYPE IN THE APPROPRIATE ANSWER YES OR NO FOLLOWED BY A <CARRIAGE RETURN>. (NOTE: ALL MULTIPLE DEVICES MUST BE THE SAME)

IF AN INCORRECT ANSWER IS TYPED, THE PROGRAM WILL TYPE "?" AND WILL REPEAT THE MESSAGE OF 4.3.3.20

4.3.3.22 THE PROGRAM WILL TYPE "ARE YOU RUNNING IN MAINT. MODE EXTERNAL ? AND DO YOU HAVE THE EXTERNAL MODEM BYPASS JUMPER CONNECTOR ON ? (Y OR N)-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.3.23 TYPE IN THE APPROPRIATE ANSWER YES OR NO FOLLOWED BY A <CARRIAGE RETURN>. (NOTE: ALL MULTIPLE DEVICES MUST BE THE SAME)

IF AN INCORRECT ANSWER IS TYPED, THE PROGRAM WILL TYPE "?" AND WILL REPEAT THE MESSAGE OF 4.3.3.22

4.3.3.24 THE PROGRAM WILL TYPE "R" TO INDICATE THAT IT HAS STARTED AND WILL CONFERENCE TESTING AT TEST 1

4.3.4 PROGRAM RESTART WITH SW01=1
NOTE: THIS WILL ONLY WORK WHEN A SINGLE DEVICE IS SELECTED
,,,IT WILL NOT WORK IF MULTIPLE DEVICES ARE SELECTED
IF MULTIPLE DEVICES WERE PREVIOUSLY SELECTED, LOAD 000200,
AND SELECT SW00=1 AND ANSWER "NO" TO THE MULTIPLE DEVICE QUESTION
SEE 4.3.3

4.3.4.1 LOAD 000200

4.3.4.2 SET SW01=1

4.3.4.3 PRESS START
NOTE: IF THE SOFTWARE SWITCH REGISTER IS SELECTED THEN THE FOLLOWING
WILL BE TYPED AFTER THE PROGRAM IDENTIFIES ITSELF:
SWR=XXXXXX NEW= (REFER TO SECTION 5. FOR OPERATOR'S OPTION)

4.3.4.4 THE PROGRAM WILL TYPE "TEST PC-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.4.5 TYPE IN THE ADDRESS OF THE TEST AT WHICH THE PROGRAM IS TO BE STARTED FOLLOWED BY A <CARRIAGE RETURN>

4.3.4.6 THE PROGRAM WILL TYPE "R" TO INDICATE THAT IT HAS STARTED TESTING AT THE SELECTED TEST

NOTE: CARE MUST BE TAKEN WHEN THIS FEATURE IS USED
SINCE THERE IS NO PROTECTION AGAINST SELECTING AN ADDRESS
THAT IS IN THE MIDDLE OF A TEST

4.3.5 PROGRAM RESTART WITH SW02 =1
NOTE: THIS WILL ONLY WORK WHEN A SINGLE DEVICE IS SELECTED

SEE NOTE IN 4.3.4 FOR MORE DETAILS

4.3.5.1 LOAD ADDRESS 000200

4.3.5.2 SET SW02 =1

NOTE: IT MAY BE ADVANTAGEOUS TO SET SW01=1 (OPTIONAL)

4.3.5.3 PRESS START

NOTE: IF THE SOFTWARE SWITCH REGISTER IS SELECTED THEN THE FOLLOWING
WILL BE TYPED AFTER THE PROGRAM IDENTIFIES ITSELF:
SWR=XXXXXX NEW= (REFER TO SECTION 5. FOR OPERATOR'S OPTION)

4.3.5.4 THE PROGRAM WILL TYPE "LOCK ON SELECTED TEST ? (Y OR N)-"
AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.5.5 TYPE IN THE APPROPRIATE ANSWER YES OR NO FOLLOWED BY A
(CARRIAGE RETURN)

IF A NO ANSWER IS GIVEN: THIS LOCK ON TEST WILL BE IGNORED
AND THE PROGRAM WILL TYPE "R" TO INDICATE THAT IT HAS STARTED
TESTING AT TEST 1

4.3.5.6 IF A YES ANSWER WAS GIVEN: THE PROGRAM WILL ACT AS FOLLOWS...
THE PROGRAM WILL TYPE "R" TO INDICATE THAT IT HAS STARTED
TESTING AT TEST 1 AND WILL REMAIN IN TEST 1 UNTIL HALTED
OR IF ANY KEY IS STRUCK ON THE TELETYPE. THE PROGRAM
WILL FREEZE ON THE NEXT TEST UNTIL A KEY IS STRUCK ON
THE TELETYPE AND SO FORTH THRU THE PROGRAM. IF SW01 =1 IT
WILL PERFORM AS IN SECTION 4.3.4 ALLOWING ONE TO FREEZE
ON A SELECTED TEST RATHER THAN DEFAULTING TO TEST 1

5. OPERATING PROCEDURE

IF THE DIAGNOSTIC IS RUN ON A CPU WITHOUT A SWITCH
REGISTER THEN A SOFTWARE SWITCH REGISTER IS USED WHICH ALLOWS
THE USER TO SELECT SWITCH OPTIONS AS THE HARDWARE SWITCH REGISTER.
IF THE HARDWARE SWITCH REGISTER DOES NOT EXIST OR IF ONE DOES
AND IT CONTAINS ALL ONES (177777) THEN THE SOFTWARE SWITCH
REGISTER (LOC. 176) IS USED.

CONTROL:

THIS PROGRAM ALSO SUPPORTS THE DYNAMIC LOADING OF THE SOFTWARE SWITCH
REGISTER (LOC. 176) FROM THE TTY. THIS CAN BE ACCOMPLISHED BY
DOING THE FOLLOWING:

- 1) TYPE CONTROL G (1G): THIS WILL ALLOW THE TTY TO ENTER DATA INTO
LOC. 176 AT SELECTED POINTS WITHIN THE PROGRAM.
- 2) THE PROGRAM WILL THEN TYPE: SWR=XXXXXXNEW= (XXXXXX IS THE OCTAL CONTENTS
OF THE SOFTWARE SWITCH REGISTER.)
- 3) AFTER THE "NEW=" HAS BEEN TYPED THEN THE OPERATOR CAN DO ONE
OF THE FOLLOWING AT THE TTY:

A) TYPE A NUMBER TO BE LOADED INTO LOC. 176 FOLLOWED BY A <CR>.
(ONLY NUMBERS BETWEEN 0-7 WILL BE ACCEPTED AND ONLY 6 NUMBERS
WILL BE ALLOWED)
IF A <CR> IS THE FIRST KEY DEPRESSED THE SOFTWARE SWITCH
REGISTER CONTENTS WILL NOT BE CHANGED.

B) IF A CONTROL U (<U>) IS DEPRESSED THEN THE PROGRAM WILL SEND YOU
BACK TO STEP 2.

5.1 OPERATIONAL SWITCH SETTINGS

SW15 =1 HALT ON ERROR
SW14 =1 LOOP ON CURRENT TEST
SW13 =1 INHIBIT ERROR TYPEOUT
SW11 =1 INHIBIT ITERATIONS
SW10 =1 ESCAPE TO NEXT TEST ON ERROR
SW08 =1 LOOP ON ERROR
SW02 =1 LOCK ON TEST
SW01 =1 RESTART PROGRAM AT SELECTED TEST
SW00 =1 RESELECT VECTOR AND CONTROL REGISTER ADDRESSES
& PARAMETERS AFTER A PROGRAM RESTART
TO INHIBIT "END OF PRSS" TYPEOUT - TURN TELETYPE OFF

6. ERRORS

6.1 ERROR HALTS THERE ARE FOUR DISTINCT ERROR TYPEOUTS

NOTE: IF THE SOFTWARE SWITCH REGISTER IS TO BE CHANGED AFTER A HALT
THE THE OPERATOR IS REQUIRED TO TYPE A <IG> BEFORE DEPRESSING CONTINUE.
THE FOLLOWING WILL BE TYPED:
SWR=XXXXXX NEW= (REFER TO SECTION 5. FOR OPERATOR OPTION)

6.1.1 PC+2 = ERROR PC WHERE PC +2 IS THE ADDRESS OF THE CALL TO THE ERROR HANDLER +2

REFER TO THE ABOVE "HLT" IN DIAGNOSTIC FOR ERROR DESCRIPTION

CHECK ADDRESS 2 RXCSR: TO LOCATE THE DEVICE PRESENTLY UNDER
TEST WHEN RUNNING MULTIPLE DEVICES

6.1.2 PC +2 = REGISTER ERROR PC REGISTER EXPECTED ACTUAL 16XXXX YYYYYY ZZZZZZ

WHERE 16XXXX IS THE ADDRESS OF THE FAILING DEVICE REGISTER

WHERE YYYYYY IS THE EXPECTED CONTENTS OF THAT REGISTER

WHERE ZZZZZZ IS THE ACTUAL CONTENTS OF THAT REGISTER

6.1.3 PC +2 = RECEIVER ERROR PC REGISTER EXPECTED ACTUAL 16XXXX YYYYYY ZZZZZZ

WHERE 16XXXX IS THE ADDRESS OF THE FAILING RECEIVER (RXDBUF) REGISTER

WHERE YYYYYY IS THE EXPECTED DATA CONTENTS OF THAT REGISTER

WHERE ZZZZZZ IS THE ACTUAL DATA CONTENTS OF THAT REGISTER

- 6.1.4 PC +2 = TRANSMITTER ERROR PC
REGISTER EXPECTED ACTUAL
16XXXX YYYYYY ZZZZZZ

WHERE 16XXXX IS THE ADDRESS OF THE FAILING TRANSMITTER (TXCSR) REGISTER

WHERE YYYYYY IS THE EXPECTED CONTENTS OF THAT REGISTER

WHERE ZZZZZZ IS THE ACTUAL CONTENTS OF THAT REGISTER

- 6.1.5 ERROR DESCRIPTIONS
SEE LISTINGS FOR DETAILS OF ERRORS

6.2 ERROR RECOVERY

- 6.2.1 SW15 =0
IF THE PROGRAM IS RUN WITH SW15 =0 ,NO OPERATOR ACTION IS
REQUIRED TO CONTINUE TESTING

- 6.2.2 SW15 =1
IF THE PROGRAM IS RUN WITH SW15 =1 ,TO CONTINUE TESTING
AFTER THE PROGRAM HAS HALTED ,PRESS THE PROCESSOR
CONSOLE "CONTINUE SWITCH"

NOTE: THE PC + 2 OF THE "HLT" WILL BE DISPLAYED IN THE DATA LIGHTS

- 6.2.3 ILLEGAL INTERRUPTS
IF AN INTERRUPT OCCURS TO A VECTOR ADDRESS NOT SELECTED
DURING PROGRAM INITIALIZATION, THE PROGRAM WILL HALT IN
THE TRAP CATCHER. THE ADDRESS AT WHICH THE PROGRAM
HALTS IS 2 GREATER THAN THE ADDRESS TO WHICH THE INTERRUPT
OCCURED. THE PROGRAM MUST BE RESTARTED AT 000200 TO
RECOVER FROM THIS ERROR.

- 6.2.4 ADDITIONAL TROUBLESHOOTING AIDS ERRCNT: & PASCNT:
CHECK THESE TWO TAG LOCATIONS FOR TOTAL # OF ERRORS AND PASSES RESPECTIVELY.
LOADING 000200 AND RESTARTING WILL CLEAR THESE LOCATIONS.

- 6.3 END OF PASS ROUTINE
THIS TYPEOUT IS MENTIONED HERE FOR CONVENIENCE
IT IS IN THE FORM:

END OF PASS TAPE Y
16XXXX = DEVICE

WHERE Y IS THE TAPE LOADED

WHERE 16XXXX IS THE DEVICE'S BASE REGISTER ADDRESS

TO INHIBIT THIS TYPEOUT - TURN TELETYPE OFF

7. RESTRICTIONS

7.1 MULTIPLE DEVICES

UP TO 16(10) DEVICES MAY BE TESTED. HOWEVER, THEY
MUST HAVE CONTIGUOUS ADDRESSES AND VECTORS

NOTE: IF ALL DEVICES UNDER TEST HAVE THE SAME INTERRUPT VECTOR
YOU CAN CHANGE "ZERO: ADD #10,BASEIV ;NEXT BLOCK
(VECTORS)" TO "ZERO: ADD #0,BASEIV";
THEY BY THE VECTOR ADDRESSES WILL NOT BE
UPDATED AFTER EACH PASS.

7.2 DISQUALIFYING DEVICES WHEN RUNNING MULTIPLE DEVICES

WHEN RUNNING MULTIPLE DEVICES AN ACTIVE BIT IS SET
FOR EACH DEVICE RUNNING UNDER TEST IE. BIT 0 FOR
DEVICE 0 BIT 15 FOR DEVICE 15
TO DISQUALIFY DEVICES:

7.2.1 IF DEVICE 0 IS TO BE DISQUALIFIED, SIMPLY RESTART
PROGRAM WITH SW00 =1 AND OMIT THE FIRST DEVICE.7.2.2 IF HOWEVER, DEVICES 1 THRU 15 OR ANY COMBINATION THEREOF
ARE TO BE DISQUALIFIED....LOAD THE LOCATION OF ACTREG:
OBSERVE THE ACTIVE BITS (ACTIVE =1, NONACTIVE = 0)
AND DEPOSIT 0 WHERE THOSE DEVICES ARE TO BE DISQUALIFIED

7.2.2.1 TO RESTART...LOAD 000200 IN SWR AND DEPRESS START....
THE PROGRAM WILL CONTINUE WITH THE DEVICE IT WAS IN BEFORE HALTING.

7.2.2.2ORLOAD 000200 WITH SW00 =1 AND DEPRESS START....
ANSWER THE QUESTION :1ST DEVICE : ETC.....
.....THE PROGRAM WILL CONTINUE WITH DEVICE 0.....

7.2.2.3 IF ALL DEVICES ARE DISQUALIFIED BY MISTAKE THE PROGRAM
WILL TYPEOUT AN ERROR MESSAGE.....LOAD & START AT 000200

7.3 CABLE DELAYS

NOTE: EXTERNAL LOOP BACK TESTS ONLY (MODEM CABLE WITH H315 CONNECTOR ON)

7.3.1 TO PROVIDE SUFFICIENT DELAY FOR CLOCK SIGNAL OVER THE CABLE,
LOCATION "HOLD:" MUST BE MODIFIED TO ACCOMODATE FOR FASTER MACHINES.
PRESENTLY "HOLD:" =20 IS SUFFICIENT TIME ON AN 11/20 MACHINE.
IF RUNNING ON AN 11/40 OR AN 11/45 "HOLD:" MUST BE PATCHED TO 40

BASICALLY DON'T TRY TO EXCEED 10K TO 12K RATE USING THE EIA DRIVERS

7.4 TO USE THE "XOR" TESTER THE BRANCH AROUND THE "XOR"
CODE MUST BE PATCHED TO A "NOP". (SEE LISTINGS FOR DETAILS)

8. DEFAULT PARAMETERS:

1ST DEVICE: RECEIVER CONTROL REGISTER ADDRESS- RXCSR: 160040

VECTOR ADDRESS-

DURIV: 770

L01

DZDUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 11
HELLO.P11 03-AUG-76 00:00

SEQ 0011

ARE YOU RUNNING MULTIPLE DEVICES ?- NO MULTD: 0
LAST DEVICE: RECEIVER CONTROL REGISTER ADDRESS- LASTADD: 0
DU PRIORITY LEVEL- LEVEL 5 DUPRT: LEVEL 5
OF SYNC CHARS SELECTED - 2 SYNCH0: 377
IS SEC XMIT JUMPER # 6 IN ?- YES SEXMIT: 377
IS SEC REC JUMPER # 5 IN ?- YES SEREC: 377
IS OPT CLR ENABLE JUMPER # 4 IN ?- YES OPTCLR: 377
DO YOU HAVE THE EXTERNAL MODEM BYPASS JUMPER
CONNECTOR ON (H315)- YES JMRBY: 377

9. PROGRAM DESCRIPTION

10. FLOW CHARTS: RECEIVER FLOW, TRANSMITTER FLOW, TRANSMITTER & RECEIVER FLOW
11. LISTINGS

M01

DZDUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 15
 DZDUF.C.P11 05-AUG-76 00:00

SEQ 0012

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552      .ENABLE ABS
553
554      ;DU11 DZDUF-C TAPE F
555      ;COPYRIGHT 1973, DIGITAL EQUIPMENT CORP., MAYNARD, MASS. 01754
556
557      ;STARTING PROCEDURE
558      ;LOAD PROGRAM
559      ;PRESS START
560      ;PROGRAM WILL TYPE "DU11 DZDUF-C TAPE F "
561      ;PROGRAM WILL TYPE "R" TO INDICATE THAT TESTING HAS STARTED
562      ;AT THE END OF A PASS, PROGRAM WILL TYPE "END OF PASS TAPE F"
563      ;AND THEN RESUME TESTING
564
565
566      ;SWITCH REGISTER OPTIONS
567
568      100000      SW15=100000      :=1, HALT ON ERROR
569      040000      SW14=40000      :=1, LOOP ON CURRENT TEST
570      020000      SW13=20000      :=1, INHIBIT ERROR TYPEOUT
571      010000      SW12=10000
572      004000      SW11=4000      :=1, INHIBIT ITERATIONS
573      002000      SW10=2000      :=1, ESCAPE TO NEXT TEST ON ERROR
574      001000      SW09=1000      :=1, LOOP WITH CURRENT DATA
575      000400      SW08=400      :=1, LOOP ON ERROR
576      000100      SW06=100
577      000040      SW05=40
578      000020      SW04=20
579      000010      SW03=10
580      000004      SW02=4
581      000002      SW01=2
582      000001      SW00=1
583
584      ;LOCK ON TEST SELECT
585      ;RESTART PROGRAM AT SELECTED TEST
586      ;RESELECT VECTOR AND CONTROL REGISTER
587      ;ADDRESS AFTER PROGRAM RESTART

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;REGISTER DEFINITIONS
R0=%0      ;GENERAL REGISTER
R1=%1      ;GENERAL REGISTER
R2=%2      ;GENERAL REGISTER
R3=%3      ;GENERAL REGISTER
R4=%4      ;GENERAL REGISTER
R5=%5      ;GENERAL REGISTER
SP=%6      ;PROCESSOR STACK POINTER
PC=%7      ;PROGRAM COUNTER

;LOCATION EQUIVALENCIES
DSWR=177570 ;HARDWARE SWITCH REGISTER LOC.
DLIGHTS=177570 ;HARDWARE DISPLAY REGISTER LOC.
PS=177776   ;PROCESSOR STATUS WORD
STACK=1100  ;START OF PROCESSOR STACK

;INSTRUCTION DEFINITIONS
PUSH1SP=5746 ;DECREMENT PROCESSOR STACK 1 WORD =TST -(SP)
POP1SP=5726  ;INCREMENT PROCESSOR STACK 1 WORD =TST (SP)+
PUSHR0=10046 ;SAVE R0 ON STACK =MOV R0,-(SP)
POPR0=12600  ;RESTORE R0 FROM STACK =MOV (SP)+,R0
PUSH2SP=24646 ;DECREMENT STACK TWICE =CMP -(SP),-(SP)
POP2SP=22626  ;INCREMENT STACK TWICE =CMP (SP)+,(SP)+
.EQUIV ENT,HLT ;BASIC DEFINITION OF ERROR CALL

BIT15=100000
BIT14=40000
BIT13=20000
BIT12=10000
BIT11=4000
BIT10=2000
BIT9=1000
BIT8=400
BIT7=200
BIT6=100
BIT5=40
BIT4=20
BIT3=10
BIT2=4
BIT1=2
BIT0=1

;PROCESSOR LEVELS
LEVEL7=340
LEVEL6=300
LEVEL5=240
LEVEL4=200
LEVEL3=140
LEVEL2=100
LEVEL1=040
LEVEL0=000

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641      ;REGISTER DEFINITIONS
642      ;RXCSR BIT DEFINITIONS
643      DSC=BIT15      :DATA SET CHANGE
644      RING=BIT14      :RING
645      CTS=BIT13      :CLR TO SEND
646      CARRIER=BIT12 :CARRIER DETECT
647      RECACT=BIT11    :REC ACTIVE
648      SRO=BIT10      :SEC REC DATA
649      DSR=BIT9       :DATA SET RDY
650      STPSYN=BIT8     :STRIP SYNC
651      RXDONE=BIT7     :REC DONE
652      RXINTR=BIT6     :REC INTR ENABLE
653      DSCINTR=BIT5    :DSC INTR ENABLE
654      SYNCH=BIT4      :SYNC SEARCH
655      STD=BIT3        :SEC XMIT DATA
656      RTS=BIT2        :REQ TO SEND
657      DTR=BIT1       :DATA TERM RDY
658      VOID=BIT0
659      ;RXDBUF BIT DEFINITIONS
660      RXERR=BIT15     :REC ERROR
661      OVRUN=BIT14     :OVERRUN
662      FRMERR=BIT13    :FRAME ERROR
663      PARERR=BIT12    :PARITY ERROR
664      ;PARCSR BIT DEFINITIONS
665      PAREN=BIT9      :PARITY ENABLE
666      EVPAR=BIT8      :EVEN PARITY SENSE
667      ;PARCSR WRD DEFINITIONS
668      SYNINT=30000    :SYNC EXTERNAL MODE
669      SYNEXT=20000    :SYNC INTERNAL MODE
670      ISYN=0         :ISOC MODE
671      FIVE=0         :WORD LENGTH 5 BITS
672      SIX=2000       :WORD LENGTH 6 BITS
673      SEVEN=4000     :WORD LENGTH 7 BITS
674      EIGHT=6000    :WORD LENGTH 8 BITS
675      NOPAR=0        :NO PARITY
676      ODDPAR=1000    :ODD PARITY
677      EVEPAR=1400    :EVEN PARITY
678      ;TXCSR BIT DEFINITIONS
679      DNR=BIT15       :DATA NOT AVAILABLE
680      MTDATA=BIT14    :MAINT DATA
681      CLK=BIT13       :CLK
682      BITW=BIT10      :BIT WINDOW
683      MRESET=BIT8     :MASTER RESET
684      TXDONE=BIT7     :XMIT DONE
685      TXINTR=BIT6     :XMIT INTR ENABLE
686      DNAINTE=BIT5    :DMA INTR ENAB
687      SEND=BIT4       :SEND
688      HUX/FDX=BIT3    :HUX/FDX
689      BREAK=BIT0      :BREAK
690      ;TXCSR WRD DEFINITIONS
691      USER=0          :USER MODE
692      MINT=4000       :MAINT INT MODE
693      MEXT=10000      :MAINT EXT MODE
694      SYSTST=14000    :SYSTEM TEST MODE
695      ;TRAPCATCHER FOR ILLEGAL INTERRUPTS

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696                                     ;STANDARD INTERRUPT VECTORS
697
698
699                                     . = 24
700 000024 000024 .PFAIL .POWER FAIL HANDLER
701 000026 000340 340 .SERVICE AT LEVEL 7
702 000030 007062 .HLT .ERROR HANDLER
703 000032 000340 340 .SERVICE AT LEVEL 7
704 000034 007030 .TRPSRV .GENERAL HANDLER DISPATCH SERVICE
705 000036 000340 340 .SERVICE AT LEVEL 7
706
707                                     ;SOFTWARE SWITCH REGISTER
708
709                                     . = 174
710 000174 000700 DISPRG: .WORD 0 .SOFTWARE DISPLAY REG.
711 000176 000000 SWREG: .WORD 0 .SOFTWARE SWITCH REGISTER
712 000200 000167 001054 JMP .START .GO TO START OF PROGRAM
713
714
715                                     . = 1100
716 001100 . = 1100
717
718                                     ;INDIRECT POINTERS
719
720 001100 177570 SWR: 177570 .SWITCH REGISTER POINTER
721 001102 177570 LIGHTS: 177570 .DISPLAY REGISTER POINTER
722 001104 177560 TKCSR: 177560 .TELETYPE KEYBOARD CONTROL REGISTER
723 001106 177562 TKDBR: 177562 .TELETYPE KEYBOARD DATA BUFFER
724 001110 177564 TPCSR: 177564 .TELEPRINTER CONTROL REGISTER
725 001112 177566 TPDOR: 177566 .TELEPRINTER DATA BUFFER
726
727                                     ;PROGRAM CONTROL PARAMETERS
728
729 001114 000000 RTRN: 0 .SCOPE ADDRESS FOR LOOP ON TEST
730 001116 000700 NEXT: 00 .ADDRESS OF NEXT TEST TO BE EXECUTED
731 001120 000700 LOCK: 00 .ADDRESS FOR LOCK ON CURRENT DATA
732 001122 000000 ICOUNT: 00 .NUMBER OF ITERATIONS THAT CURRENT TEST WILL BE EXECUTED
733 001124 000700 LPENT: 00 .NUMBER OF ITERATIONS COMPLETE
734 001126 000700 TESTNO: 00 .NUMBER OF TEST IN PROGRESS
735 001130 000700 PNT: 00 .NUMBER OF PASSES COMPLETED
736 001132 000700 ENT: 00 .TOTAL NUMBER OF ERRORS
737 001134 000000 LSTERR: 0 .PC OF LAST ERROR CALL
738
739                                     ;PROGRAM VARIABLES
740
741 001136 000020 HOLD: 20 .TEMPORARY STORAGE=DELAY TIME FOR CABLES
742 001140 000700 SHIFT: 00 .TEMPORARY STORAGE= # OF SHIFTS PER CHAR
743 001142 000700 COUNT: 00 .TEMPORARY STORAGE= # OF TIMES A CHAR WILL BE SENT
744 001144 000700 T1: 00 .TEMPORARY STORAGE
745 001146 000700 T2: 00 .TEMPORARY STORAGE
746 001150 000700 T3: 00 .TEMPORARY STORAGE
747 001152 000700 T4: 00 .TEMPORARY STORAGE
748 001154 000700 T5: 00 .TEMPORARY STORAGE
749 001156 000700 S0: 00 .RD STORAGE
750 001160 000700 S1: 00 .R1 STORAGE
751 001162 000000 SAVR2: 0 .R2 STORAGE

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002

DZOUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 19
 NZOUFC.P11 05-AUG-76 00:00

SEQ 0016

752	001164	000000
753	001166	000000
754	001170	000000
755	001172	000000
756	001174	000000

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SAVR4
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:R3 STORAGE
:R4 STORAGE
:R5 STORAGE
:STACK POINTER STORAGE
:PROGRAM COUNTER STORAGE

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757                                     ;PROGRAM CONVERSATIONAL PARAMETERS
758 001176 377 SYNCNO: .BYTE 377 ;# OF SYNC CHARS REQ'D FOR SYNC'ZATION
759 001177 377 SEXMIT: .BYTE 377 ;SEC XMIT JUMPER "IN"
760 001200 377 SEREC: .BYTE 377 ;SEC REC JUMPER "IN"
761 001201 377 OPTCLR: .BYTE 377 ;OPTIONAL JUMPER CLR "IN"
762 001202 000 MULTD: .BYTE 0 ;NO MULTIPLE DEVICE FLAG
763 001203 377 JMRBY: .BYTE 377 ;EXTERNAL MODEM BYPASS JUMPER "IN"
764 .EVEN
765
766                                     ;PROGRAM MULTIPLE DEVICE PARAMETERS
767 001204 000000 ROTADD: 0 ;PROG CONTROLLED 1ST DEVICE ADDR
768 001206 000000 KEE ADD: 0 ;SAVED 1ST DEVICE ADDR
769 001210 000000 LASTADD: 0 ;LAST DEVICE RXCSR ADDR
770 001212 000000 BASEIV: 0 ;PROG CONTROLLED IV
771 001214 000000 KEEP IV: 0 ;SAVED INTR VECTOR
772 001216 000000 ACTREG: 0 ;ACTIVE REGISTER, NOTIFY THIS
773                                     ;LOCATION TO DISQUALIFY OR QUALIFY
774                                     ;DEVICES (1= RUN, 0= DON'T RUN)
775 001220 000000 ROTADD: 0 ;ROTATING POINTER FOR ACTREG. POINTS
776                                     ;TO DEVICE PRESENTLY UNDER TEST WHEN RUNNING MULTIPLE DE
777
778                                     ;PROGRAM CONTROL FLAGS
779
780 001222 000 INIFLG: .BYTE 0 ;PROGRAM INITIALIZATION FLAG
781 001223 000 STFLG: .BYTE 0 ;TEST START FLAG
782 001224 000 ENRFLG: .BYTE 0 ;ERROR OCCURRED FLAG
783 001225 000 LOCKFLG: .BYTE 0 ;LOCK ON CURRENT TEST FLAG
784
785                                     ;DEFINITIONS FOR TRAP SUBROUTINE CALLS
786                                     ;POINTERS TO SUBROUTINES CAN BE FOUND
787                                     ;IN THE TABLE IMMEDIATELY FOLLOWING THE DEFINITIONS
788
789 001226 .TRPTAB:
790 ;*****
791 ;*****
792 001226 104400 .SCOPE SCOPE=TRAP+0 ;CALL TO SCOPE LOOP AND ITERATION HANDLER
793 001226 005614 .SCOPE SCOP1=TRAP+1 ;CALL TO LOOP ON CURRENT DATA HANDLER
794 001230 104401 .SCOP1 TYPE=TRAP+2 ;CALL TO TELETYPE OUTPUT ROUTINE
795 001230 006000 .TYPE INSTR=TRAP+3 ;CALL TO ASCII STRING INPUT ROUTINE
796 001232 104402 .INSTR INSTR=TRAP+4 ;CALL TO INPUT ERROR HANDLER
797 001232 007000 .INSTR PARAM=TRAP+5 ;CALL TO NUMERICAL DATA INPUT ROUTINE
798 001234 104403 .PARAM SAVOS=TRAP+6 ;CALL TO REGISTER SAVE ROUTINE
799 001234 005060 .SAVOS RESOS=TRAP+7 ;CALL TO REGISTER RESTORE ROUTINE
800 001236 104404 .RESOS CONVRT=TRAP+10 ;CALL TO DATA OUTPUT ROUTINE
801 001236 007176 .CONVRT CNVRT=TRAP+11 ;CALL TO DATA OUTPUT ROUTINE WITHOUT CR/LF
802 001240 104405 .CNVRT SETFLG=TRAP+12 ;CALL TO FLAG SET ROUTINE
803 001240 006230
804 001242 104406
805 001242 007444
806 001244 104407
807 001244 006504
808 001246 104410
809 001246 007536
810 001250 104411
811 001250 006542
812 001250 104412

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813 001252 006762 .SETFLG
814 001254 104413 .CKSWR CKSWR=TRAP+13 ;CALL TO ALLOW SWREG TO BE LOADED FROM TTY
815 001254 007476 .CNTLU CNTLU=TRAP+14 ;CALL TO ALLOW LOADING OF SWREG FROM TTY
816 001256 104414
817 001256 007552
818 *****
819 *****
820
821 ;PROGRAM INITIALIZATION
822 ;LOCK OUT INTERRUPTS
823 ;SET UP PROCESSOR STACK
824 ;SET UP PC R FAIL VECTOR
825 ;CLEAR PROGRAM CONTROL FLAGS AND COUNTS
826 ;TYPE TITLE MESSAGE
827
828 001260 012767 000340 176510 .START: MOV #340,PS ;LOCK OUT INTERRUPTS
829 001266 012706 001100 MOV #STACK,SP ;SET UP STACK
830 001272 012737 007332 000024 MOV #PFAIL,#24 ;SET UP POWER FAIL VECTOR
831 001300 005067 177620 CLR LPCNT ;CLEAR # OF ITERATION COMPLETED LOCATION
832 001304 105067 177713 CLAB STFLG ;CLEAR START FLAG
833 001310 005067 177614 CLR PASCNT ;CLEAR PASS COUNT
834 001314 105067 177704 CLAB ERRFLG ;CLEAR ERROR FLAG
835 001320 005067 177606 CLR ERRCNT ;CLEAR ERROR COUNT
836 001324 005067 177604 CLR LSTERR ;CLEAR LAST ERROR POINTER
837 001330 012767 000001 177570 MOV #1,TESTNO ;SET UP FOR TEST 1
838 001336 012767 001260 177550 MOV #.START,RTRN ;SET UP FOR POWER FAIL BEFORE
839 ;TESTING STARTS
840 001344 105767 177652 TSTB INIFLG ;HAS INITIALIZATION BEEN PERFORMED
841 001350 001004 BNE ONCE
842 001352 104402 007652 TYPE #TITLE ;TYPE TITLE MESSAGE
843 001356 105167 177640 COMB INIFLG ;IF NOT SET FLAG AND DO
844 001362 012767 177570 177510 ONCE: MOV #DSWR,SWR ;RELOAD HARDWARE SWITCH REGISTER INTO POINTER
845 001370 012767 177570 177504 MOV #DLIGHTS,LIGHTS ;RELOAD HARDWARE DISPLAY REGISTER INTO POINTER
846 001376 013746 000006 MOV #4,-(SP) ;SAVE VECTORS
847 001402 013746 000004 MOV #24,-(SP)
848 001406 012737 001426 000004 MOV #64,#24 ;SET UP FOR TIMEOUT
849 001414 022777 177777 177456 CMP #1,DSWR ;REFERENCE HARDWARE SWITCH REGISTER
850 001422 001402 BEQ 658
851 001424 000407 BR 668
852 001426 022626 648: CMP (SP)+,(SP)+ ;ADJUST STACK
853 001430 012767 000176 177442 658: MOV #SWREG,SWR ;POINT TO SOFTWARE SWITCH REG
854 001436 012767 000174 177436 MOV #DIS-REG,LIGHTS ;POINT TO SOFT DISPLAY REG
855 001444 012637 000004 668: MOV (SP)+,#24 ;RESTORE VECTORS
856 001450 012637 000006 MOV (SP)+,#26
857 001454 005737 000042 TST #42 ;UNDER MONITOR
858 001460 001005 BNE 678 ;IS SWREG USED
859 001462 022767 000176 177410 CMP #SWREG,SWR
860 001470 001001 BNE 678
861 001472 104414 CNTLU
862 001474 032777 000001 177376 678: BIT #SHOO,DSWR ;RESELECT VECTOR & CONTROL REG?
863 001502 001002 BNE 18
864 001504 000167 000446 JMP .BEGIN
865 001510 012700 000300 18: MOV #300,R0 ;RESTORE VECTOR AREA TO TRAPCATCHER
866 001514 012701 000302 MOV #302,R1 ;START AT LOCATION 300
867 001520 012702 000004 MOV #4,R2
868 001524 010110 28: MOV R1,(R0)

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869	001526	000011			CLR	(R1)	
870	001530	000020			ADD	R2,R0	
871	001532	000031			ADD	R2,R1	
872	001534	000041	001000		CMP	#1000,R1	;END AT LOCATION 771;
873	001536	000071			BLT	28	
874	001542	104403			INSTR		;OUTPUT MESSAGE & GET INPUT STRING
875	001544	007726			MREGAD		MESSAGE
876	001546	104405			PARAM		CONVERT STRING
877	001550	160000				160000	LOW LIMIT
878	001552	167776				167776	HIGH LIMIT
879	001554	011562			DUBASE		STORE AT THIS LOCATION
880	001556	001				1	MASK
881	001557	001				1	HOW MANY TIMES + 2
882	001560	016767	007776	177420	MOV	DUBASE,KEEPADD	;SAVE
883	001566	004767	007636		JSR	PC,DUBADR	
884	001572	016767	177410	177404	MOV	KEEPADD,BASEADD	;RESTORE FOR ROTATION
885	001600	104403			INSTR		;OUTPUT MESSAGE & GET INPUT STRING
886	001602	007704			MVECTO		MESSAGE
887	001604	104405			PARAM		CONVERT STRING
888	001606	000300				300	LOW LIMIT
889	001610	000776				776	HIGH LIMIT
890	001612	012104			DURIV		STORE AT THIS LOCATION
891	001614	001				1	MASK
892	001615	004				4	HOW MANY TIMES + 2
893	001616	016767	010262	177370	MOV	DURIV,KEEPIV	;SAVE
894	001624	016767	010254	177360	MOV	DURIV,BASEIV	;SET UP FOR ROTATION
895	001632	104403			INSTR		;OUTPUT MESSAGE & GET INPUT STRING
896	001634	010007			MULT		MESSAGE
897	001636	104412			SETFLG		SET FLAG BASED UPON INPUT STRING
898	001640	001202			MULTD		THIS FLAG
899	001642	105767	177334		TSTB	MULTD	;ARE THERE MULTIPLE DEVICES
900							;ON THE SYSTEM ?
901	001646	104406			BMI	B88	;YES,ASK NEXT QUESTION
902	001650	000767	177342		CLR	ACTREG	
903	001654	000767	177340		CLR	ROTADD	
904	001660	000167	000140		JMP	OUTMUL	;JUMP AROUND NEXT QUESTION
905	001664				B88:		
906	001664	104403			INSTR		;OUTPUT MESSAGE & GET INPUT STRING
907	001666	010066			MLASTD		MESSAGE
908	001670	104405			PARAM		CONVERT STRING
909	001672	160000				160000	LOW LIMIT
910	001674	167776				167776	HIGH LIMIT
911	001676	001210			LASTADD		STORE AT THIS LOCATION
912	001700	001				1	MASK
913	001701	001				1	HOW MANY TIMES + 2
914					THE FOLLOWING ROUTINE SETS UP ACTREG FOR THE FIRST TIME		
915	001702	012767	000001	177310	18:	MOV	#1,ROTADD ;SET UP POINTER
916	001710	000767	177302		CLR	ACTREG	;CLR ACTIVE REGISTER
917	001714	000767	177300	177274	28:	BIS	ROTADD,ACTREG ;MAKE THIS DEVICE ACTIVE
918	001722	000241			CLC		
919	001724	006167	177270		ROL	ROTADD	;SET UP POINTER
920	001730	104421			BCS	38	;ARE YOU OUT OF RANGE ?
921	001732	002767	000010	177244	ADD	#10,BASEADD	;SET UP BASE ADDRESS
922	001740	002767	177244	177236	CMP	LASTADD,BASEADD	;IS THIS THE LAST DEVICE ?
923	001746	101362			BMI	28	;NO DO IT AGAIN
924	001750	056767	177244	177240	BIS	ROTADD,ACTREG	;THIS ASSUMES THAT THERE ARE AT

H02

DZDUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 23
DZDUF.C.P11 05-AUG-76 00:00

SEQ 0020

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925          ;LEAST TWO DEVICES WHEN YOU ANSWER YES TO
926          ;MULTIPLE DEVICE QUESTION
927 001756 012767 000001 177234 45:  MOV      ;1. ROTADD ;SET UP FOR LATER USE IN END OF PASS ROUTINE
928 001764 016767 177216 177212  MOV      KEEPADD,BASEADD ;DITTO
929 001772 000414  BR      OUTMUL ;CONTINUE QUESTIONS
930 001774 016767 177206 177202 35:  MOV      KEEPADD,BASEADD ;RESTORE
931 001772 104403  INSTR     ;OUTPUT MESSAGE & GET INPUT STRING
932 001772 010251  MRRANGE  ;MESSAGE
933 001772 104403  PARAM     ;CONVERT STRING
934 001772 160000  160000    ;LOW LIMIT
935 001772 167776  167776    ;HIGH LIMIT
936 001772 001210  LASTADD   ;STORE AT THIS LOCATION
937 001772 001     ;MASK
938 001772 001     ;HOW MANY TIMES + 2
939 001772 000167 177656  JMP      15      ;DO IT AGAIN
940 001772 104403  INSTR     ;OUTPUT MESSAGE & GET INPUT STRING
941 001772 010535  MLEVEL   ;MESSAGE
942 001772 104403  PARAM     ;CONVERT STRING
943 001772 000004  4         ;LOW LIMIT
944 001772 000007  7         ;HIGH LIMIT
945 001772 011424  DUPRT     ;STORE AT THIS LOCATION
946 001772 000     ;MASK
947 001772 001     ;HOW MANY TIMES + 2
948 001772 004767 007306  JSR      PC,DLEV
949 001772 004767 007306  ;COMPARE THE FIRST CHARACTER IN THE TELETYPE INPUT
950 001772 004767 007306  ;BUFFER TO THE CHARACTERS "1" AND "2"
951 001772 004767 007306  ;IF THE CHARACTER IS "1" CLEAR THE FLAG
952 001772 004767 007306  ;IF THE CHARACTER IS "2" SET THE FLAG
953 001772 004767 007306  AAA:
954 001772 004767 007306  INSTR     ;OUTPUT MESSAGE & GET INPUT STRING
955 001772 004767 007306  MSYNC    ;MESSAGE
956 001772 004767 007306  CMPB     8'1,INBUF ;IS IT "1" ?
957 001772 004767 007306  BNE      15      ;000
958 001772 004767 007306  CLAB     SYNCNO
959 001772 004767 007306  BR      45
960 001772 004767 007306  CMPB     8'2,INBUF ;IS IT "2" ?
961 001772 004767 007306  BNE      25
962 001772 004767 007306  MOVB     8-1,SYNCO ;377
963 001772 004767 007306  BR      45
964 001772 004767 007306  25:  INSTR     ;RETRY
965 001772 004767 007306  BR      35
966 001772 004767 007306  45:  NOP
967 001772 004767 007306  INSTR     ;OUTPUT MESSAGE & GET INPUT STRING
968 001772 004767 007306  MIRE6   ;MESSAGE
969 001772 004767 007306  SETFLG  ;SET FLAG BASED UPON INPUT STRING
970 001772 004767 007306  SEXMIT   ;THIS FLAG
971 001772 004767 007306  INSTR     ;OUTPUT MESSAGE & GET INPUT STRING
972 001772 004767 007306  MIRE5   ;MESSAGE
973 001772 004767 007306  SETFLG  ;SET FLAG BASED UPON INPUT STRING
974 001772 004767 007306  SEXMIT   ;THIS FLAG
975 001772 004767 007306  INSTR     ;OUTPUT MESSAGE & GET INPUT STRING
976 001772 004767 007306  MIRE4   ;MESSAGE
977 001772 004767 007306  SETFLG  ;SET FLAG BASED UPON INPUT STRING
978 001772 004767 007306  SEXMIT   ;THIS FLAG
979 001772 004767 007306  INSTR     ;OUTPUT MESSAGE & GET INPUT STRING
980 001772 004767 007306  INSTR

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DZDUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 24
 DZDUF-C.P11 05-AUG-76 00:00

SEQ 0021

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981 002150 011017 NEXTJ ;MESSAGE
982 002152 104412 SETFLG ;SET FLAG BASED UPON INPUT STRING
983 002154 001203 JMRBY ;THIS FLAG
984
985 ;TEST START AND RESTART
986
987 002156 012767 000340 175612 .BEGIN: MOV #340,PS ;LOCK OUT INTERRUPTS
988 002164 012706 001100 MOV #STACK,SP ;SET UP STACK
989 002170 005737 000042 TST #42 ;IS PROGRAM UNDER MONITOR CONTROL
990 002174 001056 BNE 3$
991 002176 105767 177000 TSTB MULTD ;DON'T ALLOW LOCK ON TEST IF RUNNING
992 ;MULTIPLE DEVICES
993 002178 001407 BEQ 5$ ;IF NO TEST FOR LOCK ON TEST
994 002180 016767 003564 003464 MOV BRW,TTST ;RESTORE NORMAL SCOPE LOOP
995 002182 015767 003560 003460 MOV BRX,TTST+2 ;DITTO
996 002184 004444 BR 3$ ;JUMP AROUND IF YES
997 002186 002777 000004 176650 5$: BIT #BIT2,DSWR ;CHECK FOR LOCK ON TEST
998 002188 001416 BEQ 1$
999 002190 104403 INSTR ;OUTPUT MESSAGE & GET INPUT STRING
1000 002192 010472 MLOCK ;MESSAGE
1001 002194 104412 SETFLG ;SET FLAG BASED UPON INPUT STRING
1002 002196 001225 LOKFLG ;THIS FLAG
1003 002198 105767 176757 TSTB LOKFLG ;IS LOCK ON TEST OPTION SELECTED
1004 002200 001407 BEQ 1$
1005 002202 012767 000340 003420 MOV #340,TTST
1006 002204 012767 000340 003414 MOV #340,TTST+2
1007 002206 004444 BR 2$ ;SET UP TO LOCK
1008 002208 016767 003502 003402 1$: MOV BRW,TTST
1009 002210 016767 003576 003376 MOV BRX,TTST+2 ;LOCK NOT SELECTED, SET UP FOR NORMAL SCOPE LOOP
1010 002212 002777 000002 176570 2$: BIT #SM01,DSWR ;IF SM01=1, GET STARTING PC
1011 002214 001410 BEQ 3$
1012 002216 010403 INSTR ;OUTPUT MESSAGE & GET INPUT STRING
1013 002218 010457 MLOCK ;MESSAGE
1014 002220 004444 BR 3$ ;CONVERT STRING
1015 002222 002777 TST1 ;LOW LIMIT
1016 002224 003004 TLAST ;HIGH LIMIT
1017 002226 001114 RTRN ;STORE AT THIS LOCATION
1018 002228 001 .BYTE 1 ;MASK
1019 002230 001 .BYTE 1 ;HOW MANY TIMES + 2
1020 002232 000403 BR 4$
1021 002234 012767 002350 176554 3$: MOV #TTST1,RTRN ;START AT TEST 1
1022 002236 104402 010453 4$: TYPE R ;TYPE R
1023 002238 000177 176544 JMP #RTRN ;START TESTING
1024

```

```

1025      ; THIS TEST VERIFYS MATCH DETECT & DATA RDY
1026      ; FLAGS FOR EVERY POSSIBLE MATCH CHARACTER
1027      ; BY OBSERVING RECACT BIT
1028      ; IT WILL TAKE TWO SYNC * CHARACTERS TO GET RECACT BIT
1029      ; DEPENDENT ON MONITOR .....
1030      ; IF ONE SYNC STRAP IS SELECTED IT WILL
1031      ; ONLY TAKE ONE SYNC CHARACTER BEFORE RECACT TO
1032      ; ASSERT
1033      ; MODE: SYNC INTERNAL
1034      ; LENGTH: FIVE
1035      ; SYNC CHARACTER FOR MATCH: B/C
1036      ; THIS TEST USES THE TRANSMITTER AND RECEIVER CHIPS
1037
1038 002350 012767 000001 176550 TST1:  MOV    #1,TSTNO      ;SAVE THIS
1039 002356 012767 002676 176532      MOV    #TST2,NEXT      ;GO TO THIS TEST WHEN THRU
1040 002364 012767 000000 176526      MOV    #3$,LOCK      ;SET UP FOR SCOPE LOOP
1041 002372 052777 000000 007474      BIS    #MRESET,ATXCSR  ;MASTER RESET
1042 002400 016703 007460      MOV    RXBUF,R3      ;SET UP FOR ERROR MESSAGE
1043      ;SET SYNC INTERNAL FIVE NO PARITY 0 SYNC REGISTER
1044 002404 012704 030000      MOV    #SYNINTFIVE,NOPAR,R4 ;CREATE PARAMETERS
1045 002410 012777 000000 007456 1$:  MOV    #MINT!SEND,ATXCSR  ;SET SEND & MAINT INTER
1046 002416 010477 007446      MOV    R4,PARCSR      ;LOAD CSR
1047 002422 052777 000020 007430      BIS    #SYNSCH,ATXCSR  ;SET SYNC SEARCH
1048      ;POKE CLK TO GET INTO SYNCHRONIZATION
1049      ;BOTH THE LOGIC & RECEIVER
1050 002430 052777 020000 007436      BIS    #CLK,ATXCSR     ;POKE CLK UP
1051 002436 042777 000000 007430      BIC    #CLK,ATXCSR     ;POKE CLK DOWN
1052 002444 110477 007430      MOV    R4,ATXBUF      ;LOAD DATA CHARACTER
1053      ;POKE CLK TO GET TRANSMITTER & RECEIVER INTO SYNCHRONIZATION
1054 002450 052777 020000 007416      BIS    #CLK,ATXCSR     ;POKE CLK UP
1055 002456 042777 020000 007410      BIC    #CLK,ATXCSR     ;POKE CLK DOWN
1056 002464 032777 004000 007366      BIT    #RECACT,ATXCSR ;RECACT ?
1057 002472 001401      BEQ    2$
1058 002474 104000      PLT
1059      ;RECACT SHOULD NOT BE SET
1060 002476 000404 2$:  BR      4$
1061 002500 010477 007364 3$:  MOV    R4,PARCSR      ;LOAD PARCSR WITH PARAMETERS
1062 002504 110477 007370      MOV    R4,ATXBUF      ;LOAD SYNC CHAR
1063 002510 012767 000002 176424 4$:  MOV    #2,COUNT      ;# OF SYNC CHARS
1064 002516 005777 007352 5$:  TST    ATXCSR      ;DNA ?
1065 002522 100001      BPL      6$
1066 002524 104000      PLT      ;DNA SHOULD NOT BE SET OR....
1067 002526      6$:
1068      ;IT SHOULD BE CLEARED FROM PREVIOUS READ
1069 002526 012767 000005 176404 7$:  MOV    #5,SHIFT      ;# OF SHIFTS
1070 002534      7$:
1071 002534 052777 020000 007332      BIS    #CLK,ATXCSR     ;POKE CLK UP
1072 002542 042777 000000 007324      BIC    #CLK,ATXCSR     ;POKE CLK DOWN
1073 002550 005367 176364      DEC    SHIFT      ;# OF SHIFTS
1074 002554 001367 7$:  BNE      7$
1075 002556 005367 176360      DEC    COUNT      ;# OF SYNC CHARS
1076 002562 001403      BEQ    8$
1077      ;TEST SYNCNO TO SEE HOW MANY SYNC CHARACTERS NEEDED
1078 002564 105767 176406      TSTB   SYNCNO
1079 002570 100752      BMI      8$
1080 002572 032777 004000 007260 8$:  BIT    #RECACT,ATXCSR ;RECACT ?

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K02

DZDUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 26
DZDUF-C.P11 05-AUG-76 00:00

SEQ 0023

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1081 002600 001001      BNE      95
1082 002602 104000      HLT
1083 002604              95:      ;REACT FAILED TO SET,POSSIBLE
1084              ;THAT THE RECEIVER FAILED TO MATCH
1085              ;THE SYNC CHARACTER
1086 002604 017701 007254  MOV     2PXDBUF,R1      ;SAVE ACTUAL
1087 002610 010400      MOV     R4,R0      ;SAVE EXPECTED
1088 002612 042700 177400  BIC     2177400,R0    ;CLR UPPER BYTE
1089 002616 020001      CMP     R0,R1      ;DO THEY COMPARE ?
1090 002620 0C1401      BEQ     105
1091 002622 104002      HLT     2
1092 002624              105:     ;IF REACT FAILED ALONG WITH THIS
1093              ;...IT PROBABLY IS A TRANSMITTER ERROR
1094              ;HOWEVER...IF ONLY THIS FAILED IT
1095              ;PROBABLY IS A RECEIVER ERROR
1096 002624 104401      SCOPE1
1097              ;POKE CLK TO SEE DNA...DNA COMES UP ON THE FIRST
1098              ;BIT OF THE NEXT CHARACTER IF NO CHARACTER IS LOADED INTO
1099              ;TXDBUF
1100 002626 052777 020000 007240  BIS     2CLK,2TXCSR    ;POKE CLK UP
1101 002634 005777 007234      TST     2TXCSR    ;DNA?
1102 002640 100401      BMI     115
1103 002642 104000      HLT     ;DNA DID NOT ASSERT
1104 002644              115:
1105              ;SET UP CONDITIONS FOR NEXT SYNC CHARACTER MATCH
1106 002644 052777 000400 007222  BIS     2MRESET,2TXCSR ;MASTER RESET
1107 002652 032777 000020 007200  BIT     2SYNSCH,2TXCSR ;SYNC SEARCH = 0 ?
1108 002660 001401      BEQ     125
1109 002662 104000      HLT     ;SYNC SEARCH SHOULD BE NOT SET
1110 002664              125:
1111 002664 005204      INC     R4
1112 002666 122704 000040      CMPB    240,R4    ;IS THIS THE LAST CHARACTER ?
1113 002672 001246      BNE     15
1114 002674 104400      SCOPE    15      ;NO

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1115      ;; THIS TEST VERIFYS MATCH DETECT & DATA RDY
1116      ;; FLAGS FOR EVERY POSSIBLE MATCH CHARACTER
1117      ;; BY OBSERVING RECACT BIT
1118      ;; IT WILL TAKE TWO SYNC * CHARACTERS TO GET RECACT BIT
1119      ;; * DEPENDENT ON MONITOR
1120      ;; IF ONE SYNC STRAP IS SELECTED IT WILL
1121      ;; ONLY TAKE ONE SYNC CHARACTER BEFORE RECACT TO
1122      ;; ASSERT
1123      ;; MODE: SYNC INTERNAL
1124      ;; LENGTH: SIX
1125      ;; SYNC CHARACTER FOR MATCH: B/C
1126      ;; THIS TEST USES THE TRANSMITTER AND RECEIVER CHIPS
1127
1128 002676 012767 000002 176222 TST2:  MOV    #2,TSTNO      ;SAVE THIS
1129 002704 012767 003224 176204      MOV    #TST3,NEXT      ;GO TO THIS TEST WHEN THRU
1130 002712 012767 003026 176200      MOV    #3$,LOCK      ;SET UP FOR SCOPE LOOP
1131 002720 052777 000400 007146      BIS    #MRESET,ATXCSR  ;MASTER RESET
1132 002726 016703 007132      MOV    RXDBUF,R3      ;SET UP FOR ERROR MESSAGE
1133      ;SET SYNC INTERNAL SIX NO PARITY 0 SYNC REGISTER
1134 002732 012704 032000      MOV    #SYNINT!SIX!NOPAR,R4  ;CREATE PARAMETERS
1135 002736 012777 004020 007130 1$:  MOV    #MINT!SEND,ATXCSR  ;SET SEND & MAINT INTER
1136 002744 010477 007120      MOV    R4,APARCSR      ;LOAD CSR
1137 002750 052777 000020 007102      BIS    #SYNSCH,ARXCSR  ;SET SYNC SEARCH
1138      ;POKE CLK TO GET INTO SYNCHRONIZATION
1139      ;BOTH THE LOGIC & RECEIVER
1140 002756 052777 020000 007110      BIS    #CLK,ATXCSR      ;POKE CLK UP
1141 002764 042777 020000 007102      BIC    #CLK,ATXCSR      ;POKE CLK DOWN
1142 002772 110477 007102      MOVB    R4,ATXDBUF      ;LOAD DATA CHARACTER
1143      ;POKE CLK TO GET TRANSMITTER & RECEIVER INTO SYNCHRONIZATION
1144 002776 052777 020000 007070      BIS    #CLK,ATXCSR      ;POKE CLK UP
1145 003004 042777 020000 007062      BIC    #CLK,ATXCSR      ;POKE CLK DOWN
1146 003012 032777 004000 007040      BIT    #RECACT,ARXCSR  ;RECACT ?
1147 003020 001401      BEQ    2$
1148 003022 104000      HLT
1149 003024      2$:
1150 003024 000404      BR    4$
1151 003026 010477 007036      3$:  MOV    R4,APARCSR      ;LOAD PARCSR WITH PARAMETERS
1152 003032 110477 007042      MOVB    R4,ATXDBUF      ;LOAD SYNC CHAR
1153 003036 012767 000002 176076 4$:  MOV    #2,COUNT      ;# OF SYNC CHARS
1154 003044 005777 007024      5$:  TST    ATXCSR      ;DNA ?
1155 003050 100001      BPL    6$      ;BR IF NOT SET
1156 003052 104000      HLT      ;DNA SHOULD NOT BE SET OR....
1157 003054      6$:
1158      ;IT SHOULD BE CLEARED FROM PREVIOUS READ
1159 003054 012767 000006 176056      MOV    #6,SHIFT      ;# OF SHIFTS
1160 003062      7$:
1161 003062 052777 020000 007004      BIS    #CLK,ATXCSR      ;POKE CLK UP
1162 003070 042777 020000 006776      BIC    #CLK,ATXCSR      ;POKE CLK DOWN
1163 003076 005367 176036      DEC    SHIFT      ;# OF SHIFTS
1164 003102 001367      INC    7$
1165 003104 005367 176032      DEC    COUNT      ;# OF SYNC CHARS
1166 003110 001403      BEQ    8$
1167      ;TEST SYNCNO TO SEE HOW MANY SYNC CHARACTERS NEEDED
1168 003112 105767 176060      TSTB    SYNCNO
1169 003116 100752      BMI    5$      ;TWO SYNC CHARACTERS..
1170 003120 032777 004000 006732 8$:  BIT    #RECACT,ARXCSR  ;RECACT ?

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M02

DZDUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 28
 DZDUF-C.P11 05-AUG-76 00:00

SEQ 0025

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1171 003126 001001      BNE      9S
1172 003130 104000      HLT
1173 003132              9S:
1174                      ;REACT FAILED TO SET, POSSIBLE
1175                      ;THAT THE RECEIVER FAILED TO MATCH
1176                      ;THE SYNC CHARACTER
1176 003132 017701 006726 MOV      2RXDBUF,R1      ;SAVE ACTUAL
1177 003136 010400      MOVB     R4,R0      ;SAVE EXPECTED
1178 003140 042700 177400 BIC      8177400,R0      ;CLR UPPER BYTE
1179 003144 020001      CMP      R0,R1      ;DO THEY COMPARE ?
1180 003146 001401      BEQ      10S
1181 003150 104002      HLT      2
1182 003152              10S:
1183                      ;...IT PROBABLY IS A TRANSMITTER ERROR
1184                      ;HOWEVER,...IF ONLY THIS FAILED IT
1185                      ;PROBABLY IS A RECEIVER ERROR
1186 003152 104401      SCOPI
1187                      ;POKE CLK TO SEE DNA...DNA COMES UP ON THE FIRST
1188                      ;BIT OF THE NEXT CHARACTER IF NO CHARACTER IS LOADED INTO
1189                      ;TXDBUF
1190 003154 052777 020000 006712 BIS      @CLK,@TXCSR      ;POKE CLK UP
1191 003162 005777 006706      TST      @TXCSR      ;DNA?
1192 003166 100401      BMI      11S
1193 003170 104000      HLT      ;DNA DID NOT ASSERT
1194 003172              11S:
1195                      ;SET UP CONDITIONS FOR NEXT SYNC CHARACTER MATCH
1196 003172 052777 000400 006674 BIS      @MRESET,@TXCSR      ;MASTER RESET
1197 003200 032777 000020 006652 BIT      @SYNSCH,@TXCSR      ;SYNC SEARCH = 0 ?
1198 003206 001401      BEQ      12S
1199 003210 104000      HLT      ;SYNC SEARCH SHOULD BE NOT SET
1200 003212              12S:
1201 003212 005204      INC      R4
1202 003214 122704 000100      CMPEB   8100,R4      ;IS THIS THE LAST CHARACTER ?
1203 003220 001246      BNE      1S
1204 003222 104400      SCOPE
  
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1205      ; THIS TEST VERIFYS MATCH DETECT & DATA RDY
1206      ; FLAGS FOR EVERY POSSIBLE MATCH CHARACTER
1207      ; BY OBSERVING RECACT BIT
1208      ; IT WILL TAKE TWO SYNC * CHARACTERS TO GET RECACT BIT
1209      ; * DEPENDENT ON MONITOR
1210      ; IF ONE SYNC STRAP IS SELECTED, IT WILL
1211      ; ONLY TAKE ONE SYNC CHARACTER BEFORE RECACT TO
1212      ; ASSERT
1213      ; MODE: SYNC INTERNAL
1214      ; LENGTH: SEVEN
1215      ; SYNC CHARACTER FOR MATCH: B/C
1216      ; THIS TEST USES THE TRANSMITTER AND RECEIVER CHIPS
1217
1218 003224 012767 000003 175674 TST3: MOV #3,TSTNO ;SAVE THIS
1219 003232 012767 003552 175656      MOV #TST4,NEXT ;GO TO THIS TEST WHEN THRU
1220 003240 012767 003354 175652      MOV #3$,LOCK ;SET UP FOR SCOPE LOOP
1221 003246 052777 000400 006620      BIS #MRESET,ATXCSR ;MASTER RESET
1222 003254 016703 006604      MOV RXDBUF,R3 ;SET UP FOR ERROR MESSAGE
1223      ;SET SYNC INTERNAL, SEVEN, NO PARITY, 0 SYNC REGISTER
1224 003260 012704 034000      MOV #SYNINT!SEVEN!NOPAR,R4 ;CREATE PARAMETERS
1225 003264 012777 004020 006602 1$: MOV #MINT!SEND,ATXCSR ;SET SEND & MAINT INTER
1226 003272 010477 006572      MOV R4,APARCSR ;LOAD CSR
1227 003276 052777 000020 006554      BIS #SYNSCH,ATXCSR ;SET SYNC SEARCH
1228      ;POKE CLK TO GET INTO SYNCHRONIZATION
1229      ;BOTH THE LOGIC & RECEIVER
1230 003304 052777 020000 006562      BIS #CLK,ATXCSR ;POKE CLK UP
1231 003312 042777 020000 006554      BIC #CLK,ATXCSR ;POKE CLK DOWN
1232 003320 110477 006554      MOVB R4,ATXDBUF ;LOAD DATA CHARACTER
1233      ;POKE CLK TO GET TRANSMITTER & RECEIVER INTO SYNCHRONIZATION
1234 003324 052777 020000 006542      BIS #CLK,ATXCSR ;POKE CLK UP
1235 003332 042777 020000 006534      BIC #CLK,ATXCSR ;POKE CLK DOWN
1236 003340 032777 004000 006512      BIT #RECACT,ATXCSR ;RECACT ?
1237 003346 001401      BEQ 2$
1238 003350 104000      HLT ;RECACT SHOULD NOT BE SET
1239 003352      2$:
1240 003352 000404      BR 4$
1241 003354 010477 006510      3$: MOV R4,APARCSR ;LOAD PARCSR WITH PARAMETERS
1242 003360 110477 006514      MOVB R4,ATXDBUF ;LOAD SYNC CHAR
1243 003364 012767 000002 175550 4$: MOV #2,COUNT ;# OF SYNC CHARS
1244 003372 005777 006476      5$: TST ATXCSR ;DNA ?
1245 003376 100001      BPL 6$ ;BR IF NOT SET
1246 003400 104000      HLT ;DNA SHOULD NOT BE SET OR....
1247 003402      6$:
1248      ;IT SHOULD BE CLEARED FROM PREVIOUS READ
1249 003402 012767 000007 175530      7$: MOV #7,SHIFT ;# OF SHIFTS
1250 003410      BIS #CLK,ATXCSR ;POKE CLK UP
1251 003410 052777 020000 006456      BIC #CLK,ATXCSR ;POKE CLK DOWN
1252 003416 042777 020000 006450      DEC SHIFT ;# OF SHIFTS
1253 003424 005367 175510      BNE 7$
1254 003430 001367      DEC COUNT ;# OF SYNC CHARS
1255 003432 005367 175504      BEQ 8$
1256 003436 001403      ;TEST SYNCNO TO SEE HOW MANY SYNC CHARACTERS NEEDED
1257      TSTB SYNCNO
1258 003440 105767 175532      BMI 5$ ;TWO SYNC CHARACTERS..
1259 003444 100752      8$: BIT #RECACT,ATXCSR ;RECACT ?
1260 003446 032777 004000 006404

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1261 003454 001001      BNE      95
1262 003456 104000      HLT
1263 003460              95:
1264
1265                      ;RECAT FAILED TO SET, POSSIBLE
1266 003460 017701 006400  MOV      2RXDBUF, A1      ;SAVE ACTUAL
1267 003464 010400      MOV      R4, R0      ;SAVE EXPECTED
1268 003466 042700 177400  BIC      2177400, R0      ;CLR UPPER BYTE
1269 003472 020001      CMP      R0, R1      ;DO THEY COMPARE ?
1270 003474 001401      BEQ      105
1271 003476 104002      HLT      2
1272 003500              105:
1273
1274                      ;...IT PROBABLY IS A TRANSMITTER ERROR
1275                      ;HOWEVER...IF ONLY THIS FAILED IT
1276 003500 104401      SCOPE1
1277                      ;POKE CLK TO SEE DNA...DNA COMES UP ON THE FIRST
1278                      ;BIT OF THE NEXT CHARACTER IF NO CHARACTER IS LOADED INTO
1279                      ;TXDBUF
1280 003512 052777 020000 006364  BIS      2CLK, 2TXCSR      ;POKE CLK UP
1281 003510 055777 006360      TST      2TXCSR      ;DNA?
1282 003514 106401      BMI      115
1283 003516 104000      HLT      ;DNA DID NOT ASSERT
1284 003520              115:
1285                      ;SET UP CONDITIONS FOR NEXT SYNC CHARACTER MATCH
1286 003520 052777 000400 006346  BIS      2MRESET, 2TXCSR      ;MASTER RESET
1287 003526 032777 000020 006324  BIT      2SYNSCH, 2TXCSR      ;SYNC SEARCH = 0 ?
1288 003534 001401      BEQ      125
1289 003536 104000      HLT      ;SYNC SEARCH SHOULD BE NOT SET
1290 003540              125:
1291 003540 005204      INC      R4
1292 003542 122704 000200      CMPB     200, R4      ;IS THIS THE LAST CHARACTER ?
1293 003546 001246      BNE
1294 003550 104400      SCOPE      15      ;NO

```

```

1295      THIS TEST VERIFYS MATCH DETECT & DATA RDY
1296      FLAGS FOR EVERY POSSIBLE MATCH CHARACTER
1297      BY OBSERVING RECACT BIT
1298      IT WILL TAKE TWO SYNC * CHARACTERS TO GET RECACT BIT
1299      * DEPENDENT ON MONITOR .....
1300      IF ONE SYNC STRAP IS SELECTED IT WILL
1301      ONLY TAKE ONE SYNC CHARACTER BEFORE RECACT TO
1302      ASSERT
1303      MODE: SYNC INTERNAL
1304      LENGTH: EIGHT
1305      SYNC CHARACTER FOR MATCH: B/C
1306      THIS TEST USES THE TRANSMITTER AND RECEIVER CHIPS
1307
1308 003552 012767 000004 175346 TST4: MOV R4,TSTNO ;SAVE THIS
1309 003560 012767 004100 175330      MOV R1,TST5 NEXT ;GO TO THIS TEST WHEN THRU
1310 003566 012767 003702 175324      MOV R3,LOCK ;SET UP FOR SCOPE LOOP
1311 003574 052777 000400 006272      BIS R4,SET,ATXCSR ;MASTER RESET
1312 003602 016703 006256      MOV R0,RJF,R3 ;SET UP FOR ERROR MESSAGE
1313      ;SET SYNC INTERNAL,EIGHT NO PARITY,0 SYNC REGISTER
1314 003606 012704 035000      MOV R5,INT: EIGHT: NOPAR, R4 ;CREATE PARAMETERS
1315 003612 012777 004020 006254 15: MOV R1,INT: SEND, ATXCSR ;SET SEND & MAINT INTER
1316 003620 010477 005244      MOV R4,PARCSR ;LOAD CSR
1317 003624 052777 000020 006226      BIS R5,SYNSCH,ATXCSR ;SET SYNC SEARCH
1318      ;POKE CLK TO GET INTO SYNCHRONIZATION
1319      ;BOTH THE LOGIC & RECEIVER
1320 003632 052777 020000 006234      BIS R5,CLK,ATXCSR ;POKE CLK UP
1321 003640 042777 020000 006226      BIC R5,CLK,ATXCSR ;POKE CLK DOWN
1322 003646 110477 006226      MOV R4,ATXCSR ;LOAD DATA CHARACTER
1323      ;POKE CLK TO GET TRANSMITTER & RECEIVER INTO SYNCHRONIZATION
1324 003652 052777 020000 006214      BIS R5,CLK,ATXCSR ;POKE CLK UP
1325 003658 042777 020000 006206      BIC R5,CLK,ATXCSR ;POKE CLK DOWN
1326 003664 032777 004000 006164      BIT R5,RECACT,ATXCSR ;RECACT ?
1327 003674 001401      BEQ 25
1328 003676 104000      HLT ;RECACT SHOULD NOT BE SET
1329 003700      25:
1330 003700 000404      BR 45
1331 003702 010477 006162      35: MOV R4,PARCSR ;LOAD PARCSR WITH PARAMETERS
1332 003706 110477 006166      MOV R4,ATXCSR ;LOAD SYNC CHAR
1333 003712 012767 000002 175222 45: MOV R2,COUNT ;# OF SYNC CHARS
1334 003720 052777 006150      55: TST ATXCSR ;DNA ?
1335 003724 100701      BPL 65 ;BR IF NOT SET
1336 003726 104000      HLT ;DNA SHOULD NOT BE SET OR....
1337 003730      65:
1338      ;IT SHOULD BE CLEARED FROM PREVIOUS READ
1339      68.,SHIFT ;# OF SHIFTS
1340 003736      75: MOV
1341 003736 052777 020000 006130      BIS R5,CLK,ATXCSR ;POKE CLK UP
1342 003744 042777 020000 006122      BIC R5,CLK,ATXCSR ;POKE CLK DOWN
1343 003752 005367 175162      DEC SHIFT ;# OF SHIFTS
1344 003756 001367      BNE 75
1345 003760 005367 175156      DEC COUNT ;# OF SYNC CHARS
1346 003764 001403      BEQ 85
1347      ;TEST SYNCNO TO SEE HOW MANY SYNC CHARACTERS NEEDED
1348 003766 105767 175204      TSTB SYNCNO
1349 003772 100752      BMI 55 ;TWO SYNC CHARACTERS..
1350 003774 032777 004000 006056 85: BIT R5,RECACT,ATXCSR ;RECACT ?

```

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1351 004002 001001      BNE      99
1352 004004 104000      HLT
1353 004006      98:
1354      ;REACT FAILED TO SET, POSSIBLE
1355      ;THAT THE RECEIVER FAILED TO MATCH
1356      ;THE SYNC CHARACTER
1357 004006 017701 006052  MOV      2RXDBUF, R1      ;SAVE ACTUAL
1358 004012 010400      MOV      R4, R0      ;SAVE EXPECTED
1359 004014 042700 177400  B*      8177400, R0      ;CLR UPPER BYTE
1360 004020 020001      CMP      R0, R1      ;DO THEY COMPARE ?
1361 004022 001401      BEQ      108
1362 004024 104002      HLT      2
1363      108:
1364      ;IF REACT FAILED ALONG WITH THIS
1365      ;... IT PROBABLY IS A TRANSMITTER ERROR
1366      ;HOWEVER, IF ONLY THIS FAILED IT
1367      ;PROBABLY IS A RECEIVER ERROR
1368 004026 104401      SCOPE1
1369      ;POKE CLK TO SEE DNA... DNA COMES UP ON THE FIRST
1370      ;BIT OF THE NEXT CHARACTER IF NO CHARACTER IS LOADED INTO
1371      ;TXDBUF
1372 004030 052777 020000 006036  BIS      @CLK, 2TXCSR      ;POKE CLK UP
1373 004036 005777 006032      TST      2TXCSR      ;DNA?
1374 004042 100401      BMI      118
1375 004044 104000      HLT      ;DNA DID NOT ASSERT
1376      118:
1377      ;SET UP CONDITIONS FOR NEXT SYNC CHARACTER MATCH
1378 004046 052777 000400 006020  BIS      @SET, 2TXCSR      ;MASTER RESET
1379 004054 001777 000020 005776  BIT      @SYNSCH, 2RXCSR      ;SYNC SEARCH = 0 ?
1380 004062 001401      BEQ      128
1381 004064 104000      HLT      ;SYNC SEARCH SHOULD BE NOT SET
1382      128:
1383 004066 005204      INC      R4
1384 004070 122704 000000      CMPB     @0, R4      ;IS THIS THE LAST CHARACTER ?
1385 004074 001246      BNE
1386 004076 104400      SCOPE

```

004100	012767	000005	175020
004106	012767	004312	175002
004114	012777	000400	005752
004122	012777	000000	005740
004130	052777	000400	005736
004136	012777	004020	005730
004144	012777	027426	005716
004152	016703	005706	
004156	005004		
004160	110477	005714	
004164	052777	000020	005666
004172	052777	020000	005674
004200	042777	020000	005656
004206	012767	000011	174724
004214	010400		
004216			
004216	052777	020000	005650
004224	042777	020000	005642
004232	005367	174702	
004236	022767	000003	174674
004244	001003		
004246	005204		
004250	110477	005624	
004254	005767	174660	
004260	001356		
004262	105777	005572	
004266	100401		
004270	104000		
004272			
004272	017701	005566	
004276	020001		
004300	001401		
004302	104002		
004304			
004304	105704		
004306	001337		
004310	104400		

```

TSTS:  MOV      $5,TSTNO      ;SAVE THIS
        MOV      $7,6,NEXT    ;GO TO THIS TEST WHEN THRU
        BIS      #1,XT,@TXCSR ;MASTER RESET
        MOV      $5,XT,@TXCSR ;SET THE MODE
        BIS      #MRESET,@TXCSR ;MASTER RESET

;SET MAINTENANCE MODE & SEND
;NOTE:BIT W/DON'T CLK ARE CLEARED (MTDATA=0)
        MOV      #MINT!SEND,@TXCSR

;SET MODE # OF BITS,PARITY SENSE & LOAD SYNC REG
        MOV      #SYNEXT!EIGHT!EVEPAR!26,@PARCSR
        MOV      R0,R3        ;SETUP FOR ERROR MSG
        CLR      R4           ;FOR DATA CHAR CREATION
        MOVB     R4,@TXDBUF    ;LOAD CHARACTER
        BIS      #SYNCH,@TXCSR ;SET SEARCH SYNC

;GET INTO SYNCHRONIZATION
        BIS      #CLK,@TXCSR   ;POKE CLK UP
        BIC      #CLK,@TXCSR   ;POKE CLK DOWN
18:     MOV      #9,SHIFT      ;# OF SHIFTS
        MOV      R4,R0        ;EXPECTED

28:     BIS      #CLK,@TXCSR    ;POKE CLK UP
        BIC      #CLK,@TXCSR    ;POKE CLK DOWN
        DEC      SHIFT         ;# OF SHIFTS
        CMP      #3,SHIFT      ;TIME TO LOAD NEXT CHAR ?
        BNE      38            ;NO ?
        INC      R4            ;GENERATE NEXT CHAR
        MOVB     R4,@TXDBUF     ;LOAD NEXT CHARACTER
38:     TST      SHIFT          ;IS IT 0 ?
        BNE      28            ;NO
        TSTB     @TXCSR        ;RXDONE = 1 ?
        BMI      58            ;RXDONE SHOULD BE SET
58:     MOV      @TXDBUF,R1     ;ACTUAL
        CMP      R0,R1         ;COMPARE EXP VS ACT
        BEQ      68            ;CHARACTERS SHOULD COMPARE
68:     HLT      2
68:     TSTB     R4            ;LAST CHARACTER ?
        BNE      18            ;NO
48:     SCOPE

```

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1435      ; THIS TEST PERFORMS A BINARY COUNT DATA PATTERN ON
1436      ; BOTH THE TRANSMITTER AND RECEIVER LOGIC
1437      ; MODE: SYNC EXTERNAL (SYNEXT)
1438      ; LENGTH: EIGHT PLUS PARITY
1439      ; PARITY: 000PAR
1440      ; MAINT. MODE: MINT
1441
1442      004312 012767 000006 174606 TST6:  MOV  #6,TSTNO      ;SAVE THIS
1443      004320 012767 004524 174570      MOV  #TST7,NEXT      ;GO TO THIS TEST WHEN THRU
1444      004326 052777 000400 005540      BIS  #ESET,ATXCSR    ;MASTER RESET
1445      004334 012777 020000 005526      MOV  #SYNEXT,APARCSR ;SET THE MODE
1446      004342 052777 000400 005524      BIS  #MRESET,ATXCSR   ;MASTER RESET
1447
1448      ;SET MAINTENANCE MODE & SEND
1449      ;NOTE: BIT WINDOWS CLK ARE CLEARED (MTDATA=0)
1450      004350 012777 004020 005516      MOV  #MINT,SEND,ATXCSR
1451
1452      ;SET MODE # OF BITS, PARITY SENSE & LOAD SYNC REG
1453      004356 012777 027026 005504      MOV  #SYNEXT,EIGHT,000PAR,26,APARCSR
1454      004364 016703 005474      MOV  #RXDBUF,R3      ;SETUP FOR ERROR MSG
1455      004370 005004      CLR  R4      ;FOR DATA CHAR CREATION
1456      004372 110477 005502      MOV  #R4,ATXDBUF    ;LOAD CHARACTER
1457      004376 052777 000020 005454      BIS  #SYNSCH,ATXCSR  ;SET SEARCH SYNC
1458
1459      ;GET INTO SYNCHRONIZATION
1460      004404 052777 020000 005462      BIS  #CLK,ATXCSR    ;POKE CLK UP
1461      004412 042777 020000 005454      BIC  #CLK,ATXCSR    ;POKE CLK DOWN
1462      004420 012767 000011 174512 15:  MOV  #9,SHIFT      ;# OF SHIFTS
1463      004426 010400      MOV  R4,R0      ;EXPECTED
1464
1465      25:  BIS  #CLK,ATXCSR    ;POKE CLK UP
1466      004430 052777 020000 005436      BIC  #CLK,ATXCSR    ;POKE CLK DOWN
1467      004436 042777 020000 005430      DEC  SHIFT      ;# OF SHIFTS
1468      004444 005367 174470      CMP  #3,SHIFT      ;TIME TO LOAD NEXT CHAR ?
1469      004450 022767 000003 174462      BNE  #3      ;NO ?
1470      004456 001003      INC  R4      ;GENERATE NEXT CHAR
1471      004460 005204      MOV  #R4,ATXDBUF    ;LOAD NEXT CHARACTER
1472      004462 110477 005412      TST  SHIFT      ;IS IT 0 ?
1473      004466 005767 174446 35:  BNE  #25      ;
1474      004472 001356      TST  #ATXCSR    ;RXDONE = 1 ?
1475      004474 105777 005360      BMI  #55      ;RXDONE SHOULD BE SET
1476      004480 100401      HLT
1477      004482 104000 55:  MOV  #RXDBUF,R1      ;ACTUAL
1478      004484 017701 005354      CMP  R0,R1      ;COMPARE EXP VS ACT
1479      004486 020001      BEQ  #65      ;
1480      004488 001401      HLT 2      ;CHARACTERS SHOULD COMPARE
1481      004490 104002 65:  TST  #R4      ;LAST CHARACTER ?
1482      004492 105704      BNE  #15      ;NO
1483      004494 001337      45:  SCOPE
1484      004496 104400

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1495      ; THIS TEST PERFORMS A BINARY COUNT DATA PATTERN ON
1496      ; BOTH THE TRANSMITTER AND RECEIVER LOGIC
1497      ; MODE: SYNC EXTERNAL (SYNEXT)
1498      ; LENGTH: EIGHT PLUS PARITY
1499      ; PARITY: EVEPAR
1500      ; MAINT. MODE: NEXT
1501
1502      004524 012767 000007 174374 TST7:  MOV     #7,TSTNO      ;SAVE THIS
1503      004532 012767 000004 174356      MOV     #TSTB,NEXT      ;GO TO THIS TEST WHEN THRU
1504      004540 105767 174437      TSTB     JMRBY      ;JUMP AROUND TEST ?
1505      004544 100116      BPL      45      ;YES ?
1506      004546 052777 000400 005320      BIS     #MRESET,ATXCSR ;MASTER RESET
1507      004554 012777 020000 005306      MOV     #SYNEXT,SPARCSR ;SET THE MODE
1508      004562 052777 000400 005304      BIS     #MRESET,ATXCSR ;MASTER RESET
1509
1510      ;SET MAINTENANCE MODE & SEND
1511      ;NOTE: BIT WINDOW&CLK ARE CLEARED (MTDATA=0)
1512      004570 012777 010020 005276      MOV     #NEXT!SEND,ATXCSR
1513
1514      ;SET MODE, # OF BITS, PARITY SENSE & LOAD SYNC REG
1515      004576 012777 027426 005264      MOV     #SYNEXT!EIGHT!EVEPAR!26,SPARCSR
1516      004604 016703 005254      MOV     RXDBUF,R3      ;SETUP FOR ERROR MSG
1517      004610 005004      CLR      R4      ;FOR DATA CHAR CREATION
1518      004612 110477 005262      MOV     R4,ATXDBUF      ;LOAD CHARACTER
1519      004616 052777 000020 005234      BIS     #SYNSCH,ATXCSR ;SET SEARCH SYNC
1520
1521      ;GET INTO SYNCHRONIZATION
1522      004624 052777 020000 005242      BIS     #CLK,ATXCSR ;POKE CLK UP
1523      ;WAIT FOR CABLE & DRIVER DELAYS
1524      004632 016702 174300      MOV     HOLD,R2 ;WAIT THIS AMT
1525
1526      648:  DEC     R2      ;WAIT
1527      004636 005302      BNE     648
1528      004640 001376      ;EXIT...
1529
1530      004642 042777 020000 005224      BIC     #CLK,ATXCSR ;POKE CLK DOWN
1531      ;WAIT FOR CABLE & DRIVER DELAYS
1532      004650 016702 174262      MOV     HOLD,R2 ;WAIT THIS AMT
1533
1534      658:  DEC     R2      ;WAIT
1535      004654 005302      BNE     658
1536      004656 001376      ;EXIT...
1537
1538      004660 012767 000011 174252 18:  MOV     #9,SHIFT      ;# OF SHIFTS
1539      004666 010400      MOV     R4,R0      ;EXPECTED
1540
1541      28:  BIS     #CLK,ATXCSR ;POKE CLK UP
1542      ;WAIT FOR CABLE & DRIVER DELAYS
1543      004670 052777 020000 005176      MOV     HOLD,R2 ;WAIT THIS AMT
1544
1545      668:  DEC     R2      ;WAIT
1546      004676 016702 174234      BNE     668
1547      004702 005302      ;EXIT...
1548      004704 001376      BIC     #CLK,ATXCSR ;POKE CLK DOWN
1549      ;WAIT FOR CABLE & DRIVER DELAYS
1550      004706 042777 020000 005160      MOV     HOLD,R2 ;WAIT THIS AMT
1551
1552      678:  DEC     R2      ;WAIT
1553      004714 016702 174216      BNE     678
1554      004720 005302
1555      004722 001376

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H03

DZDUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 36
 DZDUF0.P11 05-AUG-76 00:00

SEQ 0033

1541					:EXIT...
1542	004724	005367	174210		DEC SHIFT ;# OF SHIFTS
1543	004730	022767	000003	174202	38,SHIFT ;TIME TO LOAD NEXT CHAR ?
1544	004736	011173			BNE 38 ;NO ?
1545	004740	011173			INC R4 ;GENERATE NEXT CHAR
1546	004742	110477	005132		MOVB R4,2TXDBUF ;LOAD NEXT CHARACTER
1547	004746	005767	174166	38:	TST SHIFT ;IS IT 0 ?
1548	004752	001346			BNE 28
1549	004754	105777	005100		TSTB 28,CSR ;RXDONE = 1 ?
1550	004760	100401			BMI 58
1551	004762	104000			HLT ;RXDONE SHOULD BE SET
1552	004764			58:	
1553	004764	017701	005074		MOV 2RXDBUF,R1 ;ACTUAL
1554	004770	010001			CMP R0,R1 ;COMPARE EXP VS ACT
1555	004772	001401			BEQ 68
1556	004774	104002			HLT 2 ;CHARACTERS SHOULD COMPARE
1557	004776			68:	
1558					:CHECK OUT MODEM BYPASS JUMPER
1559	004776	105704			TSTB R4 ;LAST CHARACTER ?
1560	005000	001327			BNE 18 ;NO
1561	005002	104400		48:	SCOPE

DZDUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 37
 DZDUF.C.P11 05-AUG-76 00:00

SEQ 0034

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1562      ; THIS TEST PERFORMS A BINARY COUNT DATA PATTERN ON
1563      ; BOTH THE TRANSMITTER AND RECEIVER LOGIC
1564      ; MODE: SYNC EXTERNAL (SYNEXT)
1565      ; LENGTH: EIGHT PLUS PARITY
1566      ; PARITY: 000PAR
1567      ; MAINT. MODE: NEXT
1568
1569      005004 012767 000010 174114 TSTB:  MOV  #8,TSTNO      ;SAVE THIS
1570      005012 012767 005264 174076      MOV  #.FOP,NEXT      ;GO TO THIS TEST WHEN THRU
1571      005020 105767 174157      TSTB  JUMP,JY      ;JUMP AROUND TEST ?
1572      005024 105116      BPI  48      ;YES ?
1573      005026 052777 000400 005040      BIS  #MRESET,ATXCSR ;MASTER RESET
1574      005034 012777 020000 005026      MOV  #SYNEXT,APARCSR ;SET THE MODE
1575      005042 052777 000400 005024      BIS  #MRESET,ATXCSR ;MASTER RESET
1576
1577      ;SET MAINTENANCE MODE & SEND
1578      ;NOTE: BIT WINDOW&CLK ARE CLEARED (MTDATA=0)
1579      005050 012777 010020 005016      MOV  #NEXT!SEND,ATXCSR
1580
1581      ;SET MODE # OF BITS, PARITY SENSE & LOAD SYNC REG
1582      005056 012777 027026 005004      MOV  #SYNEXT!EIGHT!000PAR!26,APARCSR
1583      005064 016703 004774      MOV  RXDBUF,R3      ;SETUP FOR ERROR MSG
1584      005070 005004      CLR  R4      ;FOR DATA CHAR CATION
1585      005072 110477 005002      MOVB  R4,ATXDOF      ;LOAD CHARACTER
1586      005076 052777 000020 004754      BIS  #SYNCH,ATXCSR ;SET SEARCH SYNC
1587
1588      ;GET INTO SYNCHRONIZATION
1589      005104 052777 020000 004762      BIS  #CLK,ATXCSR ;POKE CLK UP
1590      005112 016702 174020      ;WAIT FOR CABLE & DRIVER DELAYS
1591      005116      MOV  HOLD,R2 ;WAIT THIS AMT
1592      005116 005302      648:  DEC  R2      ;WAIT
1593      005120 001376      BNE  648
1594
1595      ;EXIT...
1596      005122 042777 020000 004744      BIC  #CLK,ATXCSR ;POKE CLK DOWN
1597      005130 016702 174002      ;WAIT FOR CABLE & DRIVER DELAYS
1598      005134      MOV  HOLD,R2 ;WAIT THIS AMT
1599      005134 005302      658:  DEC  R2      ;WAIT
1600      005136 001376      BNE  658
1601
1602      ;EXIT...
1603      005140 012767 000011 173772 18:  MOV  #9,SHIFT      ;# OF SHIFTS
1604      005146 010400      MOV  R4,R0 ;EXPECTED
1605      005150      28:
1606      005150 052777 020000 004716      BIS  #CLK,ATXCSR ;POKE CLK UP
1607      005156 016702 173754      ;WAIT FOR CABLE & DRIVER DELAYS
1608      005162      MOV  HOLD,R2 ;WAIT THIS AMT
1609      005162 005302      668:  DEC  R2      ;WAIT
1610      005164 001376      BNE  668
1611
1612      ;EXIT...
1613      005166 042777 020000 004700      BIC  #CLK,ATXCSR ;POKE CLK DOWN
1614      005174 016702 173736      ;WAIT FOR CABLE & DRIVER DELAYS
1615      005180      MOV  HOLD,R2 ;WAIT THIS AMT
1616      005180 005302      678:  DEC  R2      ;WAIT
1617      005182 001376      BNE  678

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J03

DZDUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 38
 DZDUF.C.P11 05-AUG-76 00:00

SEQ 0035

1618					:EXIT...
1619	005204	005367	173730		DEC SHIFT ;# OF SHIFTS
1620	00210	022767	000003	173722	CHP #3,SHIFT ;TIME TO LOAD NEXT CHAR ?
1621	00016	001003			BNE 3\$;NO ?
1622	00000	000104			INC R4 ;GENERATE NEXT CHAR
1623	00012	110477	004652		MOVB R4,2TXDBUF ;LOAD NEXT CHARACTER
1624	00006	005767	173706	3\$:	TST SHIFT ;IS IT 0 ?
1625	00002	001346			BNE 2\$
1626	00004	105777	004620		TSTB 2RXCSR ;RXDONE = 1 ?
1627	00040	100401			BMI 5\$
1628	00042	104000			HLT ;RXDONE SHOULD BE SET
1629	00044			5\$:	
1630	00050	017701	004614		MOV 2RXDBUF,R1 ;ACTUAL
1631	00050	020001			CHP R0,R1 ;COMPARE EXP VS ACT
1632	00002	001401			BEQ 6\$
1633	00004	104002			HLT 2 ;CHARACTERS SHOULD COMPARE
1634	005256			6\$:	
1635					:CHECK OUT MODEM BYPASS JUMPER
1636	005356	105704			TSTB R4 ;LAST CHARACTER ?
1637	00060	001327			BNE 1\$;NO
1638	005262	104400		4\$:	SCOPE

```

1639
1640
1641
1642
1643
1644
1645
1646 005264 104402
1647 005264 010426
1648 005270 104410 005522
1649 005274 104402 010147
1650 005300 105767 173676
1651 005304 001511
1652 005306 005767 173704
1653 005312 001007
1654 005314 104402 010161
1655 005320 016700 173672
1656 005324 000000
1657
1658 005326 000167 173726
1659 005332 012767 000010 173644 RUNIT:
1660 005340 012767 000010 173644 ZERO:
1661 005346 000241
1662 005350 006167 173644
1663 005354 103410
1664
1665 005356 036767 173636 173632
1666 005364 001762
1667 005366 004767 000034
1668 005372 000167 000174
1669 005376 012767 000001 173614 2S:
1670
1671 005404 016767 173576 173572
1672 005412 016767 173576 173572
1673 005420 004767 000002
1674 005424 000441
1675 005426 016767 173552 004126 REPLAY:
1676 005434 004767 003770
1677 005440 016767 173546 004436
1678 005446 062767 000002 173536
1679 005454 016767 173532 004434
1680 005462 062767 000002 173522
1681 005470 016767 173516 004412
1682 005476 062767 000002 173506
1683 005504 016767 173502 004400
1684 005512 016767 004366 173472
1685 005520 000207
1686
1687 005522 000001
1688 005524 006 002
1689 005526 012060
1690
1691 005530
1692 005532 005067 173400
1693 005534 005067 173464
1694 005536 005267 173364

;END OF PASS
;TYPE NAME OF TEST
;UPDATE PASS COUNT
;CHECK FOR EXIT TO ACT-11
;RESTART TEST

.EOP: TYPE
MEPASS
CONVRT ,OUTCRY
TYPE ,DEVICE
TSTB MULTO
BEQ CCC
TST ACTREG
BNE RUNIT
TYPE MCON
MOV ACTREG,RO
HALT
;SELECT SWITCHES & HIT CONTINUE
JMP .START
ADD #10,BASEADD
ADD #10,BASEIV
CLC
ROL
BCS
ROTADD
2S
BIT ROTADD,ACTREG
BEQ RUNIT
JSR PC,REPLAY
JMP RESTART
MOV #1,ROTADD

MOV KEEPADD,BASEADD
MOV KEEPIV,BASEIV
JSR PC,REPLAY
BR CCC
MOV BASEADD,DUBASE
JSR PC,DUBADD
MOV BASEIV,DURIV
ADD #2,BASEIV
MOV BASEIV,DURIS
ADD #2,BASEIV
MOV BASEIV,DUTIV
ADD #2,BASEIV
MOV BASEIV,DUTIS
MOV DURIV,BASEIV
RTS PC

OUTCRY: 1
.BYTE 6,2
RXCSR

CCC:
CLR LSTERR
CLR ERRFLG
INC PASCNT

;TYPE NAME OF TEST
;ARE YOU RUNNING MULTIPLE DEVICES ?
;NO, JUMP AROUND
;ARE ANY DEVICES ACTIVE ?
;YES
;NO
;DISPLAY ACTREG
;SELECT SOMETHING TO RUN @ ACTREG:
;PUT SW00 =1)
;START OVER AGAIN.....YOU DESELECTED EVERYTHING
;NEXT BLOCK (ADDRESSES)
;NEXT BLOCK (VECTORS)
;UP DATE ROTATING POINTER
;IS IT THE LAST DEVICE
;TO BE TESTED IN THIS PASS ?
;TEST THIS DEVICE FOR ACTIVE STATUS
;IF NOT ACTIVE, TRY NEXT ADDRESS
;CALCULATE NEW PARAMETERS
;YES IT WAS ACTIVE, TEST THIS DEVICE
;OK!, NOW SET UP ROTATING
;POINTER FOR NEXT MULTIPLE PASS
;RESTORE BASE ADDRESS
;RESTORE BASE INTERRUPT VECTORS
;CALC NEW PARAMETERS
;JUMP AROUND REPLAY
;SET UP FOR NEW ADDRESSES
;CREATE NEW ADDRESSES
;CREATE DURIV
;CREATE DURIS
;CREATE DUTIV
;CREATE DUTIS
;RESTORE

```

```

1695 005544 016777 173360 173330      MOV      PASCNT,ALIGHTS      ;DISPLAY PASS COUNT
1696 005552 013701 000042      MOV      #42,R1      ;CHECK FOR ACT-11 OR DOP
1697 005556 001405      BEQ      RESTART      ;IF NOT, CONTINUE TESTING
1698 005560 000005      RESET
1699 005562 004711      LOGICAL: JSR      PC,(R1)
1700 005564 000240      NOP
1701 005566 000240      NOP
1702 005570 000240      NOP
1703 005572 012767 000340 172176 RESTART: MOV      #340,PS      ;PREVENT INTERRUPTS (PRIO: 7)
1704 005560 104413      CKSWR      ;CHECK FOR IG
1705 005562 012767 002350 173304      MOV      #TST1,RTRN
1706 005560 000167 174534      JMP      TST1
1707
1708      ;SCOPE LOOP AND ITERATION HANDLER
1709
1710 0055614      .SCOPE:
1711      ;**** START OF CODE FOR THE X OR TESTER ****
1712 0055614 000424      BR      4$
1713
1714 0055616 013746 000004      MOV      #4,-(SP)      ;IF RUNNING ON THE X OR TESTER CHANGE
1715 0055622 012737 005642 000004      MOV      #15,#4      ;THIS INSTRUCTION TO A "NOP"(NOP=240)
1716 0055630 005737 177060      TST      #177060      ;SAVE CONTENTS OF ERROR VECTOR
1717 0055634 012637 000004      MOV      (SP)+,#4      ;SET FOR TIME OUT
1718 0055640 000404      BR      2$      ;TIME OUT ON X OR ?
1719 0055642 022626      1$: CMP      (SP)+,(SP)+      ;RESTORE ERROR VECTOR
1720 0055644 012637 000004      MOV      (SP)+,#4      ;GO TO NEXT TEST
1721 0055650 000403      BR      3$      ;CLEAR THE STACK AFTER A TIMEOUT
1722 0055652 016767 173240 173234 2$: MOV      NEXT,RTRN      ;RESTORE ERROR VECTOR
1723 0055660 016716 173230 3$: MOV      RTRN,(SP)      ;LOOP ON PRESENT TEST
1724 0055664 000002      RTI      ;SET UP NEXT TEST IN RTRN
1725 0055666      4$:      ;**** END OF CODE FOR THE X OR TESTER ****      ;SET UP STACK FOR RTI
1726 0055666 104413      CKSWR      ;CHECK FOR IG
1727 0055670 032777 040000 173202      BIT      #SW14,PSWR      ;LOOP ON CURRENT TEST ?
1728 0055676 001407      TTST: BEQ      1$
1729 0055700 000432      BR      3$
1730 0055702 105777 173176      TSTB      #TKCSR      ;TEST TTY FLAG
1731 0055706 100027      BPL      3$
1732 0055710 017700 173172      MOV      #TKDBR,R0      ;CLR DONE BIT
1733 0055714 000412      BR      2$      ;IF A TTY KEY IS STRUCK GO TO NEXT TST
1734 0055716 032777 004000 173154 1$: BIT      #SW11,PSWR      ;INHIBIT ITERATIONS ?
1735 0055724 001006      BNE      2$
1736 0055726 005267 173172      INC      LPCNT
1737 0055732 026767 173166 173162      CMP      LPCNT,ICOUNT      ;CHECK FOR ITERATION CNT FINISH
1738 0055740 101412      BLOS      3$
1739 0055742 105067 173256      2$: CLRB      ERRFLG
1740 0055746 005067 173152      CLR      LPCNT
1741 0055752 012767 000005 173142      MOV      #5,ICOUNT      ;SET UP ITERATION COUNT
1742 0055760 016767 173132 173126      MOV      NEXT,RTRN      ;SET UP NEXT TEST IN RTRN
1743 0055766 016716 173122      3$: MOV      RTRN,(SP)      ;SET UP STACK FOR RTI
1744 0055772 000002      RTI
1745 0055774 001407      BRW: 1407      ;RESTORE "BEQ 1$" INSTRUCTION
1746 0055776 000432      BRX: 432      ;RESTORE "BR 3$" INSTRUCTION
1747
1748      ;CHECK FOR FREEZE ON CURRENT DATA
1749
1750 006000 104413      .SCOPE: CKSWR      ;CHECK FOR IG

```

M03

DZDUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 41
 DZDUF-C.P11 05-AUG-76 00:00

SEQ 0038

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1751 006002 032777 001000 173070      BIT      #SW09, @SWR
1752 006010 001402                BEQ      1$
1753 006012 016716 173102                MOV      LOCK, (SP)
1754 006016 000002                1$: RTI
1755
1756                                ;TELETYPE OUTPUT ROUTINE
1757
1758 006020 010546                .TYPE: MOV      R5, -(SP)
1759 006022 017605 000002                MOV      @2(SP), R5
1760 006026 062766 000002 000002                ADD      #2, 2(SP)
1761 006034 105715                1$: TSTB      (R5)                ;LOOK FOR "0"
1762 006036 001406                BEQ      3$
1763 006040 105777 173044                2$: TSTB      @TPCSR                ;TEST DONE BIT
1764 006044 100375                BPL      2$
1765 006046 112577 173040                MOVB      (R5)+, @TPDBR                ;TYPE CHAR
1766 006052 000770                BR       1$                ;DO IT AGAIN UNTIL "0" IS SEEN
1767 006054 012605                3$: MOV      (SP)+, R5
1768 006056 000002                RTI
1769
1770                                ;ASCII STRING INPUT ROUTINE
1771
1772 006060 010346                .INSTR: MOV      R3, -(SP)
1773 006062 010446                MOV      R4, -(SP)
1774 006064 017667 000004 000010                MOV      @4(SP), MSG
1775 006072 062766 000002 000004                ADD      #2, 4(SP)
1776 006100 104402                .INST1: TYPE
1777 006102 000000                .MSG: 0
1778 006104 012704 011214                MOV      @INBUF, R4
1779 006110 012703 000007                MOV      #7, R3
1780 006114 105777 172764                1$: TSTB      @TKCSR
1781 006120 100375                BPL      1$
1782 006122 117714 172760                MOVB      @TKDBR, (R4)
1783 006126 142714 000200                BICB      #200, (R4)
1784 006132 121427 000025                CMPB      (R4), #25                ;IS IT <10>
1785 006136 001003                BNE      200$
1786 006140 104402 010336                TYPE, MCRLF
1787 006144 000755                BR       .INST1
1788 006146 122427 000015                200$: CMPB      (R4)+, #15
1789 006152 001423                BEQ      INSTR2
1790 006154 117777 172726 172730                MOVB      @TKDBR, @TPDBR
1791 006162 105777 172722                2$: TSTB      @TPCSR
1792 006166 100375                BPL      2$
1793 006170 005303                DEC      R3
1794 006172 001350                BNE      1$
1795 006174 000702                BR       .INSTG
1796 006176 010346                .INSTE: MOV      R3, -(SP)
1797 006180 010446                MOV      R4, -(SP)
1798 006182 104402                .INSTG: TYPE
1799 006184 010332                MOV      R4M
1800 006186 005737 007474                TST      @RDSW
1801 006188 001402                BEQ      400$
1802 006190 104402 010336                TYPE, MCRLF
1803 006192 000727                400$: BR       .INST1
1804 006194 012604                INSTR2: MOV      (SP)+, R4
1805 006196 012603                MOV      (SP)+, R3
1806 006198 000002                RTI

```



```

1807
1808                                     ;CONVERT ASCII STRING TO OCTAL
1809
1810 .PARAM: MOV      RS, -(SP)
1811          MOV      R4, -(SP)
1812          MOV      4(SP), R5
1813          MOV      (R5)+, LOLIM
1814          MOV      (R5)+, HILIM
1815          MOV      (R5)+, DEVAOR
1816          MOV      (R5)+, LOBITS
1817          MOV      (R5)+, AORCNT
1818          MOV      RS, 4(SP)
1819 PARAM1: CLR      RS
1820          MOV      #INBUF, R4
1821          CMPB     #15, (R4)
1822          BEQ      PARERR
1823 1S:      CMPB     (R4), #60
1824          BLT      PARERR
1825          CMPB     (R4), #67
1826          BGT      PARERR
1827          BICB     #60, (R4)
1828          BISB     (R4)+, RS
1829          CMPB     #15, (R4)
1830          BEQ      LIMITS
1831          ASL      RS
1832          ASL      RS
1833          ASL      RS
1834          BR       1S
1835 PARERR: CMPB     #15, (R4)          ;IS FIRST CHARACTER A <CR>
1836          BNE      120$
1837          TST      #RDSW           ;IS CKSWR ROUTINE BEING USED
1838          BNE      PARTI
1839 120$:  INSTER
1840          BR       PARAM1
1841
1842                                     ;TEST TO SEE IF NUMBER IS WITHIN LIMITS
1843
1844 LIMITS: CMP      RS, HILIM
1845          BHI      PARERR
1846          CMP      RS, LOLIM
1847          BLO      PARERR
1848          BITB     LOBITS, RS
1849          BNE      PARERR
1850
1851                                     ;STORE NUMBER AT SPECIFIED ADDRESS
1852
1853 1S:      MOV      DEVAOR, R4
1854          MOV      RS, (R4)+
1855          ADD      #2, RS
1856          DECB     AORCNT
1857          BNE      1S
1858 PARTI:  MOV      (SP)+, R4
1859          MOV      (SP)+, RS
1860          RTI
1861 LOLIM:  0
1862 HILIM:  0

```



```

1863 006440 000000      DEVAR: 0
1864 006442 000000      LOBITS: 0
1865      006443      ADRCNT=LOBITS+1
1866
1867      ;SAVE PC OF TEST THAT FAILED AND RO-R5
1868
1869 006444 016667 000004 172522 .SAV05: MOV      4(SP),SAVPC
1870
1871      ;SAVE RO-R5
1872
1873 006452 010567 172512      SV05:  MOV      R5,SAVR5
1874 006456 010467 172504      MOV      R4,SAVR4
1875 006462 010367 172476      MOV      R3,SAVR3
1876 006466 010267 172470      MOV      R2,SAVR2
1877 006472 010167 172462      MOV      R1,SAVR1
1878 006476 010067 172451      MOV      R0,SAVR0
1879 006502 000002      RTI
1880
1881      ;RESTORE RO-R5
1882
1883 006504 016700 172446      .RES05: MOV      SAVR0,R0
1884 006510 016701 172444      MOV      SAVR1,R1
1885 006514 016702 172442      MOV      SAVR2,R2
1886 006520 016703 172440      MOV      SAVR3,R3
1887 006524 016704 172436      MOV      SAVR4,R4
1888 006530 016705 172434      MOV      SAVR5,R5
1889 006534 000002      RTI
1890
1891      ;CONVERT OCTAL NUMBER TO ASCII AND OUTPUT TO TELEPRINTER
1892
1893 006536 104402      .CONVR: TYPE
1894 006540 010336      MCRLF
1895 006542 010046      .CNVRT: MOV      R0,-(SP)
1896 006544 010146      MOV      R1,-(SP)
1897 006546 010346      MOV      R3,-(SP)
1898 006550 010446      MOV      R4,-(SP)
1899 006552 010546      MOV      R5,-(SP)
1900 006554 017601 000012      MOV      2(12(SP),R1
1901 006556 016767 012470 172362      MOV      TEMP,TEMP3
1902 006566 062766 000002 000012      ADD      #2,12(SP)
1903 006574 012167 000154      MOV      (R1)+,WPCNT
1904 006600 112167 000152      15:  MOV8    (R1)+,CHRCNT
1905 006604 112167 000147      MOV9    (R1)+,SPACNT
1906 006610 013167 000144      MOV      2(R1)+,BINWRO
1907 006614 016704 000140      25:  MOV      BINWRO,R4
1908 006620 116705 000132      MOV8    CHRCNT,R5
1909 006624 012700 011254      MOV      #TEMP,R0
1910 006630 010403      35:  MOV      R4,R3
1911 006632 042703 177770      BIC      #177770,R3
1912 006636 062703 000060      ADD      #060,R3
1913 006642 110320      MOV8    R3,(R0)+
1914 006644 006204      RSR      R4
1915 006646 042704 100000      BIC      #10000,R4
1916 006652 006204      RSR      R4
1917 006654 006204      RSR      R4
1918 006656 005306      DEC      R5

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```

;SHIFT FOR NEXT #
;CLUGE TO STOP BIT 15 PROPAGATING.
;DITTO
;DITTO

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1919 006660 001363      BNE      3S
1920 006662 012703 011314  MOV      #MDATA,R3
1921 006664 114723      4S:  MOV      -(R0),(R3)+
1922 006666 105367 000062  DECB     CHRCNT
1923 006668 001374      BNE      4S
1924 006670 105767 000055  TSTB     SPACNT
1925 006672 001405      BEQ      6S
1926 006674 112723 000040  5S:  MOV      #040,(R3)+
1927 006676 105367 000043  DECB     SPACNT
1928 006678 001373      BNE      5S
1929 006680 105013 6S:  CLRB     (R3)
1930 006682 104402  TYPE
1931 006684 011314  MDATA
1932 006686 005367 000024  DEC      WROCNT
1933 006688 001323      BNE      1S
1934 006690 016767 172212 002314  MOV      TEMP3,TEMP
1935 006692 012625  MOV      (SP)+,R5
1936 006694 012604  MOV      (SP)+,R4
1937 006696 012603  MOV      (SP)+,R3
1938 006698 012601  MOV      (SP)+,R1
1939 006700 012620  MOV      (SP)+,R0
1940 006702 000002  RTI
1941 006704 000000  WROCNT: 0
1942 006706 000000  CHRCNT: 0
1943 006708 006757  SPACNT=CHRCNT+1
1944 006710 000000  BINR0: 0

;COMPARE THE FIRST CHARACTER IN THE TELETYPE INPUT
;BUFFER TO THE CHARACTERS "N" AND "Y"
;IF THE CHARACTER IS "N" CLEAR THE FLAG
;IF THE CHARACTER IS "Y" SET THE FLAG

1945 006712 017605 000000  .SETFLG: MOV      @ (SP),R5
1946 006714 122767 000116 002220  CMPB     @'N,INBUF      ;IS IT "N" ?
1947 006716 001002      BNE      1S
1948 006718 105015      CLRB     (R5)      ;000
1949 006720 000406      BR      2S
1950 006722 122767 000131 002204  1S:  CMPB     @'Y,INBUF      ;IS IT "Y" ?
1951 006724 001005      BNE      3S
1952 006726 112715 177777      MOV      @-1,(R5)      ;377
1953 006728 062716 000002  2S:  ADD      @2,(SP)
1954 006730 007002      RTI
1955 006732 104404  3S:  INSTER      ;RETRY
1956 006734 000755      BR      .SETFLG
1957 006736      ;TRAP DISPATCH SERVICE
1958 006738      ;ARGUMENT OF TRAP IS EXTRACTED
1959 006740      ;AND USED AS OFFSET TO OBTAIN POINTER
1960 006742      ;TO SELECTED SUBROUTINE

1961 006744      .TRPSR: MOV      (SP),-(SP)      ;GET PC OF RETURN
1962 006746      SUB      @2,(SP)      ;PC OF TRAP
1963 006748      MOV      @ (SP),(SP)      ;GET TRAP
1964 006750      ASL      (SP)      ;MULTIPLY TRAP ARG BY 2
1965 006752      BIC      @177001,(SP)      ;CLEAR UNWANTED BITS
1966 006754      ADD      @.TRPTAB,(SP)      ;POINTER TO SUBROUTINE ADDRESS
1967 006756      MOV      @ (SP),(SP)      ;SUBROUTINE ADDRESS
1968 006758 011646      .TRPSR: MOV      (SP),-(SP)      ;GET PC OF RETURN
1969 006760 162716 000002      SUB      @2,(SP)      ;PC OF TRAP
1970 006762 017616 000000      MOV      @ (SP),(SP)      ;GET TRAP
1971 006764 006316      ASL      (SP)      ;MULTIPLY TRAP ARG BY 2
1972 006766 042716 177001      BIC      @177001,(SP)      ;CLEAR UNWANTED BITS
1973 006768 062716 001226      ADD      @.TRPTAB,(SP)      ;POINTER TO SUBROUTINE ADDRESS
1974 006770 017616 000000      MOV      @ (SP),(SP)      ;SUBROUTINE ADDRESS

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1975 007060 000136 JNP 2(SP)+ ;GO TO SUBROUTINE
1976
1977 ;ERROR HANDLER
1978
1979 007062 104413 .HLT: CKSWR ;CHECK FOR IG
1980 007064 032777 020000 172006 BIT #SW13,2SWR ;INHIBIT ERROR TYPE OUT ?
1981 007072 001061 BNE HALTS
1982 007074 011667 172034 CMP (SP),LSTERR
1983 007100 011667 BEQ IS
1984 007102 011667 172026 MOV (SP),LSTERR
1985 007106 10567 172112 CLR8 ERRFLG
1986 007112 104406 IS: SAVOS
1987 007114 011667 MOV (SP),RS
1988 007116 16275 000002 SUB #2,RS
1989 007122 011504 MOV (RS),R4
1990 007124 006304 RSL R4
1991 007126 051504 ADD (RS),R4
1992 007130 011504 RSL R4
1993 007132 011504 BIC #177001,R4
1994 007136 062704 012030 ADD #ERRTAB,R4
1995 007142 012467 000040 MOV (R4)+,ERRMSG
1996 007146 012467 010046 MOV (R4)+,DATAHD
1997 007152 011467 000054 MOV (R4),DATAPP
1998 007156 105767 172042 TSTB ERRFLG
1999 007162 001403 BEQ TYPMSG
2000 007164 005767 000042 TST DATAPP
2001 007170 001014 BNE TYPDAT
2002 007172 104410 TYPMSG: CONVRT
2003 007174 007324 177777 172020 ERTAB0
2004 007176 112767 MOV8 #1,ERRFLG
2005 007204 104402 TYPE
2006 007206 000000 ERRMSG: 0
2007 007210 005767 000004 TST DATAHD
2008 007214 001402 BEQ TYPDAT
2009 007216 104402 TYPE
2010 007220 000000 DATAHD: 0
2011 007222 005767 000004 TYPDAT: TST DATAPP
2012 007226 001402 BEQ RESREG
2013 007230 104410 CONVRT
2014 007232 000000 DATAPP: 0
2015 007234 104407 RESREG: RESOS
2016 007236 005777 171636 HALTS: TST 25 R
2017 007242 100005 BPL EXITER
2018 007244 010046 PUSHRO
2019 007246 016600 000002 MOV 2(SP),R0
2020 007250 000000 HALT
2021 007254 012600 POPRO
2022 007256 104413 EXITER: CKSWR
2023 007260 005267 171646 INC ERRCNT
2024 007264 032777 000400 171606 BIT #SW08,2SWR
2025 007272 001007 BNE IS
2026 007274 032777 002000 171576 BIT #SW10,2SWR
2027 007202 001407 BEQ 25
2028 007204 016767 171606 171602 MOV NEXT,RTN
2029 007312 012706 001100 IS: MOV #STACK,SP
2030 007316 000177 171572 JMP 2RTN

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E04

DZDUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 46
 DZDUF-C.P11 05-AUG-76 00:00

SEQ 0043

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2031 007322 000002      26: RTI
2032 007324 000001      LRTABO: 1
2033 007326 006        002      .BYTE 6,2
2034 007330 001174      SAVPC
2035      ;ENTER HERE ON POWER FAILURE
2036
2037
2038 007332 010046      .PFAIL: MOV R0,-(SP)      ;SAVE R0-R5 ON PROCESSOR STACK
2039 007334 010146      MOV R1,-(SP)
2040 007336 010246      MOV R2,-(SP)
2041 007340 010346      MOV R3,-(SP)
2042 007342 010446      MOV R4,-(SP)
2043 007344 010546      MOV R5,-(SP)
2044 007346 016746 170452  MOV R6,-(SP)
2045 007352 010667 171614  MOV SP,SAVSP      ;SAVE STACK POINTER
2046 007356 012767 007370 170440  MOV #RESTART,24      ;SET UP FOR POWER UP TRAP
2047 007364 001000      HALT      ;HALT ON POWER DOWN NORMAL
2048 007366 000777      IS: BR IS
2049
2050      ;PROCESSOR WILL TRAP HERE WHEN POWER IS RESTORED
2051
2052 007370 016706 171576  RESTAR: MOV SAVSP,SP      ;RESTORE STACK POINTER
2053 007374 012605      MOV (SP)+,R5      ;RESTORE R0-R5
2054 007376 012634      MOV (SP)+,R4
2055 007380 011503      MOV (SP)+,R3
2056 007382 011502      MOV (SP)+,R2
2057 007384 011501      MOV (SP)+,R1
2058 007386 011500      MOV (SP)+,R0
2059 007390 012767 007332 170406  MOV #.PFAIL,24      ;SET UP FOR POWER FAILURE
2060 007392 012767 000340 170352  MOV #340,PS
2061 007394 012706 001100      MOV #STACK,SP
2062 007396 005067 001620      CLR TEMP
2063 007398 005267 001614      IS: INC TEMP
2064 007400 001375      BNE IS
2065 007402 104410      CONVRT
2066 007404 007466      PFTAB
2067 007406 104402      TYPE
2068 007408 010341      MPFAIL
2069 007410 00167 171546      CLR ERPF LG
2070 007412 00167 171522      CLR LSTERR
2071 007414 00177 171426      JMP #RTN
2072 007416 000301      PFTAB: 1
2073 007420 006        002      .BYTE 6,2
2074 007422 001114      RTRN
2075
2076
2077      ;CHECK SWITCH REGISTER ROUTINE. CHECKS FOR 16 TO ALLOW CHANGING
2078      ;OF LOC. 176.
2079      ;LOCATIONS USED:
2080 007474 000000      RDSW: .WORD 0
2081
2082
2083 007476 005737 000042      .CKSWR: TST #42
2084 007502 001042      BNE OUT
2085 007504 022767 000176 171366  CMP #S.WREG,SWR      ;SOFTWARE SWITCH REGISTER PRESENT
2086 007512 001036      BNE OUT      ;NO, GET OUT

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604

DZDUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 48
DZDUF.C.P11 05-AUG-76 00:00

SEQ 0045

2143 010110 053111 051105 041440
2144 010116 047117 051124 046117
2145 010124 051040 043505 051511
2146 010132 042524 020122 042101
2147 010140 051104 051505 026523
2148 010146 000
2149 010147 075 042504 044526
2150 010154 042503 020040 000
2151 010161 015 044012 053517
2152 010166 047040 053517 041040
2153 010174 047522 047127 041440
2154 010200 053517 020077 027056
2155 010210 051456 046105 041505
2156 010216 020124 047523 042515
2157 010224 044124 047111 020107
2158 010230 047524 051040 047125
2159 010240 040040 041501 051124
2160 010246 043505 000
2161 010251 015 047412 052125
2162 010256 047440 020106 040522
2163 010254 043516 035105 042522
2164 010272 054524 042520 046040
2165 010270 051501 020124 042504
2166 010276 044 6 047503 051040
2167 010314 041500 051123 040440
2168 010322 042104 042522 051523
2169 010330 07055
2170 010332 04040 000077
2171 010336 05015 000
2172 010341 05000 053517
2173 010346 051105 047522 041501
2174 010354 043514 047522 041504
2175 010352 051120 047522 042123
2176 010357 020115 047522 042101
2177 010376 051101 020124 042101
2178 010404 043040 051505 047522
2179 010412 047111 051505 047522
2180 010420 051107 051505 047522
2181 010428 043015 047522 047522
2182 010436 043015 047522 047522
2183 010444 043015 047522 047522
2184 010452 043015 047522 047522
2185 010457 043015 047522 047522
2186 010457 043015 047522 047522
2187 010457 043015 047522 047522
2188 010457 043015 047522 047522
2189 010457 043015 047522 047522
2190 010457 043015 047522 047522
2191 010457 043015 047522 047522
2192 010457 043015 047522 047522
2193 010457 043015 047522 047522
2194 010457 043015 047522 047522
2195 010457 043015 047522 047522
2196 010457 043015 047522 047522
2197 010457 043015 047522 047522
2198 010457 043015 047522 047522
2199 010457 043015 047522 047522
2200 010457 043015 047522 047522

DEVICE: .ASCIZ /=DEVICE /

MCOM: .ASCIZ <15><12>/HOW NOW BROWN COM? ...SELECT SOMETHING TO RUN @ACTREG/

MRANGE: .ASCIZ <15><12>/OUT OF RANGE:RETYPE LAST DEVICE RXCSR ADDRESS-/

MM: .ASCIZ / ?/

MCRLF: .ASCIZ <15><12>

MPFAIL: .ASCIZ / POWER FAILURE, PROGRAM RESTART AT TEST IN PROGRESS/

MEPASS: .ASCIZ <15><12>/END OF PASS TAPE F/

MR: .ASCIZ <15><12>/R/

MTSTPC: .ASCIZ <15><12>/TEST PC-/

MLOCK: .ASCIZ <15><12>/LOCK ON SELECTED TEST? (Y OR N)-/

MLEVEL: .ASCIZ <15><12>/DU PRIORITY LEVEL-/

MSYNC: .ASCIZ <15><12>/# OF SYNC CHARS SELECTED (1 OR 2)-/


```
INBUF: .BLKB 40
TEMP: .BLKB 40
```

011214 000040
011254 000040

```

2295 011314 000040      MDATA: .BLKB 40
                          ;*****
                          ;UTILITIES
                          ;*****
                          ;THIS UTILITY CALCULATES PRIORITY LEVEL
DULEV: ASL      DUPRT      ;SHIFT LEFT
        ASL      DUPRT
        ASL      DUPRT
        ASL      DUPRT
        ASL      DUPRT
        MOV      DUPRT,LESS1 ;MOVE THIS TO LESS1
        SUB      #1,LESS1    ;CREATE LESS1
        BIC      #37,LESS1   ;CLEAR TNZVC
        RTS
        PC
DUPRT:  LEVEL5
LESS1:  LEVEL4 ;LEVEL TO ALLOW INTERRUPTS

        ;NEW DU ADDRESSES
DUAADR: MOV      DI      ,RXCSR ;XXX0
        INC      DI
        MOV      DI      ,HRXCSR ;XXX1
        INC      DI
        MOV      DI      ,RXDBUF ;XXX2
        MOV      DI      ,PARCSR ;XXX2
        INC      DI
        MOV      DUBASE,HRXDBUF ;XXX3
        MOV      DUBASE,HPARCSR ;XXX3
        INC      DUBASE
        MOV      DI      ,TXCSR ;XXX4
        INC      DI
        MOV      DI      ,HTXCSR ;XXX5
        INC      DI
        MOV      DI      ,TXDBUF ;XXX6
        INC      DI
        MOV      DUBASE,HTXDBUF ;XXX7
        RTS
        PC
DUBASE: 0

        ;THIS UTILITY POKES THE MAINT DATA BASED UPON THE
        ;INFORMATION CONTAINED IN TEMP1 AND IT IS
        ;SHIFTED IN BY THE CONTENTS OF SHIFT
RPOKE:  BIC      MTDATA,ATXCSR
        CLR      TEMP2
        ROR      TEMP1 ;FORCE CARRY
        ROR      TEMP2 ;PICK UP CARRY IN BIT 15
        ASR      TEMP2 ;SHIFT INTO BIT 14
        BIC      #BIT15,TEMP2 ;CLR BIT 15
        BIS      TEMP2,ATXCSR ;POKE MAINT DATA
        BIC      #CLK,ATXCSR ;POKE CLK
        BIS      #CLK,ATXCSR
        DFC      SHIFT
        B.E      RPOKE
        RTS
        PC

        ;THIS ROUTINE CALCULATES ODD PARITY FOR AN 8 BIT CHAR

```


J04

DZDUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 51
 DZDUF-C.P11 05-AUG-76 00:00

SEQ 0048

```

2311 011652 016767 167266 167266 0008: MOV TEMP1,TEMP2 ;SAVE TEMP1
2312 011660 005067 167264 CLR TEMP3
2313 011664 012727 000010 MOV #8.,(PC)+
2314 011670 000000 1S: 0
2315 011672 006067 167250 2S: ROR TEMP2
2316 011676 005567 167246 ADC TEMP3
2317 011702 005367 177762 DEC 1S
2318 011706 001371 BNE 2S
2319 011710 006067 167234 ROR TEMP3
2320 011714 103404 BCS 3S
2321 011716 052767 000400 167220 BIS #BIT8,TEMP1 ;SET ODD PARITY
2322 011724 000400 BR 4S
2323 011726 042767 000400 167210 3S: BIC #BIT8,TEMP1 ;CLR EVEN PARITY
2324 011734 000207 4S: RTS PC ;TEMP1 NOW HAS ODD PARITY CHARACTER
2325 011736 016767 167202 167202 :THIS ROUTINE CALCULATES EVEN PARITY FOR AN 8 BIT CHARACTER
2326 011744 005067 167200 EVEN8: MOV TEMP1,TEMP2 ;SAVE TEMP1
2327 011750 012727 000010 CLR TEMP3
2328 011754 000000 MOV #8.,(PC)+
2329 011756 006067 167164 1S: 0
2330 011762 005567 167162 2S: ROR TEMP2
2331 011766 005367 177762 ADC TEMP3
2332 011772 001371 DEC 1S
2333 011774 006067 167150 BNE 2S
2334 012000 103004 ROR TEMP3
2335 012002 052767 000400 167134 BCC 3S
2336 012010 000403 BIS #BIT8,TEMP1 ;SET EVEN PARITY
2337 012012 042767 000400 167124 3S: BR 4S
2338 012020 000207 4S: BIC #BIT8,TEMP1 ;CLR ODD PARITY
2339 012022 062716 000002 :TEMP1 NOW HAS EVEN PARITY CHARACTER
2340 012026 000002 TRPREG: ADC #2,(SP) ;ALLOW IT TO "CRUNCH" INTO HLT BACK
2341 012030 012114 RTI ;IN MAIN PART OF THE PROGRAM
2342 012032 000000 :ERROR HLT TABLE
2343 012034 000000 .ERRTAB: EMO ;HLT 0 BIT ERROR (GENERAL)
2344 012036 012130 000000 0
2345 012040 012101 000000 EM1 ;HLT 1 REGISTER ERROR
2346 012042 012102 000000 DH1
2347 012044 012172 000000 DT1
2348 012046 012101 000000 EM2 ;HLT 2 RECEIVER ERROR
2349 012050 012102 000000 DH1
2350 012052 012234 000000 DT1
2351 012054 012301 000000 EM3 ;HLT 3 TRANSMITTER ERROR
2352 012056 012322 000000 DH1
2353 012060 160040 000000 DT1
2354 012062 160041 RXCSR: 160040 ;DEFAULT DU ADDRESSES
2355 012064 160042 HRXCSR: 160041
2356 012066 160043 RXCSR: 160042
2357 012068 160042 160043
2358 012070 160042 160043
2359 012072 160043 TXCSR: 160044
2360 012074 160044

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K04

DZDUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 52
DZDUF.C.P11 05-AUG-76 00:00

SEQ 0049

2367	012076	160045			HTXCSR: 160045
2368	012100	160046			TXDBUF: 160046
2369	012102	160047			HTXDBUF: 160047
2370					:DEFAULT DU VECTORS
2371	012104	000770			DURIV: 770 :REC INTR VECTOR
2372	012106	000772			DURIS: 772 :REC INTR STATUS
2373	012110	000774			DUTIV: 774 :XMIT INTR VECTOR
2374	012112	000776			DUTIS: 776 :XMIT INTR STATUS
2375					:ERROR MESSAGES
2376	012114	036440	042440	051122	EMO: :ASCIZ / = ERROR PC/
2377	012122	051117	050040	000103	
2378	012130	036440	051040	043505	EM1: :ASCIZ / = REGISTER ERROR PC/(15)(12)(1)/REGISTER /
2379	012136	051511	042440	020122	
2380	012144	051105	042440	020122	
2381	012152	041520	005015	051001	
2382	012160	043505	051511	042524	
2383	012166	020122	000040		
2384	012172	036440	051040	041505	EM2: :ASCIZ / = RECEIVER ERROR PC/(15)(12)(1)/REGISTER /
2385	012200	044505	042526	020122	
2386	012206	051105	047522	020122	
2387	012214	041520	005015	051001	
2388	012222	043505	051511	042524	
2389	012230	020122	000040		
2390	012234	036440	051040	040522	EM3: :ASCIZ / = TRANSMITTER ERROR PC/(15)(12)(1)/REGISTER /
2391	012242	051516	044515	052124	
2392	012250	051105	042440	051122	
2393	012256	051117	051040	008503	
2394	012264	000412	042522	044507	
2395	012272	052123	051105	020040	
2396	012300	000			
2397					:DATA HEADERS FOR ERROR MESSAGES
2398	012301	105	050130	041505	DM1: :ASCIZ /EXPECTED ACTUAL/
2399	012306	042524	030104	040440	
2400	012314	052103	040525	000114	
2401					.EVEN
2402					:DATA TABLES FOR ERROR MESSAGES
2403	012322	000003			DT1: 3
2404	012324	006	004		:BYTE 6,4
2405	012326	001164			:REGISTER
2406	012330	006	004		:BYTE 6,4
2407	012332	001156			:EXPECTED DATA
2408	012334	006	002		:BYTE 6,2
2409	012336	001160			:ACTUAL DATA
2410		000001			.END

DZDUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 54

020UFC.P11 05-AUG-76 00:00

CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0050

AAR	002046	954#													
ACTREG	001216	772#	902*	916*	917*	924*	1652	1655	1665						
ADRCNT=	006443	1817#	1856#	1865#											
BASRD	001204	767#	884#	921*	922	928*	930*	1659*	1671*	1675					
BASEIV	001212	770#	894#	1660*	1672*	1677	1678*	1679	1680*	1681	1682*	1683	1684*		
BBS	001664	901	905#												
BINMRD	006760	1906*	1907	1944#											
BITH =	002000	682#													
BITO =	000001	630#	658	689											
BITI =	000002	629#	657												
BITIO =	002000	620#	648	682											
BITII =	004000	619#	647												
BITI2 =	010000	618#	646	663											
BITI3 =	020000	617#	645	662	681										
BITI4 =	040000	616#	644	661	680										
BITI5 =	100000	615#	643	660	679	2302									
BIT2 =	000004	628#	656	997											
BIT3 =	000010	627#	655	688											
BIT4 =	000020	626#	654	687											
BIT5 =	000040	625#	653	686											
BIT6 =	000100	624#	652	685											
BIT7 =	000200	623#	651	684											
BIT8 =	000400	622#	650	666	683		2323	2338	2340						
BIT9 =	001000	621#	649	665											
BREAK =	000001	689#													
BRW	005774	994	1008	1745#											
BRX	005776	995	1009	1746#											
CARDCT=	010000	646#													
CCC	005530	1651	1674	1691#											
CHRCNT	006756	1904#	1908	1922#	1942#	1943									
CKSWR =	104413	814#	1704	1726	1750	1979	2022								
CLK =	020000	681#	1050	1051	1054	1055	1071	1072	1100	1140	1141	1144	1145	1161	
		1162	1190	1230	1231	1234	1235	1251	1252	1280	1320	1321	1324	1325	
		1341	1342	1370	1409	1410	1414	1415	1459	1460	1464	1465	1511	1518	
		1528	1535	1588	1595	1605	1612	2304	2305						
		816#	851												
CNTRLU =	104414	810#	2036												
CMVRT =	104411	803#	1648	2002	2013	2065									
COMVRT=	104410	743#	1063*	1075*	1153*	1165*	1243*	1255*	1333*	1345*					
COUNT	001142	645#													
CTS =	020000	1997#	2000	2011	2014#				</						

DZDUF-C MRCY11 27(1006) 01-OCT-76 09:48 PAGE 56

DZOUFC.P11 05-AUG-76 20:00

CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0052

LOCK	001120	731#	1040*	1130*	1220*	1310*	1753												
LOGICA	005562	1699#																	
LOKFLG	001225	783#	1002	1003															
LOLIM	006434	1813#	1846	1861#															
LPCNT	001124	733#	831#	1736*	1737	1740*													
LSTERR	001134	737#	836*	1692*	1982	1984*	2070*												
MCOM	010161	1654	2151#																
MCRLF	010336	1786	1802	1894	2103	2171#													
MDATA	011314	1920	1931	2255#															
MEPASS	010426	1647	2181#																
NEXT =	010000	693#	1502	1579															
NEXTJ	011017	981	2227#																
NINT =	004000	692#	1045	1135	1225	1315	1400	1450											
MLASTD	010066	907	2140#																
MLEVEL	010535	942	2194#																
MLOCK	010472	1000	2188#																
MMULT	010007	896	2132#																
MPFAIL	010341	2068	2172#																
MMH	010332	1799	2170#																
MR	010453	1022	2185#																
MRANGE	010251	932	2161#																
M-EGAD	007726	875	2123#																
M-RESET=	000400	683#	1041	1106	1131	1196	1221	1286	1311	1376	1394	1396	1444	1416					
		1496	1498	1573	1575														
MSYNC	010562	956	2198#																
MTDATA=	040000	680#	2297																
MTITLE	007652	842	2115#																
MTSTPC	010457	1013	2186#																
MULTO	001202	762#	898	899	991	1650													
MVECTO	007704	886	2120#																
M-TRE4	010743	977	2219#																
M-TRE5	010676	973	2212#																
M-TRE6	010630	969	2205#																
N-XT	001116	730#	1039*	1129*	1219*	1309*	1393*	1443*	1493*	1570*	1722	1742	2028						
NUPAR =	000000	675#	1044	1134	1224	1314													
OOOPAK=	001000	676#	1453	1562															
OOR	011952	2311#				</													

OZDUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 57

DZOUFC.P11 05-AUG-76 00:00

CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0053

[illegible]

DZDUF-C MACY11 27(100%) 01-OCT-76 09:48 PAGE 58
DZDUF-C.P11 05-AUG-76 00:00 CROSS REFERENCE TABLE -- USER SYMBOLS

STPSYN#	000400	650#												
SVDS	006452	1873#												
SWR	001100	720#	844#	849	853#	859	862	997	1010	1727	1734	1751	1980	2016
SURFEG	000176	2024	2026	2005										
SURFEGC	007616	711#	813	859	2085	2101	2108							
SW00	000001	582#	2106#											
SW01	000002	581#	862											
SW02	000004	580#	1010											
SW03	000010	579#												
SW04	000020	578#												
SW05	000040	577#												
SW06	000100	576#												
SW07	000400	575#	2024											
SW08	001000	574#	1751											
SW09	002000	573#	2026											
SW10	004000	572#	1734											
SW11	010000	571#												
SW12	020000	570#	1980											
SW13	040000	569#	1727											
SW14	100000	568#												
SYNCHO	001176	758#	959#	963#	1078	1168	1258	1348						
SYNXT	020000	669#	1395	1403	1445	1453	1497	1505	1574	1582				
SYNINT	030000	669#	1044	1134	1224	1314								
SYNSCH	000020	654#	1047	1107	1137	1197	1227	1287	1317	1377	1407	1457	1509	1586
SYSTST	014000	694#												
TEMP	011294	1901	1909	1934#	2062#	2063#	2254#							
TEMP1	001144	744#	2299#	2311	2321#	2223#	2328	2338#	2340#					
TEMP2	001146	745#	2293#	2300#	2301#	2302#	2303	2311#	2315#	2328#	2332#			
TEMP3	001150	746#	1901#	1934	2312#	2316#	2319#	2329#	2333#	2336#				
TEMP4	001152	747#												
TEMP5	001154	748#												
TXCSR	001104	722#	1730	1780	2097									
TXCSR	001106	723#	1732	1782	1790	2089								
TLAST	005004	1016	1640#											
TFCSR	001110	724#	1763	1791										
TFCSR	001112	725#	1765#	1790#										
TRPOK	007040	1971#												
TRPREG	012022	2343#												
TSTNO	001126	734#	837#	1039#	1128#	1218#	1308#	1392#	1442#	1462#	1569#			
TST1	002360	1015	1021	1036#	1705	1706								
TST2	002362	1039	1129#											
TST3	003324	1129	1210#											
TST4	003552	1219	1311#											

DZOUF-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 59
DZOUFC.P11 05-AUG-76 00:00 CROSS REF

SE0 0055

TXDBUF	012100	2303#	2304#	2305#	2366#									
		1052#	1053#	1142#	1152#	1232#	1242#	1322#	1332#	1406#	1420#	1456#	1470#	1508#
		1546#	1565#	1623#	2268#	2368#								
TGONE=	000200	694#												
TXINTS=	000100	685#												
TYPORT	000000	2001	2008	2011#										
TYPE =	104402	796#	842	1022	1646	1649	1654	1776	1786	1798	1802	1893	1930	2005
		2009	2037	2093	2095	2103								
TYPMMSG	007172	1999	2002#											
USER =	000000	691#												
WIND =	000001	658#												
HT	000754	1903#	1932#	1941#										
	000040	1650#												
ITG	000024	2093	2109#											
SE =	000012	552#	1039	1041#	1129	1131#	1219	1221#	1309	1311#	1393	1394#	1443	1444#
		1493	1494#	1570	1571#									
	007641	2057	2112#											
SAR	007631	2095	2110#											
SH =	000010	552#	1038	1041#	1128	1131#	1218	1221#	1308	1311#	1392	1394#	1442	1444#
		1492	1494#	1569	1571#	1640#								
SY =	000015	784#	792	794#	796#	798#	800#	802#	804#	806#	808#	810#	812#	814#
		816#	816#											
=	012340	696#	699#	709#	716#	2114#	2253#	2254#	2255#					
.BFGIN	000156	864	987#											
.CIR	007476	815	2083#											
.CNTLU	007552	817	2054#											
.CMVRT	006542	811	1875#											
.CONVR	006536	809	1893#											
.EOP	005264	1570	1645#											
.ERRTA	012030	1994	2047#											
.HLT	007063	702	1979#											
.INSTE	006176	801	1796#											
.INSTG	006202	1795	175#											
.INSTR	006060	799	1772#											
.INSTI	006108	1776#	1787	1803										
.NSG	006102	1774#	1777#	2089#	2090#	2091								
.PARRH	006230	803	1810#											
.PFALL	007132	700	877	2038#	2059									
.RESOL	006504	807	1872#											
.SECAL	006444	805	1872#											
.SECAL	006514	793	1872#											
.SECAL	006514	795	1872#											
.SETPL	006726	813	1872#	1952										
.START	001256	712	877	838	1658									

F05

DZDUFC-C MACY11 27(1006) 01-OCT-76 09:48 PAGE 62

DZDUFC.P11 05-AUG-76 00:00

CROSS REFERENCE TABLE -- MACRO NAMES

SEQ 0057

SWORDF 552#
SWORD0 552#
SWORDP 552#

. ABS. 012340 000

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

DZDUFC, DZDUFC/CRF/SOL=HELLO.P11, PARA.P11, KEET.P11, DZDUFC.P11

RUN-TIME: 16 23 2 SECONDS

RUN-TIME RATIO: 117/42=2.7

CORE USED: 18K (35 PAGES)

Special routine 8 Second, 37 hrs., 214 disk reads, 2 disk writes, 57 pages

[illegible]